

Stave production monitoring

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28/02/2019

Monitoring from January 2018 to 28/02/2019

Stave meeting

HS monitoring

HSs of previous week

D-OL-HS-L-013: 0 bad chips

D-OL-HS-U-013: 0 bad chips

B-ML-HS-L-028: 0 bad chips

F-OL-HS-L-018: 0 bad chips

F-OL-HS-U-018: 0 bad chips

T-OL-HS-L-027: 0 bad chips

T-OL-HS-U-027: 0 bad chips

HSs of this week

A-OL-HS-U-012: 0 bad chips

B-ML-HS-U-029: 0 bad chips

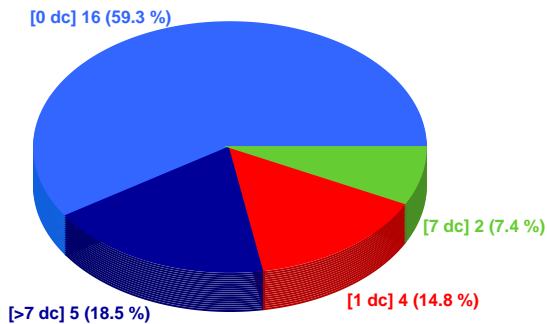
A-OL-HS-L-015: 1 bad chips

D-OL-HS-U-014: 0 bad chips

F-OL-HS-L-019: 0 bad chips

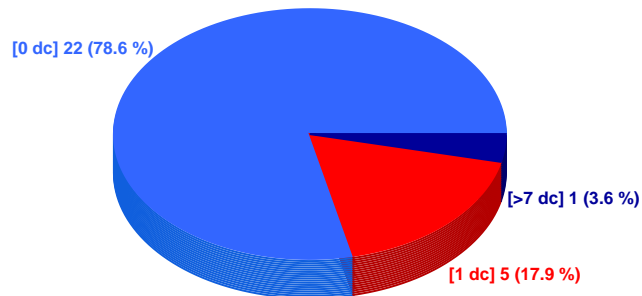
HS - Nikhef

74.07 % ok



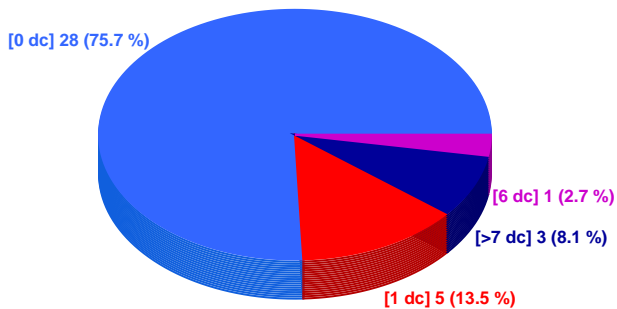
HS - Daresbury

96.43 % ok



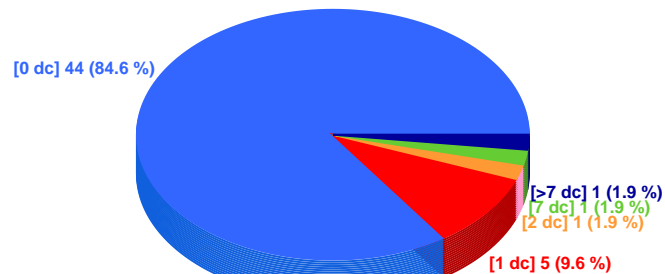
HS - Frascati

89.19 % ok



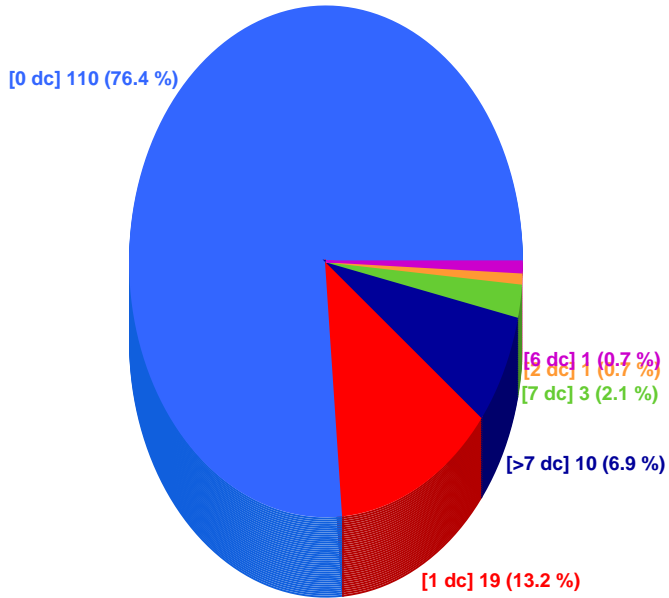
HS - Turin

96.15 % ok



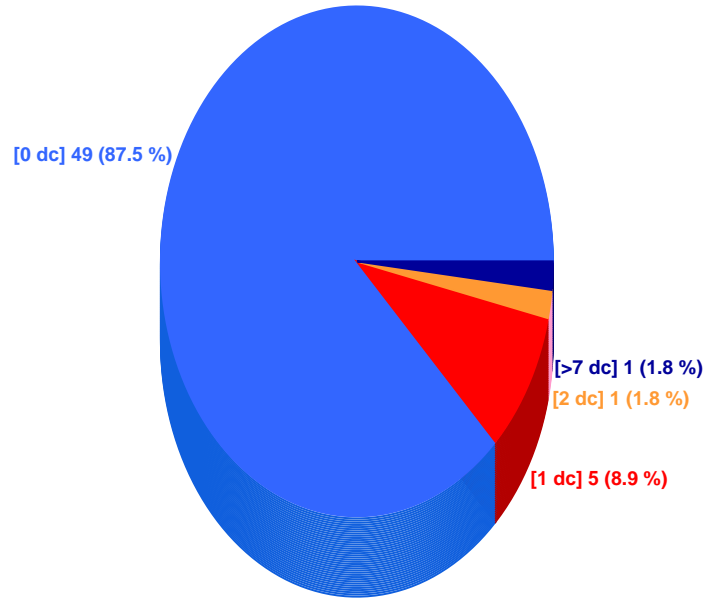
HS - OL

90.28 % ok

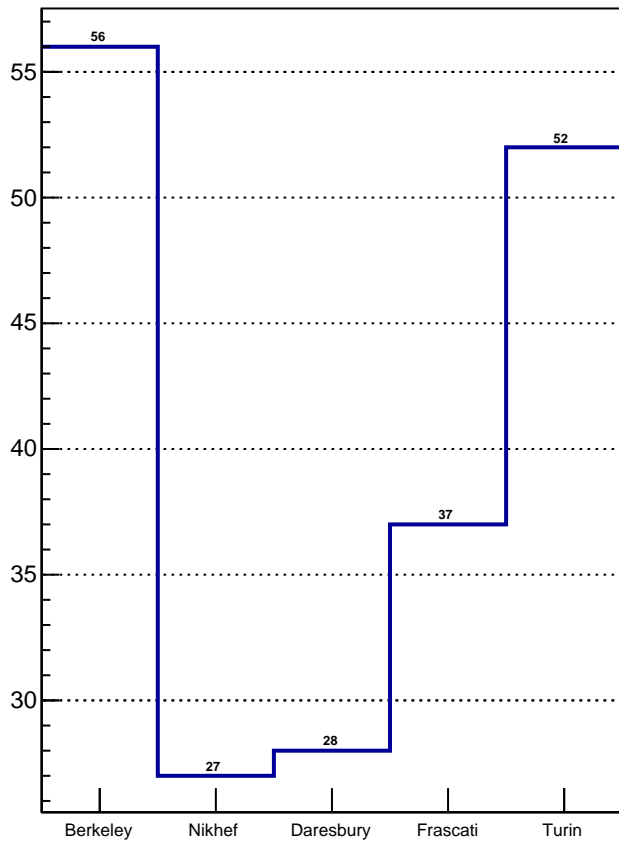


HS - ML

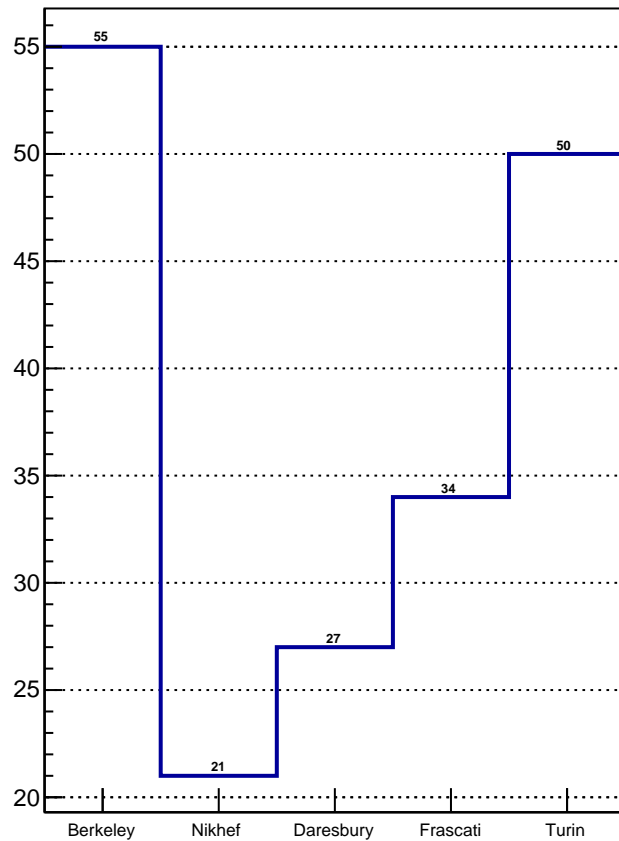
96.43 % ok



All HS



Det. Grade HS

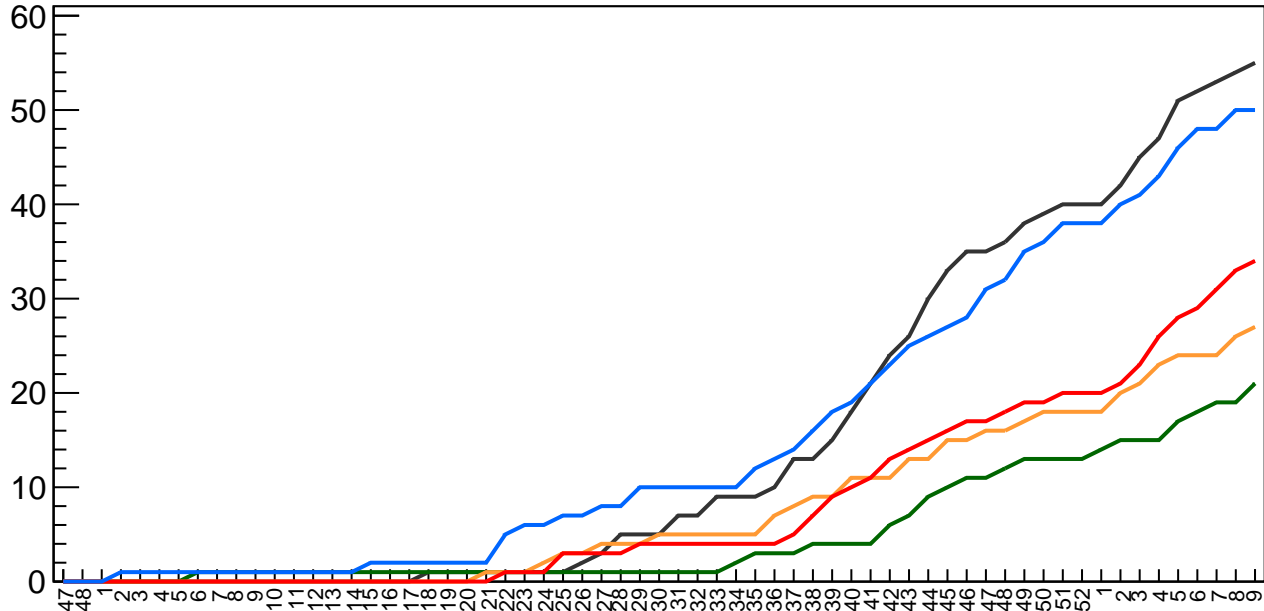


Det. grade HS vs time

Berkeley
 Daresbury
 Turin

Nikhef
 Frascati

#HS



Week

Comparison to prev. week

Berkeley: +1

Nikhef: +2

Daresbury: +1

Frascati: +1

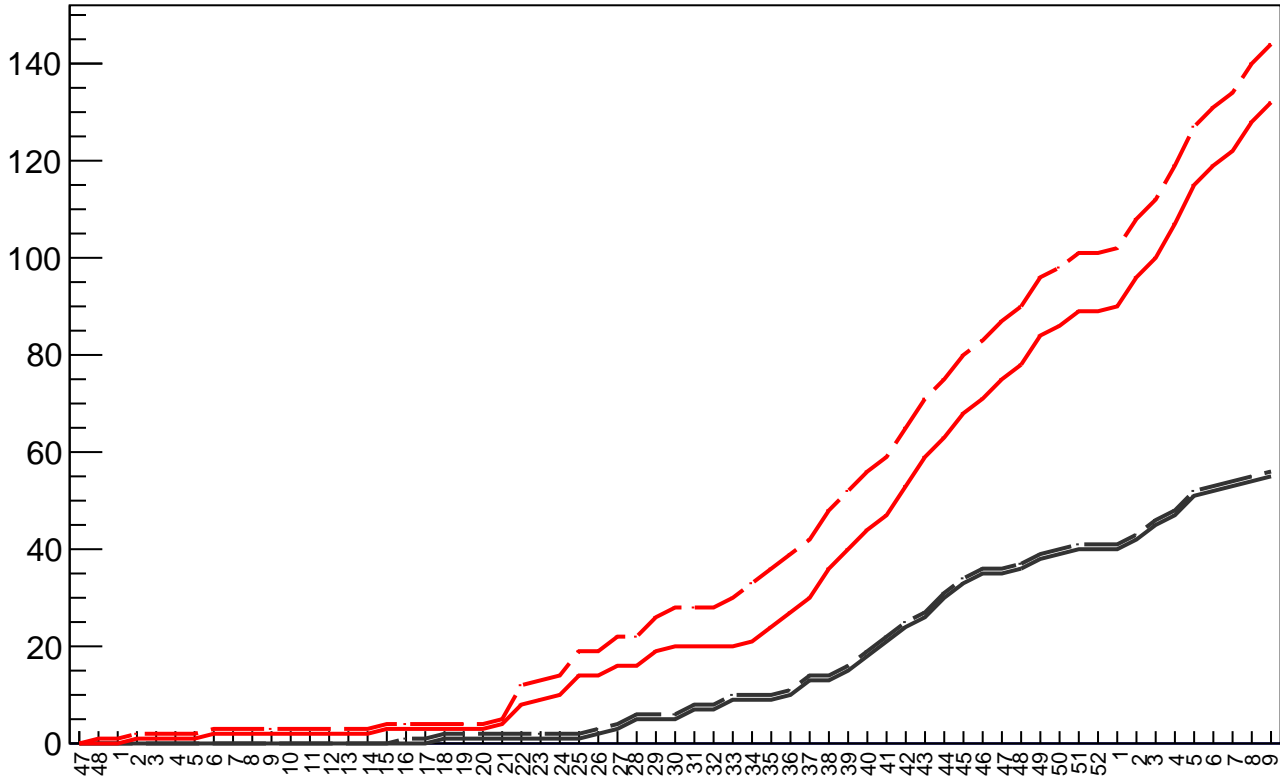
Turin: +0

Det. grade HS vs time

ML(all)
OL(all)

ML(DG)
OL(DG)

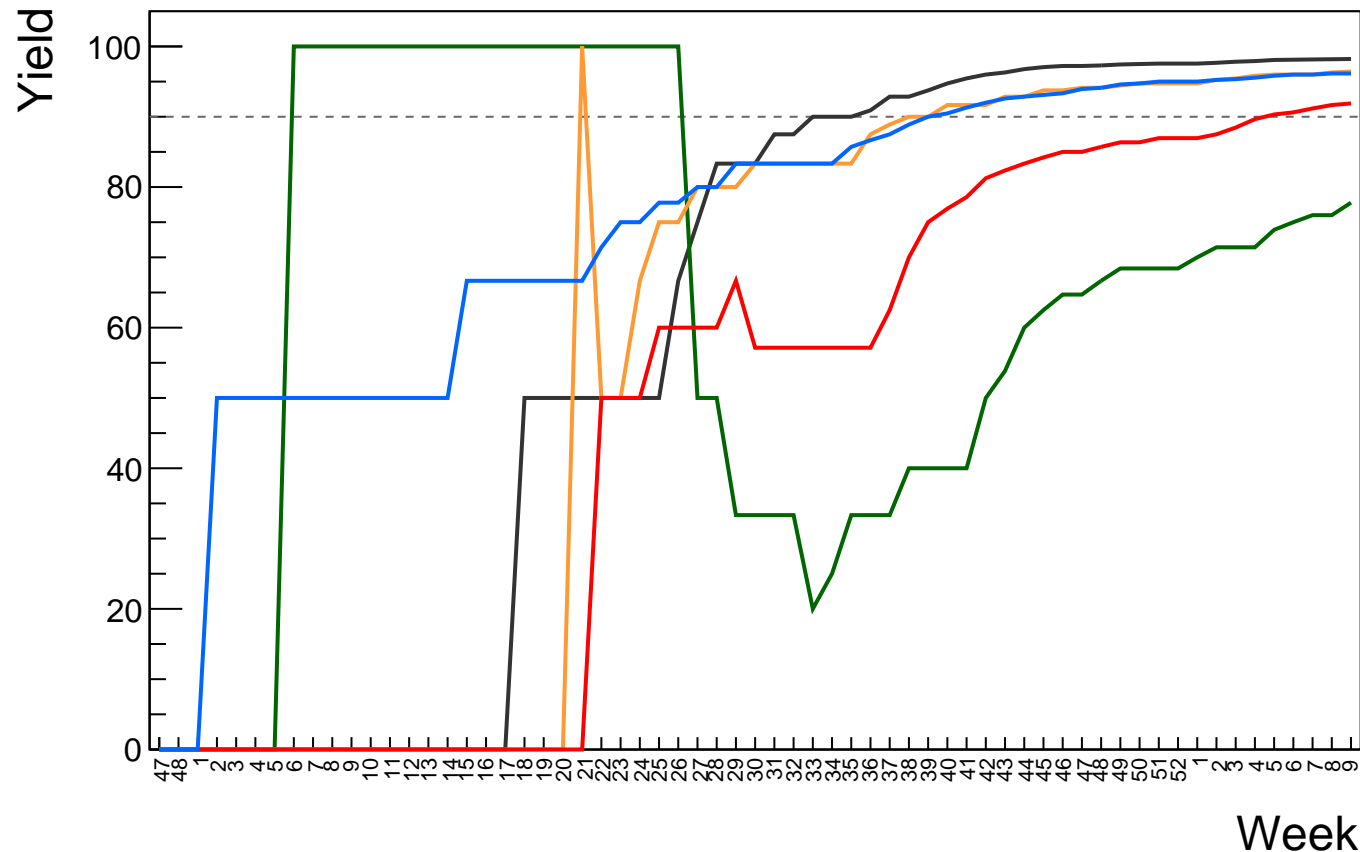
#HS



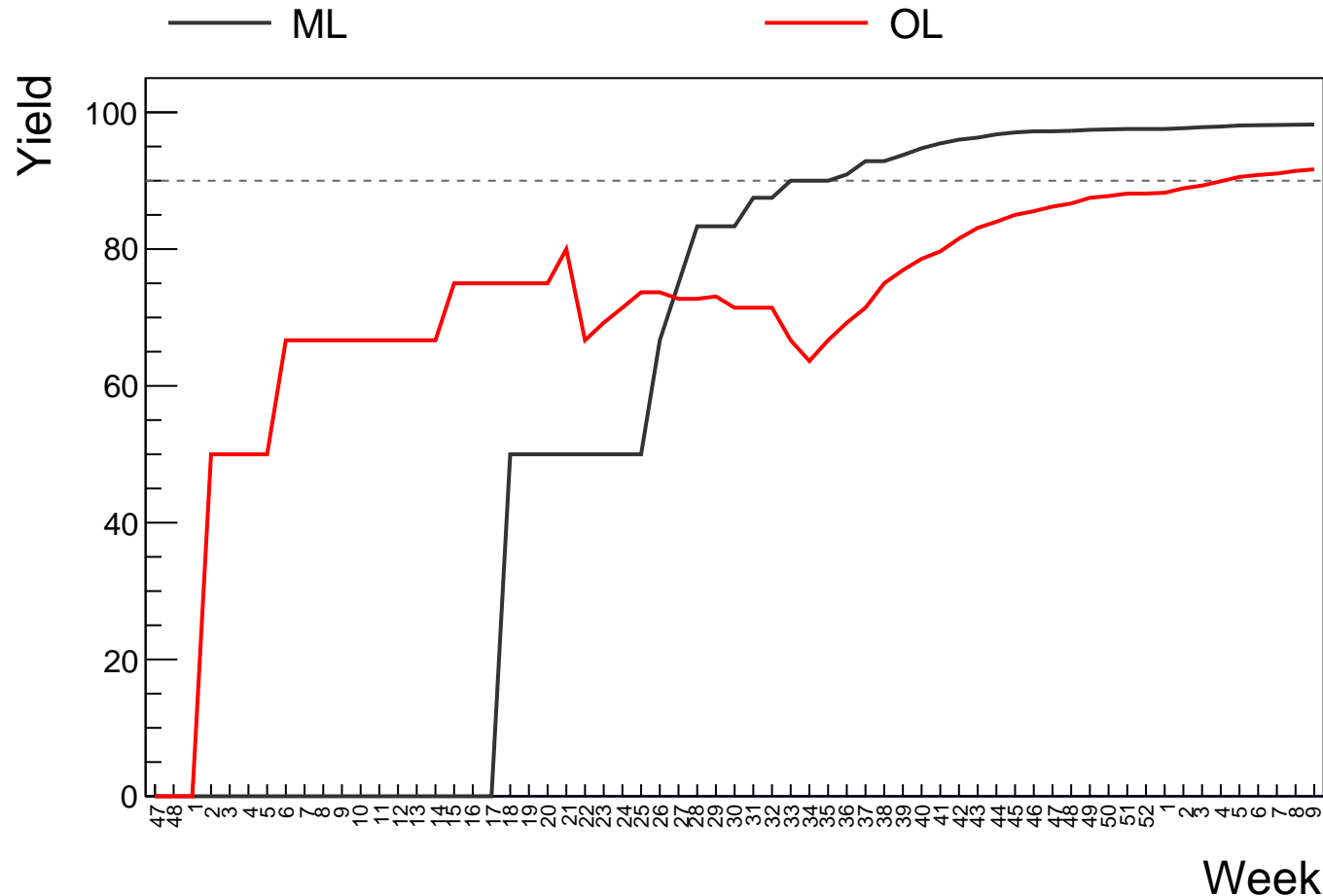
Week

HS Yield vs time

Berkeley
 Daresbury
 Turin
 Nikhef
 Frascati



HS Yield vs time



Stave monitoring

Staves of previous week

D-OL-Stave-011: $(U,L)=(0, 1)$ bad chips

F-OL-Stave-016: $(U,L)=(0, 0)$ bad chips

F-OL-Stave-017: $(U,L)=(0, 0)$ bad chips

Staves of this week

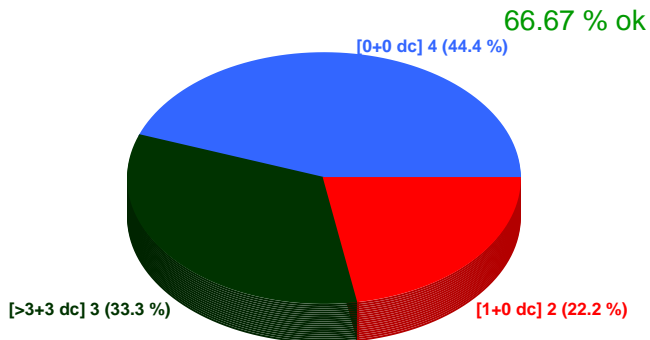
B-ML-Stave-028: $(U,L)=(0, 0)$ bad chips

D-OL-Stave-012: $(U,L)=(0, 1)$ bad chips

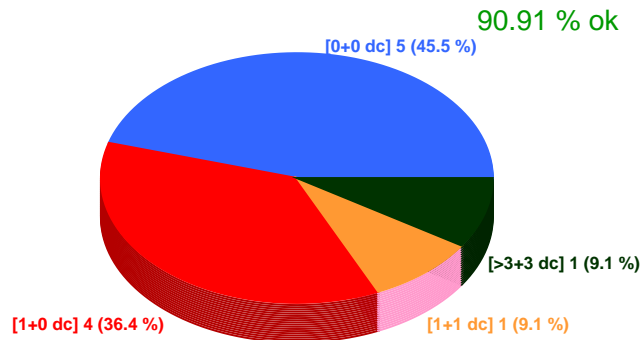
F-OL-Stave-015: $(U,L)=(0, 0)$ bad chips

T-OL-Stave-027: $(U,L)=(0, 0)$ bad chips

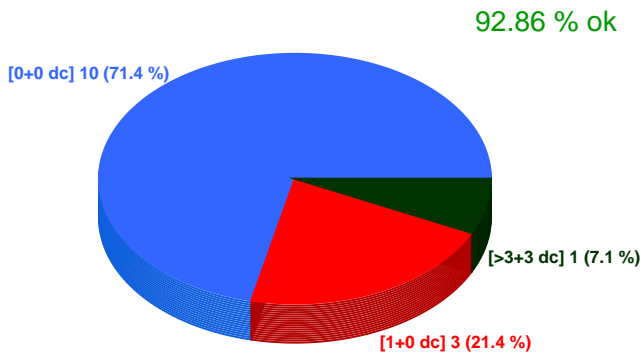
Stave - Nikhef



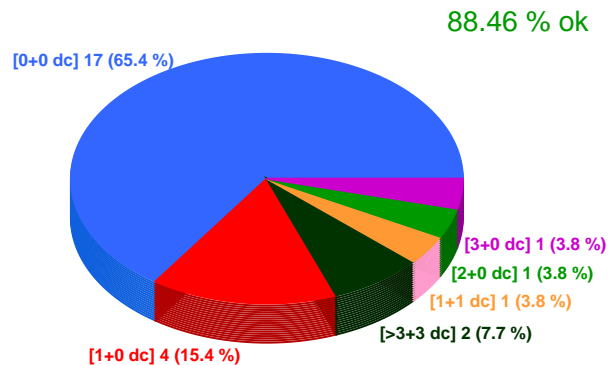
Stave - Daresbury



Stave - Frascati

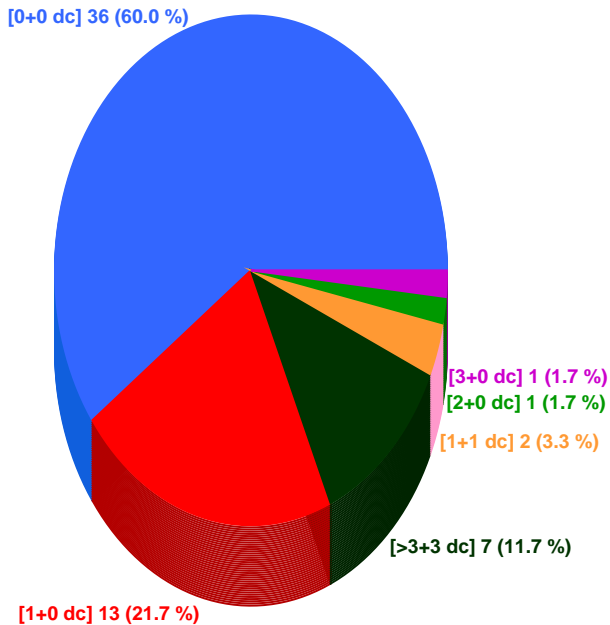


Stave - Turin



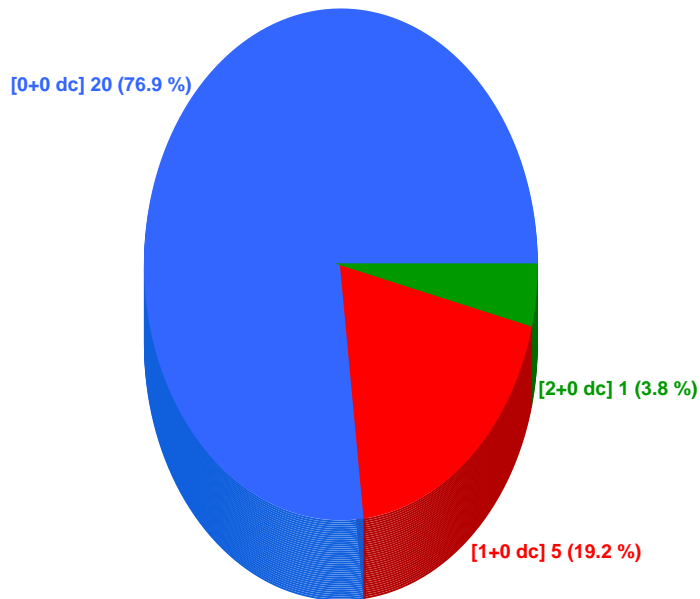
Stave - OL

86.67 % ok

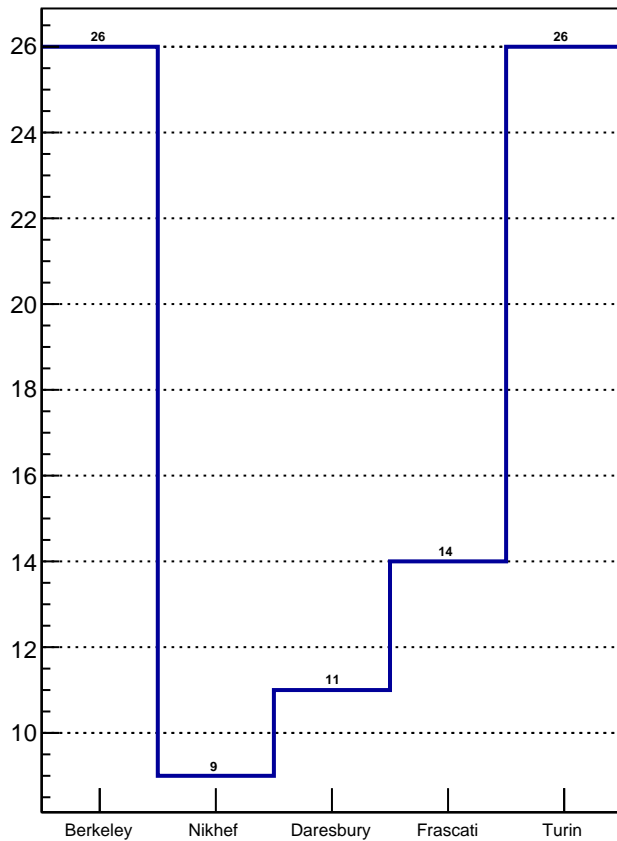


Stave - ML

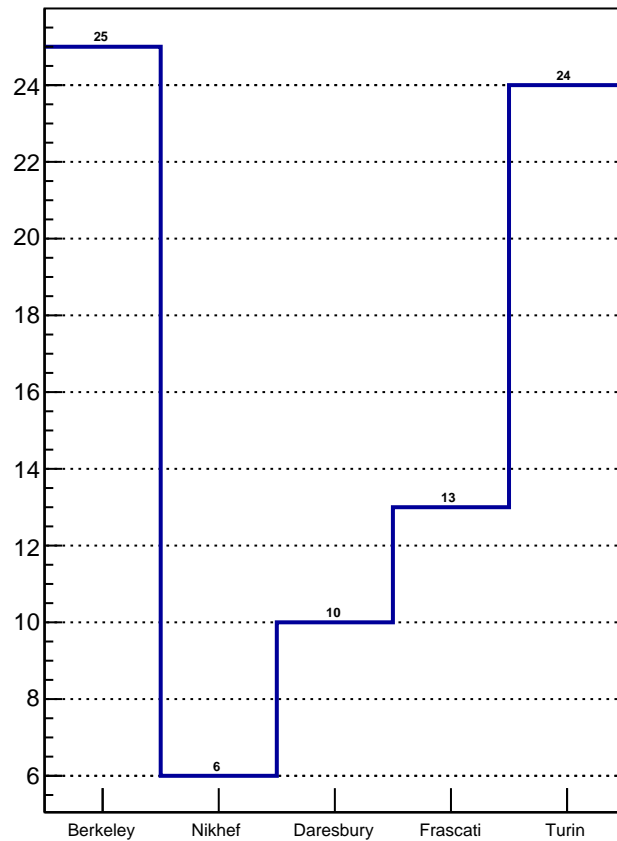
96.15 % ok



All Stave



Det. Grade Stave

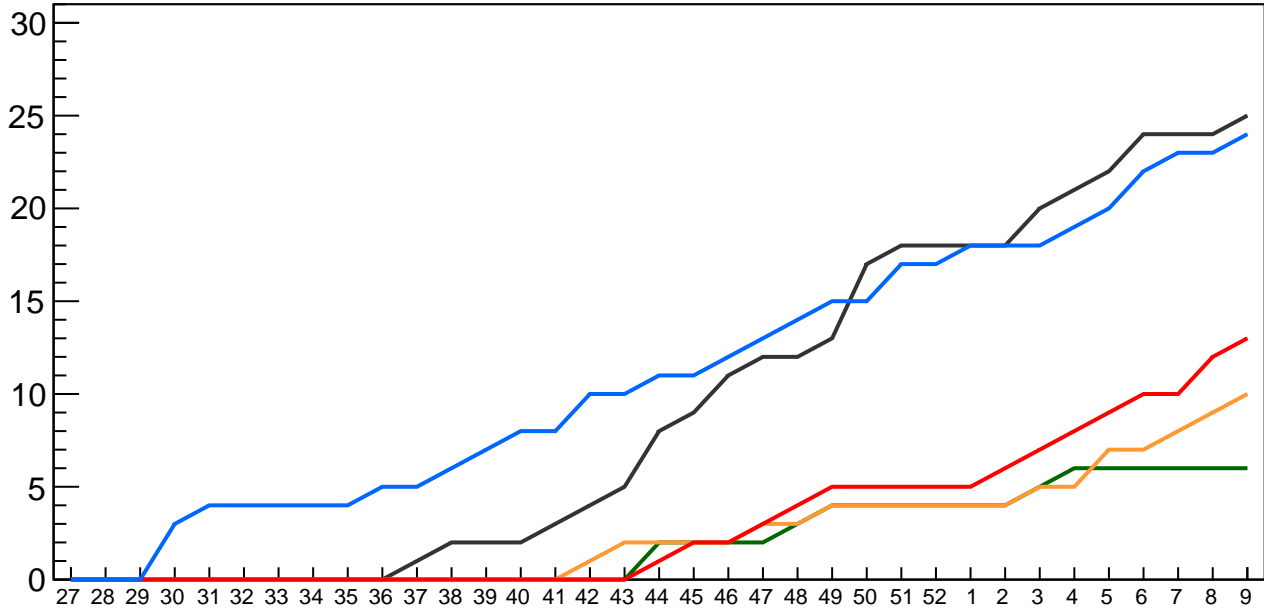


Det. grade Stave vs time

— Berkeley
— Daresbury
— Turin

— Nikhef
— Frascati

#Stave



Week

Comparison to prev. week

Berkeley: +1

Nikhef: +0

Daresbury: +1

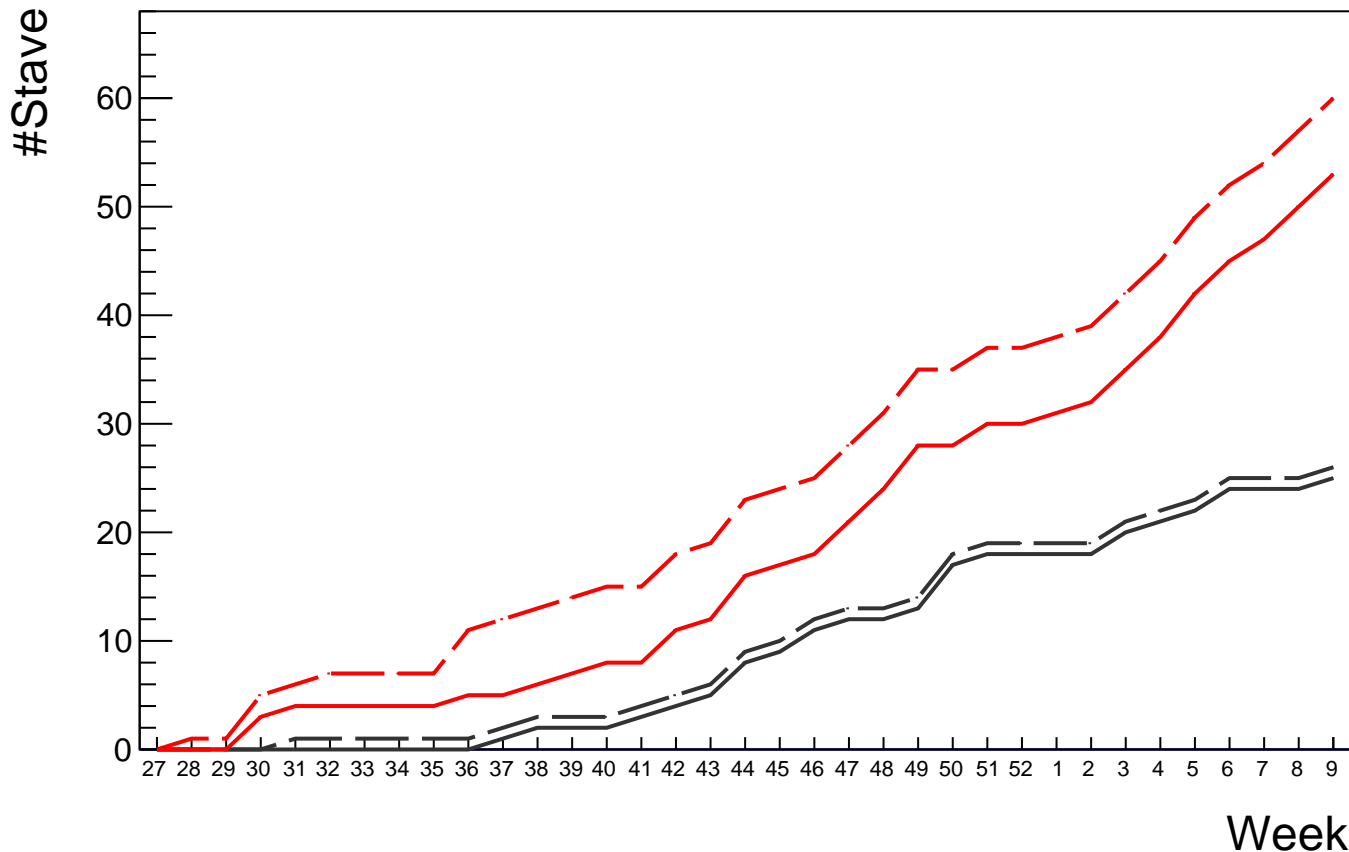
Frascati: +1

Turin: +1

Det. grade Stave vs time

ML(all)
OL(all)

ML(DG)
OL(DG)

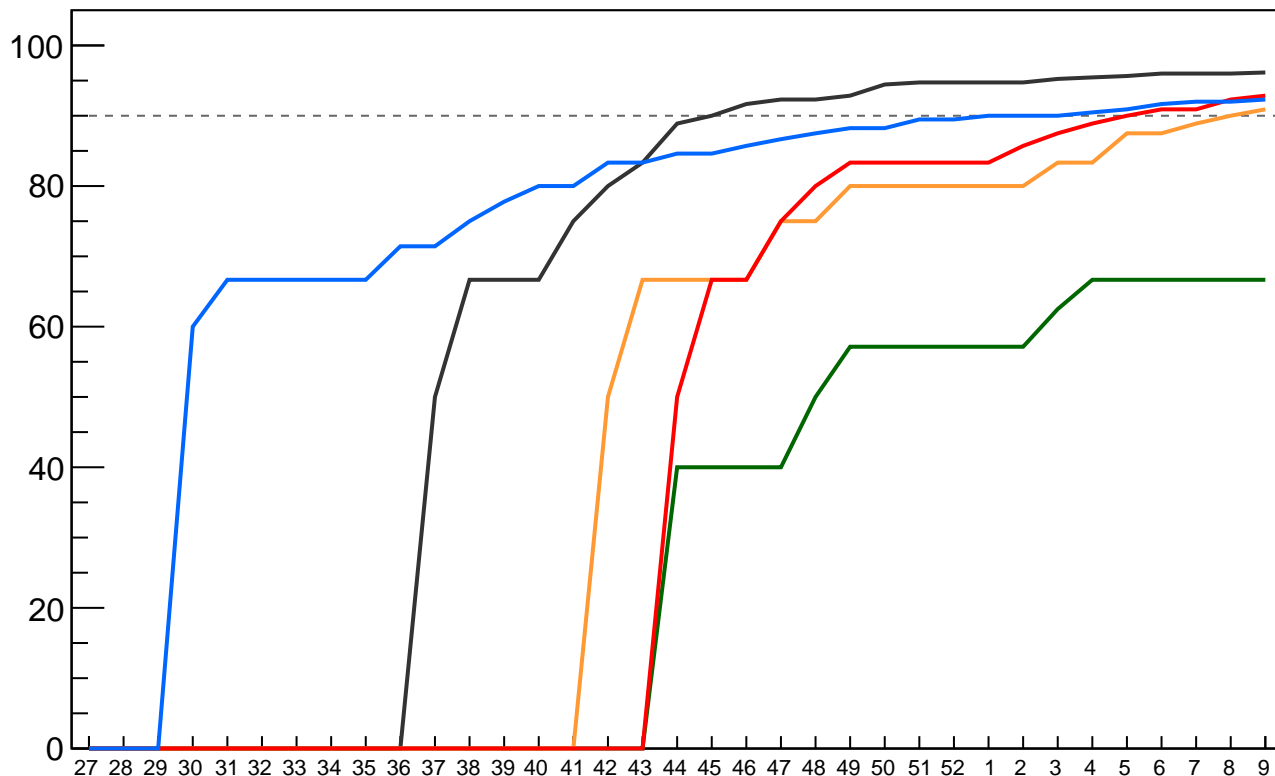


Stave yield vs time

— Berkeley
— Daresbury
— Turin

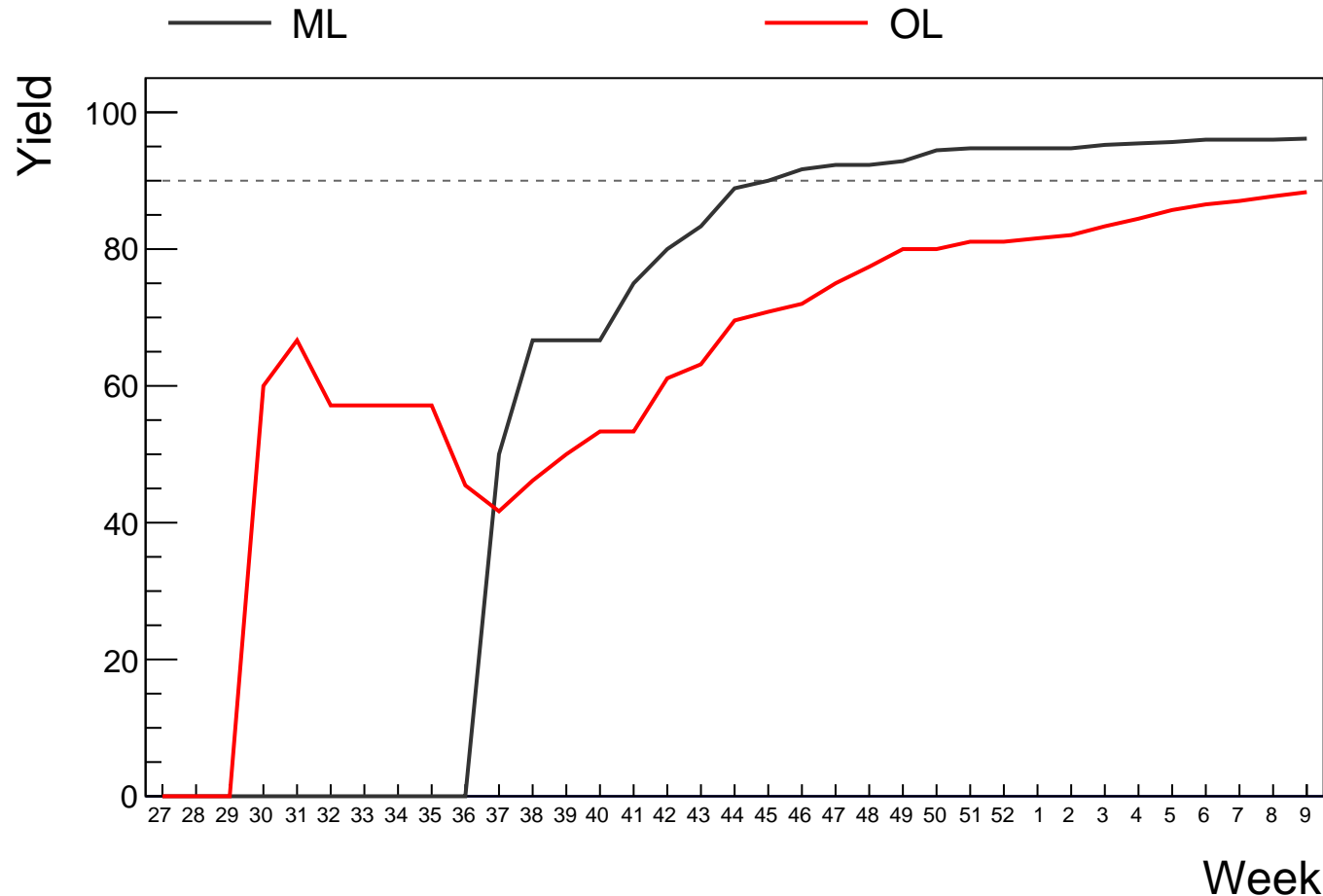
— Nikhef
— Frascati

Yield



Week

Stave yield vs time



Production rate (October 2018 - prev. week)**

Berkeley: 1.16(all) -- 1.16(DG)

Nikhef: 0.32(all) -- 0.32(DG)

Daresbury: 0.47(all) -- 0.47(DG)

Frascati: 0.63(all) -- 0.63(DG)

Turin: 0.79(all) -- 0.79(DG)

OL: 2.21(all) -- 2.21(DG)

ML: 1.16(all) -- 1.16(DG)

****Christmas holiday excluded (2 weeks)**

Stave reception @CERN

Staves qualified in the previous week

A-OL-Stave-006: (U,L)=(0, 0) bad chips

A-OL-Stave-007: (U,L)=(0, 0) bad chips

A-OL-Stave-008: (U,L)=(0, 1) bad chips

A-OL-Stave-009: (U,L)=(1, 0) bad chips

D-OL-Stave-003: (U,L)=(1, 1) bad chips

Staves qualified this week

T-OL-Stave-012: (U,L)=(0, 1)

T-OL-Stave-024: (U,L)=(0, 0)

T-OL-Stave-007: (U,L)=(0, 1)

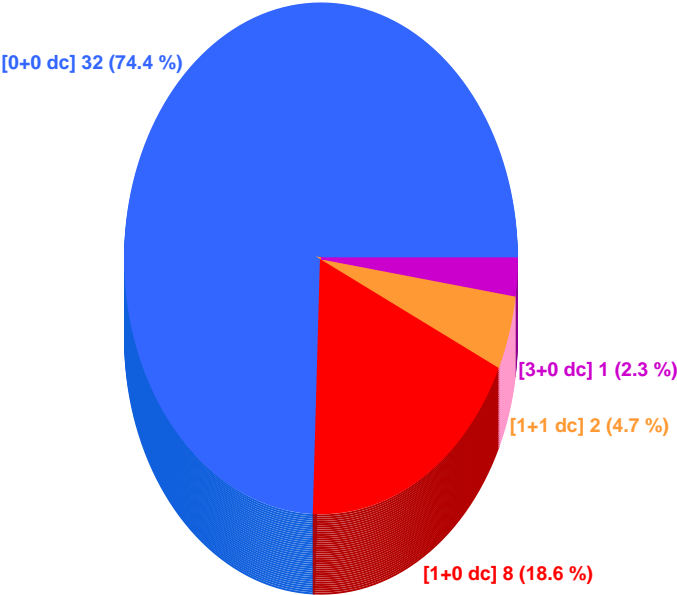
T-OL-Stave-023: (U,L)=(0, 0)

T-OL-Stave-025: (U,L)=(0, 0)

T-OL-Stave-026: (U,L)=(0, 0)

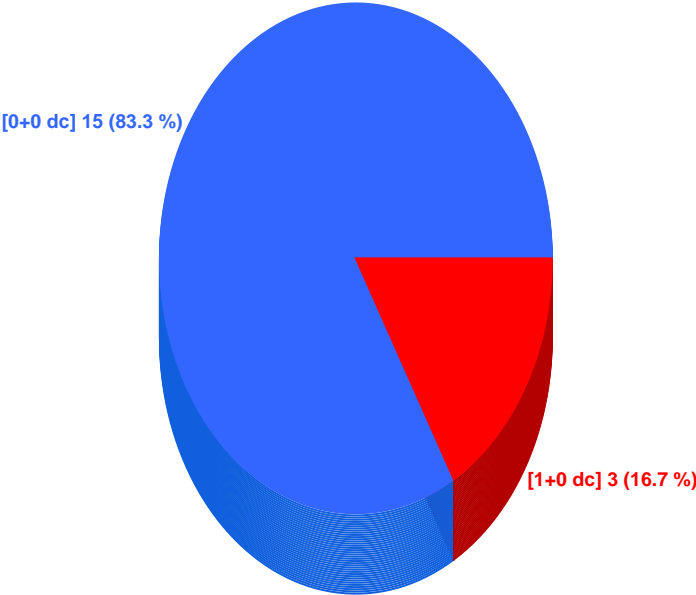
Stave - OL @CERN

97.67 % ok

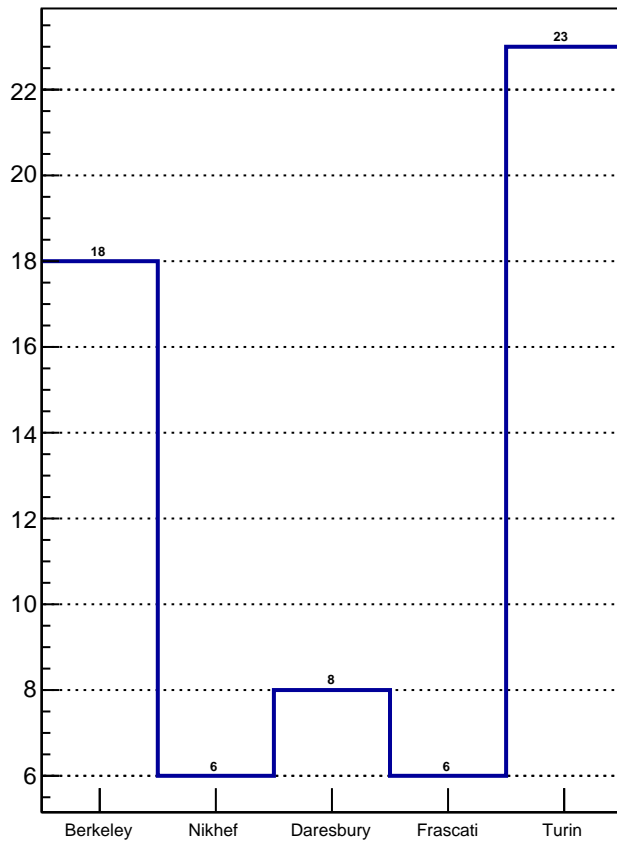


Stave - ML @CERN

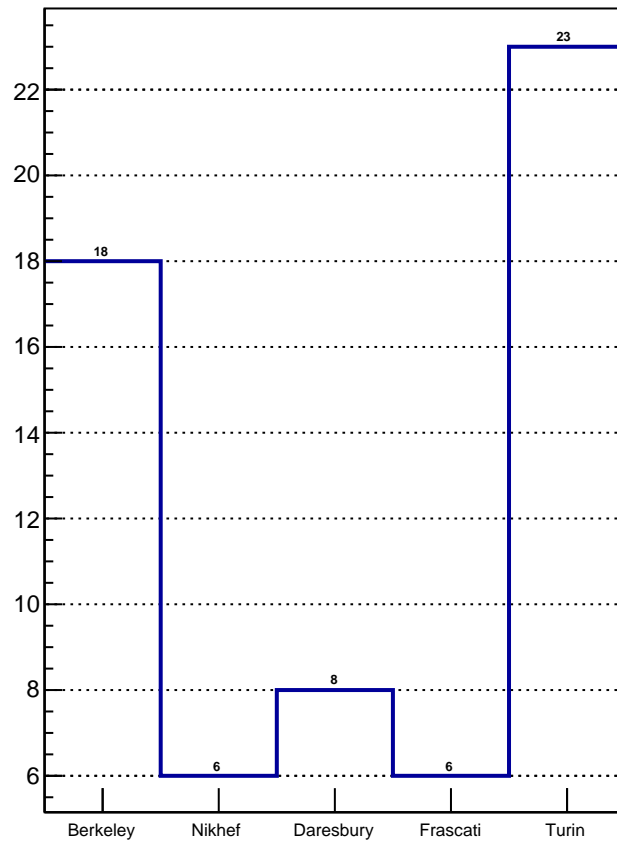
100.00 % ok



All Stave @CERN



Det. Grade Stave @CERN

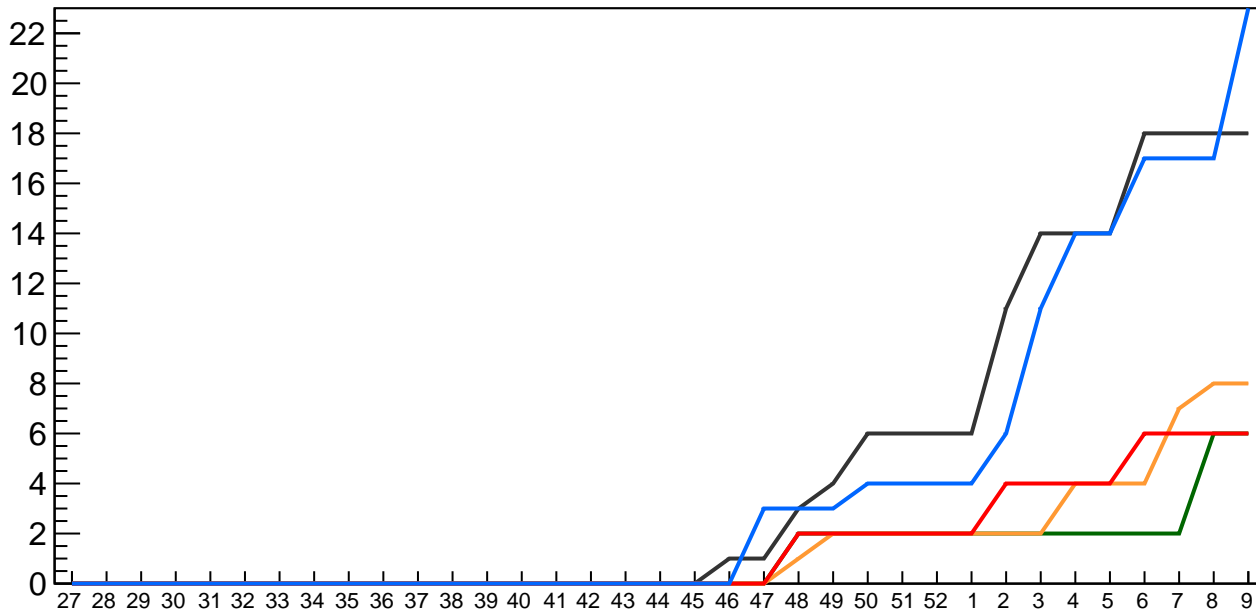


Det. grade Stave vs time @CERN

Berkeley
Daresbury
Turin

Nikhef
Frascati

#Stave



Week

Comparison to prev. week

Berkeley: +0

Nikhef: +0

Daresbury: +0

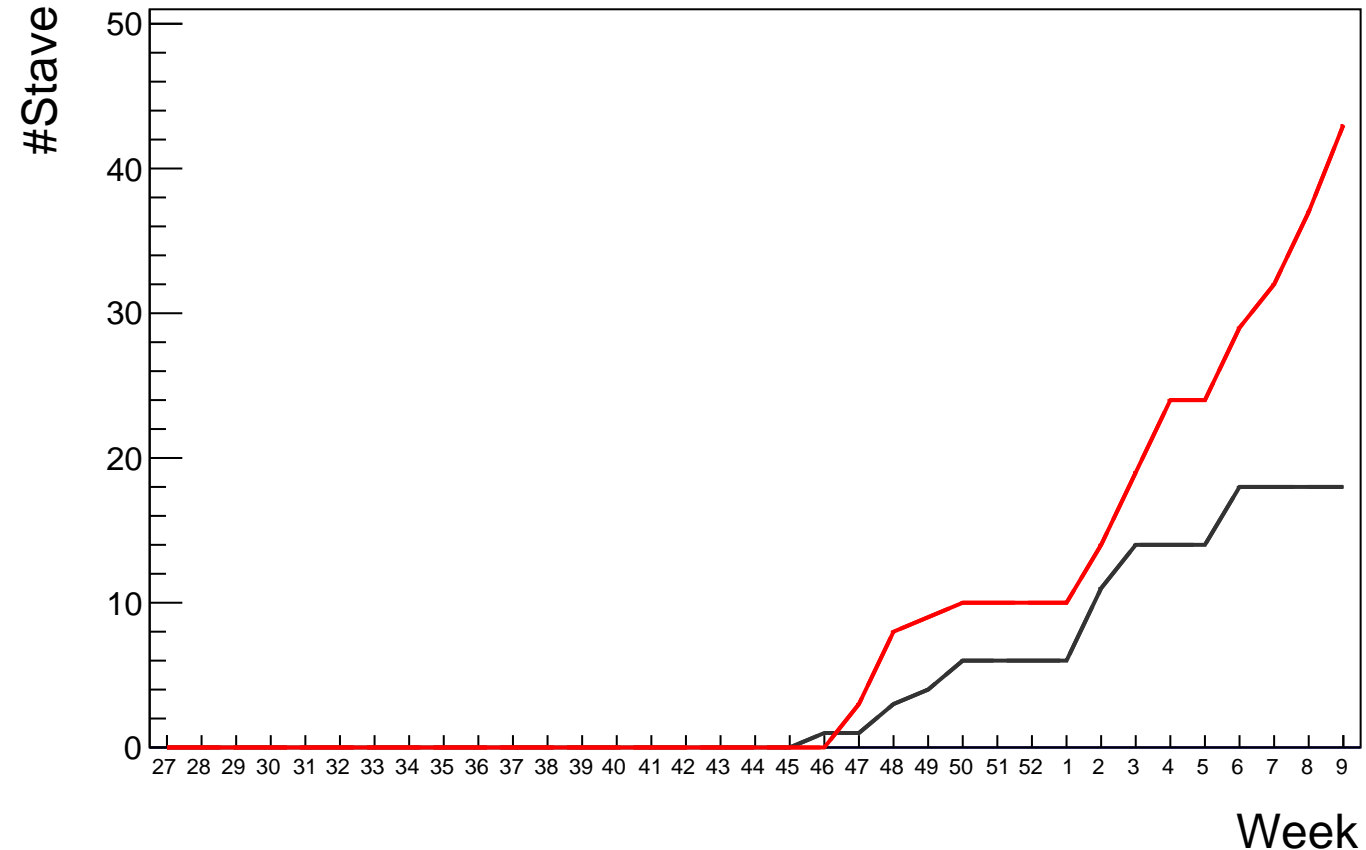
Frascati: +0

Turin: +6

Det. grade Stave vs time @CERN

ML(all)
OL(all)

ML(DG)
OL(DG)



Qualification rate (December 2018 - prev. week)**

Berkeley: 1.50(all) -- 1.50(DG)

Nikhef: 0.40(all) -- 0.40(DG)

Daresbury: 0.70(all) -- 0.70(DG)

Frascati: 0.40(all) -- 0.40(DG)

Turin: 1.40(all) -- 1.40(DG)

OL: 2.90(all) -- 2.90(DG)

ML: 1.50(all) -- 1.50(DG)

****Christmas holiday excluded (2 weeks)**

HS without a Stave

HSs (DG) not yet tested as Stave

D-OL-HS-U-008: 0 bad chips
A-OL-HS-U-001: 0 bad chips
A-OL-HS-U-009: 0 bad chips
A-OL-HS-U-012: 0 bad chips
F-OL-HS-U-014: 0 bad chips
D-OL-HS-L-013: 0 bad chips
D-OL-HS-U-013: 0 bad chips
B-ML-HS-L-014: 0 bad chips
B-ML-HS-U-014: 0 bad chips
B-ML-HS-U-017: 0 bad chips
B-ML-HS-U-027: 0 bad chips
B-ML-HS-U-029: 0 bad chips
A-OL-HS-L-011: -98 bad chips
A-OL-HS-L-013: 0 bad chips
A-OL-HS-L-014: 0 bad chips
A-OL-HS-L-015: 1 bad chips
A-OL-HS-U-013: 1 bad chips
D-OL-HS-L-008: 0 bad chips
D-OL-HS-L-010: 0 bad chips
D-OL-HS-U-014: 0 bad chips
F-OL-HS-L-005: 0 bad chips
F-OL-HS-L-013: 1 bad chips
F-OL-HS-L-018: 0 bad chips
F-OL-HS-L-019: 0 bad chips
F-OL-HS-U-004: 0 bad chips
F-OL-HS-U-013: 0 bad chips
F-OL-HS-U-018: 0 bad chips
F-OL-HS-L-002: 0 bad chips

HSs (non-DG) not yet tested as Stave

A-OL-HS-L-001: 7 bad chips -> rework(?)

A-OL-HS-L-002: 49 bad chips -> rework(?)

A-OL-HS-U-002: 7 bad chips -> rework(?)

A-OL-HS-U-003: 98 bad chips -> rework(?)

A-OL-HS-L-004: 14 bad chips -> rework(?)

A-OL-HS-L-003: 49 bad chips -> rework(?)

F-OL-HS-U-002: 8 bad chips -> rework(?)

Stave not DG

Staves not DG

A-OL-Stave-001: (U,L) = (2, 14) bad chips

A-OL-Stave-002: (U,L) = (7, 49) bad chips

A-OL-Stave-003: (U,L) = (98, 98) bad chips

D-OL-Stave-001: (U,L) = (0, 22) bad chips

F-OL-Stave-001: (U,L) = (43, 14) bad chips

B-ML-Stave-001: (U,L) = (2, 0) bad chips

T-OL-Stave-002: (U,L) = (7, 1) bad chips

T-OL-Stave-003: (U,L) = (6, 2) bad chips