

# Stave production monitoring

**Ivan Ravasenga**, *Politecnico di Torino and I.N.F.N.*

28/03/2019

Monitoring from January 2018 to 28/03/2019

Stave meeting

HS monitoring

### **HSs of previous week**

**B-ML-HS-U-231: 0 bad chips**

**A-OL-HS-L-011: 2 bad chips**

**A-OL-HS-L-012: 0 bad chips**

**D-OL-HS-L-016: 0 bad chips**

**D-OL-HS-U-016: 0 bad chips**

**F-OL-HS-L-021: 0 bad chips**

**F-OL-HS-U-022: 0 bad chips**

**T-OL-HS-L-031: 0 bad chips**

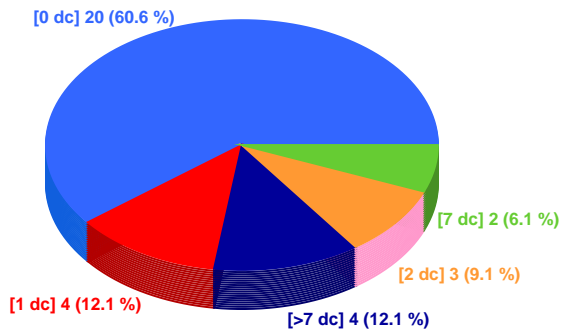
**T-OL-HS-U-031: 1 bad chips**

### **HSs of this week**

**A-OL-HS-U-016: 0 bad chips**

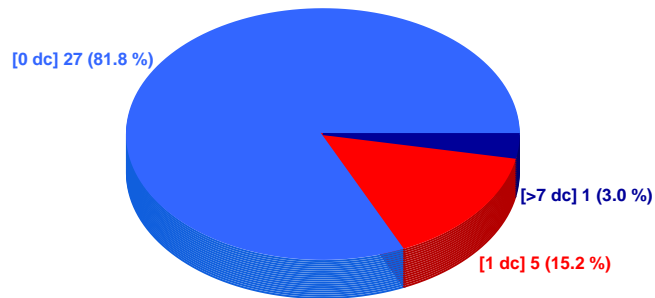
HS - Nikhef

81.82 % ok



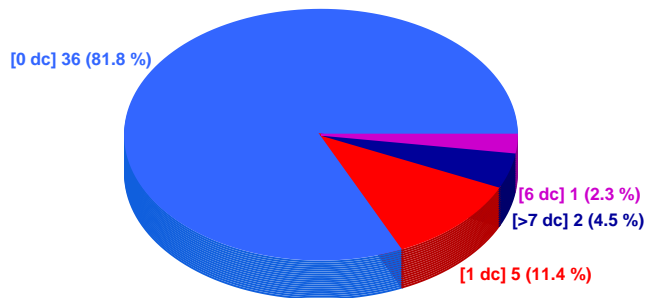
HS - Daresbury

96.97 % ok



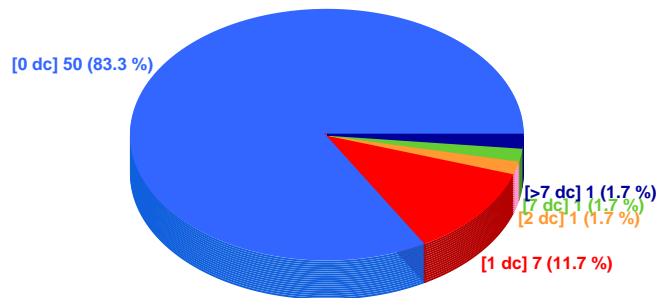
HS - Frascati

93.18 % ok



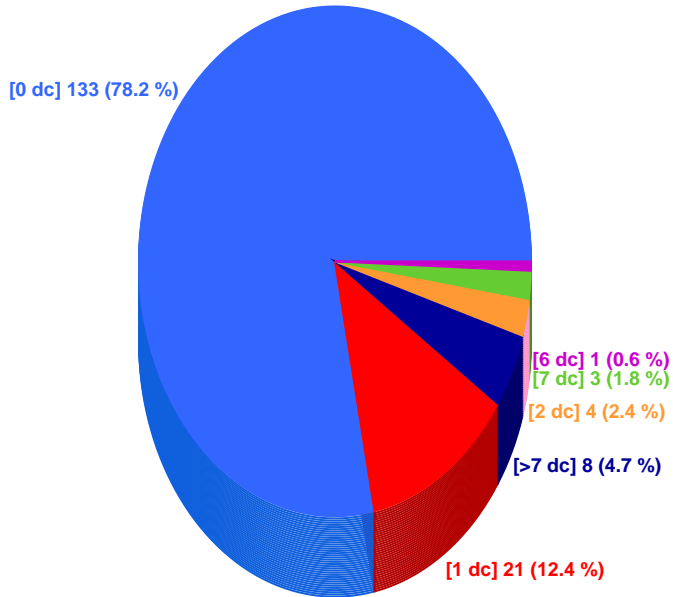
HS - Turin

96.67 % ok



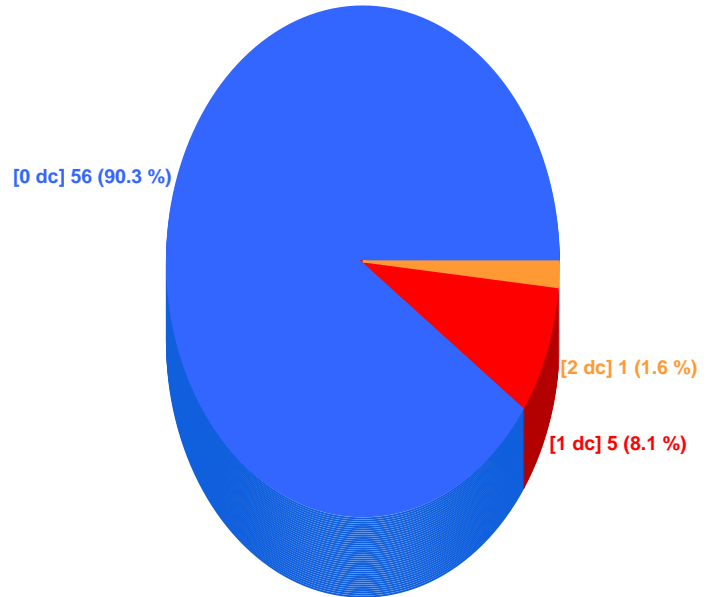
HS - OL

92.94 % ok

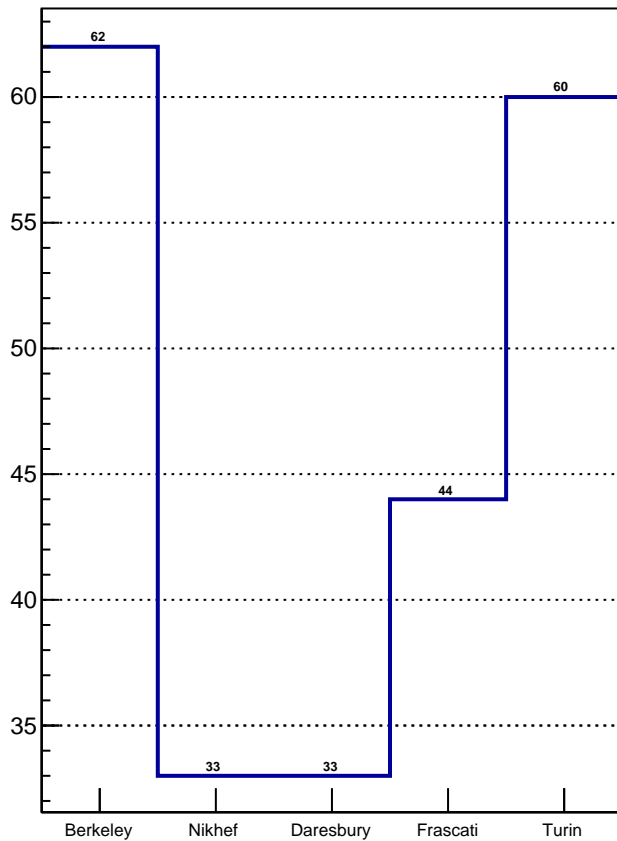


HS - ML

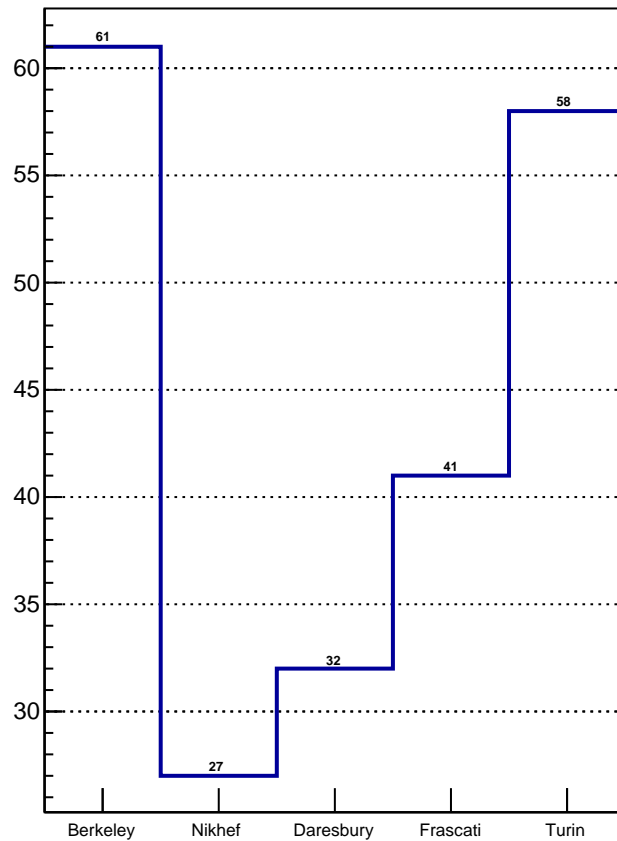
98.39 % ok



All HS



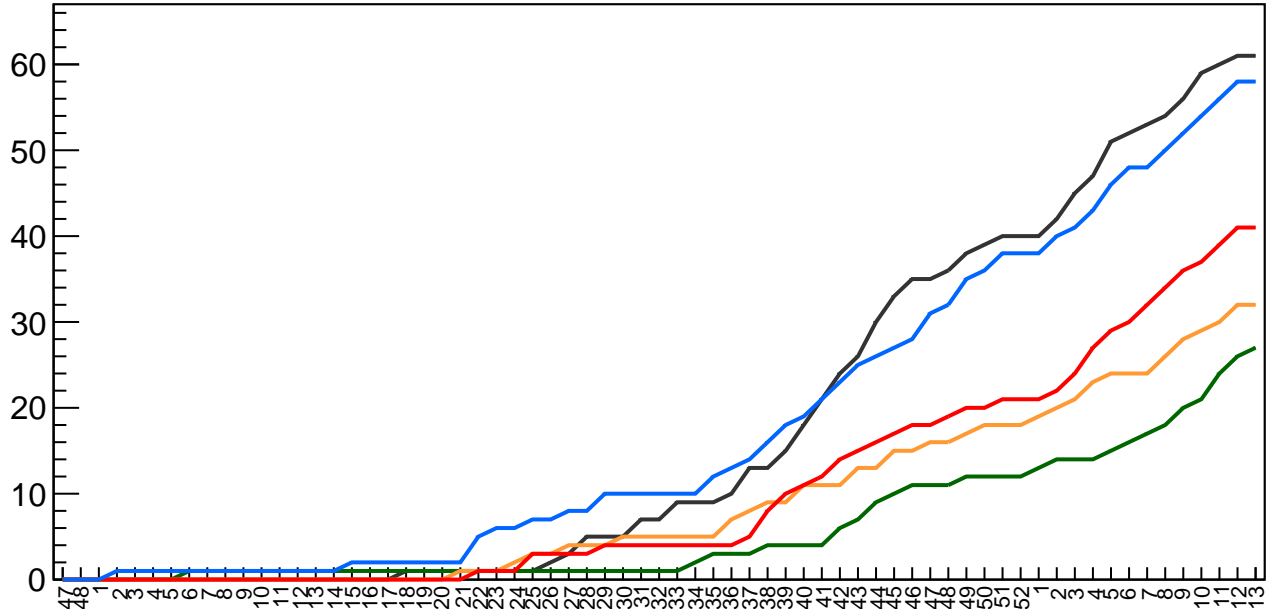
Det. Grade HS



# Det. grade HS vs time

— Berkeley  
— Daresbury  
— Turin  
— Nikhef  
— Frascati

#HS



Week

Comparison to prev. week

Berkeley: +0

Nikhef: +1

Daresbury: +0

Frascati: +0

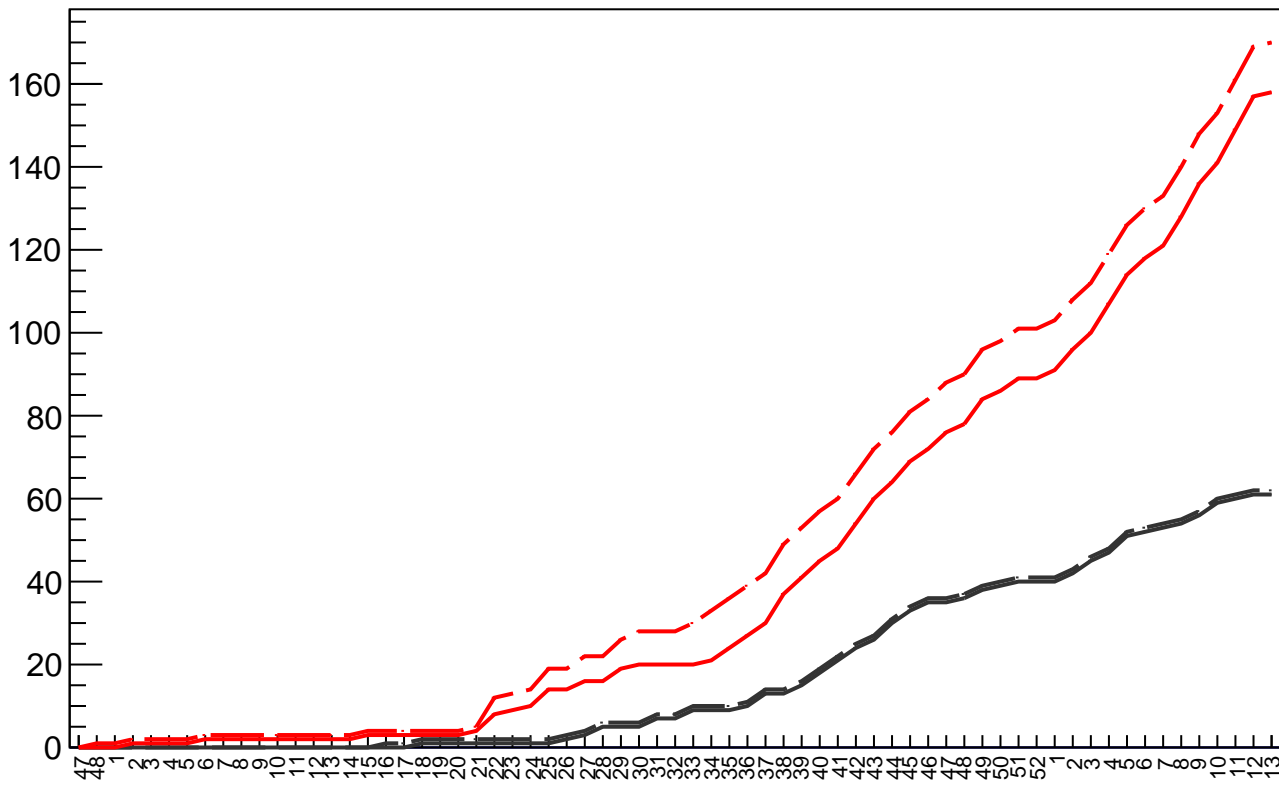
Turin: +0

# Det. grade HS vs time

ML(all)  
OL(all)

ML(DG)  
OL(DG)

#HS

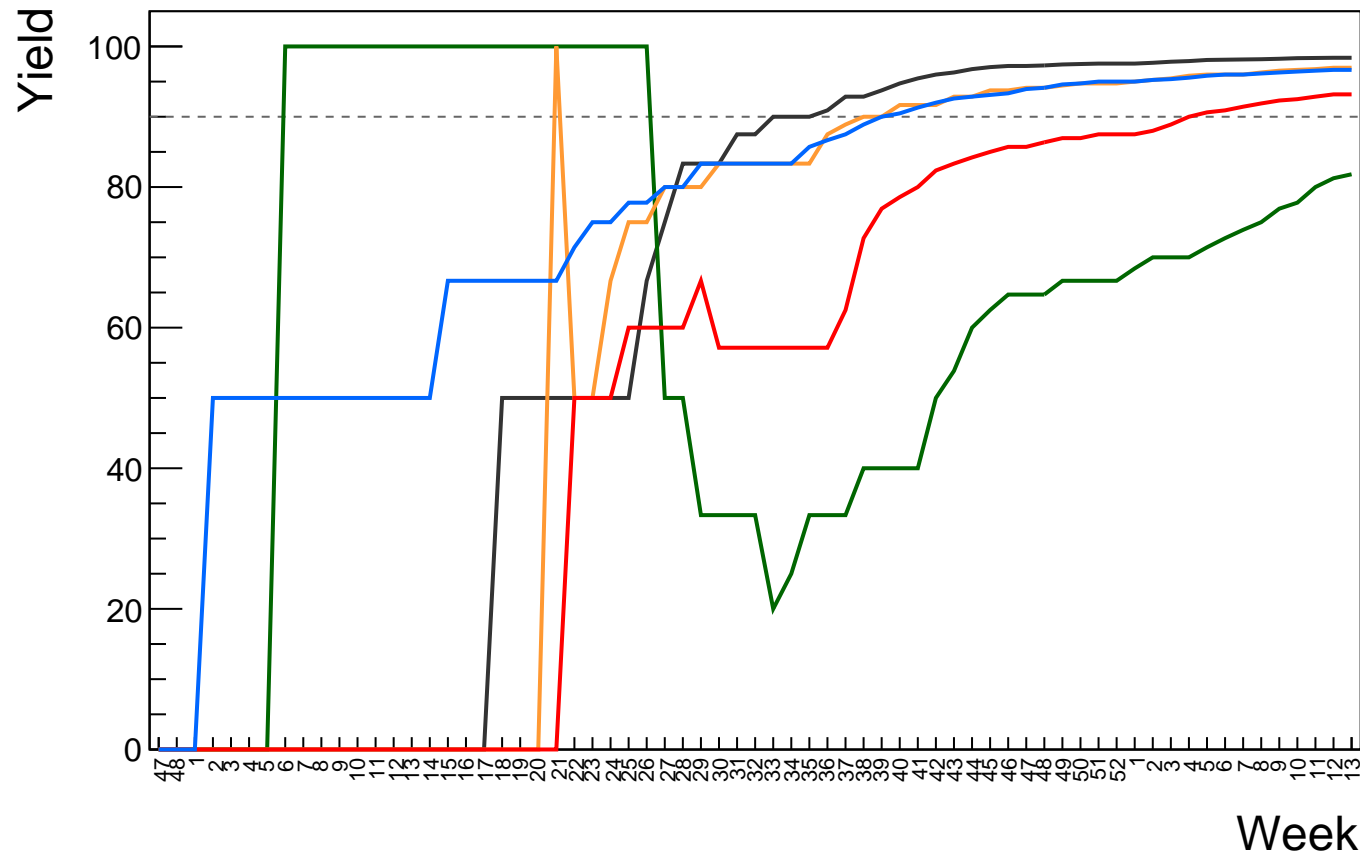




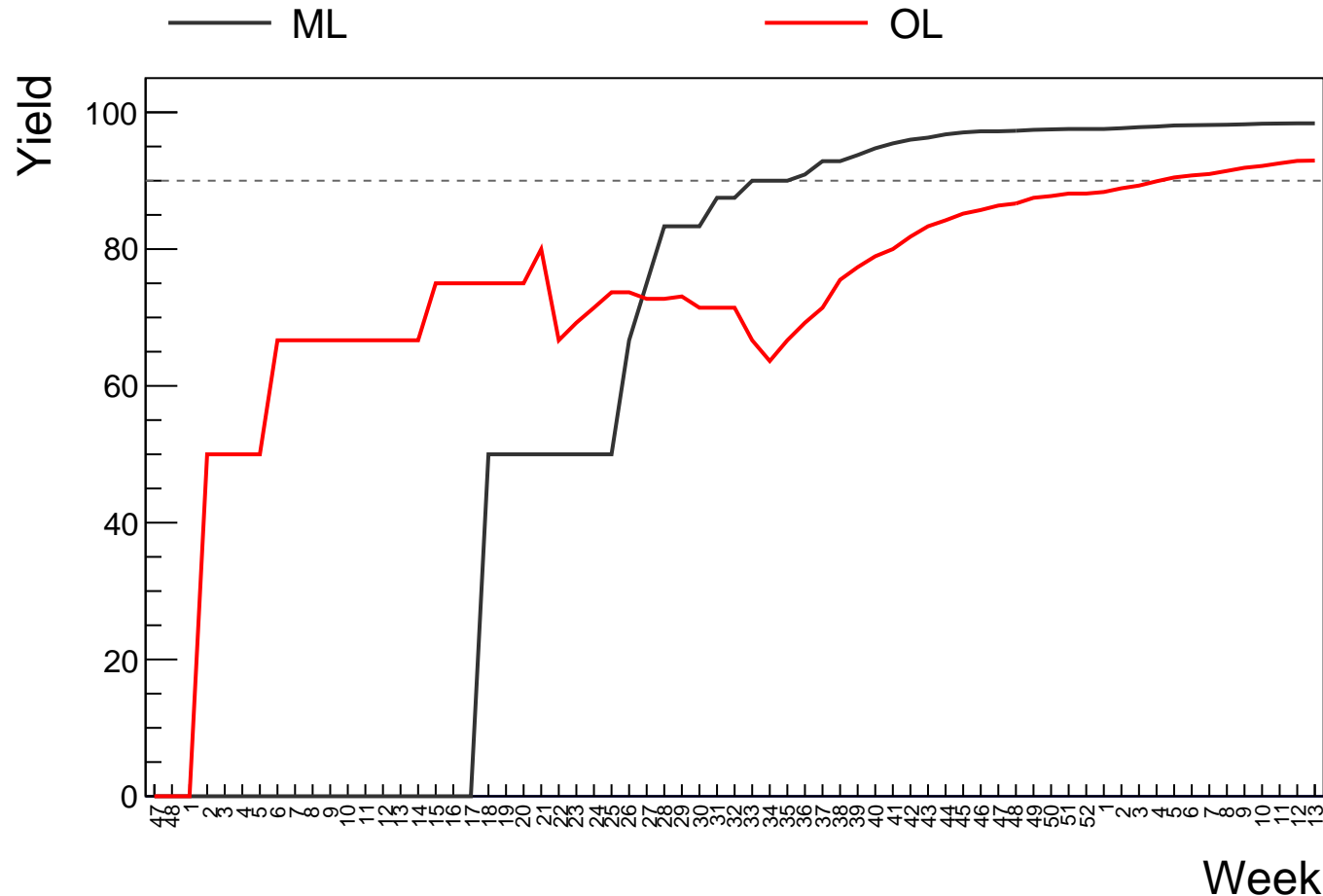
# HS Yield vs time

Berkeley  
 Daresbury  
 Turin

Nikhef  
 Frascati



# HS Yield vs time



# Stave monitoring

### **Staves of previous week**

**A-OL-Stave-004: (U,L)=(0, 0) bad chips**

**A-OL-Stave-010: (U,L)=(0, 1) bad chips**

**A-OL-Stave-014: (U,L)=(0, 0) bad chips**

### **Staves of this week**

**B-ML-Stave-031: (U,L)=(0, 0) bad chips**

**A-OL-Stave-013: (U,L)=(0, 2) bad chips**

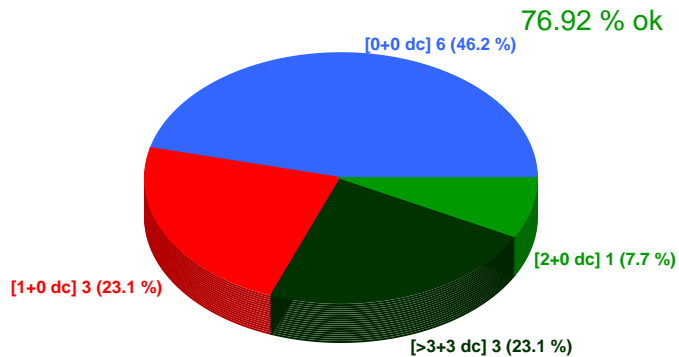
**D-OL-Stave-014: (U,L)=(0, 0) bad chips**

**D-OL-Stave-015: (U,L)=(0, 0) bad chips**

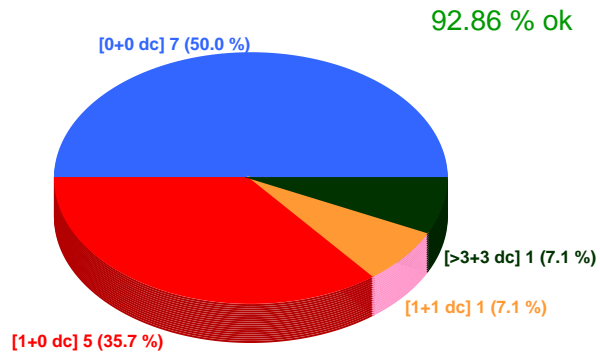
**F-OL-Stave-019: (U,L)=(0, 1) bad chips**

**T-OL-Stave-030: (U,L)=(0, 0) bad chips**

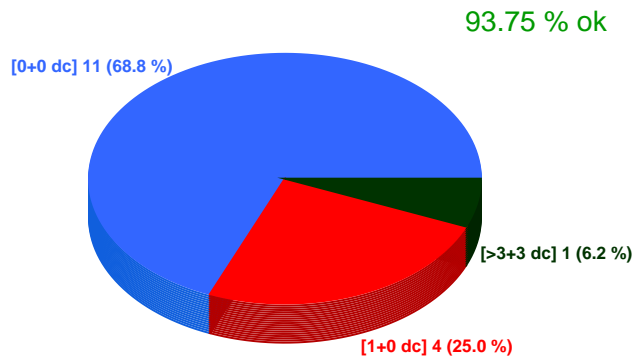
Stave - Nikhef



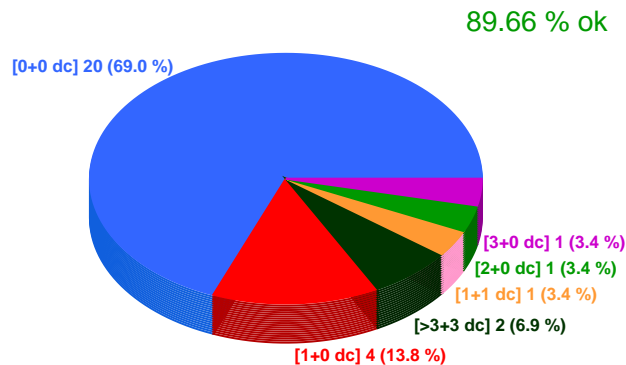
Stave - Daresbury



Stave - Frascati

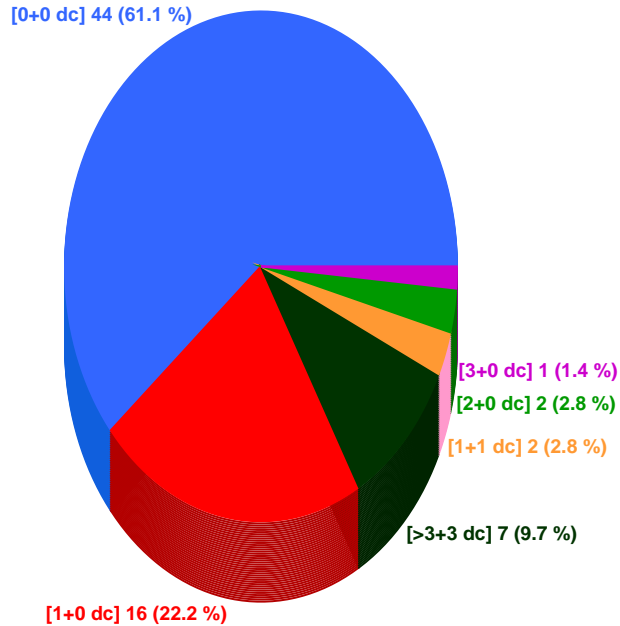


Stave - Turin



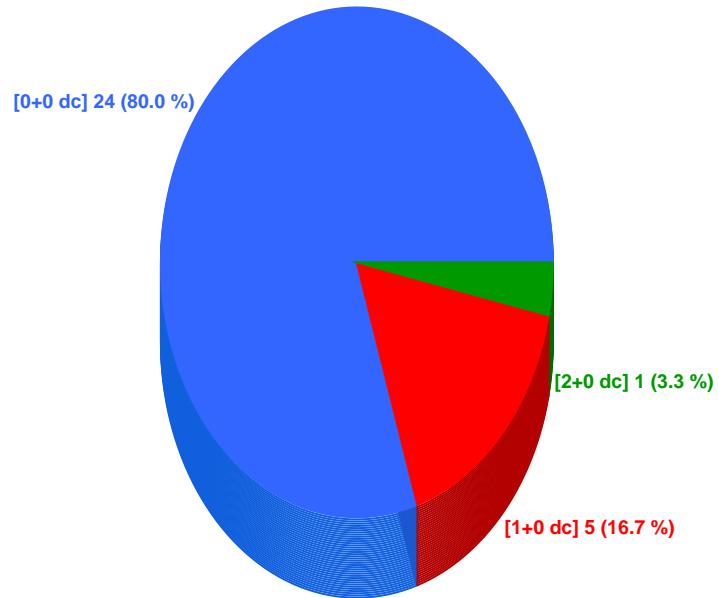
Stave - OL

88.89 % ok

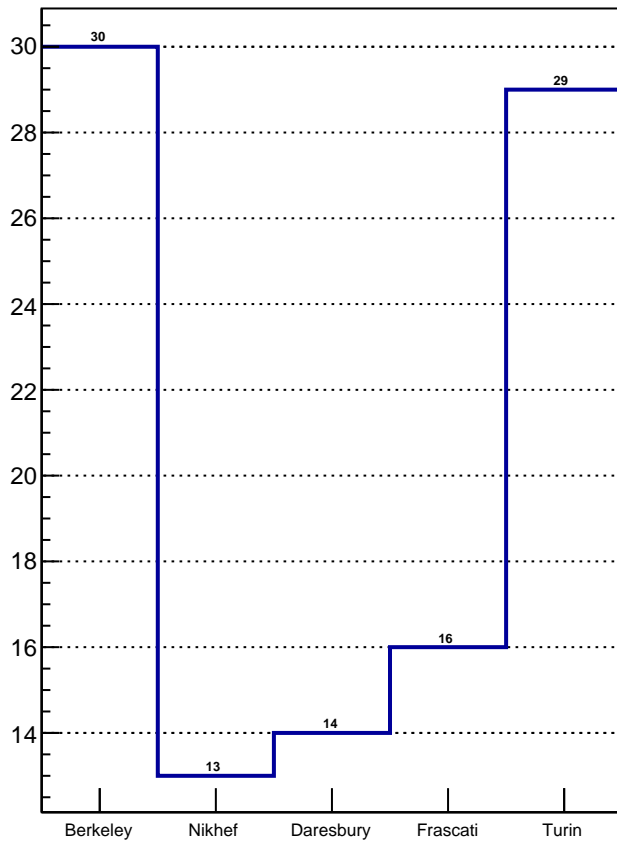


Stave - ML

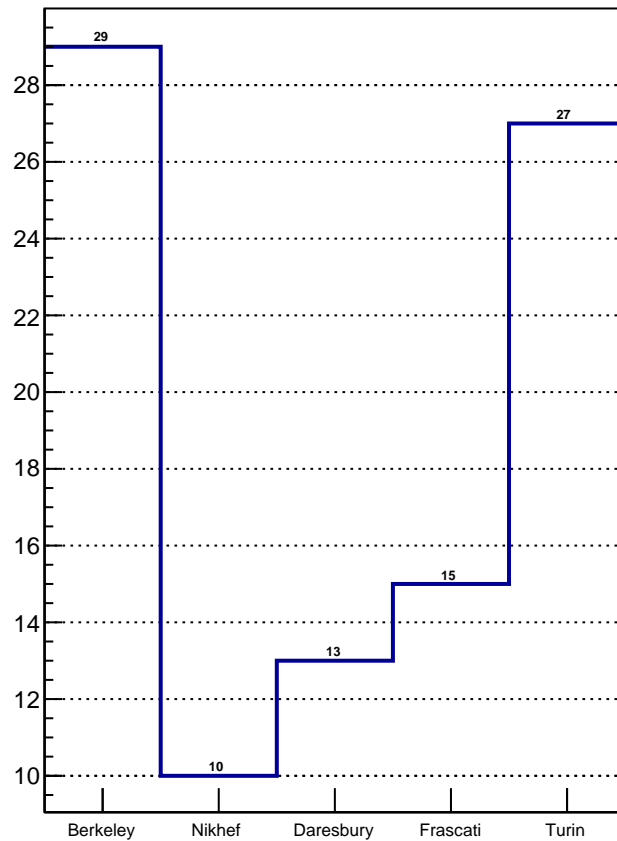
96.67 % ok



# All Stave



# Det. Grade Stave

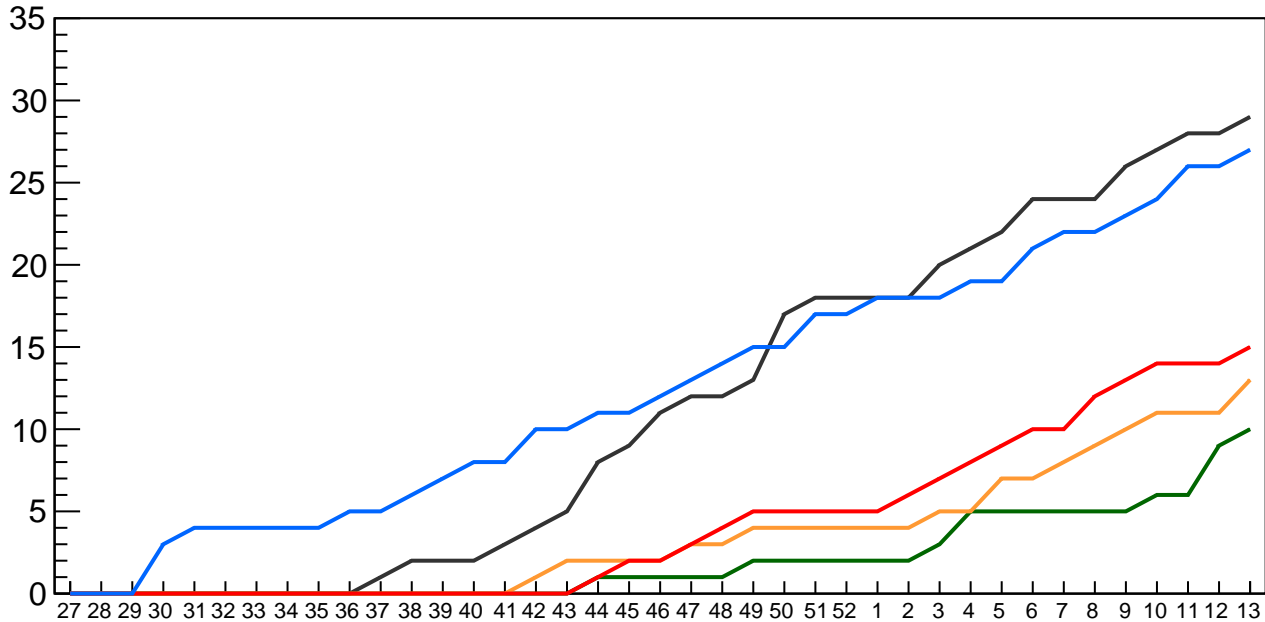


# Det. grade Stave vs time

— Berkeley  
— Daresbury  
— Turin

— Nikhef  
— Frascati

#Stave



Week

Comparison to prev. week

Berkeley: +1

Nikhef: +1

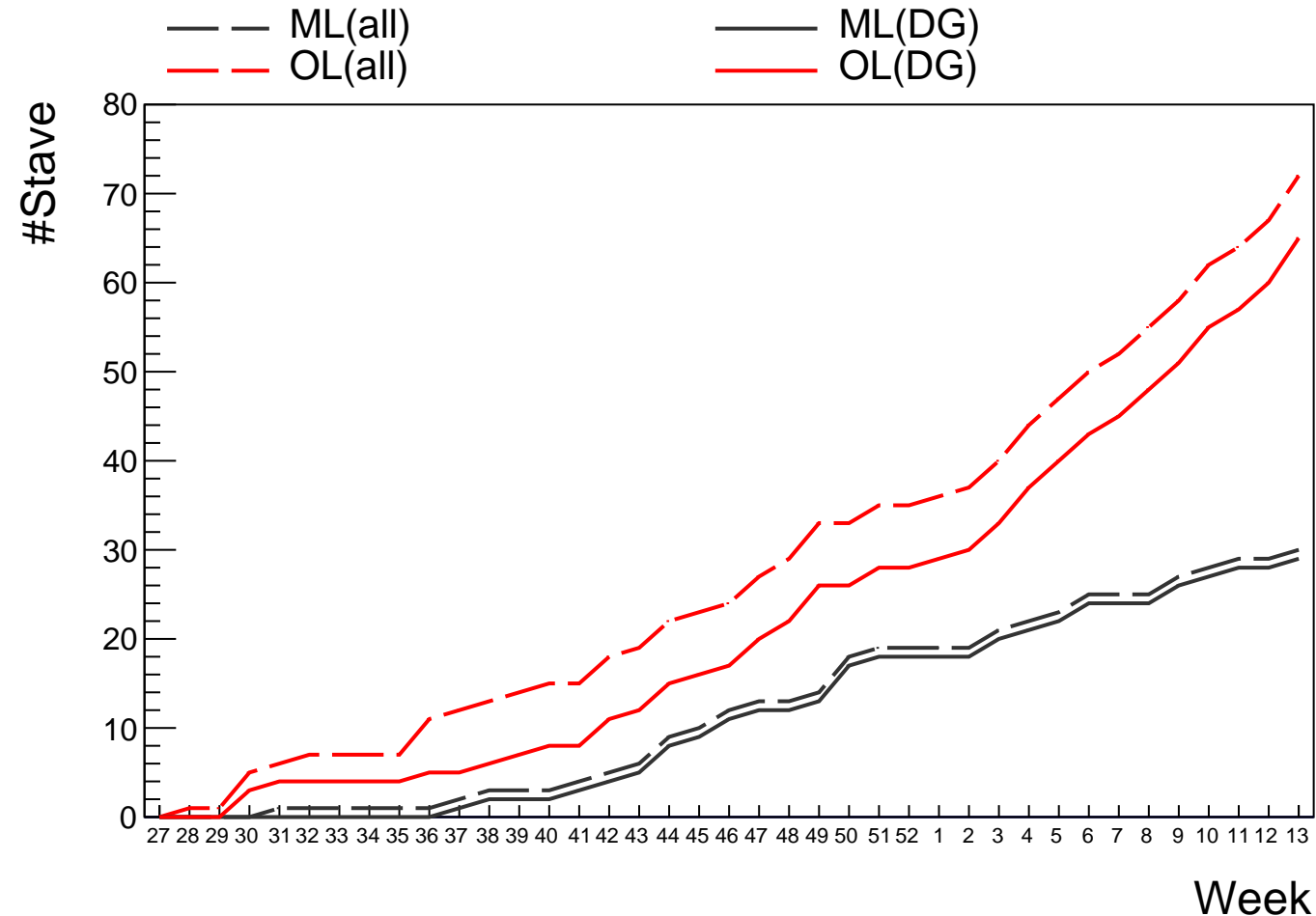
Daresbury: +2

Frascati: +1

Turin: +1



Det. grade Stave vs time

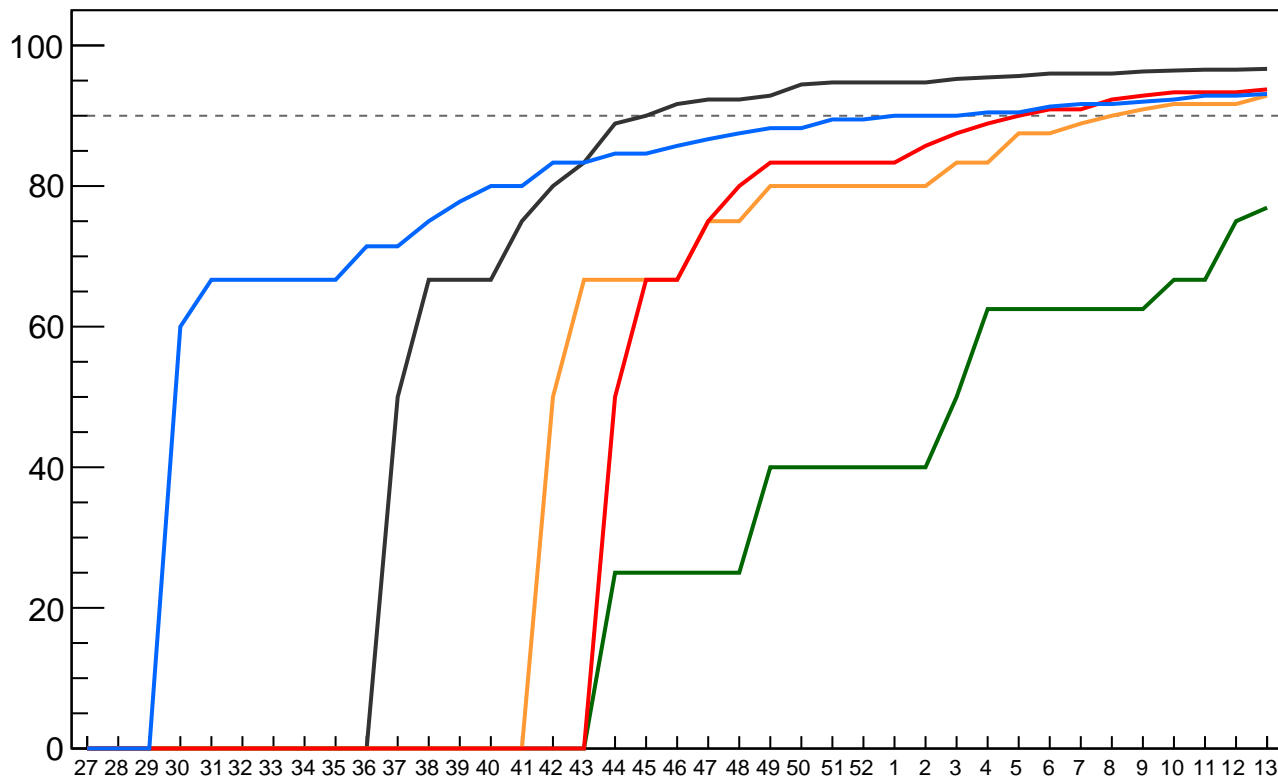


# Stave yield vs time

— Berkeley  
— Daresbury  
— Turin

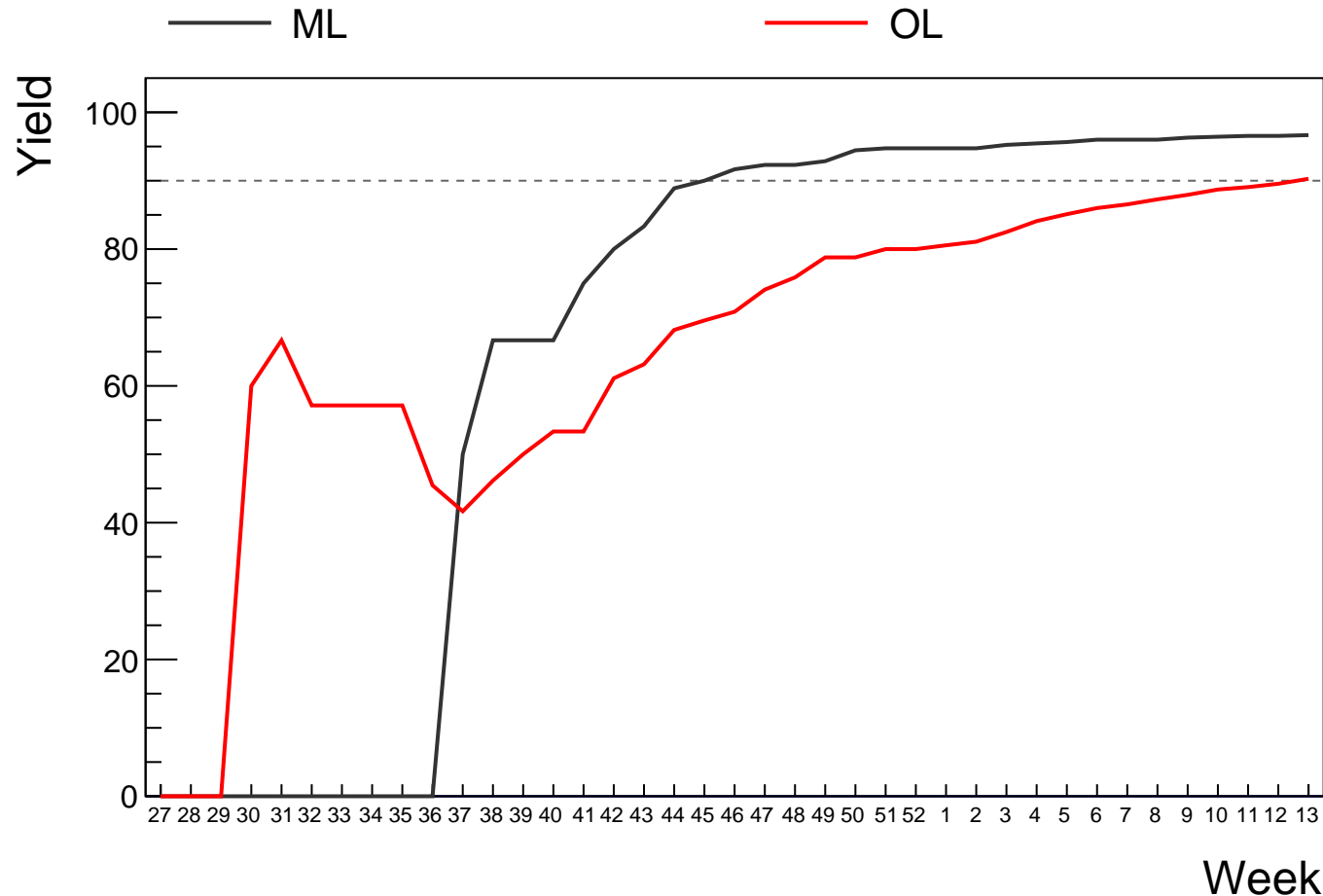
— Nikhef  
— Frascati

Yield



Week

# Stave yield vs time



**Production rate (October 2018 - prev. week)\*\***

**Berkeley: 1.13(all) -- 1.13(DG)**

**Nikhef: 0.39(all) -- 0.39(DG)**

**Daresbury: 0.48(all) -- 0.48(DG)**

**Frascati: 0.61(all) -- 0.61(DG)**

**Turin: 0.78(all) -- 0.78(DG)**

**OL: 2.26(all) -- 2.26(DG)**

**ML: 1.13(all) -- 1.13(DG)**

**\*\*Christmas holiday excluded (2 weeks)**

Stave reception @CERN

**Staves qualified in the previous week**

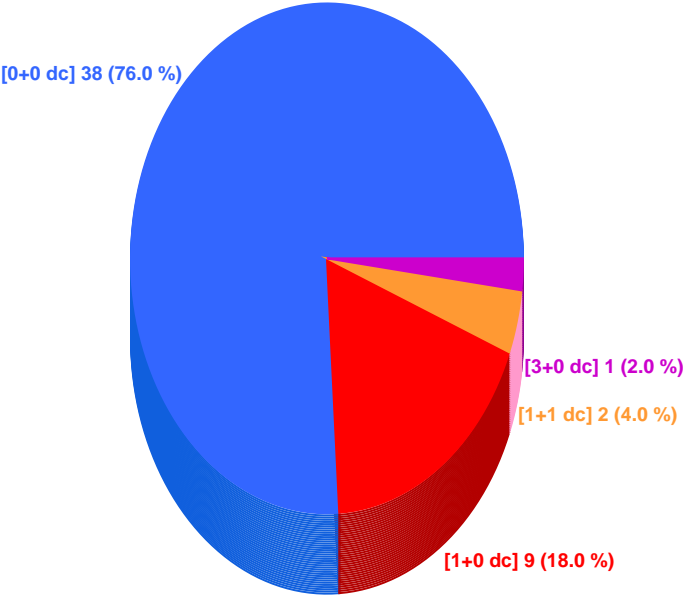
**F-OL-Stave-014: (U,L)=(0, 0) bad chips**

**F-OL-Stave-015: (U,L)=(0, 0) bad chips**

**Staves qualified this week**

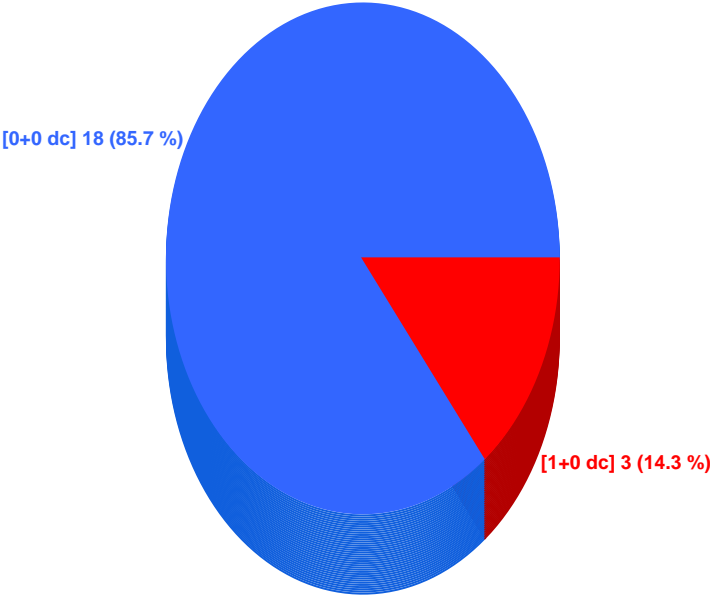
Stave - OL @CERN

98.00 % ok

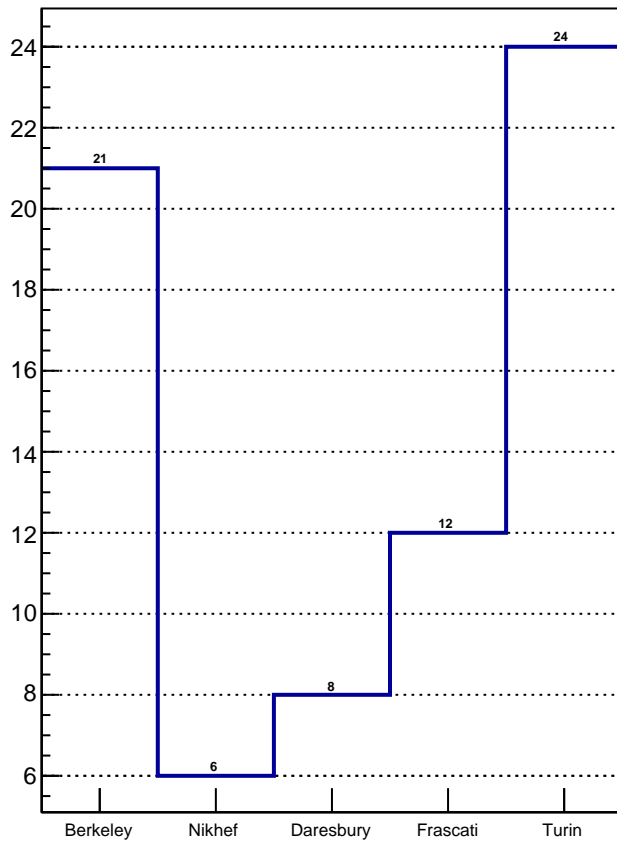


Stave - ML @CERN

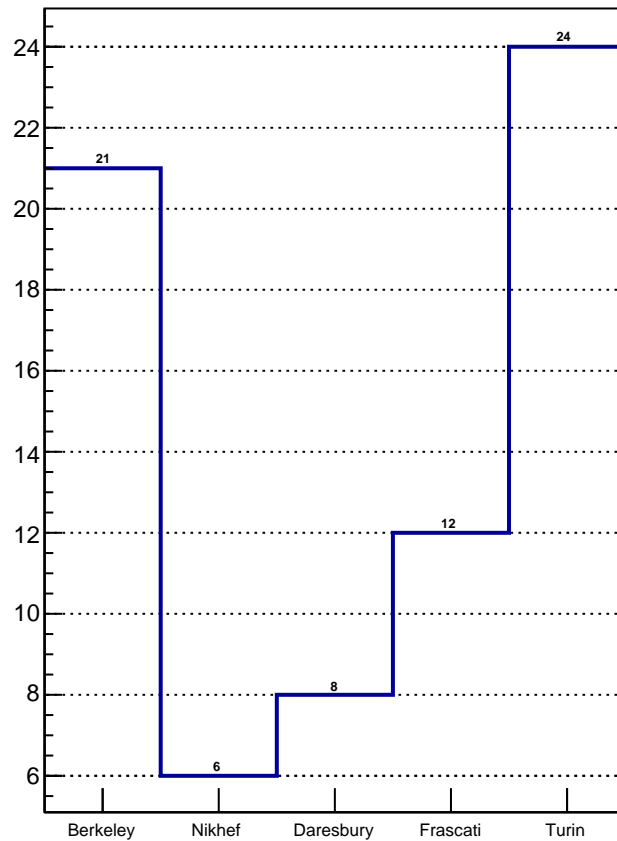
100.00 % ok



All Stave @CERN



Det. Grade Stave @CERN



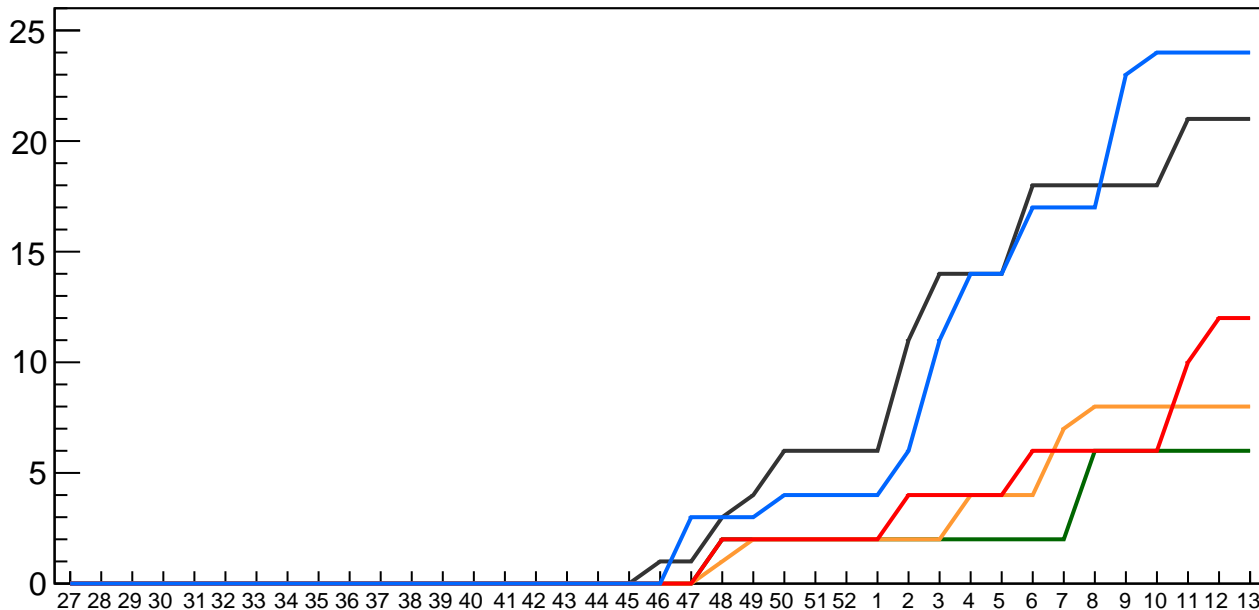


# Det. grade Stave vs time @CERN

Berkeley  
Daresbury  
Turin

Nikhef  
Frascati

#Stave



Week

Comparison to prev. week

Berkeley: +0

Nikhef: +0

Daresbury: +0

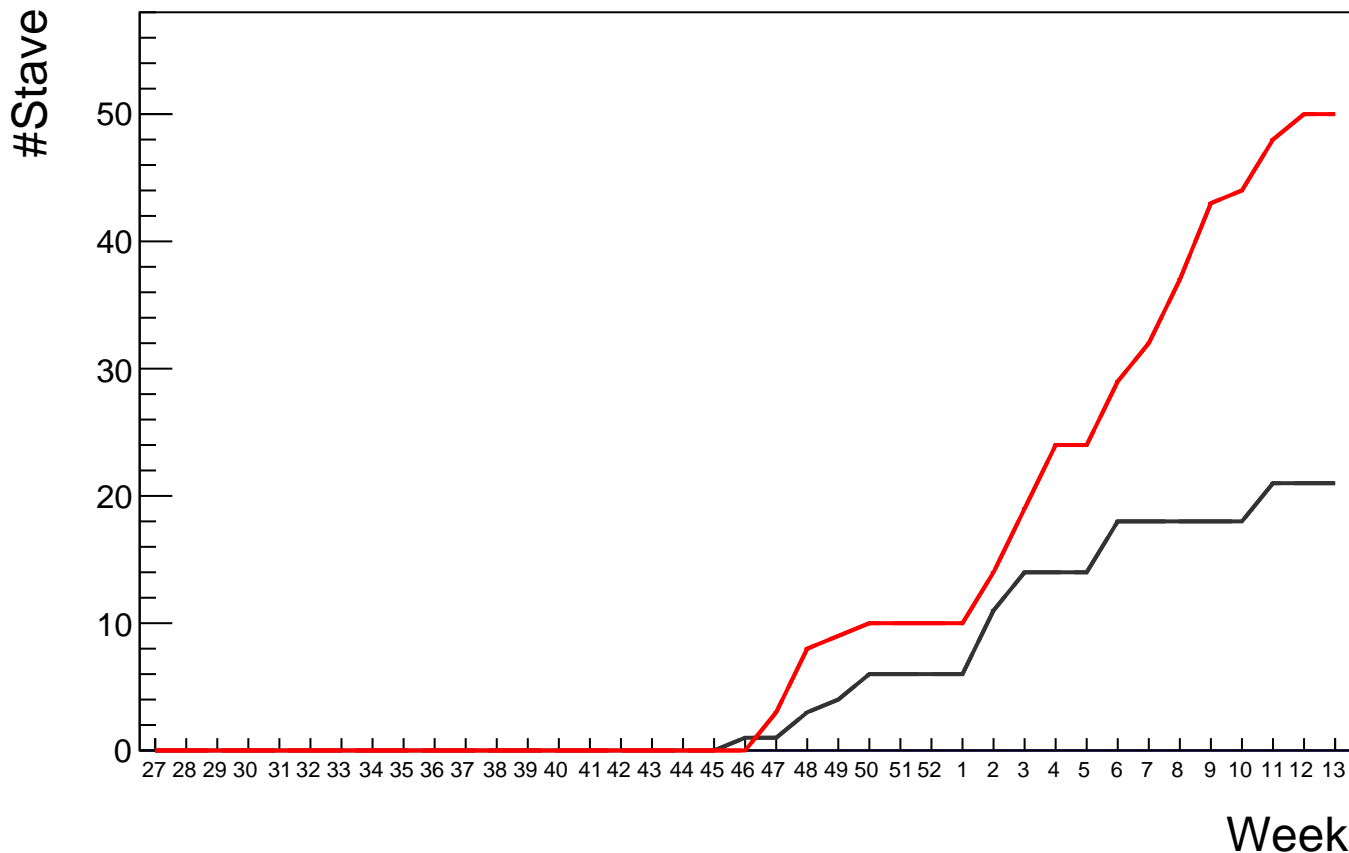
Frascati: +0

Turin: +0

# Det. grade Stave vs time @CERN

— ML(all)  
— OL(all)

— ML(DG)  
— OL(DG)



**Qualification rate (December 2018 - prev. week)\*\***

**Berkeley: 1.29(all) -- 1.29(DG)**

**Nikhef: 0.29(all) -- 0.29(DG)**

**Daresbury: 0.50(all) -- 0.50(DG)**

**Frascati: 0.71(all) -- 0.71(DG)**

**Turin: 1.50(all) -- 1.50(DG)**

**OL: 3.00(all) -- 3.00(DG)**

**ML: 1.29(all) -- 1.29(DG)**

**\*\*Christmas holiday excluded (2 weeks)**

HS without a Stave

### **HSs (DG) not yet tested as Stave**

**D-OL-HS-U-008: 0 bad chips**  
**B-ML-HS-L-014: 0 bad chips**  
**B-ML-HS-U-014: 0 bad chips**  
**A-OL-HS-L-011: 2 bad chips**  
**A-OL-HS-L-012: 0 bad chips**  
**A-OL-HS-L-013: 0 bad chips**  
**A-OL-HS-L-014: 0 bad chips**  
**A-OL-HS-U-009: 2 bad chips**  
**A-OL-HS-U-013: 1 bad chips**  
**A-OL-HS-U-016: 0 bad chips**  
**D-OL-HS-L-008: 0 bad chips**  
**D-OL-HS-L-010: 0 bad chips**  
**D-OL-HS-L-016: 0 bad chips**  
**D-OL-HS-U-016: 0 bad chips**  
**F-OL-HS-L-005: 0 bad chips**  
**F-OL-HS-L-013: 1 bad chips**  
**F-OL-HS-L-020: 0 bad chips**  
**F-OL-HS-L-021: 0 bad chips**  
**F-OL-HS-U-004: 0 bad chips**  
**F-OL-HS-U-005: 0 bad chips**  
**F-OL-HS-U-013: 0 bad chips**  
**F-OL-HS-U-020: 0 bad chips**  
**F-OL-HS-U-021: 0 bad chips**  
**F-OL-HS-U-022: 0 bad chips**  
**F-OL-HS-L-002: 0 bad chips**  
**T-OL-HS-L-031: 0 bad chips**  
**T-OL-HS-U-031: 1 bad chips**

### **HSs (non-DG) not yet tested as Stave**

**A-OL-HS-L-004: 14 bad chips -> rework(?)**

**F-OL-HS-U-002: 8 bad chips -> rework(?)**

Stave not DG

## **Staves not DG**

**A-OL-Stave-001: (U,L) = (2, 14) bad chips**

**A-OL-Stave-002: (U,L) = (7, 49) bad chips**

**A-OL-Stave-003: (U,L) = (98, 98) bad chips**

**D-OL-Stave-001: (U,L) = (0, 22) bad chips**

**F-OL-Stave-001: (U,L) = (43, 14) bad chips**

**B-ML-Stave-001: (U,L) = (2, 0) bad chips**

**T-OL-Stave-002: (U,L) = (7, 1) bad chips**

**T-OL-Stave-003: (U,L) = (6, 2) bad chips**