

Stave production monitoring

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21/03/2019

Monitoring from January 2018 to 21/03/2019

Stave meeting

HS monitoring

HSs of previous week

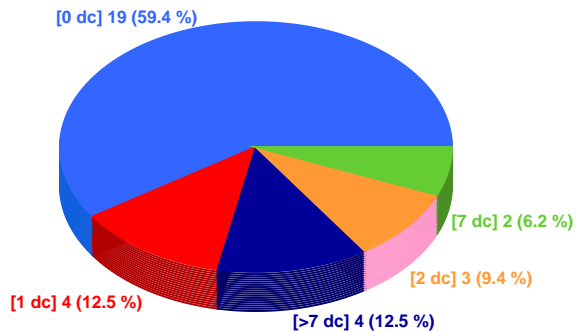
B-ML-HS-L-031: 0 bad chips
A-OL-HS-L-016: 0 bad chips
A-OL-HS-L-017: 2 bad chips
A-OL-HS-U-114: 0 bad chips
D-OL-HS-L-015: 0 bad chips
F-OL-HS-L-020: 0 bad chips
F-OL-HS-U-021: 0 bad chips
T-OL-HS-L-030: 0 bad chips
T-OL-HS-U-030: 0 bad chips

HSs of this week

A-OL-HS-L-011: 2 bad chips
A-OL-HS-L-012: 0 bad chips
D-OL-HS-L-016: 0 bad chips
D-OL-HS-U-016: 0 bad chips
F-OL-HS-L-021: 0 bad chips

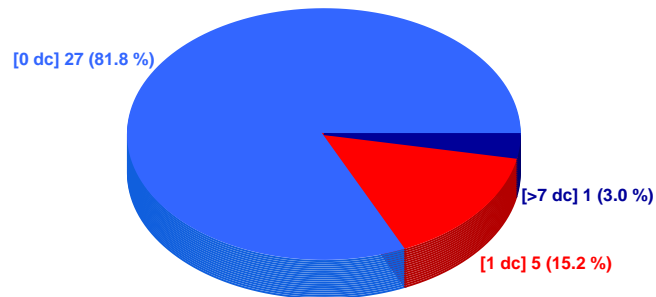
HS - Nikhef

81.25 % ok



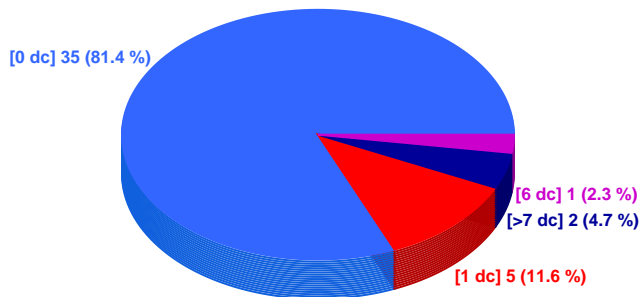
HS - Daresbury

96.97 % ok



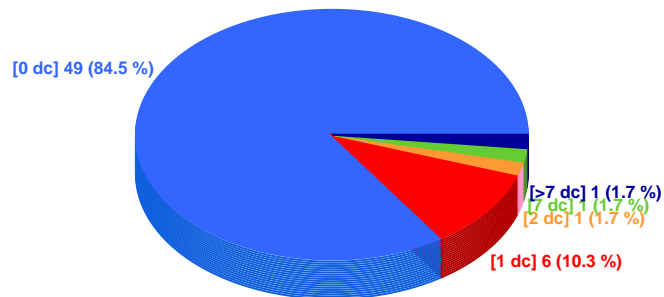
HS - Frascati

93.02 % ok



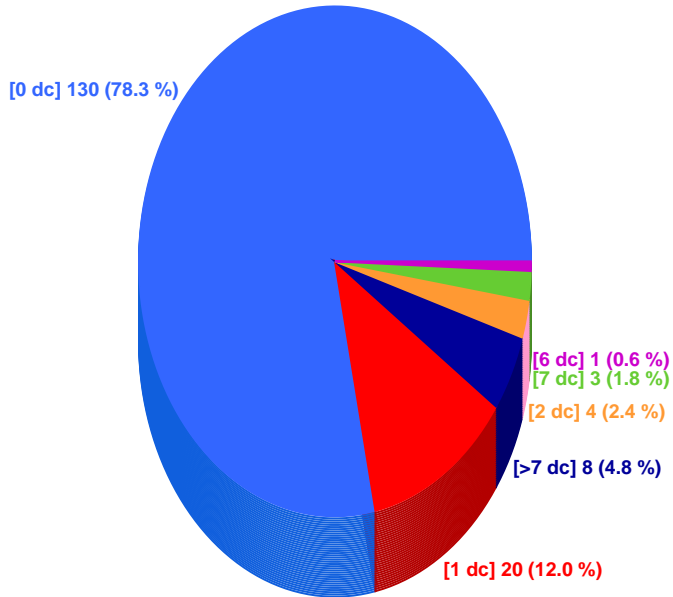
HS - Turin

96.55 % ok



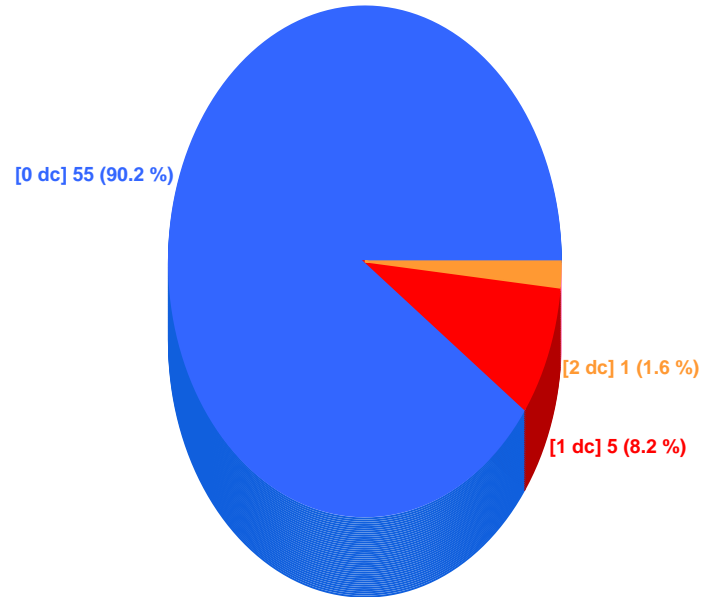
HS - OL

92.77 % ok

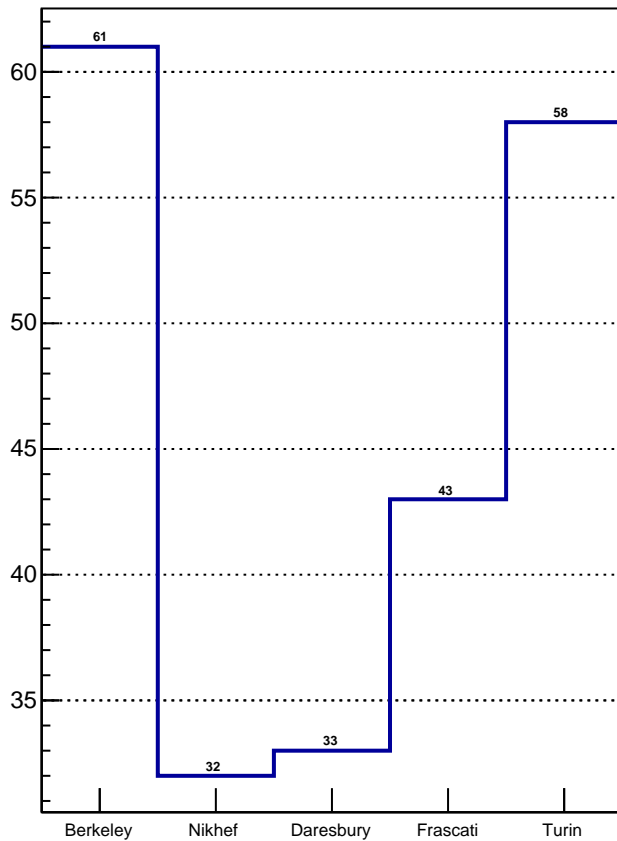


HS - ML

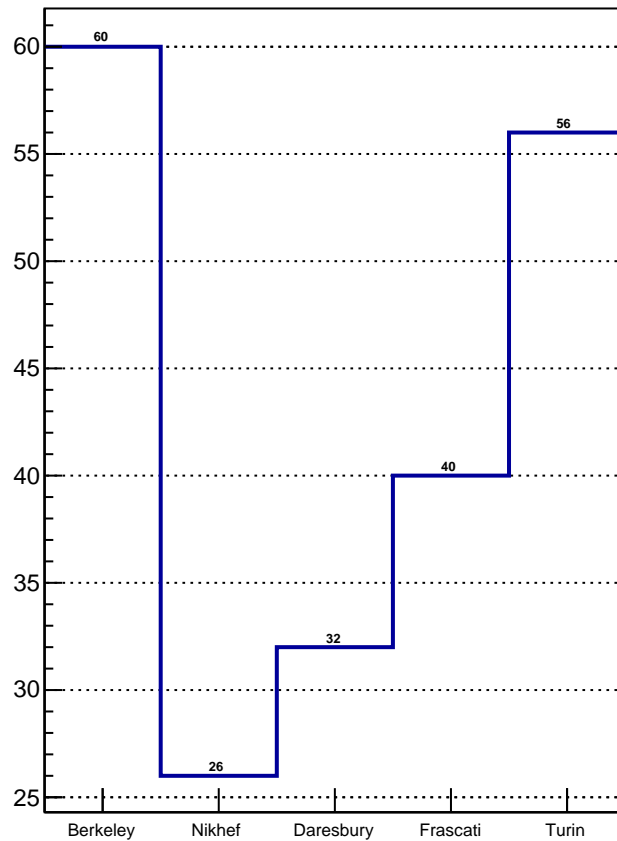
98.36 % ok



All HS



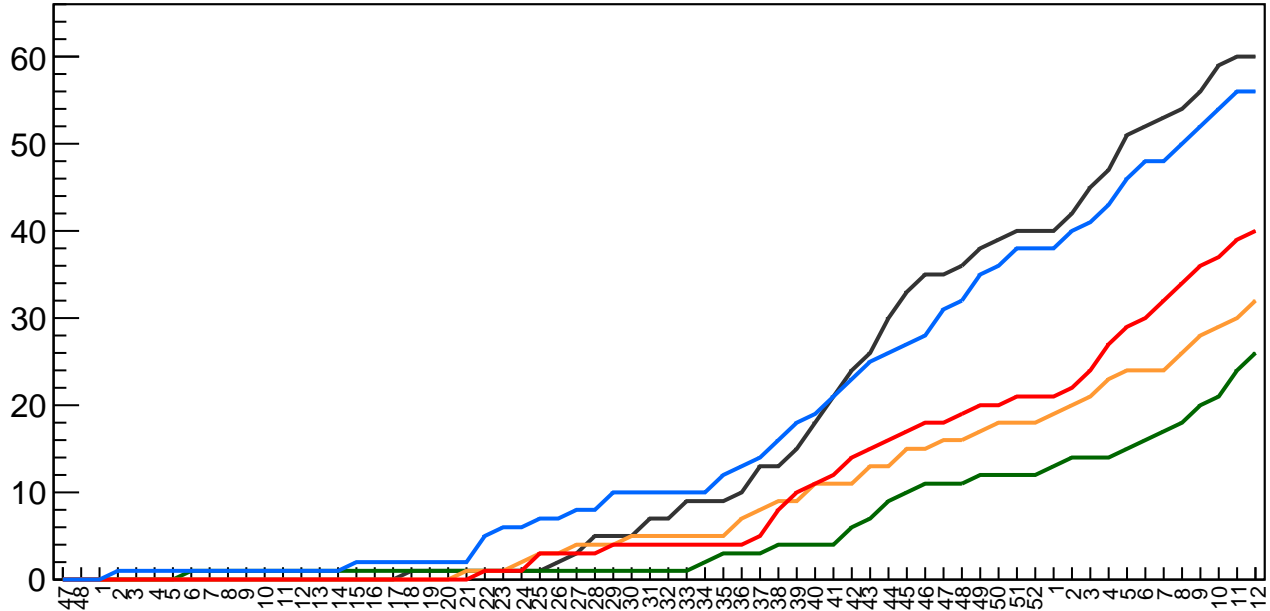
Det. Grade HS



Det. grade HS vs time

— Berkeley
— Daresbury
— Turin
— Nikhef
— Frascati

#HS



Week

Comparison to prev. week

Berkeley: +0

Nikhef: +2

Daresbury: +2

Frascati: +1

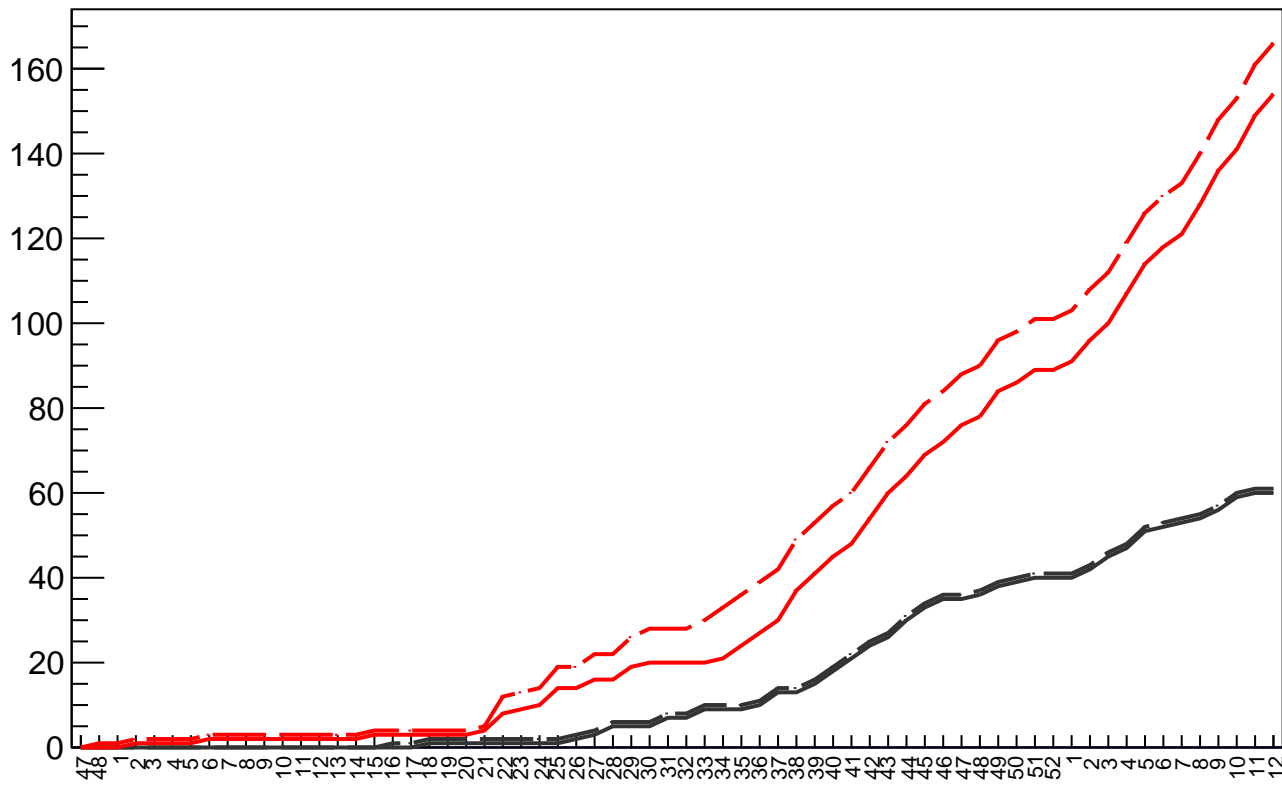
Turin: +0

Det. grade HS vs time

ML(all)
OL(all)

ML(DG)
OL(DG)

#HS

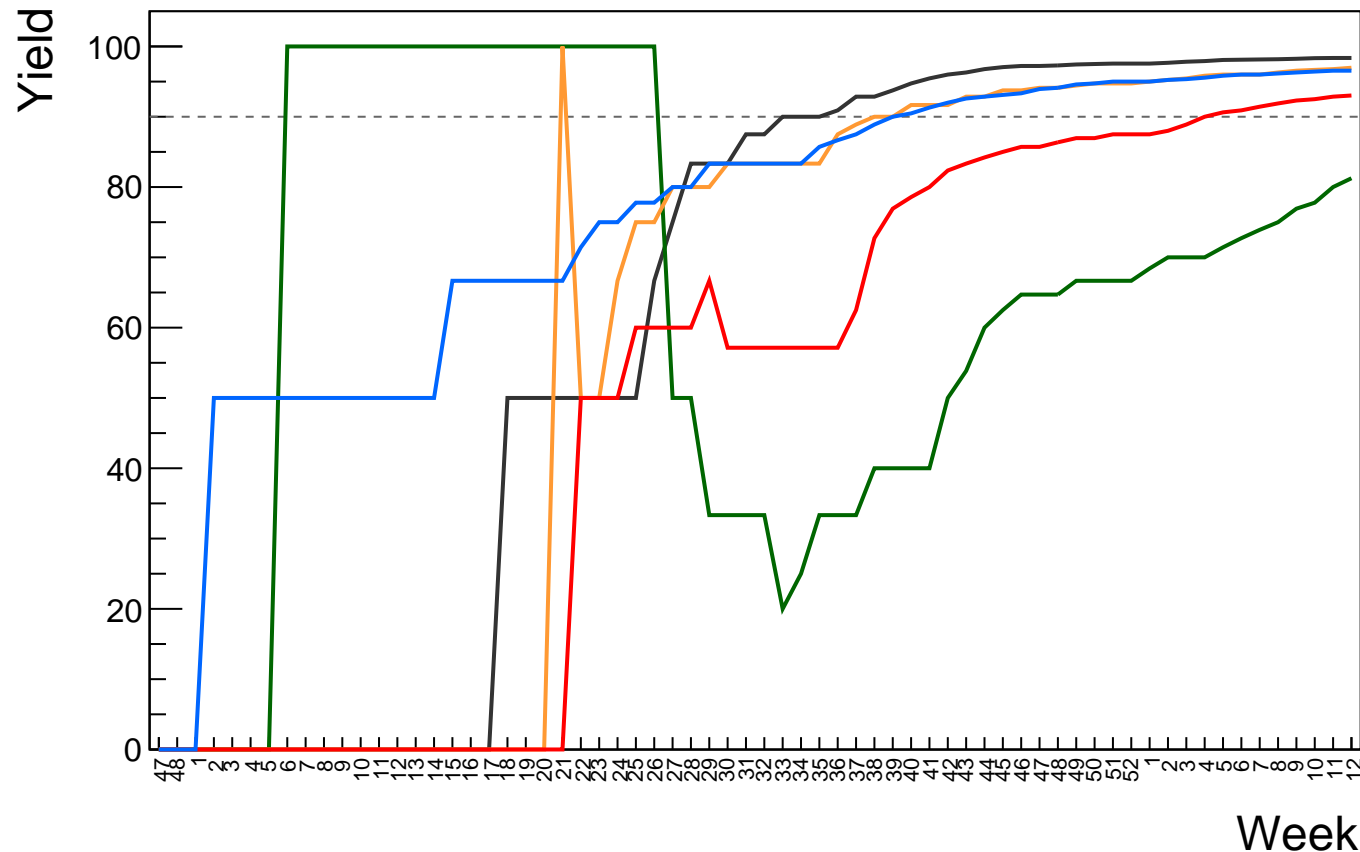


Week

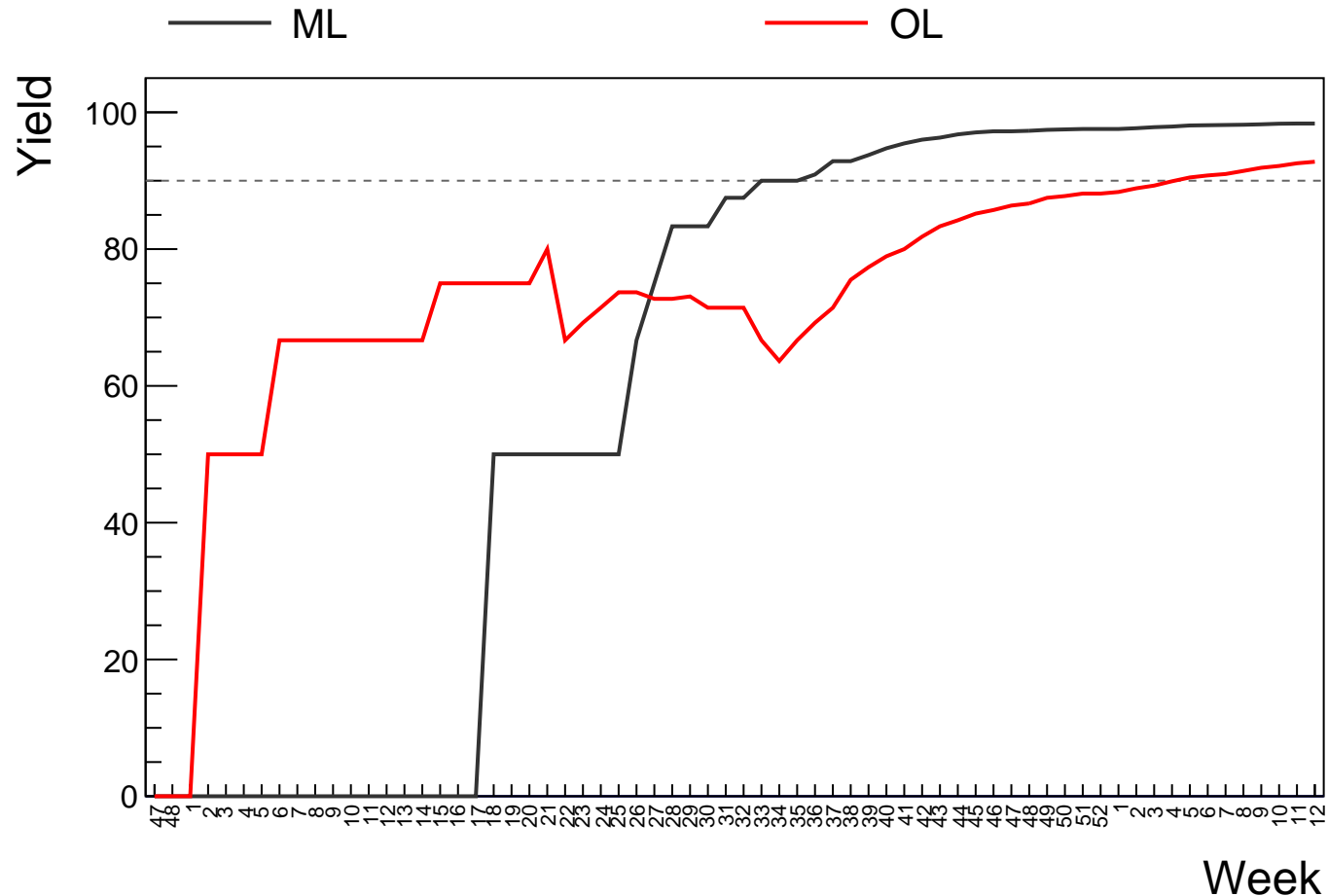
HS Yield vs time

Berkeley
 Daresbury
 Turin

Nikhef
 Frascati



HS Yield vs time



Stave monitoring

Staves of previous week

B-ML-Stave-030: $(U,L)=(0, 0)$ bad chips

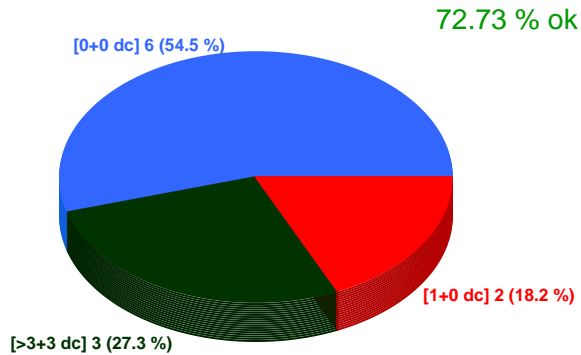
T-OL-Stave-023: $(U,L)=(0, 0)$ bad chips

T-OL-Stave-029: $(U,L)=(0, 0)$ bad chips

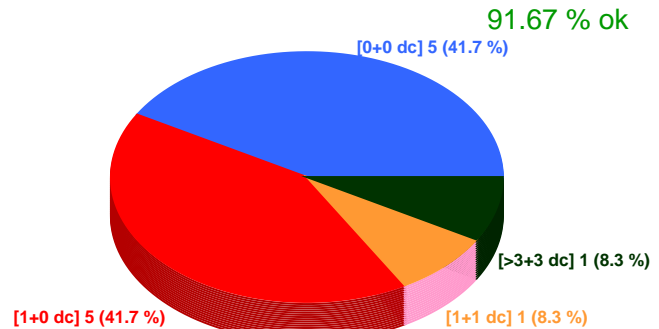
Staves of this week

A-OL-Stave-014: $(U,L)=(0, 0)$ bad chips

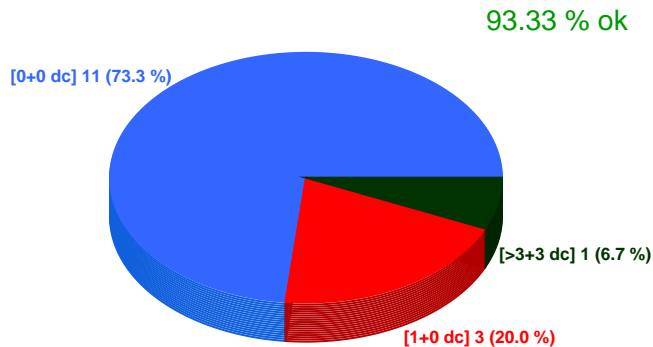
Stave - Nikhef



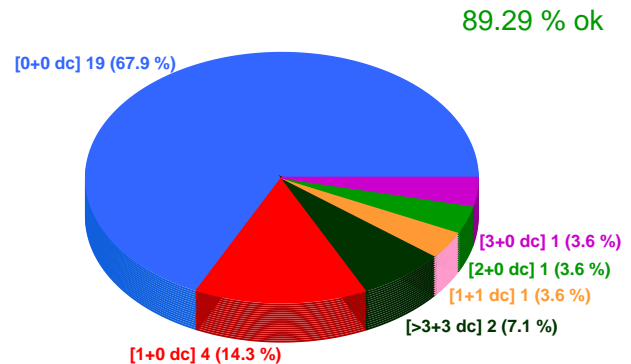
Stave - Daresbury



Stave - Frascati

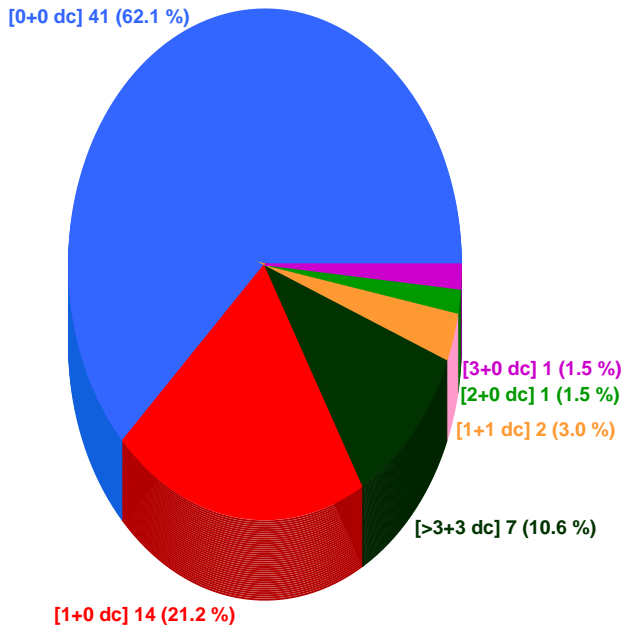


Stave - Turin



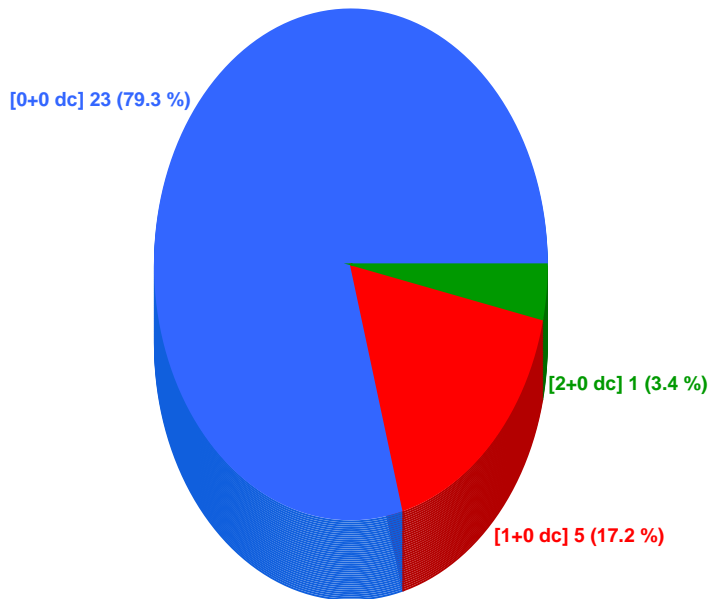
Stave - OL

87.88 % ok

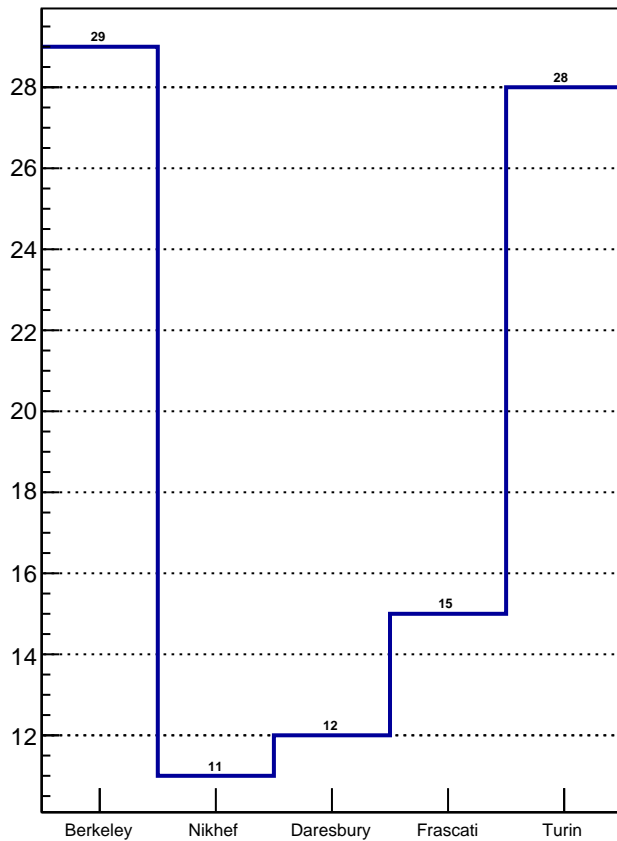


Stave - ML

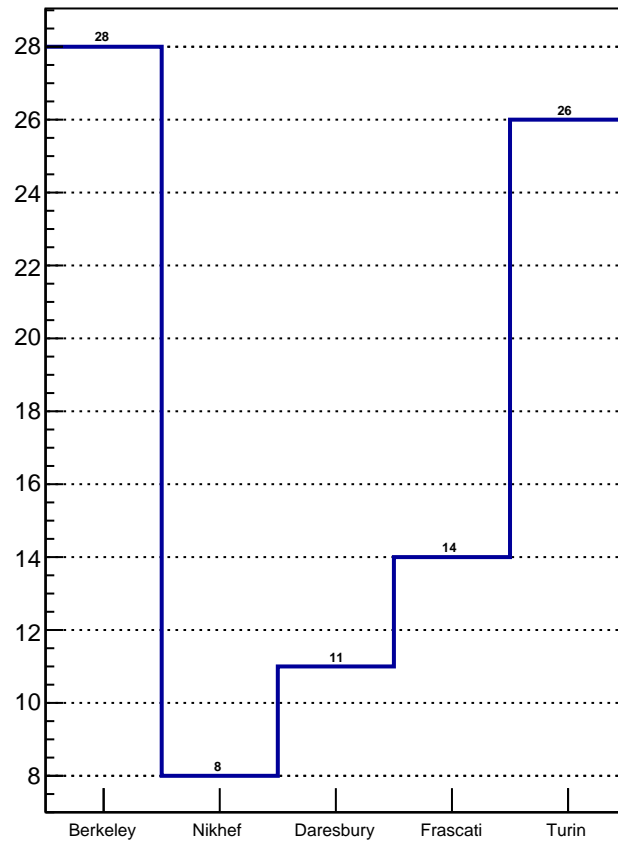
96.55 % ok



All Stave



Det. Grade Stave

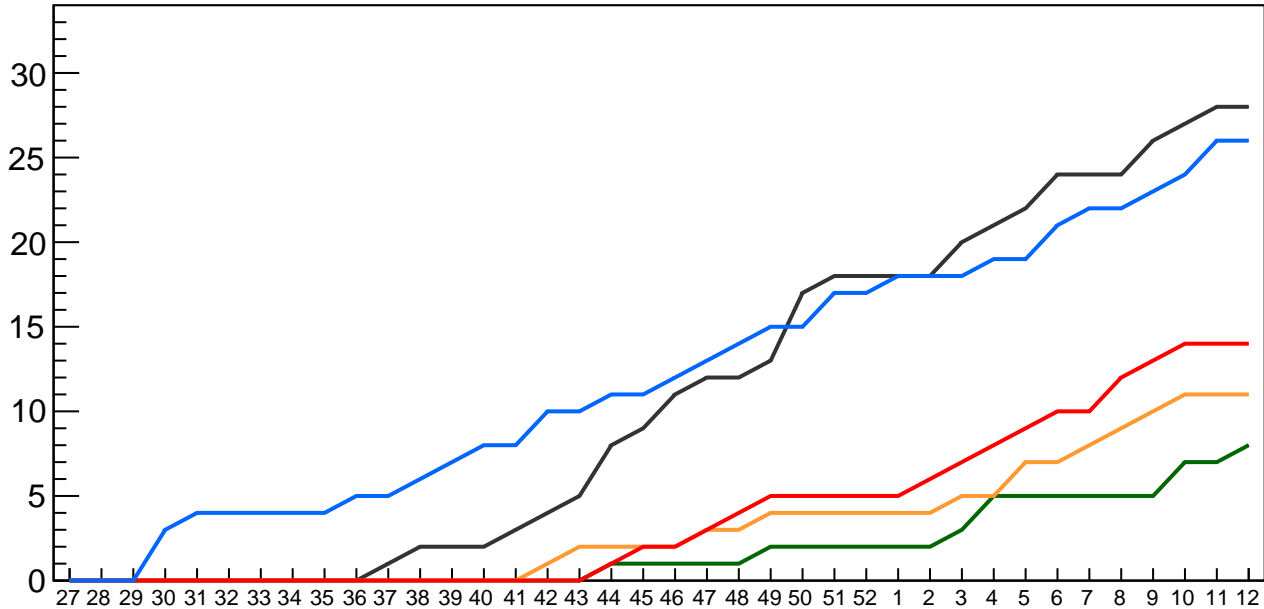


Det. grade Stave vs time

— Berkeley
— Daresbury
— Turin

— Nikhef
— Frascati

#Stave



Week

Comparison to prev. week

Berkeley: +0

Nikhef: +1

Daresbury: +0

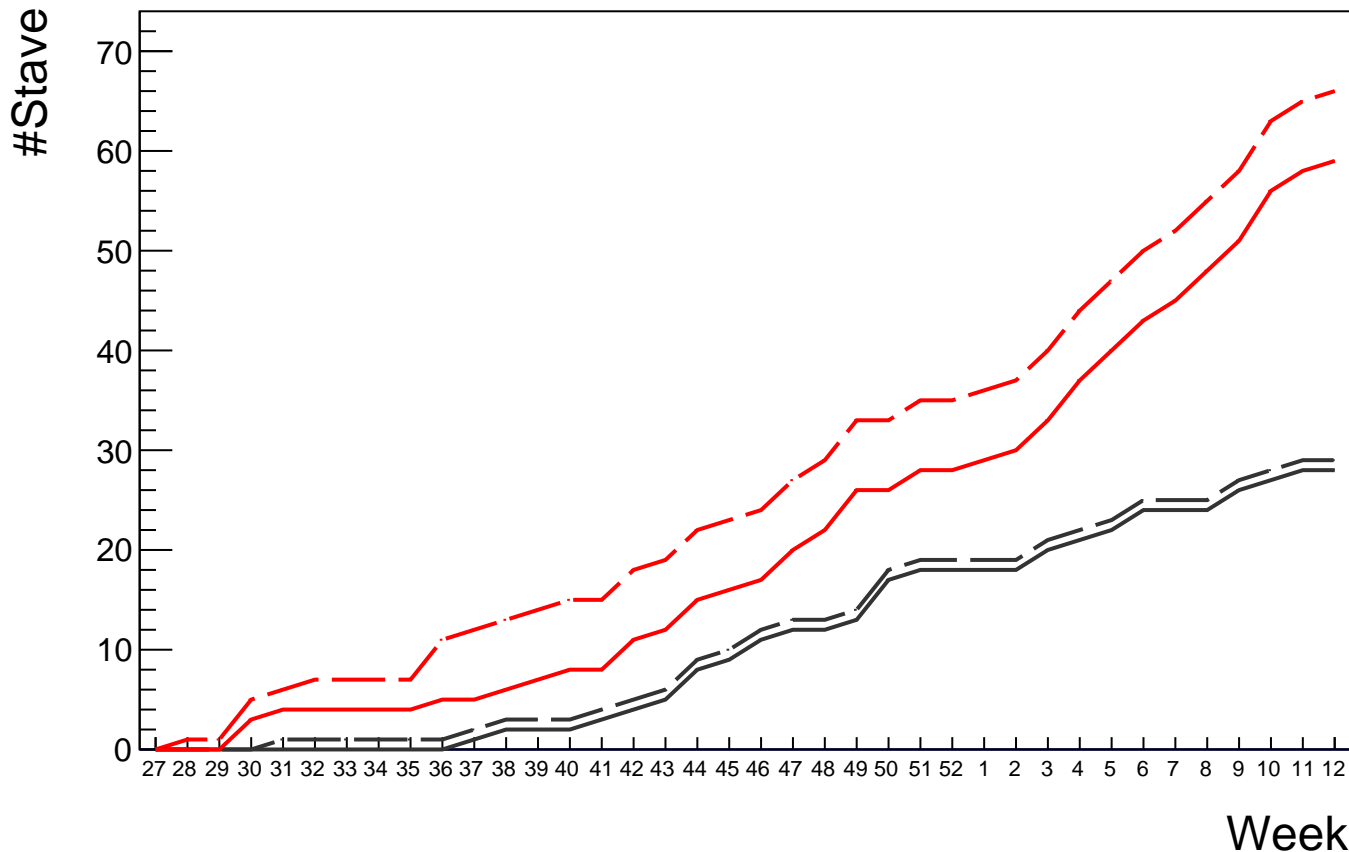
Frascati: +0

Turin: +0

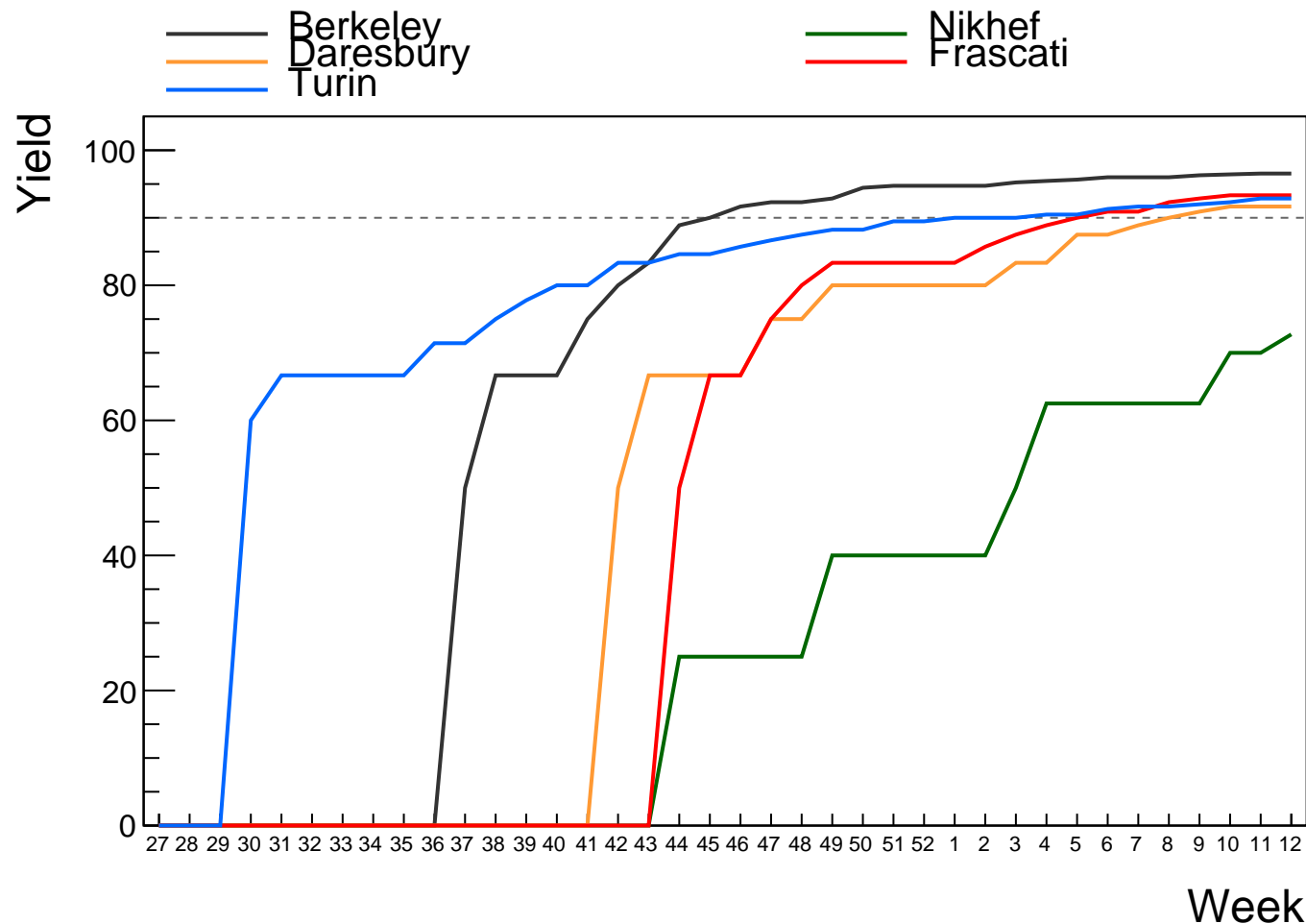
Det. grade Stave vs time

ML(all)
OL(all)

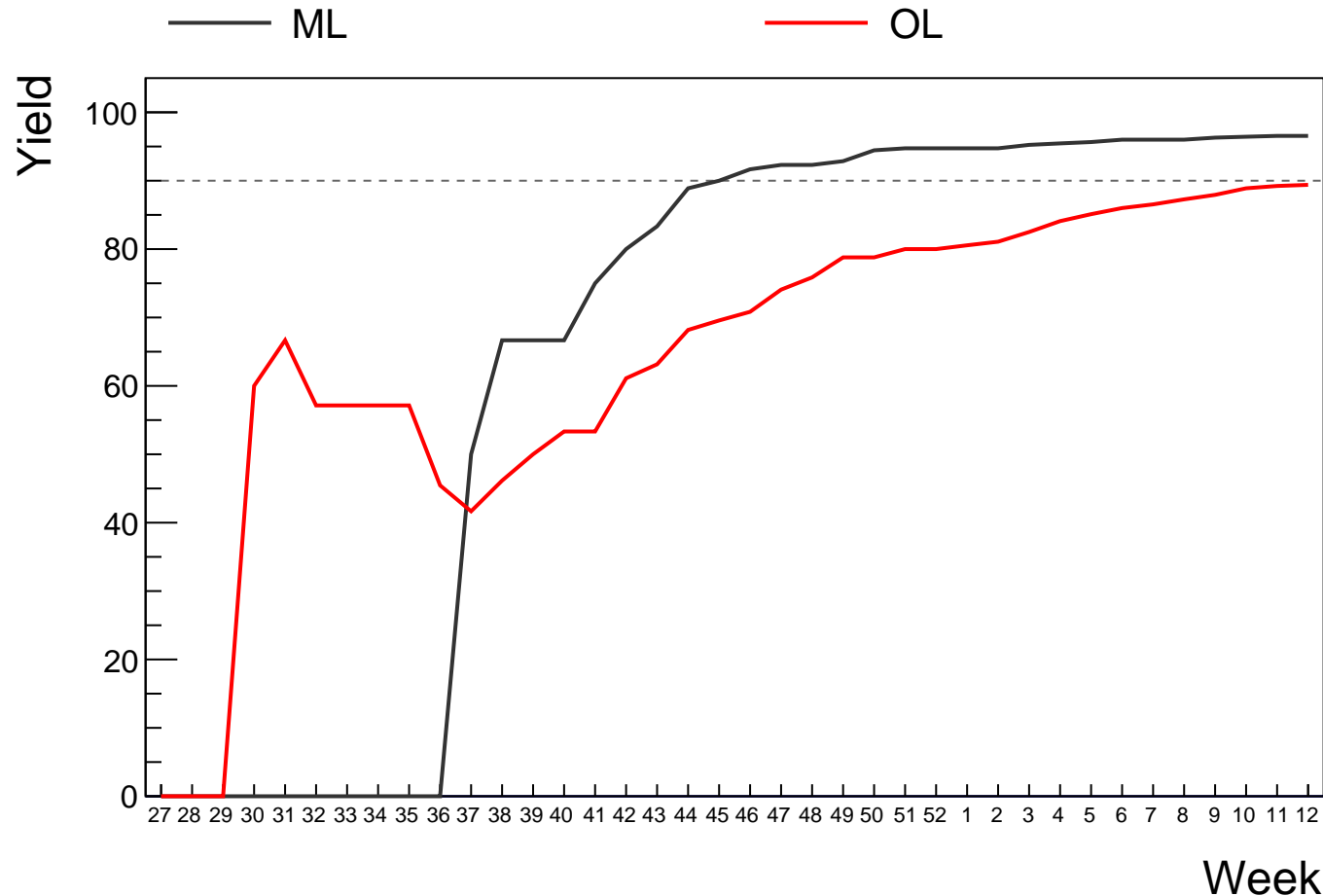
ML(DG)
OL(DG)



Stave yield vs time



Stave yield vs time



Production rate (October 2018 - prev. week)**

Berkeley: 1.18(all) -- 1.18(DG)

Nikhef: 0.32(all) -- 0.32(DG)

Daresbury: 0.50(all) -- 0.50(DG)

Frascati: 0.64(all) -- 0.64(DG)

Turin: 0.82(all) -- 0.82(DG)

OL: 2.27(all) -- 2.27(DG)

ML: 1.18(all) -- 1.18(DG)

****Christmas holiday excluded (2 weeks)**

Stave reception @CERN

Staves qualified in the previous week

B-ML-Stave-024: (U,L)=(0, 0) bad chips

B-ML-Stave-026: (U,L)=(0, 0) bad chips

B-ML-Stave-028: (U,L)=(0, 0) bad chips

F-OL-Stave-011: (U,L)=(0, 1) bad chips

F-OL-Stave-012: (U,L)=(0, 0) bad chips

F-OL-Stave-017: (U,L)=(0, 0) bad chips

F-OL-Stave-018: (U,L)=(0, 0) bad chips

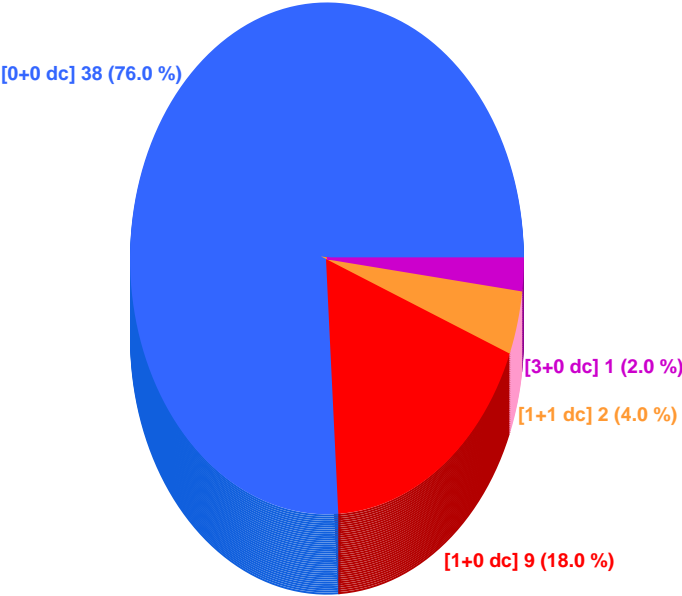
Staves qualified this week

F-OL-Stave-014: (U,L)=(0, 0)

F-OL-Stave-015: (U,L)=(0, 0)

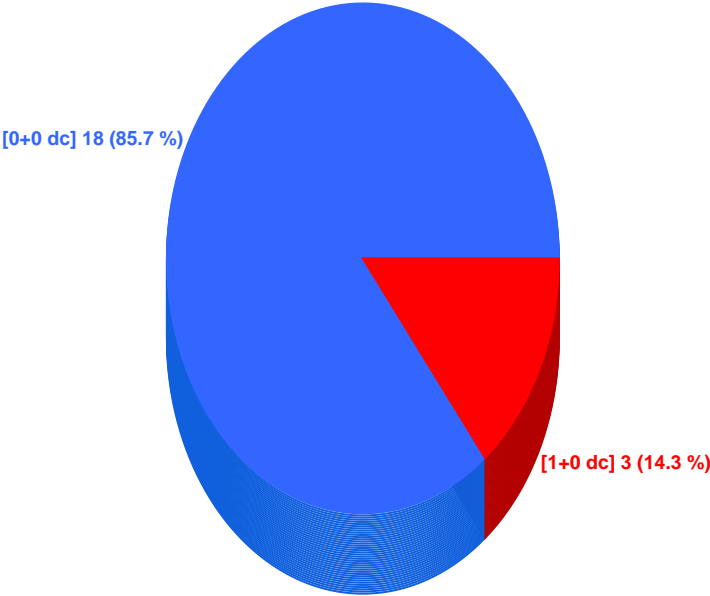
Stave - OL @CERN

98.00 % ok

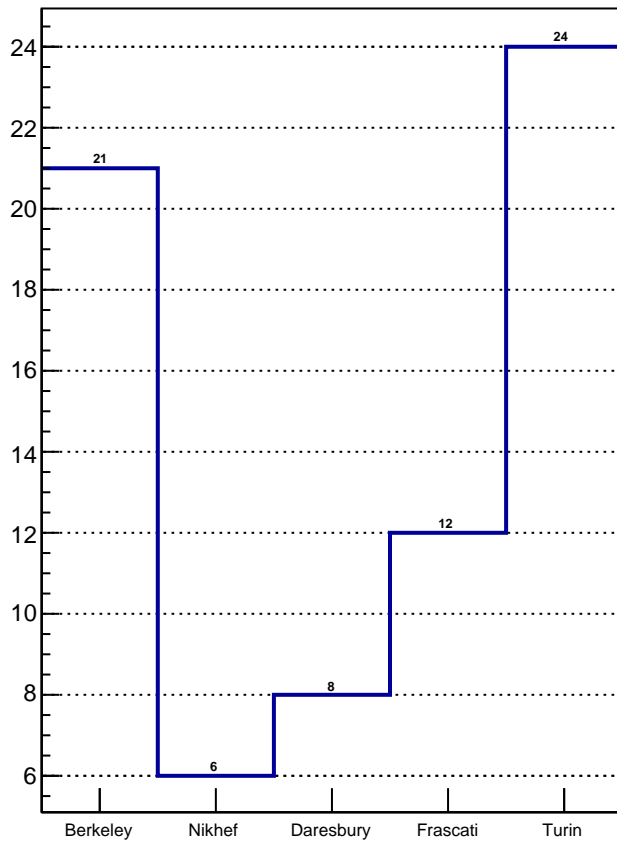


Stave - ML @CERN

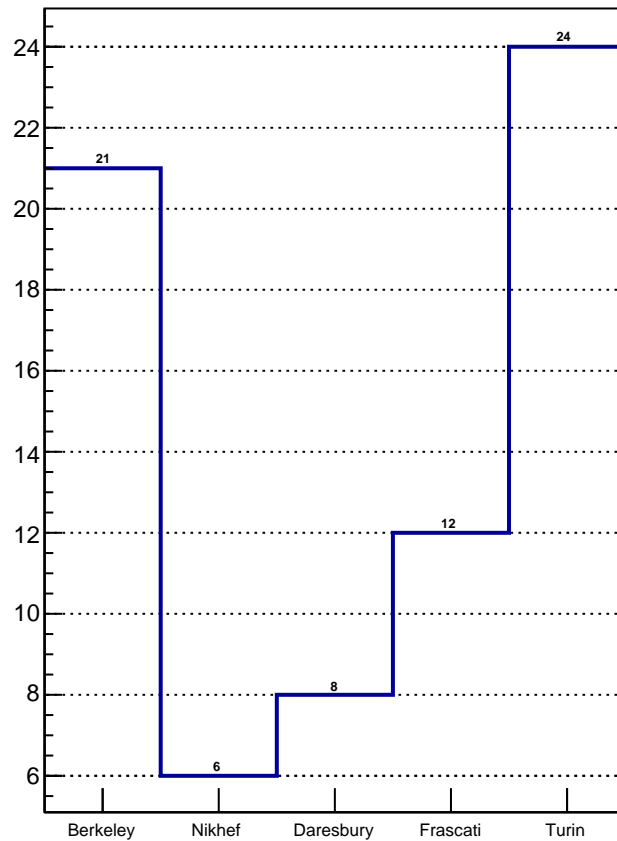
100.00 % ok



All Stave @CERN



Det. Grade Stave @CERN

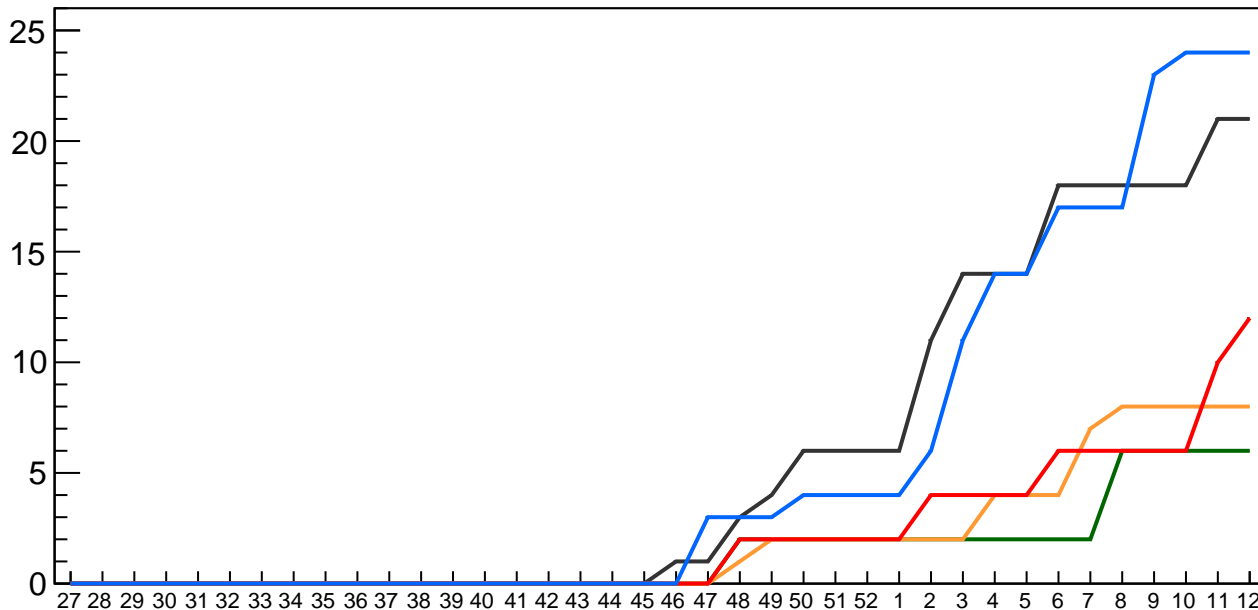


Det. grade Stave vs time @CERN

Berkeley
Daresbury
Turin

Nikhef
Frascati

#Stave



Week

Comparison to prev. week

Berkeley: +0

Nikhef: +0

Daresbury: +0

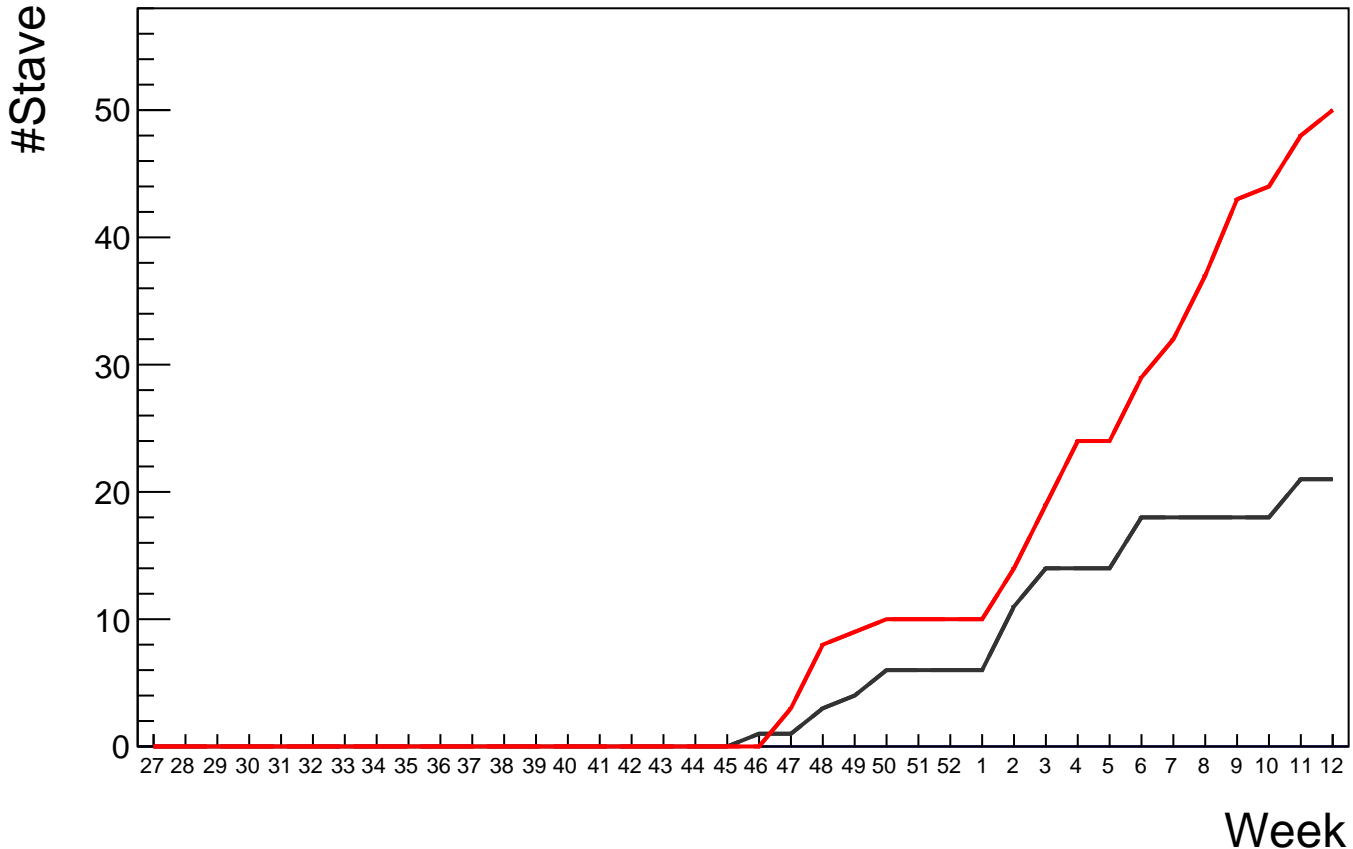
Frascati: +2

Turin: +0

Det. grade Stave vs time @CERN

— ML(all)
— OL(all)

— ML(DG)
— OL(DG)



Qualification rate (December 2018 - prev. week)**

Berkeley: 1.38(all) -- 1.38(DG)

Nikhef: 0.31(all) -- 0.31(DG)

Daresbury: 0.54(all) -- 0.54(DG)

Frascati: 0.62(all) -- 0.62(DG)

Turin: 1.62(all) -- 1.62(DG)

OL: 3.08(all) -- 3.08(DG)

ML: 1.38(all) -- 1.38(DG)

****Christmas holiday excluded (2 weeks)**

HS without a Stave

HSs (DG) not yet tested as Stave

D-OL-HS-U-008: 0 bad chips
B-ML-HS-L-014: 0 bad chips
B-ML-HS-L-031: 0 bad chips
B-ML-HS-U-014: 0 bad chips
A-OL-HS-L-011: 2 bad chips
A-OL-HS-L-012: 0 bad chips
A-OL-HS-L-013: 0 bad chips
A-OL-HS-L-014: 0 bad chips
A-OL-HS-L-017: 2 bad chips
A-OL-HS-U-009: 2 bad chips
A-OL-HS-U-013: 1 bad chips
A-OL-HS-U-114: 0 bad chips
D-OL-HS-L-008: 0 bad chips
D-OL-HS-L-010: 0 bad chips
D-OL-HS-L-015: 0 bad chips
D-OL-HS-L-016: 0 bad chips
D-OL-HS-L-114: 0 bad chips
D-OL-HS-U-015: 0 bad chips
D-OL-HS-U-016: 0 bad chips
F-OL-HS-L-005: 0 bad chips
F-OL-HS-L-013: 1 bad chips
F-OL-HS-L-019: 0 bad chips
F-OL-HS-L-020: 0 bad chips
F-OL-HS-L-021: 0 bad chips
F-OL-HS-U-004: 0 bad chips
F-OL-HS-U-005: 0 bad chips
F-OL-HS-U-013: 0 bad chips
F-OL-HS-U-019: 0 bad chips
F-OL-HS-U-020: 0 bad chips
F-OL-HS-U-021: 0 bad chips
F-OL-HS-L-002: 0 bad chips
T-OL-HS-L-030: 0 bad chips
T-OL-HS-U-030: 0 bad chips

HSs (non-DG) not yet tested as Stave

A-OL-HS-L-004: 14 bad chips -> rework(?)

F-OL-HS-U-002: 8 bad chips -> rework(?)

Stave not DG

Staves not DG

A-OL-Stave-001: (U,L) = (2, 14) bad chips

A-OL-Stave-002: (U,L) = (7, 49) bad chips

A-OL-Stave-003: (U,L) = (98, 98) bad chips

D-OL-Stave-001: (U,L) = (0, 22) bad chips

F-OL-Stave-001: (U,L) = (43, 14) bad chips

B-ML-Stave-001: (U,L) = (2, 0) bad chips

T-OL-Stave-002: (U,L) = (7, 1) bad chips

T-OL-Stave-003: (U,L) = (6, 2) bad chips