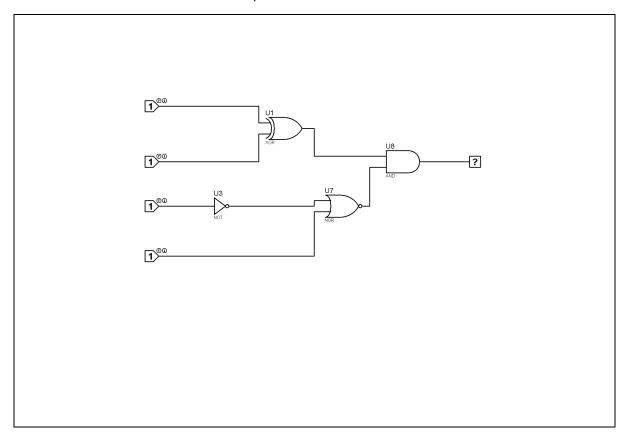
LAB 01 - Assignment

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Task: Sketch and simulate the Boolean function below on Proteus Design Suite by using minimum possible number of gates. Don't do any simplifications.

- Export your design as .BMP format and paste the image into the box below.
- To Export as .BMP File: FileExport GraphicsExport Bitmap. And place it in the box below.
 PS: Screenshots will not be accepted.



• Fill out the truth table below based on the simulation results.

Α	В	С	D	F
0	0	0	0	0
0	0	0	1	0
0	0	1	0	0
0	0	1	1	0
0	1	0	0	0
0	1	0	1	0
0	1	1	0	1
0	1	1	1	0

1	0	0	0	0
1	0	0	1	0
1	0	1	0	1
1	0	1	1	0
1	1	0	0	0
1	1	0	1	0
1	1	1	0	0
1	1	1	1	0