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DESIGN OF HIGH SPEED 32-BIT FLOATING POINT FFT PROCESSOR USING FPGA



LAP Lambert Academic Publishing Mai 2016, 2016. Taschenbuch. Condition: Neu. Neuware - The Discrete Fourier Transform (DFT) is used in a wide variety of Digital Signal Processing applications. The algorithm used to implement this transform requires intensive arithmetic computation as well as complex control and sequence functions. The designer of VLSI components is faced with problem of identifying requirements and architectures for FFT algorithm which directly support the DFT. Design goals of this book includes 32-bit floating point FFT calculation in...

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