Assignment: Designing an address decoder with flexible base address
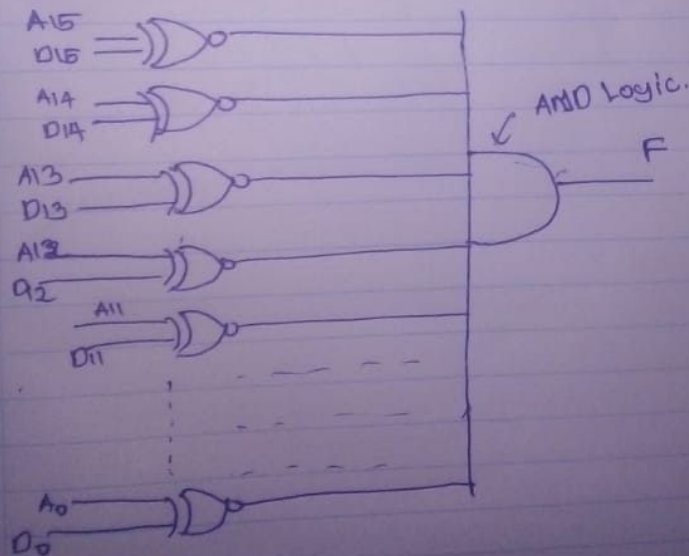**Reg No: E/15/138**
Name: M.M.M Irfan

Assignment : Address decoder with flexible base address.

Decoder with a dip switch to set the address.

we can use an XNOR gate . in each address line to achieve this.

→ assume if a dip switch is off it gives logic "0" in that line and if it is on it gives Logic "1" in that line.

Since this is a 16 bit address, we"ll use 16 input dip switch.

AND Logic.

$D_{15} - D_0$ are the inputs coming from the Dip switch.