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Silicon Design Engineer 2

Locations: San Jose, California
Categories: Engineering
Req ID: 21713
Hiring Target Min: USD $98,400.00/Yr.
Hiring Target Max: USD $147,600.00/Yr.

✉

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Job Description

WHAT YOU DO AT AMD CHANGES EVERYTHING

We care deeply about transforming lives with AMD technology to enrich our industry, our communities, and the world. Our mission is to build great products that accelerate next-generation computing experiences – the building blocks for the data center, artificial intelligence, PCs, gaming and embedded. Underpinning our mission is the AMD culture. We push the limits of innovation to

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solve the world's most important challenges. We strive for execution excellence while being direct, humble, collaborative, and inclusive of diverse perspectives. This is who we are at our best. One Company. One Team.

AMD together we advance_

THE ROLE:

Join a team of silicon design engineers who utilize their skills in logical and physical circuit optimization to innovate and implement programmable logic interfaces. Our team owns the design and implementation from RTL to final layout database delivery, as well as supports silicon validation and characterization. You'll be part of a small and focused design team with a wide scope of responsibilities and technical opportunities using the latest in circuit design tools and methodologies. Your skills will be deployed in simulation, characterization, and model development for circuit performance and power at cell, critical path, and block-level designs.

THE PERSON:

We are looking for a self-motivated team worker, who possesses good verbal and written communication skills. The ideal candidate strives to tackle challenges in creative ways with solutions that can be applied in a wide range of applications. If you have a drive for high-quality, high-accountability design work, then you will be a great fit for our team!

KEY RESPONSIBILITIES:

- Schematic capture
- Logical equivalency checks
- Test bench development
- Place and route constraint definition
- Physical design closure in APR tools
- Critical-path simulations for timing correlation with STA
- Electrical rule checks commensurate with each stage of the design Generating and characterizing models of circuit timing and power

PREFERRED EXPERIENCE:

- Knowledge in VLSI design, design automation, IC design, and RTL design

- Knowledge of floor planning, synthesis, place & route, static timing analysis, and EM/IR analysis
- Knowledge of Cadence SPICE, Synopsys Primetime, and IC compiler preferred

ACADEMIC CREDENTIALS:

- MS (preferred) or BS with additional experience in Electrical Engineering or Computer Engineering or related equivalent

COMPENSATION:

Expected to range from $98,400 to $147,600, commensurate with experience and specific skill sets

#LI-JY1

At AMD, your base pay is one part of your total rewards package. Your base pay will depend on where your skills, qualifications, experience, and location fit into the hiring range for the position. You may be eligible for incentives based upon your role such as either an annual bonus or sales incentive. Many AMD employees have the opportunity to own shares of AMD stock, as well as a discount when purchasing AMD stock if voluntarily participating in AMD's Employee Stock Purchase Plan. You'll also be eligible for competitive benefits described in more detail here (https://amd.jibeapply.com/benefits).

AMD does not accept unsolicited resumes from headhunters, recruitment agencies, or fee-based recruitment services. AMD and its subsidiaries are equal opportunity, inclusive employers and will consider all applicants without regard to age, ancestry, color, marital status, medical condition, mental or physical disability, national origin, race, religion, political and/or third-party affiliation, sex, pregnancy, sexual orientation, gender identity, military or veteran status, or any other characteristic protected by law.   We encourage applications from all qualified candidates and will accommodate applicants' needs under the respective laws throughout all stages of the recruitment and selection process.

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