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Memory Circuit Design Engineer

You applied for this job on March 12, 2023.

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 USA-CA Irvine Alton Parkway Bldg 1

 Full time

 Posted 30+ Days Ago

 R016077

Please Note:

1. If you are a first time user, please create your candidate login account before you apply for a job. (Click Sign In > Create Account)
2. If you already have a Candidate Account, please Sign-In before you apply.

Job Description:

Memory Circuit Design Engineer

We are looking for energetic and passionate memory design engineers to join our Central Engineering Group and be part of an elite memory team responsible for the development of memory compilers and custom macros of all types on the bleeding edge of process technology. Typically requires a minimum of 5+ years of relevant experience and a Bachelor in Electrical Engineering.

Job Responsibilities

- Design and build memory or circuit blocks at the gate or transistor level
- Simulate and analyze the circuit design using transistor level simulators
- Extract the layout and perform post-layout simulations and verification
- Floorplan physical implementation and leafcell layout integration to build the physical macro

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Skills Required

- Expertise in development of memory compilers and custom macros of all types; SRAMs, Register-files, Multi-ports, ROM, etc...
- Good understanding of transistor level circuit behavior and device physics
- Good understanding of signal integrity analysis, EM/IR analysis, and reliability analysis
- Good understanding of memory behavioral and physical models
- Good understanding of DFT schemes and chip level integration
- Familiar with test setups, silicon testing and debug
- Proficient in running simulators, writing automation scripts, and are tools savvy
- Good communication, interpersonal, and leadership skills
- Motivated, self-driven and good at multi-tasking

Additional Job Description:

Compensation and Benefits

The annual base salary range for this position is \$78,000.00 - \$130,000.00.

This position is also eligible for a discretionary annual bonus in accordance with relevant plan documents, and equity in accordance with equity plan documents and equity award agreements.

Broadcom offers a competitive and comprehensive benefits package: Medical, dental and vision plans, 401(K) participation including company matching, Employee Stock Purchase Program (ESPP), Employee Assistance Program (EAP), company paid holidays, paid sick leave and vacation time. The company follows all applicable laws for Paid Family Leave and other leaves of absence.

Broadcom is proud to be an equal opportunity employer. We will consider qualified applicants without regard to race, color, creed, religion, sex, sexual orientation, gender identity, national origin, citizenship, disability status, medical condition, pregnancy, protected veteran status or any other characteristic protected by federal, state, or local law. We will also consider qualified applicants with arrest and conviction records consistent with local law.

If you are located outside USA, please be sure to fill out a home address as this will be used for future correspondence.

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IC Layout Design Engineer

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 Full time

 Posted 30+ Days Ago

IC CAD Engineer

 3 Locations

 Full time

 Posted 30+ Days Ago

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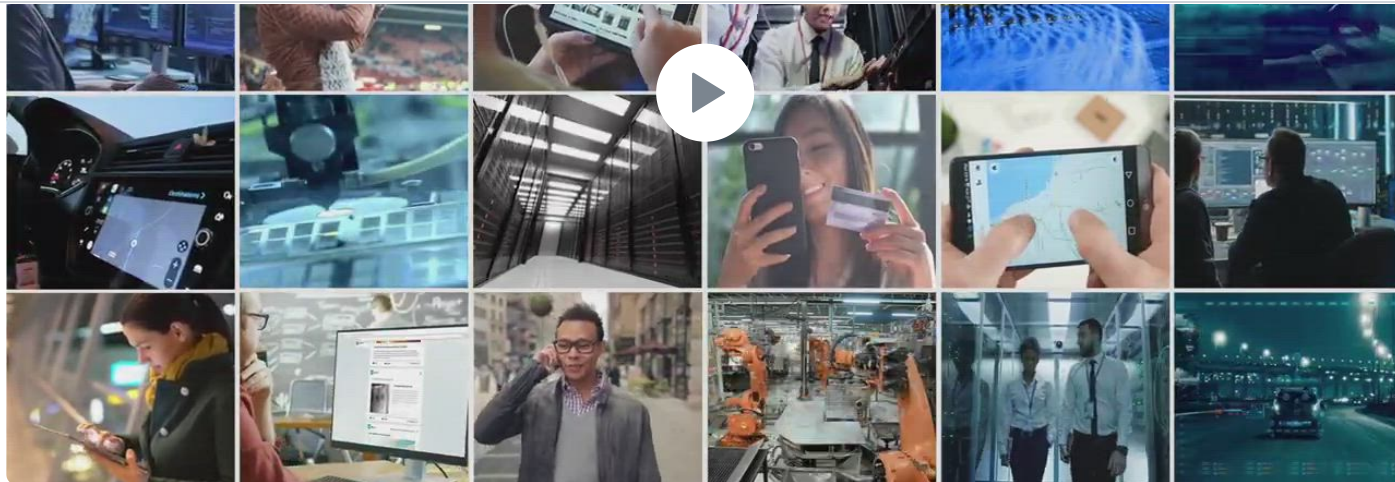
Welcome! Thank you for your interest in Broadcom!

We are a global technology leader that designs, develops and supplies a broad range of semiconductor and infrastructure software solutions.

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