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## IP VALIDATION DESIGN ENGINEER

AMD Austin, TX (On-site) 1 week ago 33 applicants



\$103,520/yr - \$155,280/yr · Full-time · Mid-Senior level



10,001+ employees · Semiconductor Manufacturing



31 connections · 71 company alumni · 11 school alumni



Skills: Application-Specific Integrated Circuits (ASIC), Silicon Validation, +8 more

Easy Apply

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### Meet the hiring team



**Andrea Jenkins** 2nd  
Sr. Talent Consultant at AMD  
Job poster  
17 mutual connections

Message

### About the job

#### WHAT YOU DO AT AMD CHANGES EVERYTHING

We care deeply about transforming lives with AMD technology to enrich our industry, our communities, and the world. Our mission is to build great products that accelerate next-generation computing experiences – the building blocks for the data center, artificial intelligence, PCs, gaming and embedded. Underpinning our mission is the AMD culture. We push the limits of innovation to solve the world's most important challenges. We strive for execution excellence while being direct, humble, collaborative, and inclusive of diverse perspectives. This is who we are at our best. One Company. One Team.

AMD together we advance\_

#### IP VALIDATION DESIGN ENGINEER

##### THE ROLE

The AMD NBIO Team is on the lookout for a dynamic, upbeat IP Validation Design Engineer to join our growing team. As a key contributor to the success of AMD's IP, you will be part of a leading team to drive and improve AMD's abilities to deliver the highest quality, industry leading technologies to market. The NBIO Team fosters and encourages continuous technical innovation to showcase successes as well as facilitate continuous career development.

##### THE PERSON

The ideal candidate has excellent written and verbal communication skills. You excel in a dynamic team environment, but have the ability to work autonomously, driving tasks to completion. Leadership and mentoring skills are a definite asset to this role.

##### Key Responsibilities

As an IP Validation Design Engineer, you will drive the planning, verification, and debug of various hardware IP for forthcoming AMD APU, CPU, Compute and Discrete Graphics SOC programs.

##### Responsibilities Include

- Defining, documenting, executing and reporting the overall PCIe functional test plan and validation strategy for a set of AMD IP's.
- Driving technical innovation to enhance AMD's capabilities in IP validation, including tool and script development, technical and procedural methodology enhancement, and various internal and cross-functional technical initiatives.
- Debugging IP issues found during pre-silicon, bring-up, system validation, and production phases of the SOC programs.

- Engaging on pre-silicon ‘shift left’ activities with cross-functional teams including but not limited to Design Verification (DV), Diagnostics, FPGA-based Emulation and other software/hardware modeling frameworks to ensure readiness for first silicon arrival, enablement of IP functionality, and debug of critical features.
- Leading collaborative technical discussions to drive resolution of technical issues and roll out technical initiatives.
- Developing knowledge of system architecture/debug and other internal IP’s.
- Supporting issues on customer platforms as requested by customer support teams.

Preferred Experience

- Experience in digital logic design/verification/post-silicon validation.
- Extensive experience with ASIC debug techniques and methodologies.
- Extensive knowledge of physical and protocol levels of common high-speed interfaces an asset.
- Extensive experience with board/platform-level debug, including clock/power delivery, sequencing, analysis, and optimization.
- Strong programming/scripting skills (eg. C/C++, Ruby, Python).
- Extensive experience with common lab equipment, including protocol/logic analyzers, oscilloscopes, etc.
- In-depth knowledge of PC architectures/PCIe protocol is an asset.

ACADEMIC CREDENTIALS

- Bachelor’s or master’s degree majoring in EE, CS or related field.

LOCATION

Austin, TX

At AMD, your base pay is one part of your total rewards package. Your base pay will depend on where your skills, qualifications, experience, and location fit into the hiring range for the position. You may be eligible for incentives based upon your role such as either an annual bonus or sales incentive. Many AMD employees have the opportunity to own shares of AMD stock, as well as a discount when purchasing AMD stock if voluntarily participating in AMD’s Employee Stock Purchase Plan. You’ll also be eligible for competitive benefits described in more detail here.

*AMD does not accept unsolicited resumes from headhunters, recruitment agencies, or fee-based recruitment services. AMD and its subsidiaries are equal opportunity, inclusive employers and will consider all applicants without regard to age, ancestry, color, marital status, medical condition, mental or physical disability, national origin, race, religion, political and/or third-party affiliation, sex, pregnancy, sexual orientation, gender identity, military or veteran status, or any other characteristic protected by law. We encourage applications from all qualified candidates and will accommodate applicants’ needs under the respective laws throughout all stages of the recruitment and selection process.*

[See less ^](#)

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Design Engineer, Austin, TX

 Set alert

Employer-provided

Pay range in Austin, TX

Exact compensation may vary based on skills, experience, and location.

Base salary  
\$103,520/yr - \$155,280/yr

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See how you compare to other applicants

Top applicants

25%

You’re in the **top 25% of 33 applicants** based on your LinkedIn profile  
33 Applicants -  
3 Applicants in the past day

To maximize your chances of getting this job, be sure to update your profile with the most relevant skills

## Most common applicant skills

You have **7 out of the 10 most common skills** among all other applicants

- |                                       |                            |
|---------------------------------------|----------------------------|
| ✓ Python (Programming Language)       | ✓ C (Programming Language) |
| ✓ MATLAB                              | ✓ C++                      |
| ✓ Microsoft Office                    | ✓ Computer Architecture    |
| ✓ Very-Large-Scale Integration (VLSI) | ✓ Verilog                  |
| ✓ Cadence Virtuoso                    | ✓ SystemVerilog            |

## Applicant seniority level

16 Entry level applicants

2 Senior level applicants

## Applicant education level

**81%** have a Master's Degree (Similar to you)  
**13%** have a Bachelor's Degree  
**6%** have a Master of Business Administration

## Applicants are in these locations

**5-10 applicants** ✓

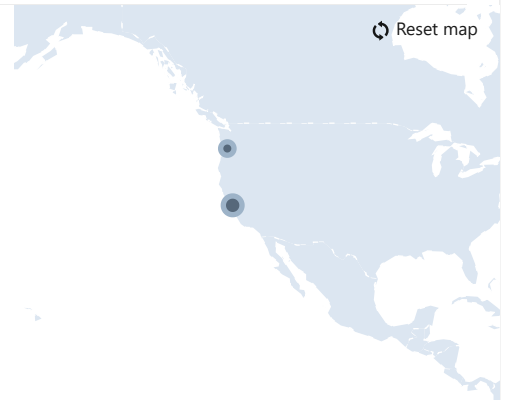
San Francisco Bay Area

**1-5 applicants**

Greater Bengaluru Area

**1-5 applicants**

Portland, Oregon Metropolitan Area



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## See if AMD is hiring people like you

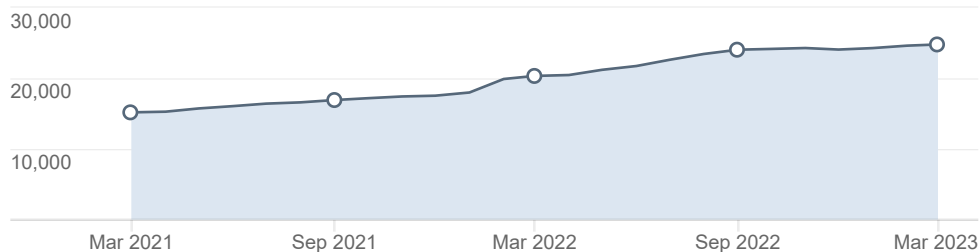
The latest hiring trends.

24,795

Total employees


▲ 63%  
Company-wide  
2 year growth


▲ 47%  
Engineering  
2 year growth



🕒 Median employee tenure · 2.3 years

AMD hires candidates from some of these companies and schools

 AMD hired 71 people from Western Digital. [See all](#)

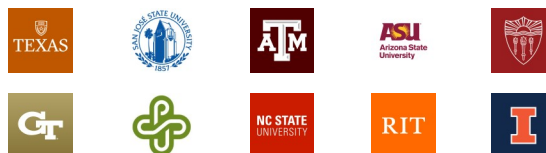
 AMD hired 11 people from Ira A. Fulton Schools of Engineering at Arizona State University. [See all](#)

Engineering hires at AMD came from these companies and more.



[See more companies](#)

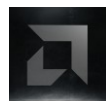
Engineering hires at AMD came from these schools and more.



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## About the company



**AMD**  
1,051,639 followers

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Semiconductors • 10,001+ employees • 24,795 on LinkedIn

For more than 50 years AMD (NASDAQ: AMD) has driven innovation in high-performance computing, graphics, and visualization technologies. AMD employees are focused on building leadership high-performance and adaptive products that push the boundaries of what is possible. Billions of people, leading Fortune 500 businesses, and cutting-edge scientific research institutions around the world rely on AMD technology daily to improve how they live, work, and play. [...show more](#)

[See more](#)

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### Mobile App Developer

Valeo  
Le Caire, Provence-Alpes-Côte d'Azur, France

🕒 Be an early applicant

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### Principal Software Engineer

Valeo  
Le Caire, Provence-Alpes-Côte d'Azur, France

🕒 Be an early applicant

New



### Manager Technology and R&D, Tissue (m/f/d)

ANDRITZ  
Graz, Styria, Austria

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New



### Finance Partner Controller

ADP  
Bucharest, Bucharest, Romania



1 connection

New



### Logistics and Customs Contractor

AMS International UAE  
Dubai, United Arab Emirates



### Major Accounts Inside Sales

ADP  
Coraopolis, PA





Insurance Services - Technical Support Specialist  
ADP





### Full Stack Developer AVP #222105

Credit Suisse  
Pune, Maharashtra, India

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New ·  Easy Apply

El Paso, TX

 1 connection

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Pre Sales Business Consultant -  
Workforce Management (Major...  
ADP  
Allentown, PA 1 connection

New

District Manager, Major Accounts  
Core  
ADP  
Burnaby, BC 7 company alumni

New

Pre Sales Business Consultant -  
Workforce Management (Major...  
ADP  
Allentown, PA


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Client Support Specialist  
ADP  
Tempe, AZ 1 connection


New


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