Introduce Yourself





Digital ASIC Design Engineer



- USA-CA Irvine Alton Parkway Bldg 2 USA-CA Irvine Alton Parkway Bldg 1
- Full time
- □ Posted 2 Days Ago
- R014542

Please Note:

- 1. If you are a first time user, please create your candidate login account before you apply for a job. (Click Sign In > Create Account)
- 2. If you already have a Candidate Account, please Sign-In before you apply.

Job Description:

- Develop architecture specifications to meet marketing and system requirements.
- Analyze design tradeoffs such as die size, power, leakage, schedule, resources, etc.
- Develop RTL (Verilog or VHDL), timing constraints, and detailed documentation.
- Perform Spyglass/Lint checks for CDC, DFT, etc.
- Support synthesis, STA, and logic equivalency checks. Implement ECOs.
- RTL & gate level simulations.
- Create verification & silicon validation plans. Silicon bring up, validation, and debugging.
- Scripting with TCL, Perl, and Unix shell scripts.
- Develop production ATE tests, support yield improvement and failure analysis.
- Report progress to the team leader on a regular basis.

Bachelor's and 2+ years of related experience required

Additional Job Description:

Compensation and Benefits

The annual base salary range for this position is \$63,000 - \$105,000.

This position is also eligible for a discretionary annual bonus in accordance with relevant plan documents, and equity in accordance with equity plan documents and equity award agreements.

Broadcom offers a competitive and comprehensive benefits package: Medical, dental and vision plans, 401(K) participation including company matching, Employee Stock Purchase Program (ESPP), Employee Assistance Program (EAP), company paid holidays, paid sick leave and vacation time. The company follows all applicable laws for Paid Family Leave and other leaves of absence.

Broadcom is proud to be an equal opportunity employer. We will consider qualified applicants without regard to race, color, creed, religion, sex, sexual orientation, gender identity, national origin, citizenship, disability status, medical condition, pregnancy, protected veteran status or any other characteristic protected by federal, state, or local law. We will also consider qualified applicants with arrest and conviction records consistent with local law.

If you are located outside USA, please be sure to fill out a home address as this will be used for future correspondence.

Similar Jobs (5)

Senior ASIC Design Engineer with Ethernet Background

- USA-CA Irvine Alton Parkway Bldg 1
- Full time
- □ Posted 5 Days Ago

IC Design Verification Engineer

- ② 2 Locations
- Full time
- □ Posted 10 Days Ago

Memory Circuit Design Engineer

- O USA-CA Irvine Alton Parkway Bldg 1
- Full time

✓ View All 5 Jobs

Welcome! Thank you for your interest in Broadcom!

We are a global technology leader that designs, develops and supplies a broad range of semiconductor and infrastructure software solutions.



For more information please visit our video library and check out our Connected by Broadcom series.

Follow us on Linked In Broadcom Inc and Broadcom Software.

Follow Us

workday.

© 2023 Workday, Inc. All rights reserved.