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# **Product Development Engineer 2**

**Locations:** Austin, Texas **Categories:** Engineering

Req ID: 21205

Hiring Target Min: USD \$75,440.00/Yr. Hiring Target Max: USD \$113,160.00/Yr.



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# **Job Description**

#### WHAT YOU DO AT AMD CHANGES EVERYTHING

We care deeply about transforming lives with AMD technology to enrich our industry, our communities, and the world. Our mission is to build great products that accelerate next-generation computing experiences – the building blocks for the data center, artificial intelligence, PCs, gaming

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solve the world's most important challenges. We strive for execution excellence while being direct, humble, collaborative, and inclusive of diverse perspectives. This is who we are at our best. One Company. One Team.

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# **AMD TEST ENGINEER**

### THE ROLE:

Position includes test planning, test creation, characterization, data analysis, and silicon debug of structural tests in leading edge process technologies. Applicant should be familiar with digital logic, microcontrollers, test development and optimization, failure mechanisms and debug processes. Solid coding skills preferred.

### THE PERSON:

Test Engineering is a cost, quality, and time-to-market driven team responsible for enabling the availability of microprocessors to our customers. Pre-silicon responsibilities for the Test Engineer include understanding product features, developing characterization test plans, highlighting test coverage holes, and developing software and hardware infrastructure to execute those plans. This requires interaction with Design and Platform Engineering and allows for the opportunity to provide test enhancement feedback for future designs. Post-silicon responsibilities include microprocessor debug & analysis and the execution of characterization plans. This may include silicon performance issues, yield issues, manufacturing processes, and device marginalities. The Test Engineer will work closely with Design and Platform Engineering to root cause issues that result from the debug, characterization and margin analysis. The Test Engineer will also provide datasheet specifications and drive improvements in yield.

# Key Responsibilities:

- Test patterns & test flows development, debug, test and characterization
- Pre-Silicon Test planning & validation, Engagement with Design
- Post Silicon Bring up of Test patterns leading to optimization for mass production enablement
- Characterization and debug of new silicon designs and process technologies
- Optimization of test flows for increased quality and cost improvement
- Analysis of part failures leading to test improvement
- Analysis of characterization data across PVT

# **Preferred Experience:**

- Strong communication skills including data presentation
- Java, Python, Perl, C/C++ coding skills
- ATE test experience Advantest V93K experience preferred.
- Experience with Mentor or Cadence or Synopsys related EDA tools a plus
- Unix and Windows proficiency
- · Modern computer architecture knowledge
- JTAG knowledge
- DFT design & methodology knowledge preferred

# **ACADEMIC CREDENTIALS:**

• BS/MS EE/CompE or equivalent

## LOCATION:

Austin, Texas

#LI-TW2

At AMD, your base pay is one part of your total rewards package. Your base pay will depend on where your skills, qualifications, experience, and location fit into the hiring range for the position. You may be eligible for incentives based upon your role such as either an annual bonus or sales incentive. Many AMD employees have the opportunity to own shares of AMD stock, as well as a discount when purchasing AMD stock if voluntarily participating in AMD's Employee Stock Purchase Plan. You'll also be eligible for competitive benefits described in more detail here (https://amd.jibeapply.com/benefits).

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