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Systems IP Performance Engineer

Samsung Austin Research and Development Center (SARC) and Advanced Computing Lab (ACL) San Jose, CA (Hybrid) 1 day ago **2 applicants**



\$166,024/yr - \$257,366/yr (from job description) · Full-time · Entry level



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Skills: C++, Performance Engineering, +8 more

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About the job

Position Summary

Samsung is a world leader in Memory, LCD and System LSI technologies that has the vision and commitment to invest in the future of technology – demonstrated by the \$17B investment in the new 3nm Fab in Texas and the commitment to invest in dramatically expanding design activities across GPU, System IP and SoC Architecture. We are currently looking for exceptional Performance Engineers to join our System IP team in our Austin, TX R & D Center (SARC) and our Advanced Computing Lab (ACL) in San Jose, CA. Our rapidly growing System IP team develops proprietary coherent interconnect and memory controller deployed in many high volume products. A wealth of growth opportunities for well qualified candidates.

Role and Responsibilities

As a Performance Engineer, you will work as part of a System IP team working on significant deliverables for the team. This is a mid to senior level position where the candidate will be in an individual contributor role, tasked with performance verification of mobile memory subsystem designs, correlation of RTL with performance model and performance modeling in C++ of memory subsystem, including interconnect, last-level cache, coherency, and memory controller.

Key responsibilities include:

- Understand and create infrastructure to run performance verification tests memory-subsystem RTL.
- Analyze performance verification test results, interact with RTL team, generate waveforms, and propose environment as well as configuration changes.
- Understand and create infrastructure to run correlation patterns on RTL and performance model.
- Analyze correlation results in both RTL and model, interact with RTL and modeling teams to drive fixes into RTL and model.
- Write performance models for the memory subsystem, including correlation fixes and new functionality.

Minimum requirements:

- BSEE, Computer Engineer or comparable and 5 + years of experience
- Experienced professional with understanding of the modeling and design process.
- Works on problems of diverse scope where analyzing data requires evaluating identifiable factors.
- Experience with industry standard RTL design tools and methodologies.

- Solid experience in C++, ideally related to performance modeling

Skills and Qualifications

Preferred candidate will possess the following:

- Directly related performance verification of mobile memory subsystem design experience
- Directly related experience with correlation of RTL with performance model
- Directly related performance modeling in C++ of memory subsystem, including interconnect, last-level cache, coherency, and memory controller.

Compensation for this role will vary among specific regions due to geographic differentials in the labor market, actual pay will be determined considering factors such as relevant skills and experience, and comparisons to other employees in the role. However, compensation in the following regions is expected to be as follows:

Colorado: Compensation is expected to be between \$144,345 to \$223,735

NYC: Compensation is expected to be between \$144,345 to \$223,735

Washington State: Compensation is expected to be between \$166,024 to \$257,366

California: Compensation is expected to be between \$166,024 to \$257,366

Regular full-time employees (salaried or hourly) have access to benefits including: medical, dental, vision, life insurance, 401(k), free onsite lunch, employee purchase program, tuition assistance (after 6 months), paid time off, student loan program, wellness incentives, and many more. In addition, regular full-time employees (salaried or hourly) are eligible for MBO bonus compensation, based on company, division, and individual performance.

#SARC

#ACL

*This position requires the ability to access information subject to U.S. export control restrictions. Applicants must have the ability to access export controlled information or be eligible to receive a government authorization to access export-controlled information

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* Samsung Electronics America, Inc. and its subsidiaries are committed to employing a diverse workforce, and provide Equal Employment Opportunity for all individuals regardless of race, color, religion, gender, age, national origin, marital status, sexual orientation, gender identity, status as a protected veteran, genetic information, status as a qualified individual with a disability, or any other characteristic protected by law.

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Base salary

\$166,024/yr - \$257,366/yr (from job description)

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- 401(k)
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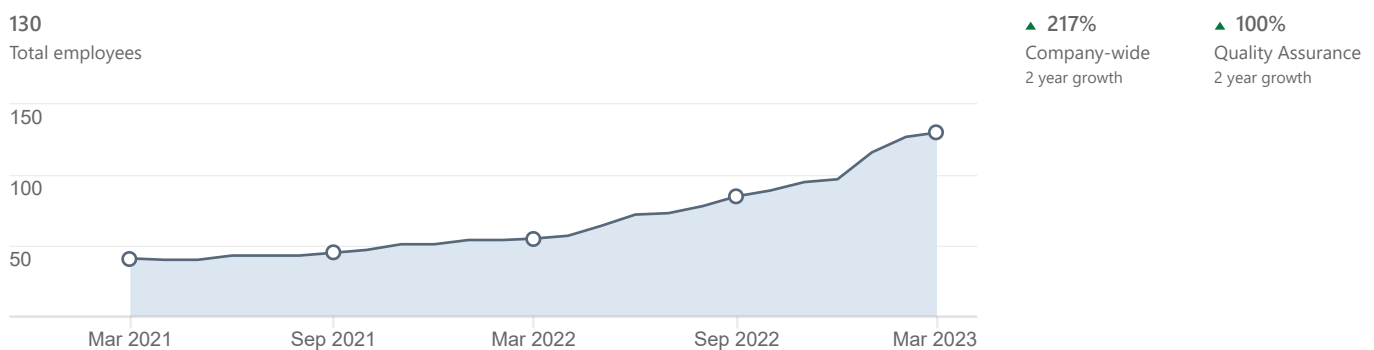


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About the company



Samsung Austin Research and Development Center (SARC) and Advanced Computing Lab (ACL)

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We exist to create human-driven innovations that defy barriers to make a better world for all.

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