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MTS Silicon Design Engineer

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AMD Irvine, CA (On-site) 1 week ago **11 applicants**



\$125,440/yr - \$188,160/yr · Full-time · Mid-Senior level



10,001+ employees · Semiconductor Manufacturing



31 connections · 71 company alumni · 11 school alumni



Skills: Power Optimization, Communication, +8 more

Job activity

Application submitted

1 second ago

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PREMIUM

Meet the hiring team



Patrick Harrington 2nd
Talent Acquisition Partner at AMD
Job poster
31 mutual connections

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About the job

WHAT YOU DO AT AMD CHANGES EVERYTHING

We care deeply about transforming lives with AMD technology to enrich our industry, our communities, and the world. Our mission is to build great products that accelerate next-generation computing experiences – the building blocks for the data center, artificial intelligence, PCs, gaming and embedded. Underpinning our mission is the AMD culture. We push the limits of innovation to solve the world's most important challenges. We strive for execution excellence while being direct, humble, collaborative, and inclusive of diverse perspectives. This is who we are at our best. One Company. One Team.

AMD together we advance_

MTS - Power Architect/Modeling Engineer

The Role

The focus of this role is to plan, build, and execute the verification of new and existing features for AMD's graphics processor IP, resulting in no bugs in the final design.

THE PERSON:

You have a passion for modern, complex processor architecture, digital design, and verification in general. You are a team player who has excellent communication skills

and experience collaborating with other engineers located in different sites/time zones. You have strong analytical and problem-solving skills and are willing to learn and ready to take on problems.

KEY RESPONSIBILITIES:

- Responsible for 3 main areas, 1. Power modeling, 2. Power optimization and 3. Power correlation of cutting edge IPs/Subsystem/SoCs across AMD family of products.
- Come up with bottoms-up estimate and top-down target for new IP/SoC
- Run power tools like PowerArtist and Primetime PX (PTPX) on various IPs/SoC.
- Review power reports and identify the outliers to debug the root cause of high power consumption blocks
- Recommend fixes in RTL and Physical design to reduce power.
- Provide feedback to IP teams to drive power optimization efforts throughout the project.
- Should be a team player, eagerness to learn new technology in low power space.
- Wherever possible come up with a new/innovative flow to simplify existing data (area vs signal-activity vs dynamic power etc..) to improve power projections

PREFERRED EXPERIENCE:

- Familiarity with low power/power optimization concepts in front-end and back-end design
- Experience with Architecture or RTL Design preferred
- Familiarity with Verdi, PowerArtist tool and PrimetimePX(PTPX) tool.

ACADEMIC CREDENTIALS:

- Bachelors or Masters degree in computer engineering/Electrical Engineering

At AMD, your base pay is one part of your total rewards package. Your base pay will depend on where your skills, qualifications, experience, and location fit into the hiring range for the position. You may be eligible for incentives based upon your role such as either an annual bonus or sales incentive. Many AMD employees have the opportunity to own shares of AMD stock, as well as a discount when purchasing AMD stock if voluntarily participating in AMD's Employee Stock Purchase Plan. You'll also be eligible for competitive benefits described in more detail here.

AMD does not accept unsolicited resumes from headhunters, recruitment agencies, or fee-based recruitment services. AMD and its subsidiaries are equal opportunity, inclusive employers and will consider all applicants without regard to age, ancestry, color, marital status, medical condition, mental or physical disability, national origin, race, religion, political and/or third-party affiliation, sex, pregnancy, sexual orientation, gender identity, military or veteran status, or any other characteristic protected by law. We encourage applications from all qualified candidates and will accommodate applicants' needs under the respective laws throughout all stages of the recruitment and selection process.

[See less ^](#)

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Member of Technical Staff, Irvine, CA

 **Set alert**

Employer-provided

Pay range in Irvine, CA

Exact compensation may vary based on skills, experience, and location.

Base salary
\$125,440/yr - \$188,160/yr

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See how you compare to other applicants

Top applicants

10%

You're in the **top 10% of 11 applicants** based on your LinkedIn profile

11 Applicants -

0 Applicants in the past day

To maximize your chances of getting this job, be sure to update your profile with the most relevant skills

Most common applicant skills

You have **7 out of the 10 most common skills** among all other applicants

- | | |
|---|---|
| ✓ C (Programming Language) | ✓ C++ |
| ✓ Verilog | ✓ MATLAB |
| ✓ Application-Specific Integrated Circuits (ASIC) | ✓ Very-Large-Scale Integration (VLSI) |
| ✓ Microsoft Office | ✓ Cadence Virtuoso |
| ✓ VHDL | ✓ Field-Programmable Gate Arrays (FPGA) |

Applicant seniority level

5 Entry level applicants

2 Senior level applicants

Applicant education level

- 82%** have a Master's Degree (Similar to you)
- 9%** have a Doctor of Philosophy
- 9%** have a Bachelor's Degree

Applicants are in these locations

1-5 applicants ✓

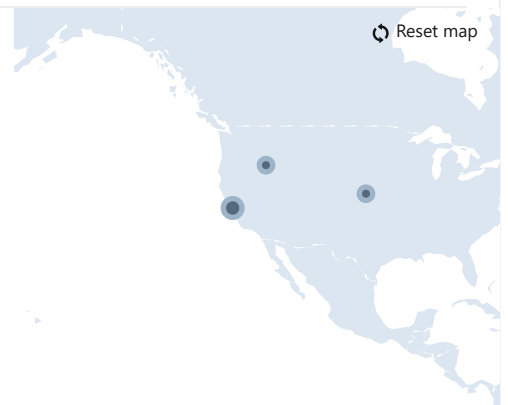
San Francisco Bay Area

1-5 applicants

United States

1-5 applicants

Boise Metropolitan Area



PREMIUM

?

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The latest hiring trends.

24,795

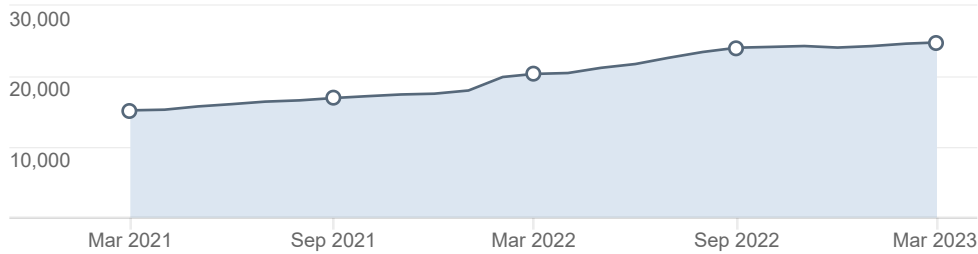
Total employees

▲ **63%**

Company-wide
2 year growth


▲ **47%**


Engineering
2 year growth



🕒 Median employee tenure · 2.3 years

AMD hires candidates from some of these companies and schools

 AMD hired 71 people from Western Digital. [See all](#)

 AMD hired 11 people from Ira A. Fulton Schools of Engineering at Arizona State University. [See all](#)

Engineering hires at AMD came from these companies and more.



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Engineering hires at AMD came from these schools and more.



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About the company



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1,051,967 followers

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For more than 50 years AMD (NASDAQ: AMD) has driven innovation in high-performance computing, graphics, and visualization technologies. AMD employees are focused on building leadership high-performance and adaptive products that push the boundaries of what is possible. Billions of people, leading Fortune 500 businesses, and cutting-edge scientific research institutions around the world rely on AMD technology daily to improve how they live, work, and play. [...show more](#)

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Presbyterian Homes & Services



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Maintenance Engineer - Australia



mehrere
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Plymouth, MN

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Friedland, Lower Saxony, Germany

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New

UGL
Milan, Lombardy, Italy

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Senior Graphic Designer (7+ Hours per week in exchange for...
SaltyLama
United States

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Presbyterian Homes & Services
Spring Park, MN

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