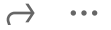# SoC Design Engineer

Intel Corporation   Austin, TX (On-site)   1 week ago   71 applicants

💼 Full-time · Entry level

🏢 10,001+ employees · Semiconductor Manufacturing

👥 118 connections · 396 company alumni · 409 school alumni

✅ Skills: Very-Large-Scale Integration (VLSI), System on a Chip (SoC), +7 more

Job activity

Go to company site ⧉

Applied on company site

38 minutes ago

About the job

Job Description

In this role responsibilities will be diverse and related to VLSI CMOS circuit design. You will own potentially multiple blocks and all related work dealing with circuit, layout, extraction, timing/noise/power analysis and as experienced engineer, provide some leadership responsibilities. Work closely with leads of other domains such as Architecture/RTL, PNR, Validation, DA, Full Chip Layout, Power, DFT, process, would be required. Work with the team and other domains to identify and implement changes to streamline the design flow for efficiency, help debug issues, mentor junior engineers and help train new hires. Working with inhouse and external industry standard tools for circuit simulation, static timing, noise, power, area, reliability would be part of the tasks along with scripting for optimizing flows, design automation.

In addition to the qualifications listed below, the ideal candidate will also have>

excellent spoken and written communication skills and very strong analytical skills.

Qualifications

You must possess the below minimum qualifications to be considered for this position. Preferred qualifications are in addition to the minimum requirements and are considered a plus factor in identifying top candidates. Experience listed below would be obtained through a combination of your schoolwork/classes/research and/or relevant previous job and/or internship experiences.

Minimum Qualifications

The candidate must have a Bachelor's degree in Electrical/Computer Engineering or Computer Science and 6+ years of experience OR a Master's degree in Electrical/Computer Engineering or Computer Science and 4+ years of experience OR a PhD degree in Electrical/Computer Engineering or Computer Science and experience in:

Taping out of chips, and a solid background in VLSI design, Logic Design, Microprocessor Architecture

Circuit Design, Logic Design, programming, static timing analysis, low power design techniques, noise mitigation techniques, layout, etc. through either academic or industry work is required

For more experienced candidates that want a leadership position then you will have at least 1+ years' experience with technical leadership and mentorship of junior teams and working in international cross site teams, etc.

Familiar with Perl or Python or Tcl,etc.

Inside this Business Group

In the Design Engineering Group (DEG), we take pride in developing the best-in-class SOCs, Cores, and IPs that power Intel's products. From development, to integration, validation, and manufacturing readiness, our mission is to deliver leadership products through the pursuit of Moore's Law and groundbreaking innovations. DEG is Intel's engineering group, supplying silicon to business units as well as other engineering teams. As a critical provider of all Intel products, DEG leadership has a responsibility to ensure the delivery of these products in a cost efficient and effective manner.

Covid Statement

Intel strongly encourages employees to be vaccinated against COVID-19. Intel aligns to federal, state, and local laws and as a contractor to the U.S. Government is subject to government mandates that may be issued. Intel policies for COVID-19 including guidance about testing and vaccination are subject to change over time.

Posting Statement

All qualified applicants will receive consideration for employment without regard to race, color, religion, religious creed, sex, national origin, ancestry, age, physical or mental disability, medical condition, genetic information, military and veteran status, marital status, pregnancy, gender, gender expression, gender identity, sexual orientation, or any other characteristic protected by local law, regulation, or ordinance.

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We offer a total compensation package that ranks among the best in the industry. It consists of competitive pay, stock, bonuses, as well as, benefit programs which include health, retirement, and vacation. Find more information about all of our Amazing Benefits here: https://www.intel.com/content/www/us/en/jobs/benefits.html

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System-on-Chip Design Engineer, Austin, TX

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Applicants for this job

71 Applicants

0 Applicants in the past day

To maximize your chances of getting this job, be sure to update your profile with the most relevant skills

Most common applicant skills

You have **6 out of the 10 most common skills** among all other applicants

- ✅ C (Programming Language)
- ✅ Python (Programming Language)
- ✅ MATLAB
- ✅ Very-Large-Scale Integration (VLSI)
- ✅ C++
- ✅ Microsoft Office
- ☑ Verilog
- ☑ Cadence Virtuoso
- ☑ Application-Specific Integrated Circuits (ASIC)
- ☑ SystemVerilog

Applicant seniority level

27 Entry level applicants

10 Senior level applicants

1 Manager level applicant

Applicant education level

77% have a Master's Degree (Similar to you)

17% have a Doctor of Philosophy

4% have a Bachelor's Degree

2% have other degrees

Applicants are in these locations

10-15 applicants ✓
Greater Phoenix Area

↻ Reset map

5-10 applicants
Austin, Texas Metropolitan Area

1-5 applicants
San Francisco Bay Area

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The latest hiring trends.

127,832
Total employees
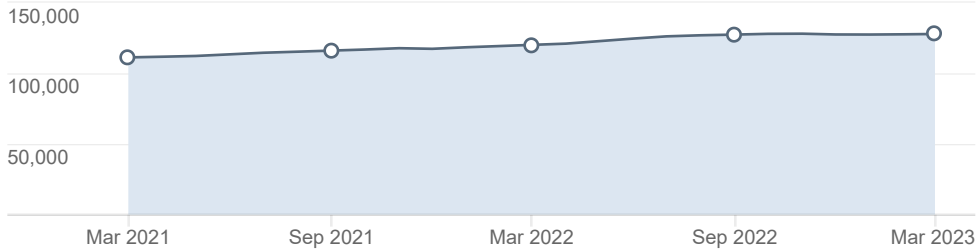
▲ **15%**
Company-wide
2 year growth

▲ **9%**
Engineering
2 year growth

150,000				
100,000				
50,000				
Mar 2021	Sep 2021	Mar 2022	Sep 2022	Mar 2023

🕐 Median employee tenure · **5.9 years**

Intel Corporation hires candidates from some of these companies and schools

Intel Corporation hired 396 people from Western Digital. **See all**

Intel Corporation hired 409 people from Ira A. Fulton Schools of Engineering at Arizona State University. **See all**

Engineering hires at Intel Corporation came from these companies and more.

Engineering hires at Intel Corporation came from these schools and more.

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About the company

Intel Corporation
3,237,566 followers

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Company photos

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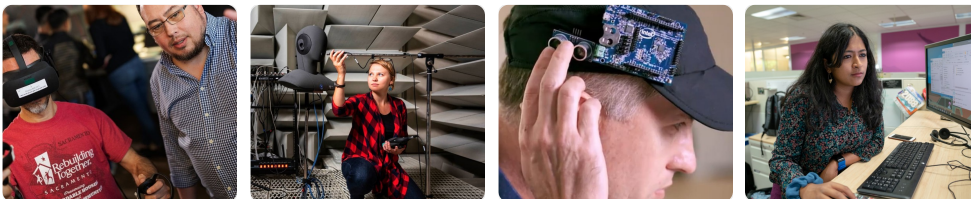
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Edwards Lifesciences
Limerick, County Limerick, Ireland
New

Accounts Payable Specialist
C.H. Robinson
Wrocław, Dolnośląskie, Poland
New

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