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Senior Silicon Validation Engineer - SOC

AMD San Jose, CA (On-site) 1 week ago 33 applicants



\$112,720/yr - \$169,080/yr · Full-time · Mid-Senior level



10,001+ employees · Semiconductor Manufacturing



31 connections · 71 company alumni · 11 school alumni



Skills: Microblaze, Assembly Language, +8 more

Easy Apply

Saved

Meet the hiring team



Eric Miles 1st

Talent Acquisition Recruiter at AMD

Job poster

252 mutual connections

Message

About the job

WHAT YOU DO AT AMD CHANGES EVERYTHING

We care deeply about transforming lives with AMD technology to enrich our industry, our communities, and the world. Our mission is to build great products that accelerate next-generation computing experiences – the building blocks for the data center, artificial intelligence, PCs, gaming and embedded. Underpinning our mission is the AMD culture. We push the limits of innovation to solve the world's most important challenges. We strive for execution excellence while being direct, humble, collaborative, and inclusive of diverse perspectives. This is who we are at our best. One Company. One Team.

AMD together we advance_

THE ROLE:

The System Software, Integration & Validation group plays a key role in the development of test systems and hardware for test & characterization of our advanced device families. Our most recent project has been for the ground-breaking Versal Adaptable Computing Acceleration Platform (ACAP), with quad core ARM processing system and programmable logic.

THE PERSON:

The ideal candidate possesses a problem-solving mindset with a drive to learn about both hardware and software technologies. A successful candidate will employ these skills to develop driver-level C code, debug failures across software, hardware, and solutions, and drive these issues to resolution by working with other teams.

KEY RESPONSIBILITIES:

We are looking for a Sr. Systems Design Engineer to join the System-level Test team in San Jose. You will be involved in the silicon life cycle from testing of emulated next-generation RTL through to silicon validation. You will have a view of the full system but also will become an expert in certain IP down to the register-level. We are looking for someone who can span the range of able to develop device drivers in software to debug on hardware.

You will be working on the future versions of Artificial Intelligence (AI) Engine system level validation.

Essential Duties

- Driving technical innovation to improve AMD's capabilities across validation, including tool and script development, technical and procedural methodology enhancement, and various internal and cross-functional technical initiatives
- Debugging issues found during the process, bring-up, validation, and production phases of SOC programs
- Working with multiple teams, and tracking test execution to make sure all features are validated and optimized on time

- Working closely with supporting technical teams to drive system-level bugs to resolution.
- Engaging in other software/hardware modeling frameworks
- Design, develop, implement, test, and verify systems based on AMD/Xilinx FPGAs with embedded MicroBlaze or ARM processors.
- Provide feedback on architecture, use cases, IP design, tools, and documentation.

PREFERRED EXPERIENCE:

- - Programming/scripting skills (e.g. C/C++, Perl, Python)
 - Debug techniques and methodologies
 - Experience with common lab equipment, including protocol/logic analyzers, oscilloscopes, etc.
 - Experience with board/platform-level debug, including delivery, sequencing, analysis, and optimization
 - Knowledge of system architecture, technical debug, and validation strategy
 - Experience understanding system use cases, determining requirements, and writing test specifications
 - Experience with embedded systems and ARM or MicroBlaze architecture and assembly language
 - A solid understanding of Verification / Validation Methodologies
 - Experience understanding system use cases, determining requirements, and writing test specifications
 - Experience with modules (UART, SPI, I2C, CAN, Ethernet, SD, GPU, DMA, PCIe etc.) validation
 - Emulation/FPGA platform experience is a plus
 - Experience creating designs in Vivado tool is a plus
 - Machine Learning experience is a plus

ACADEMIC CREDENTIALS:

- Bachelors or Masters degree in electrical or computer engineering

At AMD, your base pay is one part of your total rewards package. Your base pay will depend on where your skills, qualifications, experience, and location fit into the hiring range for the position. You may be eligible for incentives based upon your role such as either an annual bonus or sales incentive. Many AMD employees have the opportunity to own shares of AMD stock, as well as a discount when purchasing AMD stock if voluntarily participating in AMD's Employee Stock Purchase Plan. You'll also be eligible for competitive benefits described in more detail [here](#).

AMD does not accept unsolicited resumes from headhunters, recruitment agencies, or fee-based recruitment services. AMD and its subsidiaries are equal opportunity, inclusive employers and will consider all applicants without regard to age, ancestry, color, marital status, medical condition, mental or physical disability, national origin, race, religion, political and/or third-party affiliation, sex, pregnancy, sexual orientation, gender identity, military or veteran status, or any other characteristic protected by law. We encourage applications from all qualified candidates and will accommodate applicants' needs under the respective laws throughout all stages of the recruitment and selection process.

[See less ^](#)

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Senior Validation Engineer, San Jose, CA

 **Set alert**

Employer-provided

Pay range in San Jose, CA

Exact compensation may vary based on skills, experience, and location.

Base salary

\$112,720/yr - \$169,080/yr

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 **PREMIUM**



See how you compare to other applicants

Top applicants

50%

You're in the top 50% of 33 applicants based on your LinkedIn profile

33 Applicants -

0 Applicants in the past day

To maximize your chances of getting this job, be sure to update your profile with the most relevant skills

Most common applicant skills

You have 6 out of the 10 most common skills among all other applicants

- | | |
|----------------------------|---------------------------------|
| ✓ C (Programming Language) | ✓ Python (Programming Language) |
| ✓ C++ | ✓ Linux |
| ✓ MATLAB | ✓ Microsoft Office |
| ✓ Debugging | ✓ Verilog |
| ✓ Embedded Systems | ✓ Test Automation |

Applicant seniority level

20 Entry level applicants

8 Senior level applicants

1 Manager level applicant

Applicant education level

- 52% have a Master's Degree (Similar to you)
- 36% have a Bachelor's Degree
- 6% have a Doctor of Philosophy
- 6% have other degrees

Applicants are in these locations

5-10 applicants ✓

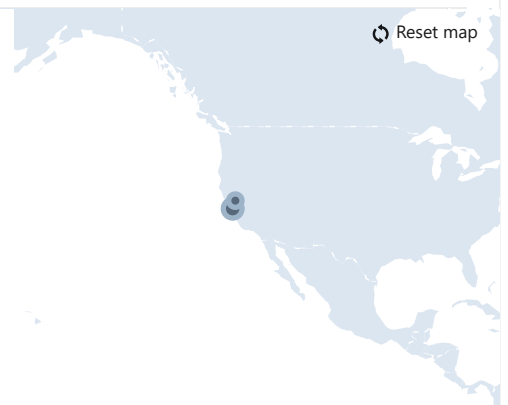
San Francisco Bay Area

1-5 applicants

Greater Bengaluru Area

1-5 applicants

Greater Sacramento



PREMIUM



See if AMD is hiring people like you

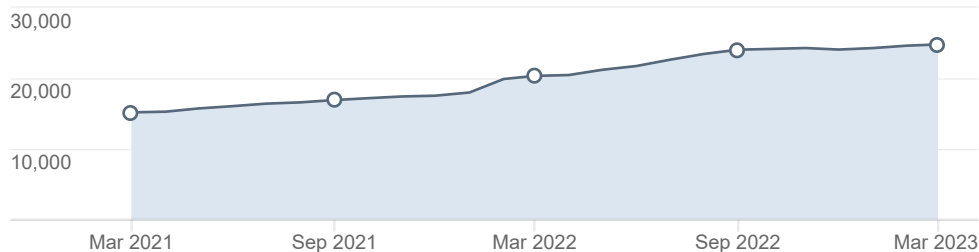
The latest hiring trends.

24,795

Total employees


▲ 63%
Company-wide
2 year growth


▲ 33%
Quality Assurance
2 year growth



🕒 Median employee tenure · 2.3 years

AMD hires candidates from some of these companies and schools

 AMD hired 71 people from Western Digital. [See all](#)

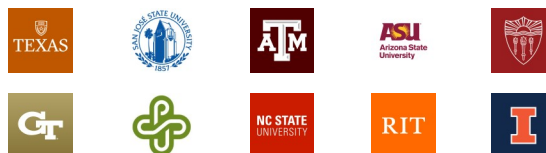
 AMD hired 11 people from Ira A. Fulton Schools of Engineering at Arizona State University. [See all](#)

Quality Assurance hires at AMD came from these companies and more.



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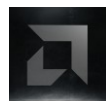
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About the company



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1,051,643 followers

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For more than 50 years AMD (NASDAQ: AMD) has driven innovation in high-performance computing, graphics, and visualization technologies. AMD employees are focused on building leadership high-performance and adaptive products that push the boundaries of what is possible. Billions of people, leading Fortune 500 businesses, and cutting-edge scientific research institutions around the world rely on AMD technology daily to improve how they live, work, and play. [...show more](#)

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Pocatello, ID

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New



Senior / Chief Cloud Platform Architect (w/m/x)
NTT DATA DACH
Berlin, Berlin, Germany

🕒 Be an early applicant

New



Lead Software Engineer - Full stack React/NodeJS
U.S. Bank
Richfield, MN

 7 company alumni

New



Junior Consultant Enterprise / IT Service Management (w/m/x)
NTT DATA DACH
Rosenheim, Bavaria, Germany

🕒 Be an early applicant

New



Critical Care Travel Nurse RN - \$2196 weekly
*Nomad Health



Junior Consultant Enterprise / IT Service Management (w/m/x)
NTT DATA DACH



Mid Corporate Relationship Manager
Elavon Europe



Senior Safety Case Engineer (Mechanical background) defence
Assystem

Bloomington, IN

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United Kingdom

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Glasgow, Scotland, United Kingdom

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ADP
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