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ASIC Design Efficiency Engineer

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 US, CA, Santa Clara
US, TX, Austin
US, NC, Durham

 Full time

 Posted 7 Days Ago

 JR1965588

We are now looking for an ASIC Design Efficiency Engineer!

NVIDIA is seeking extraordinary methodology engineers to design hardware accelerators and processors on our next-generation mobile, embedded and datacenter platforms. This position offers the opportunity to have real impact in a multifaceted, technology-focused company impacting product lines ranging from consumer graphics to self-driving cars and the growing field of artificial intelligence. Join our dynamic team today!

What you'll be doing:

- Develop innovative HW, GPU and system designs to extend the state of the art performance and efficiency.
- Understand the design and implementation, develop methodology and infrastructure to drive Performance, Power and Area (PPA) improvements.
- Execute and deliver fully verified, high performance, area and power efficient RTL to achieve design targets.
- Collaborate with architects, designers, verification and VLSI teams to craft the industries' top performing GPUs.

What we need to see:

- Bachelors Degree in EE or CE or equivalent experience.
- 2+ years of experience.
- Proficiency in SystemVerilog or similar HDL.

- You have a solid understanding of logic design and computer architecture.
- Strong interpersonal skills are required along with the ability to work in a diverse product oriented team.

NVIDIA is widely considered to be one of the technology world's most desirable employers. We have some of the most brilliant and talented people in the world working for us. Are you creative and autonomous? Do you love the challenge of constant innovation and creating the highest performance products in the industry? If so, we want to hear from you. Come, join our GPU ASIC team and help build the real-time, cost-effective computing platform driving our success across multiple fields such as Deep Learning and AI, Autonomous Driving, Gaming and High Performance Computing.

The base salary range is \$114,000 - \$194,000. Your base salary will be determined based on your location, experience, and the pay of employees in similar positions.

You will also be eligible for equity and [benefits](#).

NVIDIA is committed to fostering a diverse work environment and proud to be an equal opportunity employer. As we highly value diversity in our current and future employees, we do not discriminate (including in our hiring and promotion practices) on the basis of race, religion, color, national origin, gender, gender expression, sexual orientation, age, marital status, veteran status, disability status or any other characteristic protected by law.

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 US, CA, Santa Clara

 Full time

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 2 Locations

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Senior FPGA Prototyping Engineer - Hardware

 US, CA, Santa Clara

 Full time

 Posted 30+ Days Ago

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NVIDIA is a Learning Machine

NVIDIA pioneered accelerated computing to tackle challenges no one else can solve. Our work in AI and the metaverse is transforming the world's largest industries and profoundly impacting society.

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