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# Sr. Silicon Design Engineer

**Locations:** San Jose, California
**Categories:** Engineering
**Req ID:** 26166
**Hiring Target Min:** USD $114,400.00/Yr.
**Hiring Target Max:** USD $171,600.00/Yr.

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## Job Description

**WHAT YOU DO AT AMD CHANGES EVERYTHING**

We care deeply about transforming lives with AMD technology to enrich our industry, our communities, and the world. Our mission is to build great products that accelerate next-generation computing experiences – the building blocks for the data center, artificial intelligence, PCs, gaming and embedded. Underpinning our mission is the AMD culture. We push the limits of innovation to solve the world's most important challenges. We strive for execution excellence while being direct, humble, collaborative, and inclusive of diverse perspectives. This is who we are at our best. One Company. One Team.

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**THE ROLE:**

The focus of this role is to analyze, architect, specify, and collaborate to develop Fullchip floorplans, IPs and Features for AMD's next-gen Adaptive SoC products.

**THE PERSON:**

You have a passion for modern, complex SoC architecture with various IO peripherals and heterogenous processor systems and digital design & verification in general. You are a team player who has excellent communication skills and experience collaborating with other architects & engineers located in different sites/time-zones. You have strong analytical and problem-solving skills and are willing to learn and ready to take on problems.

**KEY RESPONSIBILITIES:**

- Participate in the development of novel SoC architecture solutions, and analyze implementation trade-offs
- Define architecture for next-gen Adaptive SoCs including fullchip floorplan, features, PPAC
- Technology lookahead, package integration technology, DTCO, PPAC
- Define architecture specifications in a clear and concise language
- Collaborate with marketing and planning, Test to specify requirements
- Work with architects, design/verification, and software for the IP & product development

**PREFERRED EXPERIENCE:**

- Strong foundation in SoC architecture, sub-20nm semiconductor technology, high speed digital circuit design
- Good analytical problem solving, and attention to details
- Working knowledge of Python, Perl
- Excellent written and verbal communication skills

**ACADEMIC CREDENTIALS:**

- BS or MS or PhD degree in Computer, Electrical or Equivalent Engineering

**LOCATION**

- San Jose, California OR Longmont, Colorado

#LI-JY1

At AMD, your base pay is one part of your total rewards package. Your base pay will depend on where your skills, qualifications, experience, and location fit into the hiring range for the position. You may be eligible for incentives based upon your role such as either an annual bonus or sales incentive. Many AMD employees have the opportunity to own shares of AMD stock, as well as a discount when purchasing AMD stock if voluntarily participating in AMD's Employee Stock Purchase Plan. You'll also be eligible for competitive benefits described in more detail here (https://amd.jibeapply.com/benefits).

*AMD does not accept unsolicited resumes from headhunters, recruitment agencies, or fee-based recruitment services. AMD and its subsidiaries are equal opportunity, inclusive employers and will consider all applicants without regard to age, ancestry, color, marital status, medical condition, mental or physical disability, national origin, race, religion, political and/or third-party affiliation, sex, pregnancy, sexual orientation, gender identity, military or veteran status, or any other characteristic protected by law. We encourage applications from all qualified candidates and will accommodate applicants' needs under the respective laws throughout all stages of the recruitment and selection process.*

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