

HW1

1.14

What is the purpose of interrupts? a
How does an interrupt differ from a trap? a
Can traps be generated intentionally by a user program? If so, for what purpose? a

1.20

Consider an SMP system similar to the one shown in Figure 1.8 (Links to an external site.). Illustrate with an example how data residing in memory could in fact have a different value in each of the local caches.

test				

2.12

What are the advantages and disadvantages of using the same system-call interface for manipulating both files and devices?

2.19

What is the main advantage of the microkernel approach to system design?
How do user programs and system services interact in a microkernel architecture?
What are the disadvantages of using the microkernel approach?

2.20

What are the advantages of using loadable kernel modules?

2.20

Choose one
System
from the
slides in
Lecture One
(Eniac,
System 360,
Multics,
Android,
etc..) and
describe in
detail (one
paragraph)
its
importance
to the
evolution of
Operating
Systems
Build and
compile
both a java
and C++
application
on a Linux
system (do
not turn in)
