target/release/cycle Total samples: 4561 \_start 0 (0.0%) of 4561 (100.0%) Focusing on: 4561 Dropped nodes with <= 22 abs(samples) Dropped edges with <= 4 samples 4561 \_\_libc\_start\_main 0 (0.0%) of 4561 (100.0%) 4561 std rt lang\_start h1ce7bbb7cafa1e10 0 (0.0%) of 4561 (100.0%) 4561 std panic catch\_unwind (inline) 0 (0.0%) of 4561 (100.0%) 4561 0 (0.0%) of 4561 (100.0%) 4561 anic\_unwind maybe\_catch\_panic 0 (0.0%) of 4561 (100.0%) 4561 cycle main hce52c30ed0a2ccfa 0 (0.0%) of 4561 (100.0%) 4561 804 . 3689 cycle bit\_vec { { impl } } cycle\_len core set range {{impl}} next (inline) 0 (0.0%) of 45 (1.0%) (inline) (inline) 768 (16.8%) 3689 (80.9%) of 804 (17.6%) 45 ₹ range {{impl}} add\_one core

ops arith {{impl}} add (inline) 45 (1.0%)