



LABORATÓRIO DE CIRCUITOS DIGITAIS

DESCREVENDO CIRCUITOS DIGITAIS COM VHDL: USO DE COMPONENT

TIAGO BARROS

UNIVERSIDADE FEDERAL DO RIO GRANDE DO NORTE – UFRN

CENTRO DE TECNOLOGIA – CT

DEPARTAMENTO DE ENGENHARIA DE COMPUTAÇÃO E AUTOMAÇÃO – DCA

COMPONENT

Circuito TOP

COMPONENT

Circuito TOP

Circuito A

Circuito B

Circuito C

COMPONENT

Circuito TOP

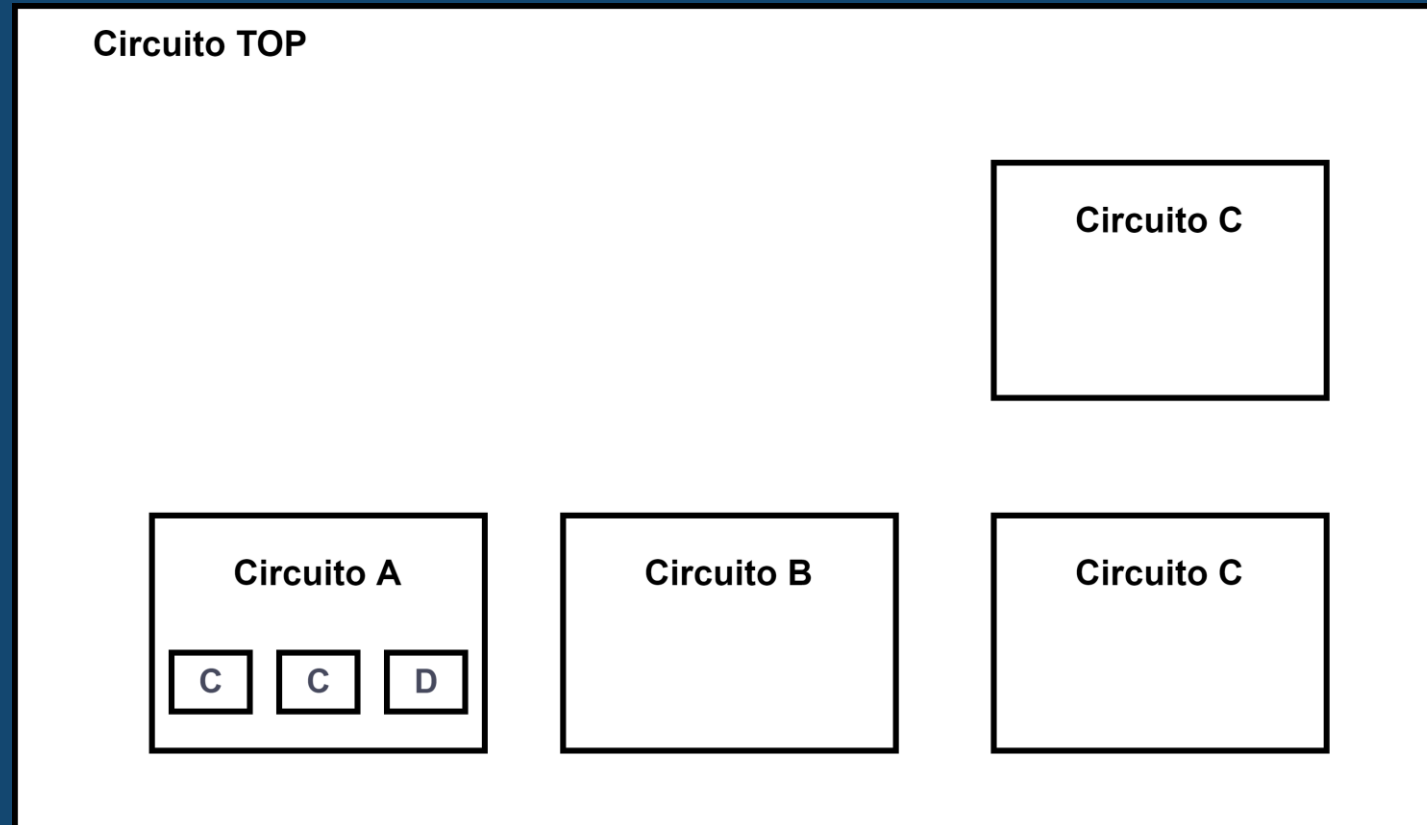
Circuito C

Circuito A

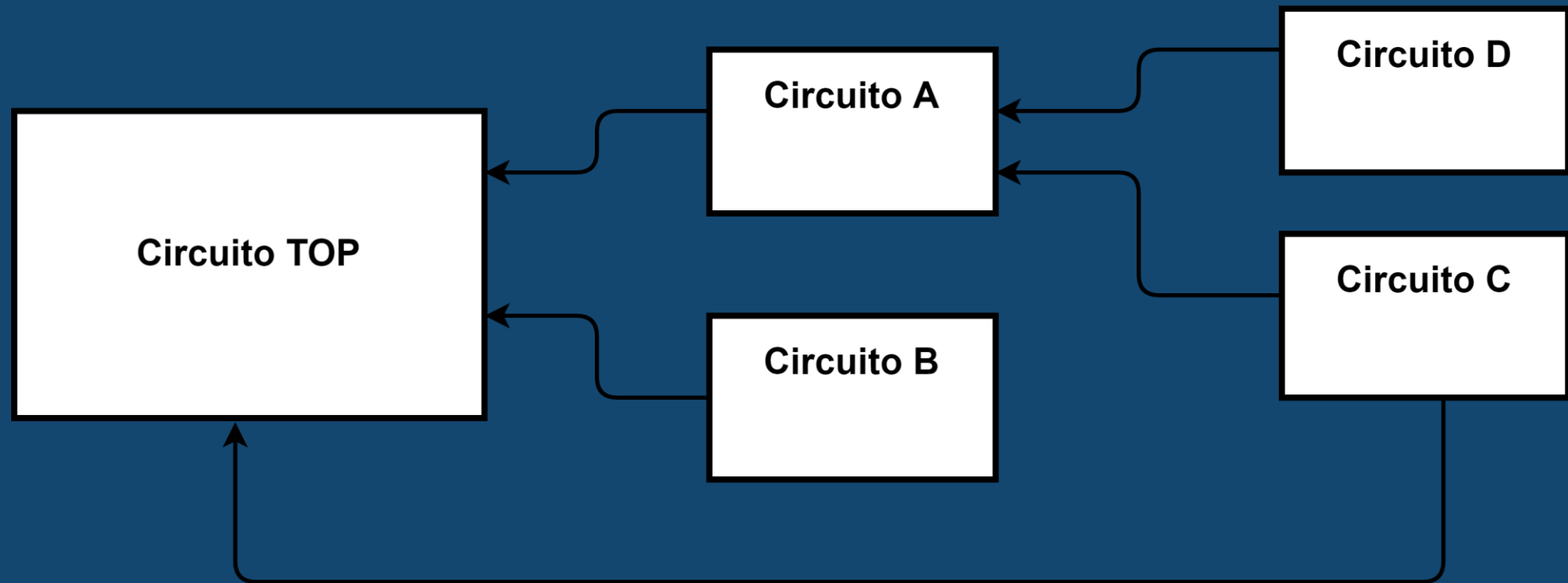
Circuito B

Circuito C

COMPONENT



COMPONENT



COMPONENT – DECLARAÇÃO

```
COMPONENT: component_name [IS]
  [GENERIC (
    const_name: const_type := const_value;
    const_name: const_type := const_value;
    . . .);]
  PORT (
    port_name: port_mode signal_type;
    port_name: port_mode signal_type;
    . . .);]
END_COMPONENT [component_name];
```

COMPONENT – INSTANCIAÇÃO

```
label: [COMPONENT]: component_name  
      [GENERIC MAP (generic_list)]  
      PORT MAP (port_list);
```