

mod0

dataInf

X x: int

eventsInf

⚡ e1: int

⚡ e2

ctrl0

R dataInf
i eventsInf

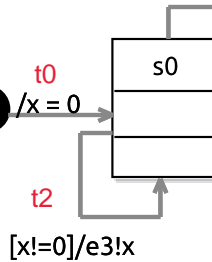
e1: int

async

e1: int

stm0

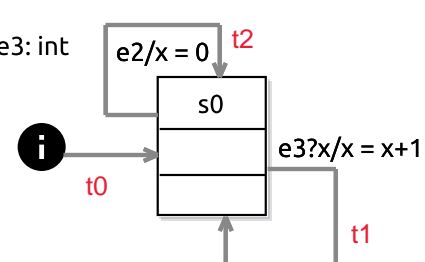
R dataInf
X l: int



e3: int

stm1

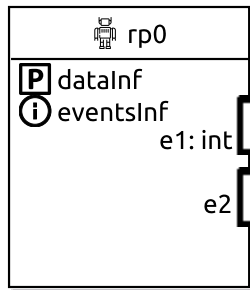
R dataInf



e3: int



e2



async

e2