



Visvesvaraya Technological University, Belagavi

Karnataka State, INDIA 95463

TRANSCRIPT AS PER RECORDS

We do not have GPA scheme of evaluation

Name : ISA AHMED YUSUF ABDULLA AL DOSERI

University Seat No : 1AY14EC031

Year of Entrance : 2014

Year of Leaving : 2018

Degree Received : Bachelor of Engineering

(Electronics & Communication Engineering)

1. Duration of the Course : 4 Years

2. Medium of Instruction : English

3. First class with distinction (FCD) : Not less than 70% of the aggregate marks in first attempt

4. First class (FC) : Less than 70% but not less than 60% of the aggregate marks in first attempt

5. Second class (SC) : Less than 60% of the aggregate marks in first attempt

SUBJECTS	Hours Per Week		Marks Obtained	Max. Marks
	Lecture	Drawing/ Practical		
I Semester				
1 Engineering Maths-I	4		86	125
2 Engineering Chemistry	4		91	125
3 Programming in C & Data Structures	4		82	125
4 Computer Aided Engineering Drawing	2	4	111	125
5 Basic Electronics	4		89	125
6 Computer Programming Lab		3	74	75
7 Engg. Chemistry Lab		3	70	75
8 Environmental Studies	2		63	75
First Attempt Total: 603 / 775 ; Class : FCD ; # 1				
II Semester				
1 Engineering Maths-II	4		111	125
2 Engineering Physics	4		85	125
3 Elements of Civil Engg. & Engg. Mechanics	4		87	125
4 Elements of Mechanical Engineering	4		81	125
5 Basic Electrical Engineering	4		93	125
6 Workshop Practice		3	65	75
7 Engineering Physics Lab		3	73	75
8 Constitution of India & Professional Ethics	2		53	75
First Attempt Total: 595 / 775 ; Class : FCD ; # 1				
III Semester				
1 Engineering Mathematics-III	4		117	125
2 Analog Electronic Circuits	4		68	125
3 Logic Design	4		60	125
4 Network Analysis	4		88	125
5 Electronic Instrumentation	4		79	125
6 Field Theory	4		92	125
7 Analog Electronics Lab		3	64	75
8 Logic Design Lab		3	69	75
First Attempt Total: 637 / 900 ; Class : FCD ; # 1				
IV Semester				
1 Engineering Mathematics - IV	4		112	125
2 Microcontrollers	4		68	125
3 Control Systems	4		75	125
4 Signals & Systems	4		62	125
5 Fundamentals of HDL	4		80	125
6 Linear ICs & Applications	4		77	125
7 Microcontrollers Lab		3	66	75
8 HDL Lab		3	75	75
First Attempt Total: 615 / 900 ; Class : FC ; # 1				

SUBJECTS		Hours Per Week		Marks Obtained	Max. Marks
		Lecture	Drawing/ Practical		
V Semester					
1 Management & Entrepreneurship	4		90	125	
2 Digital Signal Processing	4		101	125	
3 Analog Communication	4		81	125	
4 Microwaves & Radar	4		57	125	
5 Information Theory & Coding	4		95	125	
6 Fundamentals of CMOS VLSI	4		60	125	
7 DSP Lab		3	67	75	
8 Analog Communication Lab. + LIC Lab		3	67	75	
First Attempt Total		619 / 900	: Class:	FC	: # 1
VI Semester					
1 Digital Communication	4		66	125	
2 Microprocessors	4		73	125	
3 Microelectronics Circuits	4		68	125	
4 Antennas & Propagation	4		74	125	
5 Operating Systems	4		77	125	
6 Programming in C++	4		73	125	
7 Advanced Communication Lab.		3	71	75	
8 Microprocessor Lab.		3	72	75	
First Attempt Total		574 / 900	: Class:	FC	: # 1
VII Semester					
1 Computer Communication Networks	4		86	125	
2 Optical Fiber Communication	4		69	125	
3 Power Electronics	4		81	125	
4 Embedded System Design	4		75	125	
5 DSP Algorithms & Architecture	4		81	125	
6 Image Processing	4		106	125	
7 VLSI Lab.		3	69	75	
8 Power Electronics Lab.	3		60	75	
First Attempt Total		627 + 3 / 900	: Class:	FCD	: # 1
VIII Semester					
1 Wireless Communication	4		89	125	
2 Digital Switching Systems	4		96	125	
3 Network Security	4		78	125	
4 Multimedia Communication	4		93	125	
5 Project Work		6	191	200	
6 Seminar	3		43	50	
First Attempt Total		590 / 750	: Class:	FCD	: # 1
Grand total of V to VIII Semester		2413+2 out of 3450 (max.)			
Class of the Degree * : First Class with Distinction					

06/10/14CIP/06/10/14CIV18/28 is not considered for Grand Total and the Class Declaration

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*Based on First Attempt Marks of V to VIII Semesters # Number of Attempts taken to clear the semester
(* and # not applicable to Choice Based Credit System)

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Registrar (Evaluation)