

```

1 // SPDX-License-Identifier: GPL-2.0
2 /*
3  * Copyright Altera Corporation (C) 2015. All rights reserved.
4  */
5
6 #include "socfpga_cyclone5.dtsi"
7
8 / {
9     model = "Terasic DE-0 (Atlas)";
10    compatible = "terasic,de0-atlas", "altr,socfpga-cyclone5", "altr,socfpga";
11
12    chosen {
13        bootargs = "earlyprintk";
14        stdout-path = "serial0:115200n8";
15    };
16
17    memory@0 {
18        name = "memory";
19        device_type = "memory";
20        reg = <0x0 0x40000000>; /* 1GB */
21    };
22
23    aliases {
24        ethernet0 = &gmac1;
25    };
26
27    regulator_3_3v: 3-3-v-regulator {
28        compatible = "regulator-fixed";
29        regulator-name = "3.3V";
30        regulator-min-microvolt = <3300000>;
31        regulator-max-microvolt = <3300000>;
32    };
33
34    leds {
35        compatible = "gpio-leds";
36        hps0 {
37            label = "hps_led0";
38            gpios = <&portb 24 0>;
39            linux,default-trigger = "heartbeat";
40        };
41    };
42 };
43
44 &gmac1 {
45     status = "okay";
46     phy-mode = "rgmii";
47
48     txd0-skew-ps = <0>; /* -420ps */
49     txd1-skew-ps = <0>; /* -420ps */
50     txd2-skew-ps = <0>; /* -420ps */
51     txd3-skew-ps = <0>; /* -420ps */
52     rxd0-skew-ps = <420>; /* 0ps */
53     rxd1-skew-ps = <420>; /* 0ps */
54     rxd2-skew-ps = <420>; /* 0ps */
55     rxd3-skew-ps = <420>; /* 0ps */
56     txen-skew-ps = <0>; /* -420ps */
57     txc-skew-ps = <1860>; /* 960ps */
58     rxdv-skew-ps = <420>; /* 0ps */
59     rxc-skew-ps = <1680>; /* 780ps */
60
61     max-frame-size = <3800>;
62 };
63
64
65 &mmc0 {
66     vmmc-supply = <&regulator_3_3v>;
67     vqmmc-supply = <&regulator_3_3v>;
68     status = "okay";
69 };

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```
70
71  &uart0 {
72      status = "okay";
73  };
74
75  &usb1 {
76      status = "okay";
77  };
78
```