ISAMU ARTHUR POY

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EDUCATION

University of California, San Diego

M.S. Computer Engineering **University of Toronto**

B.A.Sc. Computer Engineering - CGPA: 3.5/4

La Jolla, California Sep. 2021 - present Toronto, Ontario Sep. 2017 - June 2021

TECHNICAL SKILLS

Languages: Python, C/C++, Java, JavaScript, Typescript, SQL, MATLAB, Verilog, System Verilog, Arm Assembly

Frameworks/Tools: ReactJS, Git, Linux, ROS, Vivado, Quartus, AWS Libraries: PyTorch, Tensorflow, Pandas, NumPy, Bootstrap, Jest

WORK EXPERIENCE

Software Development Engineering Intern

June 2022 - Sep. 2022

Amazon, Buyer Risk Prevention (BRP) San Diego, California

- Designed back-end infrastructure for BRP's new service to manage purchase receipts.
- Implemented design with AWS cloud services such as Lambda, Dynamo DB, EventBridge, SNS, and SQS.
- Conducted design reviews and held technical discussions with Amazon principal engineers.
- Wrote unit tests, demoed product to the team, and prepared code for production.

FPGA Research May 2020 - Sep. 2020 University of Toronto Toronto, Ontario

- Designed FPGA debugging hardware on AXI-Lite; funded by Alibaba, Xilinx, and Fidus Systems.
 - Work inspired by Microsoft Research to debug mapped streams on MPSoC FPGAs.
 - Paper published to FPGA '21, and serves as the foundation for FPGA debugging on data centers.
 - Successfully delivered technical presentation of work to over 150 research associates at FPGA '21.
 - Implemented design reduced FPGA controllability logic overhead by 45%.

Robotics Research June 2019 - Aug 2019

Hong Kong University of Science and Technology

Hong Kong

- · Developed interfaces for eye-gaze controlled robotic wheelchair in Python and Tensorflow. • Introduced practical application of novel appearance-based eye tracking algorithm.
- Paper published to EMBC '21, a leading biomedical engineering conference.
- Improved eye gaze navigation times by up to 26% with newly designed interface.

Machine Learning Research

Nagasaki University

May 2018 - Aug 2018

Nagasaki, Japan

- Utilized DeepLabV3 network to detect sick trees with images collected from Nagasaki City.
- Improved model to segment tree silhouette with accuracy of over 95%.
- Research submitted as paper to IEEE Geoscience and Remote Sensing Letters.

PROJECTS

Debug Governors | System Verilog, ModelSim, AXI-Lite, Git, FPGA

May 2020 - Sep. 2020

- Designed a memory mapped interface for the Debug Governor to serve as a debugger tool for FPGAs.
- Refactored existing code to follow a datapath and controlpath structure in System Verilog.
- Paper accepted to FPGA '21, the premier conference in FPGAs, as a publication.

Dog Breed Classifier | Python, PyTorch

May 2020 - Aug. 2020

- Developed a dog breed image classifier for a project on Convolutional Neural Networks.
- · Utilized transfer learning on existing models such as AlexNet and YOLO.
- Utilized the Stanford Dogs Data-set, and achieved testing accuracy of 90% on 11 different breeds.

Robotic Wheelchair | Pvthon. Tensorflow. ROS. Arduino

June 2019 - Aug. 2019

· Developed an eye-gaze control robotic wheelchair, and used Arduino board to create user interface.

• Tested algorithms on wheelchair robot in Shenzhen, China.

COMPETITIONS

The Data Open (2021)

- Competed in Citadel's invitational summer datathon in a team of 4 remotely.
- Utilized Pandas libraries to identify second-order impacts of Airbnb rental data for the U.S South region.

SPOKEN LANGUAGES

Spoken Languages: English, Japanese