In the appendix of this paper, we present the source data of our experiments as per the request from Reviewer-A.
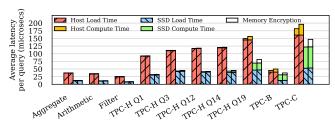

Fig. 19: Performance comparison of Host, Host+SGX, ISC, and IceClave (from left to right). We show the performance breakdown of each scheme. This figure refers to Figure 10 in the revised paper.
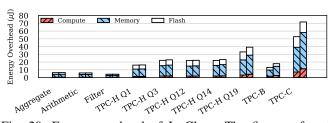

Fig. 20: Energy overhead of IceClave. The figure refers to Figure 11 in the revised paper.
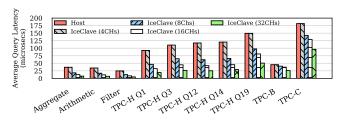

Fig. 21: IceClave performance (normalized to Host), as we vary the internal SSD bandwidth by using different number of channels. This figure refers to Figure 12 in the revised paper.
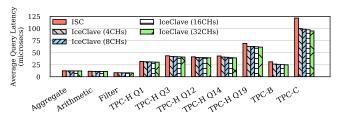

Fig. 22: IceClave performance (normalzied to ISC), as we vary the internal SSD bandwidth by using different number of channels. This figure refers to Figure 13 in the revised paper.
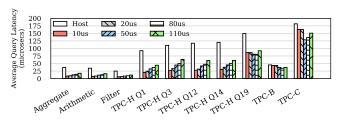

Fig. 23: Performance of IceClave as we vary the latency of accessing flash pages. This figure refers to Figure 14 in the revised paper.
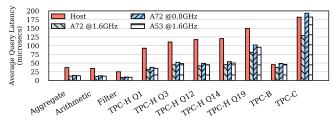

Fig. 24: IceClave performance as we vary the in-storage computing capability. This figure refers to Figure 15 in the revised paper.
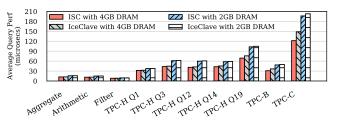

Fig. 25: IceClave performance with various SSD DRAM sizes. This figure refers to Figure 16 in the revised paper.
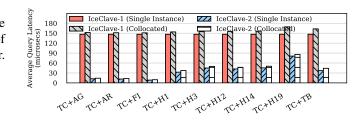

Fig. 26: IceClave performance as we run two in-storage applications concurrently. This figure refers to Figure 17 in the revised paper.
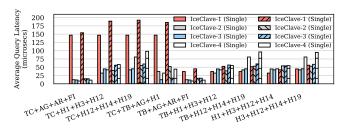

Fig. 27: IceClave performance as we run four in-storage applications concurrently. This figure refers to Figure 18 in the revised paper.