

12th IEEE International Symposium on Smart Electronic Systems

15-17 DEC 2026 @ Goa, INDIA

CALL FOR PAPERS

<http://www.ieee-isces.org>



The primary objective of IEEE-iSES is to provide a platform for both hardware and software researchers to interact under one umbrella for further development of smart electronic systems. Symposium addresses key technologies and enabling technologies for Smart Systems as well as solutions for energy, agriculture, health, biomedical, industry, housing, sustainability, social cohesion, and mobility. The smart systems entail architectures, systems software, and algorithms as key technologies. Nonetheless, other technologies such as internet, data analytics, security, cognitive science join as enabling technologies. Smart systems adapt to changing conditions such as context, application and may possess own awareness and cater to the needs of efficient sensing, storage, software, and computing. Big data which are large, complex data sets, are now a part of the Internet world. Storing and processing needs of the enormous amount of structured and unstructured data are getting increasingly challenging. At the same time, Internet of Things (IoT) and cyber-physical systems (CPS) have been evolving with simultaneous development of hardware and software and span across everyday consumer electronics. The performance and efficiency of the various generations of computing and information processing systems are dependent upon advances in both hardware and software.

IEEE-iSES is a sponsored meeting of Technical Committee on VLSI, IEEE-CS that endorses a league of successful meetings including ASAP, ISVLSI, and ARITH, are now presented as "Sister Conferences".

CONTRIBUTIONS ARE SOUGHT IN THE FOLLOWING AREAS:

- ▶ Nanoelectronic VLSI and Sensor Systems (NVS)
- ▶ Energy-Efficient, Reliable VLSI Systems (ERS)
- ▶ Hardware/Software for Internet of Things and Consumer Electronics (IoT)
- ▶ Hardware for Secure Information Processing (SIP)
- ▶ Hardware/Software for AI, Robotics, and Automation (AIR)
- ▶ Cyber Physical Systems and Social Networks (CSN)

IEEE-iSES technical program will include contributed papers and contributions from invited speakers. IEEE-iSES 2026 will have **pre-conference-tutorials**, **keynotes**, **plenary talks**, **tutorials**, **technical sessions**, **special sessions**, **expert panels**, **research demo session (RDS)**, and a **student research forum (SRF)**.

Several student travel and best paper awards will be available like each year.

IMPORTANT DEADLINES:

Submission: June 06, 2026

Join IEEE-iSES LinkedIn Group

<https://www.linkedin.com/groups/12506886>

Acceptance: August 15, 2026

Final Submission: September 15, 2026

IEEE-iSES proceedings will be published by IEEE-CS conference publications services (CPS). Authors should submit their original unpublished work of maximum 6 pages using IEEE-CS double-column conference format-template. Manuscripts in PDF format with author information (optional) should be submitted using the link: <https://cmt3.research.microsoft.com>. Selected papers from IEEE-iSES 2026 program will be invited for submission to **special issues of peer-reviewed SCOPUS indexed journals (e.g., Springer Nature Computer Science SNCS journal) as has been done in the past editions**.



The IEEE Computer Society
Technical Committee on
VLSI

General Chairs:

O. R. Jaiswal, NIT Goa, India

Uttam Ghosh, Meharry Medical College, USA

Vasantha M. H., NIT Goa, India

Program Chair: Nithin

Kumar Y. B., NIT Goa, India

Prasanth Y., Texas Woman Univ., USA

Kailash Ray, IIT Patna, India

Program Vice Chairs:

Sharad Sinha, IIT Goa, India

Laavanya Rachakonda, Univ.of North CW, USA

Special Session Chairs:

Prasun Ghosal, IIEST Shibpur, India

Virender Singh, IIT Bombay, India

Veerakumar T., NIT Goa, India

Panel Chairs:

Soumya Joshi, BITS, Hyderabad, India

Anirban Chatterjee, NIT Goa, India

Publication Chairs:

Alakananda M., Univ.of Nebraska Lincoln, USA

Veena T., NIT Goa, India

Tutorial Chairs:

Sumit Khalapure, Reneases, India

Abhijeet Taralkar Reneases, India

Ashis Kumar Dhar, NIT Durgapur, India

Local Arrangement Chairs:

Mallikarjuna E.M., NIT Goa India

Sreeraj E.S., NIT Goa India

Lokesh Kumar Bramhane, NIT Goa India

Nitin George, IIT Gandhinagar, India

Student Research Forum Chairs:

Neeraj Goyal, IIT Ropar, India

Siddarth R.K. NIT Goa, India

Seema Aarella, Austin College, USA

Research Demo Session Chairs:

Nandakumar Nambath, IIT Goa, India

Nareshkumar Reddy, NIT Trichy, India

Sibi C. Sethuraman, VIT AP, India

Finance Chairs:

Trilochan P, NIT Goa, India

Shreeharsha, MIT Manipal, India

Registration Chairs:

Shivanarayan Patidar, NIT Goa, India

Mini S., NIT Goa, India

Industry Liaison Chairs:

Abhijit Karmakar, CEERI Pilani, India

Preet Yadav, NXP Semiconductor, India

Manikanta Reddy, AMD, India

Sanmitra Naik, GF, India

Ahish S. Shylentra , NVIDIA, USA

Women in Engineering Chairs:

Jaya Dofe, California State Univ., Fullerton, USA

Pushpita Chatterjee, Meharry Medical College, USA

Pragati Patel, NIT Goa, India

Sithara Raveendran, MIETY, India

Web Chairs:

Anand Bapatla, Univ. of Central Missouri, USA

Venkataresh K., NIT Goa, India

Media Chairs:

Ayaskanta Swain, NIT Rourkela, India

Sarani Ghosal Mondal, NIT Goa, India

Publicity Chairs:

Linga Reddy C., Univ. of Agder, Norway

Theocharis Theocharides, Univ. of Cyprus, Cyprus

Thinagara Perumal, University of Putra, Malaysia

Ambika Prasad Shah, IIT Jammu

Maifuz Ali, IIIT Naya Raipur, India

Debashis Maji, VIT Vellore, India

Sujit Biswas, University of East London, UK

Vivek Sharma, GF, India

Franose Edavoor, CDAC, India

Steering Committee Chair/Vice-chairs:

Saraju P. Mohanty, University of North Texas, USA

Dhruba Ghai, Oriental University, India