12E754 Floating point representation Bian. Bioned Exponent. Decimal to Floating point. Floating point to Decimal. Floating point Addition, sub. Floating point mul. Overflow- underflow. Chapter 3 (video 1)  $12 \longrightarrow X_{21}$ addi X21, X0,12  $12.9 \times (6^3 \rightarrow \cancel{4}_{21})$  (How?) ( - f31) m

Co-eff Bare

4.5 × 10

Exponent

4.5 × 10

single precision (32-bit) ( (GY bit) double







## Normalized only one doit before the decimal/bin point Digit must be a non-zero manumber. (1) 1-8 123, 456 X (2) 1. 2345 (2) 1. 1 191+, 1=0(3) 0.123. 🔀 To 1 digit, it is zero

(4) 9.123 00 1 - 1 digit, it's mot zero

#### Steps to normalize.

the decimal point lept or right, until there is only one single digit

123.45 123

1234,5678

1.1010.00110

if you left-shift, the number of times you left shift will be added to the exponent 112.54 × 1035 (35+2) = 1.125 × 1037 (35+2) = 1.125 × 1037



LA 4.56123 × 102.



If you shift right, the number of you right shift will be substancted for the exponent

> 0.0065 = 6.5 × 103

+ 110110.11012.  $(0.000010101)_2$   $- 1.01101101 \times 2^{\circ}$   $= 1.0101101 \times 2^{-5}$ 

### IEEE Floating point foremat.

(0)			
(34)	Sig. Bit	Expon.	Fraction.
single p.	1bit	8 bit	23 pit
Double P.	1bit.	11 bit	52 bit.
(64)		en en men en e	Commence Commence of Commence

Single precision. (32bit)

Example:

MALLIN

01011 × 230. 6bit

1:101011101011 ×2<sup>30</sup>.
12 bit.

Exponent bit will be unsigned Dusing Bian.

LD 0-255.

but our 25 0 and 255

is reserved.

: range for bim = 1-254.

#### Double pression.

11 bit unsigned

0-211-04

7 0 - 2048

but 0 and 2047 are reserved.

so the range,

1 to 2047

Bian = (28-1) -1 7 127

Actual (1-127) to (25ty -127) (-126) to (+126)





#### Bias & Bia, exponent.

D size of the exponent field.

Bion = 2 n-1-1

Bired exponent = Actual expo + Bion.

Ex:
1.1011 X2 > Find the bimed expo in IEEE 754

single precision format.

Blom = 
$$2^{8-1}-1$$

O Check if the num is normalized

1) The number is not normalized.

$$41.11011 \times 2^{-8+1} =$$

11.1011 X2-8, Find the biased exponent of given number in 15-bit IEEE754 format where size of fraction is 8 bits.

Fraction = 8 bit sign-bit = 1 bit 9bit : Exponent = (15-9) = 6 bits.

Bim =  $2^{6-1}$  1 = 31

in Blomed a

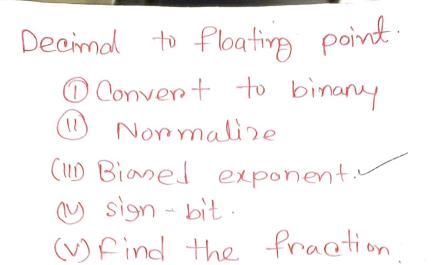
number = 11, 1011 × 2-8.

L) 1,11011 × 2-7.

Biomed exponent = (31-7) = 24 675.







$$(50.6749)_{(0)} = 110010.1010110.011$$

Normalize. 1:1001010 10110011×25

Bias = 127.  
Biased exponent = 127 +5 = (132) = 1000 0100  
sign-bit = 
$$Ov$$
.

fan-fraction = 10010101100110000 0000 15 bits 8 bits.

$\bigcirc$	1000 0100	100101010110011	00000000
		Consultation and Aller Consultation of the Con	

oating point where biased exp is 4 bits.

14848

$$(0.0232)_{10} = \frac{1(10111)_2}{2^{10}}$$

$$=(-0.0000010)_{1}^{1}$$

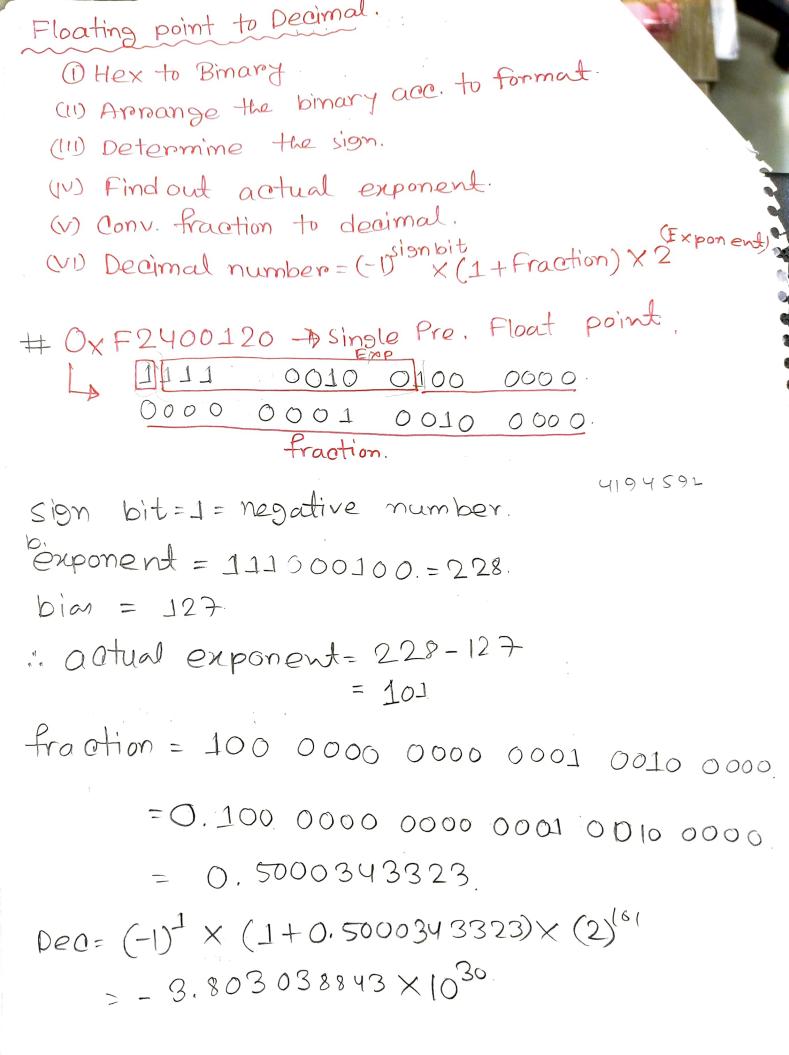
$$bian = 2^3 - 1$$

$$sign = 1$$
.

,			The same of the sa	
	1	0000110	0000	000







hd it up to 6 dec. point -3,80303884 × 1030. 7-3.80304×1030 Floating point addition. -> Numbers are in binary. Normalize A & B. Align the bin point so that lower exp match the hig -> Now add / sub. -> Normalize the nesult. -> Round if necessary. -> Overflow / anderflow. (9.999 X10') + (1.610 X15').  $\Rightarrow$  (99.99) + (0.1610) 7 11000 1111111 0101 # 0.00 1010 0100 7 (2m) exp = (1. 1000 1111 1111 0101 ×26) + 0,0000 00001010  $100 \times 26$ 



= 26(



#### Floating point mul.

- -> Make sure that the values are in binary
- -> Normalize .-> Add the expo.
- -> mutiply
- -> Normalize. > 9f it was negative

$$(1.110 \times 25) \times (11 1.11 \times 2^{-7}) \rightarrow same$$

$$=(1.110 \times 2^5) \times (1.1111 \times 2^{-5})$$

$$= (1.110 \times 1.111) \times (2^{5-5})$$

# Overflow/underflow

perfect

under.

if (yza):

overflow.

Exponent

field.

$$1.01 \times 2^{-89} \times 1.01 \times 2^{-89}$$

$$= (1.01 \times (.0)) \times 2^{-19-8}$$

range of bined exponent,

range 
$$\rightarrow (1-30)$$

: overflow underflow.





### Long multiplication

Optimize multiplier.

Jet Heradion = (multiplier bits,

Omnultiplier lat bit 1

-> multiplicant + product half MSB.

-> Multiplicant + product half MSB.

-> Dit right shift (Product) B

or, multiplier! last bit = 0.

Del bit right shift (product).

1000 X A Ø Ø Å

product, +multiplier

Ite	Multiplicant	Product
Ô	1000	6000 1001
1	1000	1000 1001 A
2	1000	0010 0000
3	1000	0001 0001
	1000.	1001 0001 A
		0100 1000. LB

Proc 64 bit



