



FRIENDSHIPS MATTER

CVCG (CHG, PAG, CIG, MVE), **SEG** (SDG, HPG, CIPG),
IPSG (PSG, MIG, CIG, PDS), **DCG**, **iCDG**, **CCG** (CHD),
SSG, **TMG**, **Intel IT & Intel Labs** are hiring!

Intel India Talent Acquisition
August 07, 2018

CORE & VISUAL COMPUTING GROUP

CHG positions:

| Job Requisition Number | Job Title |
|------------------------|---|
| JR0075020 | Low Power Methodology Engineer |
| JR0073231 | FC Timing Analysis and Sign off |
| JR0073230 | Formal Equivalence Verification(FEV) Lead |
| JR0073130 | Structural Design & Timing Engineer |
| JR0073127 | Structural Design Engineer |
| JR0073126 | Structural Design Engineer |
| JR0072799 | Static Timing Signoff Specialist |
| JR0072729 | Physical Design Expert |
| JR0071110 | Physical Design Manager |
| JR0070575 | Structural Design / Component Design Engineer |
| JR0069471 | IP-SoC Reliability Verification and PDN design Engineer |
| JR0069446 | Clock design specialist |
| JR0069437 | Logic Signoff Methodology Engineer |
| JR0069122 | Physical Design Methodology |
| JR0062787 | SOC Design Engineer |

PAG

| Job Requisition Number | Job Title |
|------------------------|----------------------------------|
| JR0065328 | Coherent Fabric Engineer |
| JR0060728 | Architecture Simulation Engineer |
| JR0060723 | Architecture Simulation Engineer |

CIG

| Job Requisition Number | Job Title |
|------------------------|------------------------------|
| JR0075571 | Senior Verification Engineer |
| JR0075574 | Product Engineering |
| JR0075576 | System Validation |
| JR0075577 | System Validation |
| JR0075578 | System Validation |
| JR0068657 | Verification Engineer |
| JR0073246 | Project analyst |
| JR0072641 | Senior Verification Engineer |
| JR0074211 | Design Engineer |

MVE

| Job Requisition Number | Job Title |
|------------------------|---|
| JR0074568 | System Validation Engineer - Power Management |
| JR0074570 | System Validation Engineer- Memory |
| JR0074571 | System Validation Engineer - Power Management |
| JR0074573 | System Validation Engineer- Security |
| JR0074575 | System Validation Engineer - USB |



Scalable Performance CPU Development Group (SDG)

SDG is Intel's Scalable Performance CPU Development Group with the vision to be the leading developer of innovative silicon technologies & products that fuel the growth of the datacenter market & beyond; SDG designs, validates, & delivers silicon products to Intel's business groups: DCG, IOTG, CCG, ADG for their product portfolios, enabling Intel's vision & mission.

SDG Vision: Be the innovative design team of silicon technologies & products that fuel Intel's growth in the datacenter market & beyond.

SDG Mission: Deliver general purpose & custom server, storage & networking products that meet our performance, schedule, & cost commitments.

| Job Requisition Number | Job Title |
|------------------------|------------------------------------|
| JR0064221 | Senior Physical Design Engineer |
| JR0066289 | Senior Verification Engineer |
| JR0066352 | IP-SS Senior Verification Engineer |
| JR0066353 | IP Logic Design Lead |
| JR0066354 | IP-SS Verification lead |

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| JR0066357 | SOC Physical Design Engineer |
| JR0066358 | SOC Physical Design Engineer |
| JR0066521 | SoC Physical Design Engineer |
| JR0066530 | Analog Design Engineer |
| JR0066535 | SOC DfX Engineer |
| JR0066577 | SD Manager |
| JR0066590 | SOC design Engineer |
| JR0066592 | SOC design Engineer |
| JR0066601 | SoC Physical Design Engineer |
| JR0066627 | SOC Design Engineer, Layout Verification methodology |
| JR0066644 | SoC Verification Engineer |
| JR0066645 | SoC verification Engineer |
| JR0066801 | Analog Layout Engineer |
| JR0066935 | DFT Engineer |
| JR0066945 | Sr STA engineer |
| JR0066947 | STA engineer |
| JR0067249 | SoC Design Engineering Manager |
| JR0067270 | Logic Design Engineer |
| JR0068462 | Validation Infrastructure Development Engineer |
| JR0068757 | SoC Design Engineer |
| JR0068907 | Validation Infrastructure Development Engineer |
| JR0069304 | SoC Pre Si Validation Engineer |
| JR0072030 | Business Operations Manager |
| JR0072244 | Pre Silicon Validation Engineer |
| JR0072248 | SOC Design Engineer |
| JR0072259 | Pre-Silicon Verification Engineer DFT |
| JR0072759 | DFT Engineer |
| JR0072916 | Engineering Manager |
| JR0073080 | Logic Design Engineer |
| JR0073081 | SoC Design Engineering Manager Structural Design |
| JR0073083 | Structural Design Engineer |
| JR0073084 | Logic Design Engineer |
| JR0073177 | Senior SOC design Engineer |
| JR0073178 | SOC Design Engineer |
| JR0074214 | Senior Pre-silicon Verification Engineer |
| JR0074669 | Pre-Si Emulation engineer |
| JR0075378 | DFT Engineering Manager |

HPG

| Job Requisition Number | Job Title |
|------------------------|--------------------------|
| JR0074707 | SOC Design Engineer Lead |
| JR0071600 | Physical Design Lead |
| JR0074255 | Lead SoC Design Engineer |

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| JR0073475 | Analog Engineer |
| JR0071935 | Analog Design Engineer |
| JR0071364 | Emulation Engineer |
| JR0071359 | Emulation Lead Engineer |
| JR0071357 | Emulation Engineer |
| JR0070809 | Analog Design Lead |
| JR0070808 | Analog Design Lead |
| JR0070416 | SoC Design Engineer |
| JR0069816 | SoC Lead |
| JR0068774 | Analog Design Engineer |
| JR0068759 | SoC Design Engineer |
| JR0066657 | SoC Design Engineer |
| JR0064817 | SoC Design Lead |
| JR0071674 | Pre-Silicon System Validation Engineer |
| JR0071888 | GLS Verification Engineer |
| JR0072229 | SOC Design Engineer |
| JR0073857 | Silicon Architect |

Cores Group (CIPG)

The **Core IP Group (CIPG)** designs the highly competitive portfolio of CPU Core IPs at the heart of the Intel® Atom™, Intel® Core® and Intel® Xeon® processor families.

- Our team of industry leaders and technical experts continually advances the effectiveness and efficiency of front-end design, validation and back-end design.
- We help drive Intel's revenue by working hand-in-hand with Intel's SoC teams and business units.

| Requisition Number | Job Title |
|--------------------|--|
| JR0057777 | Core Logic Design Manager |
| JR0057791 | Core Logic Design Lead |
| JR0057802 | Core Verification Manager |
| JR0057807 | Core Verification Manager |
| JR0057970 | Core Verification Environment Lead |
| JR0063837 | Core Datapath and Memory Design Engineer |
| JR0072287 | Core Structural Design Lead |
| JR0072292 | Core Structural Design Lead |
| JR0072294 | Core Structural Design Lead |
| JR0072295 | Core Structural Design Lead |
| JR0072298 | Core Structural Design Lead |
| JR0072299 | Core Structural Design Lead |
| JR0072313 | Core Structural Design Engineer |
| JR0072315 | Core Structural Design Engineer |
| JR0072317 | Core Structural Design Engineer |

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| JR0072318 | Core Structural Design Engineer |
| JR0072319 | Core Structural Design Engineer |
| JR0072322 | Core Structural Design Engineer |
| JR0072323 | Core Structural Design Engineer |
| JR0072454 | Core Structural Design Manager |
| JR0072455 | Core Structural Design Manager |
| JR0072456 | Core Structural Design Manager |
| JR0072457 | Core Structural Design Manager |

INFRASTRUCTURE AND PLATFORM SOLUTIONS GROUP

PSG

| Job Requisition Number | Job Title |
|------------------------|----------------------|
| JR0069588 | Application Engineer |

MIG

| Job Requisition Number | Job Title |
|------------------------|---------------------------|
| JR0075166 | Physical Design Engineer |
| JR0073235 | Sr. Logic Design Engineer |
| JR0069999 | MIG CAE Val Engineer |
| JR0065223 | Logic Design Engineer |

CIG

| Job Requisition Number | Job Title |
|------------------------|------------------------------|
| JR0054867 | Senior Verification Engineer |
| JR0055431 | Test Engineering Leader |
| JR0060441 | Product Engineering |
| JR0060449 | System Validation |
| JR0060454 | System Validation |
| JR0060461 | System Validation |
| JR0068655 | Design Engineer |
| JR0068657 | Verification Engineer |
| JR0069180 | SoC Verification Lead |
| JR0073246 | Project analyst |
| JR0072641 | Senior Verification Engineer |
| JR0074211 | Logic Design Engineer |

PDS

| Job Requisition Number | Job Title |
|------------------------|--|
| JR0075953 | Engineering Manager - Static Timing Analysis |

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| JR0075952 | Design Automation - Full Chip Integration Lead |
| JR0075951 | Sr. CAD Engineer - Physical Design |
| JR0075949 | Design Automation Engineer - FEV |

DATA CENTER GROUP

| Job Requisition Number | Job Title |
|------------------------|--|
| JR0074342 | Project Analyst |
| JR0074341 | Database PA/Business Intelligence Developer |
| JR0070029 | Cloud Platform Architect |
| JR0062159 | Senior Firmware Development Engineer |
| JR0060843 | System Validation Manager |
| JR0054925 | RSD - Analog IC Layout Design Engineer |
| JR0062320 | Post-Si Validation Engineer |
| JR0063900 | Pre- Si Verification Engineer |
| JR0062116 | Sr. Tools Development Engineer |
| JR0072803 | Sr. Smart NIC Software Application Engineer |
| JR0073931 | IP Logic Design Engineer |
| JR0062336 | Pre-Si Design-Verification Engineer |
| JR0062337 | Pre-Si Design-Verification Engineer |
| JR0058398 | Senior Digital Physical IC Design |
| JR0054926 | RSD - Digital Design Engineer |
| JR0054927 | RSD -Analog SerDes Transceiver Design Engineer |
| JR0075544 | Sr. Smart NIC Software Application Engineer |

CLIENT COMPUTING GROUP

| Job Requisition Number | Job Title |
|------------------------|---|
| JR0050052 | Hardware Engineer |
| JR0050062 | Senior Platform Debug Engineer |
| JR0050064 | Software Engineer |
| JR0052032 | Graphics Power Conservation Software Engineer |
| JR0058895 | BIOS Engineer |
| JR0071778 | Software Engineer |
| JR0072844 | Hardware Lead |
| JR0072845 | Power Delivery Engineer |
| JR0072846 | SI/PI Engineer |
| JR0072894 | PCB Layout Engineer |

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| JR0072895 | Thermal Engineer |
| JR0074876 | software engineer intern |
| JR0074955 | Program Manager |

Connected Home Division (CHD)

| Job Requisition Number | Job Title |
|------------------------|--|
| JR0062795 | Design Automation Engineer |
| JR0064191 | Software Development Engineer |
| JR0064427 | Staff Engineer - Physical Design |
| JR0064432 | Staff Engineer - Pre Silicon Verification |
| JR0064433 | Staff Engineer - Pre Silicon Verification |
| JR0067162 | Staff Engineer - Physical Design Engineer |
| JR0067163 | Staff Engineer - Physical Design Engineer |
| JR0067875 | Wireless Software Architect |
| JR0067970 | Wireless Software Qualification Manager |
| JR0069963 | Staff Engineer, IP-SoC Design |
| JR0070110 | Staff Engineer, IP-SoC Design |
| JR0070855 | Home Gateway PoC/Pathfinding Engineer |
| JR0073132 | Wi-Fi Software Validation Lead Engineer |
| JR0073884 | Linux embedded software developer for wireless com |
| JR0073912 | Wi-Fi Software Validation Lead Engineer |
| JR0073913 | Wi-Fi Software/Platform Test/Validation Engineer |
| JR0074967 | WiFi Embedded Software Developer |
| JR0075714 | Home Gateway PoC/Pathfinding Engineer |



| Job Requisition Number | Job Title |
|------------------------|---|
| JR0075529 | Audio HAL Engineer |
| JR0070585 | Zephyr RTOS DevOps Lead |
| JR0070332 | Software Development Engineer |
| JR0070307 | Runtime Software Engineer |
| JR0070305 | Runtime Software Engineer |
| JR0070304 | Runtime Software Engineer |
| JR0070137 | SW Integration Lead |
| JR0069885 | Multimedia Engineer |
| JR0069613 | Windows Power Management Debug Engineer |
| JR0068156 | Software Engineer |

TECHNOLOGY & MANUFACTURING GROUP

| Job Requisition Number | Job Title |
|------------------------|--|
| JR0073886 | South Asia EHS Manager |
| JR0075829 | Circuit Designer and Reliability Scientist |
| JR0074262 | CW Outsourcing Operations |
| JR0074778 | Supply Chain Sourcing Manager |
| JR0074772 | Memory Design Engineer |
| JR0074864 | Intel India Public Affairs Lead |
| JR0075424 | Analog Engineer |
| JR0075425 | Physical Design Engineer |

IT INDIA

| Job Requisition Number | Job Title |
|------------------------|--|
| JR0061888 | System programmer |
| JR0063645 | System programmer |
| JR0070239 | Data Analyst |
| JR0070329 | Network Engineer |
| JR0071638 | CIBR Production Services Support Analyst |
| JR0072531 | Network Engineer |
| JR0072763 | Software Engineer |
| JR0073331 | eGRC Technical Analyst |
| JR0075112 | Privacy Specialist |

Intel Labs

| Job Requisition Number | Job Title |
|------------------------|--------------------------|
| JR0073473 | Administrative Assistant |

[Click here](#) to refer job descriptions for the above positions, upload the Title and submit your referral resume to Indiaempref@intel.com.

You can also follow us on <http://jobs.intel.com/page/show/data-center-careers-asia>
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