

## **AXI4 BFM** – Quick Reference

```
axi write (awid value, awaddr value, awlen value, awsize value, awburst value, awlock value, awcache value, awprot value, awgos value,
awregion value, awuser value, wdata value, wstrb value, wuser value, buser value, bresp value, msg, clk, axi if, [scope, [msg, id panel, [config]]])
Example: axi_write(
                                                                                    Optional parameters (using named association):
          awid value
                         => x"01".
                                                                                             awid value
          awaddr value \Rightarrow x"00000004".
                                                                                             awlen_value
                         => x"01".
          awlen value
                                                                                             awsize value
          awsize value => 4.
                                                                                             awburst value
          awburst value => INCR.
                                                                                             awlock_value
          awlock value => NORMAL.
                                                                                             awcache value
          awcache_value => "0000".
                                                                                             awprot value
          awprot value => UNPRIVILEGED UNSECURE DATA.
                                                                                             awqos_value
          awgos value => "0000".
                                                                                             awregion value
          awregion_value => "0000",
                                                                                             awuser value
          awuser value => x"01".
                                                                                             wstrb_value
          wdata value
                        => t slv array'(x"12345678", x"33333333"),
                                                                                             wuser value
                        => t_slv_array'(x"F", x"F"),
          wstrb value
                        => t slv array'(x"01", x"01"),
          wuser value
                                                                                    Suggested usage: axi write(
                        => v buser value.
          buser value
                                                                                                       awaddr_value => x"00000004".
          bresp value
                         => v bresp value,
                                                                                                       awlen value
                                                                                                                     => x''01''.
          msa
                         => "Writing data to Peripheral 1".
                                                                                                       wdata value
                                                                                                                     => t slv array'(x"12345678", x"333333333"),
                         => clk,
                                                                                                                     => "Writing data to Peripheral 1");
          axi if
                         => axi_if);
                                                                                                     -- Suggested usage requires local overload (see section 5)
```

```
BFM
```

axi\_bfm\_pkg.vhd

```
axi read (arid value, araddr value, arlen value, arsize value, arburst value, arlock value, arcache value, arprot value, argos value, arregion value,
aruser_value, rdata_value, rresp_value, ruser_value, msg, clk, axi_if, [scope, [msg_id_panel, [config, [proc_name]]]])
Example: axi_read(
                                                                                      Optional parameters (using named association):
          arid value
                        => x"01".
                                                                                               arid value
          araddr value \Rightarrow x"00000004".
                                                                                               arlen value
          arlen value \Rightarrow x"01".
                                                                                               arsize_value
          arsize value => 4.
                                                                                               arburst value
          arburst_value => INCR.
                                                                                               arlock value
          arlock value => NORMAL.
                                                                                               arcache_value
          arcache value => "0000".
                                                                                               arprot value
          arprot_value => UNPRIVILEGED_UNSECURE_DATA,
                                                                                               argos value
          argos_value => "0000",
                                                                                               arregion_value
          arregion_value => "0000",
                                                                                               aruser value
          aruser_value \Rightarrow x"01",
          rdata value => v rdata value.
                                                                                      Suggested usage: axi read(
          rresp_value => v_rresp_value,
                                                                                                        araddr value => C ADDR IO.
          ruser value
                        => v ruser value.
                                                                                                        arlen value => x"01",
                        => "Read from Peripheral 1",
                                                                                                        rdata value => v data out,
          clk
                        => clk.
                                                                                                                    => "Read from IO");
                        => axi_if);
          axi_if
                                                                                                       -- Suggested usage requires local overload (see section 5)
```





```
axi_check (arid_value, araddr_value, arlen_value, arsize_value, arburst_value, arlock_value, arcache_value, arprot_value, arqos_value,
arregion value, aruser value, rdata exp., rresp exp., ruser exp., msg, clk, axi if, [alert level, [scope, [msg id panel, [config]]]])
                                                                                  Optional parameters (using named association):
Example: axi check(
                                                                                            arid_value
          arid value
                        => x"01",
                                                                                            arlen value
          araddr value \Rightarrow x"00000004",
                                                                                            arsize value
          arlen value \Rightarrow x"01".
                                                                                            arburst value
          arsize value => 4,
                                                                                            arlock value
          arburst value => INCR.
                                                                                            arcache value
          arlock value => NORMAL,
                                                                                            arprot value
          arcache value => "0000".
                                                                                            argos value
          arprot value => UNPRIVILEGED UNSECURE DATA.
                                                                                            arregion_value
          argos_value => "0000",
                                                                                            aruser_value
          arregion_value => "0000",
                                                                                            rresp exp
          aruser_value => x"01",
                                                                                            ruser_exp
          rdata exp
                        => t_slv_array'(x"12345678", x"33333333"),
                        => t_slv_array'("00", "00"),
          rresp_exp
                                                                                  Suggested usage: axi_check(
                        => t slv arrav'(x"00", x"00").
          ruser exp
                                                                                                     araddr_value => C_ADDR_IO,
                        => "Check data from Peripheral 1",
          msg
                                                                                                     arlen_value => "01",
          clk
                        => clk.
                                                                                                     rdata\_exp => t\_slv\_array'(x"12345678", x"333333333"),
          axi_if
                        => axi_if);
                                                                                                                 => "Checking data from Peripheral 1"):
                                                                                                    -- Suggested usage requires local overload (see section 5)
```

## init\_axi\_if\_signals (addr\_width, data\_width, id\_width, user\_width)

**Example**: axi if <= init axi if signals(addr width, data width, id width, user width):

## BFM Configuration record 't\_axi\_bfm\_config'

Record element	Туре	C_AXI_BFM_CONFIG_DEFAULT	Description
max_wait_cycles	natural	1000	Used for setting the maximum cycles to wait before an alert is issued when waiting for ready and valid signals from the DUT.
max_wait_cycles_severity	t_alert_level	TB_FAILURE	The above timeout will have this severity
clock_period	time	-1 ns	Period of the clock signal.
clock_period_margin	time	0 ns	Input clock period margin to specified clock_period
clock_margin_severity	t_alert_level	TB_ERROR	The above margin will have the severity
setup_time	time	-1 ns	Setup time for generated signals. Suggested value is clock_period/4.
			An alert is reported if setup_time exceed clock_period/2.
hold_time	time	-1 ns	Hold time for generated signals. Suggested value is clock_period/4.
			An alert is reported if hold_time exceed clock_period/2.
bfm_sync	t_bfm_sync	SYNC_ON_CLOCK_ONLY	When set to SYNC_ON_CLOCK_ONLY the BFM will enter on the first falling edge, estimate the clock period, synchronise the output signals and exit ¼ clock period after a succeeding rising edge.  When set to SYNC_WITH_SETUP_AND_HOLD the BFM will use the configured setup_time, hold_time and clock_period to synchronise output signals with clock edges.
match_strictness	t_match_strictness	MATCH_EXACT	Matching strictness for std_logic values in check procedures.  MATCH_EXACT requires both values to be the same. Note that the expected value



can contain the don't care operator '-'.

MATCH\_STD allows comparisons between 'H' and '1', 'L' and '0' and '-' in both values.

num_aw_pipe_stages	natural	1	Write Address Channel pipeline steps
num_w_pipe_stages	natural	1	Write Data Channel pipeline steps
num_ar_pipe_stages	natural	1	Read Address Channel pipeline steps
num_r_pipe_stages	natural	1	Read Data Channel pipeline steps
num_b_pipe_stages	natural	1	Response Channel pipeline steps
id_for_bfm	t_msg_id	ID_BFM	The message ID used as a general message ID in the AXI BFM
id_for_bfm_wait	t_msg_id	ID_BFM_WAIT	The message ID used for logging waits in the AXI BFM
id for hfm noll	t mea id	ID REM POLI	The message ID used for logging polling in the AXI RFM

# BFM non-signal parameters

awid value         std logic vector         x°0¹*         0         Identification tag for a write transaction.           awadd x-value         unsigned (7 downlo 0)         x°125 A*         None         The number of data transfer in a write transaction.           awadd x-value         unsigned (7 downlo 0)         x°125 A*         None         The number of data transfers in a write transaction.           awburst_value         the per range 1 to 128         x°25 A*         None         The number of bytes in each data transfer in a write transaction. (Must be a power of the purpose of the property of the purpose of the p	Name	Туре	Example(s)	Default value	Description
awlen_value         unsigned(7 downto 0)         x'01*         9         The number of data transfers in a write transaction           awsize_value         line per range 1 to 128         4         4         The number of bytes in each data transfer in a write transaction (Must be a power of two)           awburt_value         t_xxburst         INCR         Burst type, indicates how address changes between each transfer in a write transaction awcache, value         t_xxbox         NORIMAL         Provides information about the atomic characteristics of a write transaction in required to progress through a system           awcache_value         t_xxbox         UNPRIVILEGED_UNSECURE_DATA         Indicates how a write transaction is required to progress through a system           awgos_value         t_xxbox         UNPRIVILEGED_UNSECURE_DATA         Provides information about the atomic characteristics of a write transaction           awseign_value         t_xxbox         UNPRIVILEGED_UNSECURE_DATA         Unders>0)         Quality of Service identifier for a write transaction           awseign_value         std_logic_vector(3 downto)         "00000"         (others>0)         Quality of Service identifier for a write transaction           awseign_value         t_xxiv_array         t_xxiv_array(x'2003*, x'1234*)         None         Array of data values to be written to the address channel           wuser_value         t_xxiv_array         t_xxiv_array(x'200*, x'01*)         (o	awid_value	std_logic_vector		0	Identification tag for a write transaction
Integer range 1 to 128   Laxburst   Integer range 1 to 128   Laxburst   INCR   INCR   INCR   Burst type, indicates how address changes between each transfer in a write transaction with transaction aword.   Value   Laxburst   Laxburst   Laxburst   NORMAL   Provides information about the atomic characteristics of a write transaction   Provides information about the atomic characteristics of a write transaction   Value   Laxburst   Value   Laxburst   Value   Laxburst   Value   Laxburst   Value   Va	awaddr_value	unsigned	x"125A"	None	The address of the first transfer in a write transaction
wburst_value	awlen_value	unsigned(7 downto 0)	x"01"	0	The number of data transfers in a write transaction
Auburst_value   Laxburst   Laxb	awsize_value	Integer range 1 to 128	4	4	The number of bytes in each data transfer in a write transaction (Must be a power of
transaction    Table   Captor   Captor					two)
awlock_value         L_axlock         NORMAL         NORMAL         Provides information about the atomic characteristics of a write transaction           awcache_value         std_logic_vector(3 downto)         "0000"         (others=~0")         Indicates how a write transaction is required to progress through a system           awprot_value         Laxprot         UNPRIVILEGED_UNSECURE_DATA         Protection attributes of a write transaction is required to progress through a system           awregion_value         std_logic_vector(3 downto)         "0000"         (others=~0")         Quality of Service identifier for a write transaction           awser_value         std_logic_vector(3 downto)         "0000"         (others=~0")         Region indicator for a write transaction           watus_value         t_slv_array         t_slv_array(*2003", x*1234")         None         Array of write strobes, indicates which byte lanes hold valid data. (all "1" means all bytes are updated)           wuser_value         t_slv_array         t_slv_array(*0"0", x*01")         (others=~0") for all words         Array of write strobes, indicates which byte lanes hold valid data. (all "1" means all bytes are updated)           wuser_value         t_slv_array         t_slv_array(x*00", x*01")         (others=~0") for all words         Array of write strobes, indicates which byte lanes hold valid data. (all "1" means all bytes are updated)           wuser_value         t_slv_array         t_slv_array(x*00", x*	awburst_value	t_axburst	INCR	INCR	Burst type, indicates how address changes between each transfer in a write
awroache_value   std_logic_vector(3 downto 0)   "0000"   (others=>"0")   Indicates how a write transaction is required to progress through a system					transaction
awprot_value         t_axprot         UNPRIVILEGED_UNSECURE_DATA         UNPRIVILEGED_UNSECURE_DATA         Protection attributes of a write transaction. Privilege, security level and access type awogo_value           awgos_value         std_logic_vector(3 downto 0)         "0000"         (others=>0")         Quality of Service identifier for a write transaction           awuser_value         std_logic_vector         x"01"         (others=>0")         User-defined extension for the write transaction           wdata_value         t_slv_array         t_slv_array("2003", x"1234")         None         Array of data values to be written to the addressed registers           wstrb_value         t_slv_array         t_slv_array("1111", "1111")         (others=>1") for all words         Array of write strobes, indicates which byte lanes hold valid data. (all "1" means all bytes are updated)           wuser_value         t_slv_array         t_slv_array("200", x"01")         (others=>"0") for all words         Array of write strobes, indicates which byte lanes hold valid data. (all "1" means all bytes are updated)           wuser_value         t_slv_array         t_slv_array("200", x"01")         (others=>"0") for all words         Array of write strobes, indicates which byte lanes hold valid data. (all "1" means all bytes are updated)           wuser_value         t_slv_array         t_slv_array("200", x"01")         (others=>"0") for all words         Array of write strobes, indicates which byte lanes hold valid data. (all "1" means all	awlock_value	t_axlock	NORMAL	NORMAL	
awrego_value std_logic_vector(3 downto 0) "0000" (others=>"0") Region indicator for a write transaction awregion_value std_logic_vector(3 downto 0) "0000" (others=>"0") Region indicator for a write transaction awregion_value std_logic_vector x"01" (others=>"0") User-defined extension for the write address channel  value t_slv_array t_slv_array(x"20D3", x"1234") None Array of data values to be written to the addressed registers  wstrb_value t_slv_array t_slv_array("1111", "1111") (others=>"1") for all words Array of write strobes, indicates which byte lanes hold valid data. (all "1" means all bytes are updated)  wuser_value t_slv_array t_slv_array(x"00", x"01") (others=>"0") for all words Array of user-defined extension for the write data channel  buser_value std_logic_vector v_buser_value None Output variable containing the user-defined extension for the write response channel  bresp_value std_logic_vector v_buser_value None Output variable containing the write response which indicates the status of a write transaction  arid_value std_logic_vector x"01" (others=>"0") Identification tag for a read transaction  arid_value unsigned(7 downto 0) x"01" (others=>"0") The number of data transfer in a read transaction  arien_value unsigned(7 downto 0) x"01" (others=>"0") The number of bytes in each data transfer in a read transaction  arious t_value t_axburst INCR Burst type, indicates how address changes between each transaction  arlock_value t_axburst NORMAL NORMAL Provides information about the atomic characteristics of a read transaction  arracache_value std_logic_vector(3 downto 0) x000" (others=>"0") Indicates how a read transaction. Privilege, security level and access type  UNPRIVILEGED_UNSECURE_DATA VINPRIVILEGED_UNSECURE_DATA	awcache_value	std_logic_vector(3 downto 0)	"0000"	(others=>'0')	Indicates how a write transaction is required to progress through a system
awregion_value std_logic_vector(3 downto 0) "0000" (others⇒"0') Region indicator for a write transaction awuser_value std_logic_vector x"01" (others⇒"0') User-defined extension for the write address channel wdata_value t_slv_array t_slv_array(x"20D3", x"1234") None Array of data values to be written to the addressed registers wstrb_value t_slv_array t_slv_array("1111", "1111") (others⇒"1) for all words Array of write strobes, indicates which byte lanes hold valid data. (all '1' means all bytes are updated) wuser_value t_slv_array t_slv_array(x"00", x"01") (others⇒"0') for all words Array of user-defined extension for the write data channel bresp_value t_slv_array t_slv_array(x"00", x"01") (others⇒"0') for all words Array of user-defined extension for the write data channel bresp_value t_slv_array t_slv_array(x"00", x"01") (others⇒"0') for all words Array of user-defined extension for the write data channel bresp_value t_slv_array t_slv_array(x"00", x"01") (others⇒"0') identification tag for a read transaction  arid_value unsigned x"125A" None The address of the first transfer in a read transaction  arid_value unsigned(7 downto 0) x"01" (others⇒"0') The number of data transfers in a read transaction  arid_value t_axburst lNCR INCR Burst type, indicates how address changes between each transfer in a read transaction  ariock_value t_axburst NORMAL NORMAL Provides information about the atomic characteristics of a read transaction  arrache_value t_axport UNPRIVILEGED_UNSECURE_DATA UNPRIVILEGED_UNSECURE_DATA  arrache_value t_axport NORMINITE average in transaction is required to progress through a system	awprot_value	t_axprot	UNPRIVILEGED_UNSECURE_DATA	UNPRIVILEGED_UNSECURE_DATA	Protection attributes of a write transaction. Privilege, security level and access type
awuser_value std_logic_vector x*01* (others=>*0) User-defined extension for the write address channel  wdata_value t_slv_array t_slv_array(x*20D3*, x*1234*) None Array of data values to be written to the addressed registers  wstrb_value t_slv_array t_slv_array(**1111*, **1111*) (others=>*1) for all words Array of write strobes, indicates which byte lanes hold valid data. (all '1' means all bytes are updated)  wuser_value t_slv_array t_slv_array(x*00*, x*01*) (others=>*0') for all words Array of user-defined extension for the write data channel  buser_value std_logic_vector v_buser_value None Output variable containing the user-defined extension for the write response channel  bresp_value t_xresp v_bresp_value None Output variable containing the write response which indicates the status of a write transaction  arid_value std_logic_vector x*01* (others=>*0') Identification tag for a read transaction  arid_value unsigned x*125A* None The address of the first transfer in a read transaction  arider_value unsigned(7 downto 0) x*01* (others=>*0') The number of data transfers in a read transaction (Must be a power of two)  arisize_value t_axburst INCR INCR Burst type, indicates how address channel burst required to progress through a system  arrock_value t_axport UNPRIVILEGED_UNSECURE_DATA UNPRIVILEGED_UNSECURE_DATA Protection attributes of a read transaction. Privilege, security level and access type	awqos_value	std_logic_vector(3 downto 0)	"0000"	(others=>'0')	Quality of Service identifier for a write transaction
wdata_value         t_slv_array         t_slv_array (k*2003", x*1234")         None         Array of data values to be written to the addressed registers           wstrb_value         t_slv_array         t_slv_array ("1111", "1111")         (others⇒'1) for all words         Array of write strobes, indicates which byte lanes hold valid data. (all '1' means all bytes are updated)           wuser_value         t_slv_array         t_slv_array (x'00", x'01")         (others⇒'0') for all words         Array of user-defined extension for the write data channel           buser_value         std_logic_vector         v_buser_value         None         Output variable containing the user-defined extension for the write response channel           bresp_value         t_xresp         v_bresp_value         None         Output variable containing the write response which indicates the status of a write transaction           arid_value         std_logic_vector         x"01"         (others⇒'0')         Identification tag for a read transaction           arid_value         unsigned(7 downto 0)         x"01"         (others⇒'0')         The number of data transfers in a read transaction           ariz_value         Integer range 1 to 128         4         Burst type, indicates how address changes between each transfer in a read transaction           arlock_value         t_axlock         NORMAL         NORMAL         Provides information about the atomic characteristics of a read transaction re	awregion_value	_ 3 _ ` ` ,	"0000"	(others=>'0')	Region indicator for a write transaction
t_slv_array t_slv_array t_slv_array ("1111", "1111") (others=>'1') for all words Array of write strobes, indicates which byte lanes hold valid data. (all '1' means all bytes are updated)  wuser_value t_slv_array t_slv_array (x'00", x'01") (others=>'0') for all words Array of user-defined extension for the write data channel  buser_value std_logic_vector v_buser_value None Output variable containing the user-defined extension for the write response channel  bresp_value t_xresp v_bresp_value None Output variable containing the write response which indicates the status of a write transaction  arid_value unsigned x"01" (others=>'0') Identification tag for a read transaction  ariden_value unsigned(7 downto 0) x"01" (others=>'0') The number of data transfer in a read transaction  arizize_value lateger range 1 to 128 4  The number of bytes in each data transfer in a read transaction (Must be a power of two)  arburst_value t_axburst lnCR  Burst type, indicates how address changes between each transfer in a read transaction  arcache_value t_axport UNPRIVILEGED_UNSECURE_DATA UNPRIVILEGED_UNSECURE_DATA Protection attributes of a read transaction. Privilege, security level and access type	awuser_value	std_logic_vector		(others=>'0')	User-defined extension for the write address channel
wuser_value         t_slv_array         t_slv_array('x"00", x"01")         (others⇒'0') for all words         Array of user-defined extension for the write data channel           buser_value         std_logic_vector         v_buser_value         None         Output variable containing the user-defined extension for the write response channel           bresp_value         t_xresp         v_bresp_value         None         Output variable containing the write response which indicates the status of a write transaction           arid_value         std_logic_vector         x"01"         (others⇒'0')         Identification tag for a read transaction           arien_value         unsigned         x"125A"         None         The address of the first transfer in a read transaction           arien_value         unsigned(7 downto 0)         x"01"         (others⇒'0')         The number of data transfers in a read transaction           arien_value         Integer range 1 to 128         4         4         The number of bytes in each data transfer in a read transaction (Must be a power of two)           arburst_value         t_axburst         INCR         Burst type, indicates how address changes between each transfer in a read transaction           arlock_value         t_axlock         NORMAL         NORMAL         Provides information about the atomic characteristics of a read transaction is required to progress through a system           arport_value	wdata_value	t_slv_array	t_slv_array'(x"20D3", x"1234")	None	Array of data values to be written to the addressed registers
wuser_value         t_slv_array         t_slv_array(x*00", x*01")         (others=>0') for all words         Array of user-defined extension for the write data channel           buser_value         std_logic_vector         v_buser_value         None         Output variable containing the user-defined extension for the write response channel           bresp_value         t_xresp         v_bresp_value         None         Output variable containing the write response which indicates the status of a write transaction           arid_value         std_logic_vector         x*01"         (others=>0')         Identification tag for a read transaction           araddr_value         unsigned         x*125A"         None         The address of the first transfer in a read transaction           arlen_value         unsigned(7 downto 0)         x*01"         (others=>0')         The number of data transfers in a read transaction           arsize_value         Integer range 1 to 128         4         4         The number of bytes in each data transfer in a read transaction (Must be a power of two)           arburst_value         t_axburst         INCR         Burst type, indicates how address changes between each transfer in a read transaction           arlock_value         t_axlock         NORMAL         NORMAL         Provides information about the atomic characteristics of a read transaction           arcache_value         t_axprot         UNPR	wstrb_value	t_slv_array	t_slv_array'("1111", "1111")	(others=>'1') for all words	Array of write strobes, indicates which byte lanes hold valid data. (all '1' means all
buser_value         std_logic_vector         v_buser_value         None         Output variable containing the user-defined extension for the write response channel           bresp_value         t_xresp         v_bresp_value         None         Output variable containing the write response which indicates the status of a write transaction           arid_value         std_logic_vector         x"01"         (others=>'0')         Identification tag for a read transaction           araddr_value         unsigned (7 downto 0)         x"01"         None         The address of the first transfer in a read transaction           arsize_value         Integer range 1 to 128         4         4         The number of bytes in each data transfer in a read transaction (Must be a power of two)           arburst_value         t_axburst         INCR         Burst type, indicates how address changes between each transfer in a read transaction           arlock_value         t_axlock         NORMAL         NORMAL         Provides information about the atomic characteristics of a read transaction is required to progress through a system           argorot_value         t_axprot         UNPRIVILEGED_UNSECURE_DATA         UNPRIVILEGED_UNSECURE_DATA         Protection attributes of a read transaction. Privilege, security level and access type					, ,
bresp_value	wuser_value	t_slv_array	t_slv_array'(x"00", x"01")	(others=>'0') for all words	Array of user-defined extension for the write data channel
transaction  arid_value std_logic_vector x"01" (others=>'0') Identification tag for a read transaction  araddr_value unsigned x"125A" None The address of the first transfer in a read transaction  arlen_value unsigned(7 downto 0) x"01" (others=>'0') The number of data transfers in a read transaction  arsize_value Integer range 1 to 128 4 The number of bytes in each data transfer in a read transaction (Must be a power of two)  arburst_value t_axburst INCR INCR Burst type, indicates how address changes between each transfer in a read transaction  arlock_value t_axlock NORMAL NORMAL Provides information about the atomic characteristics of a read transaction  arcache_value t_axprot UNPRIVILEGED_UNSECURE_DATA UNPRIVILEGED_UNSECURE_DATA Protection attributes of a read transaction. Privilege, security level and access type	buser_value	std_logic_vector	v_buser_value	None	Output variable containing the user-defined extension for the write response channel
arid_value std_logic_vector x"01" (others=>'0') Identification tag for a read transaction araddr_value unsigned x"125A" None The address of the first transfer in a read transaction arlen_value unsigned(7 downto 0) x"01" (others=>'0') The number of data transfers in a read transaction arsize_value Integer range 1 to 128 4 The number of bytes in each data transfer in a read transaction (Must be a power of two)  arburst_value t_axburst INCR INCR Burst type, indicates how address changes between each transfer in a read transaction arlock_value t_axlock NORMAL NORMAL Provides information about the atomic characteristics of a read transaction arcache_value std_logic_vector(3 downto 0) "0000" (others=>'0') Indicates how a read transaction is required to progress through a system arprot_value t_axprot UNPRIVILEGED_UNSECURE_DATA UNPRIVILEGED_UNSECURE_DATA Protection attributes of a read transaction. Privilege, security level and access type	bresp_value	t_xresp	v_bresp_value	None	Output variable containing the write response which indicates the status of a write
araddr_value unsigned x"125A" None The address of the first transfer in a read transaction  arlen_value unsigned(7 downto 0) x"01" (others=>'0') The number of data transfers in a read transaction  arsize_value Integer range 1 to 128 4 The number of bytes in each data transfer in a read transaction (Must be a power of two)  arburst_value t_axburst INCR INCR Burst type, indicates how address changes between each transfer in a read transaction  arlock_value t_axlock NORMAL NORMAL Provides information about the atomic characteristics of a read transaction  arcache_value std_logic_vector(3 downto 0) "0000" (others=>'0') Indicates how a read transaction. Privilege, security level and access type					
arlen_value unsigned(7 downto 0) x"01" (others=>'0') The number of data transfers in a read transaction  arsize_value Integer range 1 to 128 4 4 The number of bytes in each data transfer in a read transaction (Must be a power of two)  arburst_value t_axburst INCR INCR Burst type, indicates how address changes between each transfer in a read transaction  arlock_value t_axlock NORMAL NORMAL Provides information about the atomic characteristics of a read transaction  arcache_value std_logic_vector(3 downto 0) "0000" (others=>'0') Indicates how a read transaction is required to progress through a system  arprot_value t_axprot UNPRIVILEGED_UNSECURE_DATA UNPRIVILEGED_UNSECURE_DATA Protection attributes of a read transaction. Privilege, security level and access type	arid_value	std_logic_vector		(others=>'0')	
arsize_value Integer range 1 to 128 4 4	araddr_value	unsigned	x"125A"	None	
two)  arburst_value t_axburst	arlen_value	unsigned(7 downto 0)	x"01"	(others=>'0')	
arburst_value t_axburst INCR INCR INCR Burst type, indicates how address changes between each transfer in a read transaction  arlock_value t_axlock NORMAL NORMAL Provides information about the atomic characteristics of a read transaction  arcache_value std_logic_vector(3 downto 0) "0000" (others=>'0') Indicates how a read transaction is required to progress through a system  arprot_value t_axprot UNPRIVILEGED_UNSECURE_DATA UNPRIVILEGED_UNSECURE_DATA Protection attributes of a read transaction. Privilege, security level and access type	arsize_value	Integer range 1 to 128	4	4	The number of bytes in each data transfer in a read transaction (Must be a power of
transaction  arlock_value t_axlock NORMAL NORMAL Provides information about the atomic characteristics of a read transaction  arcache_value std_logic_vector(3 downto 0) "0000" (others=>'0') Indicates how a read transaction is required to progress through a system  arprot_value t_axprot UNPRIVILEGED_UNSECURE_DATA UNPRIVILEGED_UNSECURE_DATA Protection attributes of a read transaction. Privilege, security level and access type					,
arlock_value t_axlock NORMAL NORMAL Provides information about the atomic characteristics of a read transaction arcache_value std_logic_vector(3 downto 0) "0000" (others=>'0') Indicates how a read transaction is required to progress through a system arprot_value t_axprot UNPRIVILEGED_UNSECURE_DATA UNPRIVILEGED_UNSECURE_DATA Protection attributes of a read transaction. Privilege, security level and access type	arburst_value	t_axburst	INCR	INCR	Burst type, indicates how address changes between each transfer in a read
arcache_value std_logic_vector(3 downto 0) "0000" (others=>'0') Indicates how a read transaction is required to progress through a system  arprot_value t_axprot UNPRIVILEGED_UNSECURE_DATA UNPRIVILEGED_UNSECURE_DATA Protection attributes of a read transaction. Privilege, security level and access type					
arprot_value t_axprot UNPRIVILEGED_UNSECURE_DATA UNPRIVILEGED_UNSECURE_DATA Protection attributes of a read transaction. Privilege, security level and access type					
		std_logic_vector(3 downto 0)			
arqos_value std_logic_vector(3 downto 0) "0000" (others=>'0') Quality of Service identifier for a read transaction	arprot_value	_ ·			<del>-</del>
	arqos_value	std_logic_vector(3 downto 0)	"0000"	(others=>'0')	Quality of Service identifier for a read transaction



				and the control of th
arregion_value	std_logic_vector(3 downto 0)	"0000"	(others=>'0')	Region indicator for a read transaction
aruser_value	std_logic_vector	x"01"	(others=>'0')	User-defined extension for the read address channel
rdata_value	t_slv_array	v_rdata_value	None	Output variable containing an array of read data
rresp_value	t_xresp_array	v_rresp_value	None	Output variable containing an array of read responses which indicates the status of a read transfer
ruser_value	t_slv_array	v_ruser_value	None	Output variable containing an array of user-defined extensions for the read data channel
rdata_exp	t_slv_array	t_slv_array'(x"ABCD", x"1234")	None	Array of expected read data values. A mismatch results in an alert 'alert_level'
rresp_exp	t_xresp_array	t_xresp_array'(OKAY, OKAY)	OKAY for all words	Array of expected read responses which indicates the status of a read transfer
ruser_exp	t_slv_array	t_slv_array'(x"01", x"01")	(others=>'0') for all words	Array of expected user-defined extensions for the read data channel
alert_level	t_alert_level	ERROR or TB_WARNING	ERROR	Set the severity for the alert that may be asserted by the procedure.
msg	string	"Set state active on peripheral 1"	None	A custom message to be appended in the log/alert.
scope	string	"AXI_BFM"	C_SCOPE ("AXI_BFM")	A string describing the scope from which the log/alert originates.  In a simple single sequencer typically "AXI_BFM". In a verification component typically "AXI_VVC".
msg_id_panel	t_msg_id_panel	shared_msg_id_panel	shared_msg_id_panel	Optional msg_id_panel, controlling verbosity within a specified scope. Defaults to a common message ID panel defined in the UVVM-Util adaptations package.
config	t_axi_bfm_config	C_AXI_BFM_CONFIG_DEFAULT	C_AXI_BFM_CONFIG_DEFAULT	Configuration of BFM behaviour and restrictions. See section 2 for details.

## BFM signal parameters

Name	Туре	Description
clk	std_logic	The clock signal used to read and write
		data in/out of the AXI4 BFM.
axi_if	t_axi_if	See table "Signal record 'axi_if"

Note: All signals are active high. See AXI4 documentation for protocol description.

For more information on the AXI4 signals, please see the AXI4 specification.

## Signal record 'axi\_if'

Re	cord element	Type
writ	e_address_channel	t_axi_write_address_channel
writ	e_data_channel	t_axi_write_data_channel
writ	e_response_channel	t_axi_write_response_channel
rea	d_address_channel	t_axi_read_address_channel
rea	d_data_channel	t_axi_read_data_channel



## Write address channel record 't axi write address channel'

Record element	Туре
write_address_channel	t_axi_write_address_channel
awid	std_logic_vector
awaddr	std_logic_vector
awlen	std_logic_vector(7 downto 0)
awsize	std_logic_vector(2 downto 0)
awburst	std_logic_vector(1 downto 0)
awlock	std_logic
awcache	std_logic_vector(3 downto 0)
awprot	std_logic_vector(2 downto 0)
awqos	std_logic_vector(3 downto 0)
awregion	std_logic_vector(3 downto 0)
awuser	std_logic_vector
awvalid	std_logic
awready	std_logic

## Read address channel record 't axi read address channel'

Record element	Туре
read_address_channel	t_axi_read_address_channel
arid	std_logic_vector
araddr	std_logic_vector
arlen	std_logic_vector(7 downto 0)
arsize	std_logic_vector(2 downto 0)
arburst	std_logic_vector(1 downto 0)
arlock	std_logic
arcache	std_logic_vector(3 downto 0)
arprot	std_logic_vector(2 downto 0)
arqos	std_logic_vector(3 downto 0)
arregion	std_logic_vector(3 downto 0)
aruser	std_logic_vector
arvalid	std_logic
arready	std_logic

## AXI parameter record types

Type name	Allowed value	
t_axburst	FIXED	
	INCR	
	WRAP	
t_axlock	NORMAL	
	EXCLUSIVE	
t_axprot	UNPRIVILEGED_NONSECURE_DATA	

## Write data channel record 't axi write data channel'

I	Record element	Туре	
ſ	write_data_channel	t_axi_write_data_channel	
	wdata	std_logic_vector	
	wstrn	std_logic_vector	
	wlast	std_logic	
	wuser	std_logic_vector	
	wvalid	std_logic	
	wready	std_logic	

## Write response channel record 't\_axi\_write\_response\_channel'

Record element	Туре
write_response_channel	t_axi_write_response_channel
bid	std_logic_vector
bresp	std_logic_vector(1 downto 0)
buser	std_logic_vector
bvalid	std_logic
bready	std_logic

## Read data channel record 't\_axi\_read\_data\_channel'

Record element	Туре
read_data_channel	t_axi_read_data_channel
rid	std_logic_vector
rdata	std_logic_vector
rresp	std_logic_vector(1 downto 0)
rlast	std_logic
ruser	std_logic_vector
rvalid	std_logic
rready	std_logic



	UNPRIVILEGED_NONSECURE_INSTRUCTION			
	UNPRIVILEGED_SECURE_DATA			
	UNPRIVILEGED_SECURE_INSTRUCTION			
	PRIVILEGED_NONSECURE_DATA			
	PRIVILEGED_NONSECURE_INSTRUCTION			
	PRIVILEGED_SECURE_DATA			
	PRIVILEGED_SECURE_INSTRUCTION			
t_xresp	OKAY			
	EXOKAY			
	SLVERR			
	DECERR			





## BFM details

## 1 BFM procedure details and examples

Procedure axi write()

### Description

axi\_write(awid\_value, awaddr\_value, awlen\_value, awsize\_value, awburst\_value, awlock\_value, awcache\_value, awprot\_value, awqos\_value, awregion\_value, awuser\_value, wdata\_value, wstrb\_value, wuser\_value, buser\_value, bresp\_value, msg, clk, axi\_if, [scope, [msg\_id\_panel, [config]]])

The axi write() procedure writes the given data to the given address of the DUT, using the AXI4 protocol. For protocol details, see the AXI4 specification.

- A log message is written if ID BFM is enabled for the specified message ID panel.

The procedure reports an alert if:

- wready does not occur within max\_wait\_cycles clock cycles (alert level: max\_wait\_cycles\_severity, set in the config)
- awready does not occur within max wait cycles clock cycles (alert level: max wait cycles severity, set in the config)
- bvalid is not set within max\_wait\_cycles clock cycles (alert level: max\_wait\_cycles\_severity, set in the config)

#### Examples:

```
axi write(
   awid value
                 => x''01'',
  awaddr value \Rightarrow x"00000004",
  awlen value => x"01",
  awsize value => 4,
  awburst value => INCR,
  awlock value => NORMAL,
   awcache value => "0000",
   awprot value => UNPRIVILEGED UNSECURE DATA,
   awqos value => "0000",
   awregion value => "0000",
   awuser value => x"01",
                => t slv array'(x"12345678", x"33333333"),
   wdata value
   wstrb value => t slv array'(x"F", x"F"),
   wuser value
               => t slv array'(x"01", x"01"),
  buser value => v buser value,
   bresp value
                => v bresp value,
                 => "Writing data to Peripheral 1",
   msq
   clk
                 => clk,
                 => axi if,
   axi if
   scope
                 => C SCOPE,
   msg id panel => shared msg id panel,
                 => C AXI BFM CONFIG DEFAULT);
   config
 axi write(
  awaddr value
                 => x"00000004",
  wdata value
                 => t slv array'(x"12345678", x"33333333"),
  buser value
                 => v buser value,
                 => v bresp value,
  bresp value
                 => "Writing data to Peripheral 1");
   msq
```

Suggested usage (requires local overload, see section 5):



```
axi_write(C_ADDR_DMA, x"AAAA", "Writing data to DMA");
axi_write(C_ADDR_MEMORY, x"FF", v_data_array, "Writing 256 data words to MEMORY");
```

### axi\_read()

axi\_read(arid\_value, araddr\_value, arlen\_value, arsize\_value, arburst\_value, arlock\_value, arcache\_value, arprot\_value, arqos\_value, arregion\_value, aruser\_value, radta\_value, rresp\_value, ruser\_value, msq, clk, axi\_if, [scope, [msg\_id\_panel, [config, [proc\_name]]]])

The axi\_read() procedure reads data from the DUT at the given address, using the AXI4 protocol. For protocol details, see the AXI4 specification. The read data is placed on the output 'rdata value' when the read has completed.

- The argument "ext\_proc\_call" is intended to be used internally, when the procedure is called by axi\_check().
- A log message is written if ID\_BFM is enabled for the specified message ID panel. This will only occur if the argument proc\_name is left unchanged.

The procedure reports an alert if:

- The received rid is different from the transmitted arid\_value
- arready does not occur within max\_wait\_cycles clock cycles (alert level: max\_wait\_cycles\_severity, set in the config)
- rvalid is not set within max\_wait\_cycles clock cycles (alert level: max\_wait\_cycles\_severity, set in the config)

#### Examples:

```
axi read(
  arid value
                => x"01",
 araddr value => x"00000004"
 arlen value \Rightarrow x"01",
 arsize value => 4,
 arburst value => INCR.
 arlock value => NORMAL,
  arcache value => "0000",
  arprot value => UNPRIVILEGED UNSECURE DATA,
  argos value => "0000",
  arregion value => "0000",
  aruser value => x"01",
  rdata value => v rdata value,
              => v rresp value,
 rresp value
  ruser value => v ruser value,
                => "Read from Peripheral 1",
  msq
  clk
                => clk.
 axi if
                => axi if,
                => C SCOPE,
  msg id panel => shared msg id panel,
  config
                => C AXI BFM CONFIG DEFAULT);
axi read(
  araddr value => x"00000004",
 rdata value => v rdata value,
 rresp value => v rresp value,
  ruser value => v ruser value,
                => "Read from Peripheral 1",
 msg
                => clk,
  clk
                => axi if);
  axi if
```

#### Suggested usage (requires local overload, see section 5):

```
axi_read(C_ADDR_IO, v_data_out, "Reading from IO device");
axi_read(C_ADDR_MEMORY, x"FF", v_data_array_out, "Reading 256 data words from MEMORY");
```



### axi check()

axi check(arid value, araddr value, arlen value, arsize value, arburst value, arlock value, arcache value, arprot value, argos value, arregion value, aruser value, rdata exp, rresp exp, ruser exp, msq, clk, axi if, [alert level, [scope, [msq id panel, [config]]]])

The axi\_check() procedure reads data from the DUT at the given address, using the AXI4 protocol. For protocol details, see the AXI4 specification. After reading data from the AXI4 bus, the read data is compared with the expected data, 'rdata exp'.

- If the check was successful, and the read data matches the expected data, a log message is written with ID BFM (if this ID has been enabled).
- If the read data did not match the expected data, an alert with severity 'alert\_level' will be reported.

The procedure also report alerts for the same conditions as the axi\_read() procedure.

#### Examples:

```
axi check(
  arid value
                => x"01",
 araddr value => x''00000004'',
 arlen value => x''01'',
 arsize value => 4,
  arburst value => INCR,
  arlock value => NORMAL,
  arcache value => "0000",
  arprot value => UNPRIVILEGED UNSECURE DATA,
  argos value => "0000",
  arregion value => "0000",
  aruser value => x"01",
  rdata exp
                => t slv array'(x"12345678", x"33333333"),
  rresp exp
                => t xresp array' (OKAY, OKAY),
               => t slv array'(x"00", x"00"),
  ruser_exp
                => "Check data from Peripheral 1",
  msq
                => clk,
  clk
  axi if
                => axi if,
  alert level => ERROR,
  scope
                => C SCOPE,
  msg id panel => shared msg id panel,
                => C AXI BFM CONFIG DEFAULT);
  config
axi check(
  \overline{\text{araddr value}} => x''00000004'',
  rdata exp
                => v rdata exp,
                => "Check data from Peripheral 1",
 msq
  clk
                 => clk.
  axi if
                 => axi if);
```

### Suggested usage (requires local overload, see section 5):

```
axi check(C ADDR UART RX, x"3B", "Checking data in UART RX register");
axi check(C ADDR MEMORY, x"FF", v rdata exp array, "Checking 256 data words from MEMORY");
```

### init axi if signals()

### init\_axi\_if\_signals(addr\_width, data\_width, id\_width, user\_width)

This function initializes the AXI4 interface. All the BFM outputs are set to zeros ('0') and BFM inputs are set to 'Z'.

Note: This function assumes that awid, bid, arid and rid shares a common width (id\_width) and that awuser, buser, aruser, ruser also share a common width (user\_width)

```
axi if <= init axi if signals(addr width, data width, id width, user width);
```



## **BFM Configuration record**

Type name: t_axi_bfm_config			
Record element	Туре	C_AXI_BFM_CONFIG_DEFAULT	
max_wait_cycles	natural	10	Used for setting the maximum cycles to wait before an alert is issued when waiting
			for ready and valid signals from the DUT.
max_wait_cycles_severity	t_alert_level	TB_FAILURE	The above timeout will have this severity
clock_period	time	-1 ns	Period of the clock signal.
clock_period_margin	time	0 ns	Input clock period margin to specified clock_period
clock_margin_severity	t_alert_level	TB_ERROR	The above margin will have the severity
setup_time	time	-1 ns	Setup time for generated signals. Suggested value is clock_period/4.
			An alert is reported if setup_time exceed clock_period/2.
hold_time	time	-1 ns	Hold time for generated signals. Suggested value is clock_period/4.
			An alert is reported if hold_time exceed clock_period/2.
bfm_sync	t_bfm_sync	SYNC_ON_CLOCK_ONLY	When set to SYNC_ON_CLOCK_ONLY the BFM will enter on the first falling edge,
			estimate the clock period, synchronise the output signals and exit 1/4 clock period
			after a succeeding rising edge.
			When set to SYNC_WITH_SETUP_AND_HOLD the BFM will use the configured
			setup_time, hold_time and clock_period to synchronise output signals with clock
			edges.
match_strictness	t_match_strictness		Matching strictness for std_logic values in check procedures.
		MATCH_EXACT	MATCH_EXACT requires both values to be the same. Note that the expected value
			can contain the don't care operator '-'.
			MATCH_STD allows comparisons between 'H' and '1', 'L' and '0' and '-' in both
			values.
num_aw_pipe_stages	natural	1	Write Address Channel pipeline steps
num_w_pipe_stages	natural	1	Write Data Channel pipeline steps
num_ar_pipe_stages	natural	1	Read Address Channel pipeline steps
num_r_pipe_stages	natural	1	Read Data Channel pipeline steps
num_b_pipe_stages	natural	1	Response Channel pipeline steps
id_for_bfm	t_msg_id	ID_BFM	The message ID used as a general message ID in the AXI BFM
id_for_bfm_wait	t_msg_id	ID_BFM_WAIT	The message ID used for logging waits in the AXI BFM
id_for_bfm_poll	t_msg_id	ID_BFM_POLL	The message ID used for logging polling in the AXI BFM
•	<del>-</del>		



## 3 Additional Documentation

For additional documentation on the AXI4 standard, please see the AXI4 specification "AMBA® AXI™ and ACE™ Protocol Specification", available from ARM.

## 4 Compilation

The AXI4 BFM may only be compiled with VHDL 2008. It is dependent on the UVVM Utility Library (UVVM-Util), which is only compatible with VHDL 2008. See the separate UVVM-Util documentation for more info. After UVVM-Util has been compiled, the axi\_bfm\_pkg.vhd BFM can be compiled into any desired library. See the UVVM Essential Mechanisms located in uvvm\_vvc\_framework/doc for information about compile scripts.

## 4.1 Simulator compatibility and setup

See README.md for a list of supported simulators. For required simulator setup see UVVM-Util Quick reference.



## 5 Local BFM overloads

A good approach for better readability and maintainability is to make simple, local overloads for the BFM procedures in the TB process. This allows calling the BFM procedures with the key parameters only e.g.

```
axi write (C ADDR PERIPHERAL 1, C TEST DATA, "Sending data to Peripheral 1");
rather than
  axi write(
    awid value
                   => x''01'',
    awaddr value => x"00000004",
    awlen value
                   => x"01",
    awsize value
                   => 4,
    awburst value => INCR,
    awlock value
                   => NORMAL,
    awcache value => "0000",
    awprot value
                   => UNPRIVILEGED UNSECURE DATA,
    awgos value
                   => "0000",
    awregion value => "0000",
    awuser value \Rightarrow x"01",
    wdata value
                  \Rightarrow t slv array'(x"12345678", x"33333333"),
    wstrb value
                 => t slv array'(x"F", x"F"),
                 => t slv array'(x"01", x"01"),
    wuser value
    buser_value
                   => v buser value,
    bresp value
                   => v bresp value,
                   => "Writing data to Peripheral 1",
    msq
                   => clk.
    clk
                   => axi if,
    axi if
    scope
                   => C SCOPE,
                   => shared msg id panel,
    msq id panel
                   => C AXI BFM CONFIG DEFAULT);
    confia
By defining the local overload as e.g.:
    procedure axi write(
      constant addr value
                            : in unsigned;
      constant data value
                           : in std logic vector;
                             : in string
      constant msg
      variable v buser value : std logic vector(C USER WIDTH-1 downto 0);
      variable v bresp value : t xresp;
    begin
      axi write(
                       => x"00",
                                                        -- Setting a default value
        awid value
        awaddr value => addr value,
                                                        -- keep as is
        awlen value
                       => x''00'''
                                                        -- Set to length=1
                                                        -- Setting a default value
        awsize value
                       => 4,
                                                        -- Setting a default value
        awburst value => INCR,
```



```
awlock value => NORMAL,
                                                                -- Setting a default value
    awcache value => "0000",
                                                               -- Setting a default value
    awprot value => UNPRIVILEGED UNSECURE DATA, -- Setting a default value
                                           D_UNSECURE_DATA, -- Setting a default value
-- keep as is
-- Setting a default value
-- Setting a default value
-- Setting a default value
-- Assigning to a local var
-- keep as is
-- Assigning to a local var
-- keep as is
    awgos value => "0000",
    awregion value => "0000",
    awuser value \Rightarrow x"01",
                       => data value,
    wdata value
    wstrb value
                     => x"f"
    wuser value
                       => x"01",
                       => v buser value,
                                                                -- Assigning to a local variable
    buser value
                        => v bresp value,
                                                                -- Assigning to a local variable
    bresp_value
                        => msq,
    msa
    clk
                        => clk.
                                                                -- Clock signal
    axi_if => axi_if,
scope => C CCOPT
                                                                -- Signal must be visible in local process scope
                                                                -- Setting a default value
    msg id panel => shared msg id panel,
                                                                -- Use global, shared msg id panel
                                                                -- Use locally defined configuration or C AXI BFM CONFIG DEFAULT
    config
                        => C AXI BFM CONFIG LOCAL);
end;
```

Using a local overload like this also allows the following – if wanted:

- Have address value as natural and convert in the overload
- Set up defaults for constants. May be different for two overloads of the same BFM
- Apply dedicated msg\_id\_panel to allow dedicated verbosity control

#### **IMPORTANT**

This is a simplified Bus Functional Model (BFM) for AXI4.

The given BFM complies with the AXI4 protocol and thus allows a normal access towards an AXI4 interface. This BFM is not AXI4 protocol checker.

For a more advanced BFM please contact Bitvis AS at <a href="support@bitvis.no">support@bitvis.no</a>



Disclaimer: This IP and any part thereof are provided "as is", without warranty of any kind, express or implied, including but not limited to the warranties of merchantability, fitness for a particular purpose and noninfringement. In no event shall the authors or copyright holders be liable for any claim, damages or other liability, whether in an action of contract, tort or otherwise, arising from, out of or in connection with this IP.