

# UART MONITOR – Quick Reference

For general information see UVVM VVC Framework Essential Mechanisms located in `uvvm_vvc_framework/doc`.

UART Monitor Configuration record `'t_uart_monitor_config'` is accessible via shared variable array `shared_uart_monitor_config(channel, instance)`.  
The UART transaction information is located in `'t_transaction_group'` accessible via shared variable array `shared_uart_monitor_transaction_info(channel, instance)`.



*uart\_monitor.vhd*

## t\_uart\_monitor\_config

Record element	Type	C_UART_MONITOR_CONFIG_DEFAULT
scope_name	string	
msg_id_panel	t_msg_id_panel	C_UART_MONITOR_MSG_ID_PANEL_DEFAULT
interface_config	t_uart_interface_config	C_UART_MONITOR_INTERFACE_CONFIG_DEFAULT
transaction_display_time	time	0 ns

## t\_uart\_interface\_config

Record element	Type	Description
bit_time	time	The time used to transfer one bit.
num_data_bits	positive range 7 to 8	Number of data bits.
parity	t_parity	The parity used, PARITY_ODD or PARITY_EVEN.
num_stop_bits	t_stop_bits	Number of stop bits, STOP_BITS_ONE, STOP_BIT_ONE_AND_HALF or STOP_BITS_TWO.

## t\_base\_transaction

Record element	Type	Description
operation	t_operation	Operation on UART line, TRANSMIT, RECEIVE or NO_OPERATION.
data	std_logic_vector	UART data.
vvc_meta <sup>1</sup>	t_vvc_meta	Only used by VVC.
transaction_status	t_transaction_status	Status of transaction, SUCCEEDED, FAILED, INACTIVE or IN_PROGRESS.
error_info	t_error_info	Error information when failed transaction.

## t\_error\_info

Record element	Type	Description
parity_bit_error	boolean	True if parity error detected.
stop_bit_error	boolean	True if stop bit error detected.

<sup>1</sup>vvc\_meta only applies for the VVC

Record hierarchy of `'t_transaction_group'` – accessible via `shared_uart_monitor_transaction_info`

Record element	Type
→ bt	t_transaction
→ operation	t_operation
→ data	std_logic_vector
→ vvc_meta <sup>1</sup>	t_vvc_meta
→ transaction_status	t_transaction_status
→ error_info	t_error_info
→ parity_bit_error	boolean
→ stop_bit_error	boolean
→ ct	t_transaction

Message IDs for UART Monitor

Message ID	Description
ID_FRAME_INITIATE	Logs start of UART frame.
ID_MONITOR	Logs information about monitored transaction.



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## Monitor entity signals

Name	Type	Direction	Description
uart_dut_rx	std_logic	Input	Input of DUTs UART RX signal.
uart_dut_tx	std_logic	Input	Input of DUTs UART TX signal.

## Monitor entity generic constants

Name	Type	Default	Description
GC_INSTANCE_IDX	natural	1	Instance number to assign the monitor.
GC_MONITOR_CONFIG	t_uart_monitor_config	C_UART_MONITOR_CONFIG_DEFAULT	Configuration of the UART monitor, both channels get initiated with this configuration.

## 1 Use of monitor transaction info

All transaction information from the UART Monitor is located in the shared variable **shared\_uart\_monitor\_transaction\_info(channel, instance)**.

Name	Type	Example(s)	Description
global_uart_monitor_transaction_trigger(channel, instance_idx)	std_logic	'1'	Global trigger pulsed when UART Monitor transaction info is available.
shared_uart_monitor_transaction_info(channel, instance_idx)	t_uart_transaction_array	global_uart_monitor_transaction(RX, 1)	Shared variable containing all UART Monitor transaction information.
channel	t_channel	TX, RX	The interface channel of the monitor instance
instance_idx	natural	1,2, etc.	Instance number of the monitor

An example of use of the global\_uart\_monitor\_transaction and shared\_uart\_monitor\_transaction\_info is seen below. A process extracts the transaction info from the shared variable when the global signal is triggered.

```

p_monitor_tx : process
    variable v_transaction : t_uart_transaction;
begin
    wait until global_uart_monitor_transaction_trigger(TX, 1) = '1';

    if (shared_uart_monitor_transaction_info(TX, 1).bt.transaction_status = SUCCEEDED or
        shared_uart_monitor_transaction_info(TX, 1).bt.transaction_status = FAILED) then
        v_transaction := shared_uart_monitor_transaction_info(TX, 1).bt;
    end if;

    -- Processing received transaction
    ...

end process p_monitor_tx;

```

# Monitor details

## 2 Monitor Configuration

Record element	Type	C_UART_MONITOR_CONFIG_DEFAULT	Description
scope_name	string		A string describing the scope from which the log/alert originates.
msg_id_panel	t_msg_id_panel	C_UART_MONITOR_MSG_ID_PANEL_DEFAULT	The message id panel used by the monitor instance.
interface_config	t_uart_interface_config	C_UART_MONITOR_INTERFACE_CONFIG_DEFAULT	The configuration for the interface.
transaction_display_time	time	0 ns	After this amount of time operation is set to NO_OPERATION and transaction_status is set to INACTIVE if not a new transaction is received. If set to 0 ns operation and transaction_status will be unchanged until the next transfer is started.

The configuration record can be accessed from the Central Testbench Sequencer through the shared variable array, e.g.:

```
shared_uart_monitor_config(TX, 1).msg_id_panel := new_msg_id_panel;
shared_uart_monitor_config(TX, 1).interface_config.num_data_bits := 8;
```

### 3 Additional Documentation

Additional documentation about UVVM and its features can be found under “/uvvm\_vvc\_framework/doc/”.  
For additional documentation on the UART protocol, please see the UART specification.

### 4 Compilation

The UART Monitor must be compiled with VHDL 2008.

It is dependent on the following libraries

- **UVVM Utility Library (UVVM-Util), version 2.13.0 and up**
- **UVVM VVC Framework, version 2.8.0 and up**
- **UART BFM**

Before compiling the UART Monitor, make sure that uvvm\_vvc\_framework and uvvm\_util have been compiled.

See UVVM Essential Mechanisms located in uvvm\_vvc\_framework/doc for information about compile scripts.

#### Compile order for the UART Monitor:

Compile to library	File	Comment
bitvis_vip_uart	transaction_pkg.vhd	UART transaction types
bitvis_vip_uart	uart_bfm_pkg.vhd	UART BFM
bitvis_vip_uart	vvc_cmd_pkg.vhd	UART VVC command types and operations
bitvis_vip_uart	monitor_cmd_pkg.vhd	UART Monitor command types and operations
bitvis_vip_uart	../uvvm_vvc_framework/src_target_dependent/td_target_support_pkg.vhd	UVVM VVC target support package, compiled into the UART VIP library.
bitvis_vip_uart	vvc_methods_pkg.vhd	UART VVC methods
bitvis_vip_uart	uart_monitor.vhd	UART Monitor

### 5 Simulator compatibility and setup

This Monitor has been compiled and tested with Modelsim version 10.5b and Riviera-PRO version 2015.10.85.

For required simulator setup see **UVVM-Util** Quick reference.

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