

# Aristotle's University of Thessaloniki Polytechnic facility Electrical and Computer Engineering Electronics and Computers Section

## Parallel and Distributed Systems First Project

Project of Isidoros Tsaousis-Seiras AEM: 10042

Code for this project can be found here

## Parallel and Distributed Systems First Project

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#### **Intro**

This report presents the results of a study on the performance of different implementations of the SCC algorithm as described in [1], in three different threading environments: openCilk, openMP, and pThread. The study was conducted to compare the of this algorithm in multi-threaded environments.

In short, he algorithm works by assigning a unique color to each vertex in the graph, then updating in parallel the colors of each vertex based on the colors of its neighboring vertices. Once all the vertices have reached a stable coloring, the algorithm performs a parallel breadth-first search on each set of vertices with the same color to find the strongly connected components. The algorithm then removes these strongly connected components from the graph and repeats the process until the graph is empty. The algorithm works because the propagation of the colors happens in reverse of the BFS, and thus intersecting sets of the two are SCCs.

A graph is represented by its adjacency matrix. This way vertices are represented by their 'id'  $\in \mathbb{N}$  and edges are represented by the contents of the matrix:

$$A_{i,j} = 1$$
 if vertex  $i \rightarrow j$   
= 0 otherwise (1)

| Times(ms)        | OpenMP | OpenCilk | pThread | serial |
|------------------|--------|----------|---------|--------|
| celegansneural   | 0,14   | 0,28     | 0,21    | 0,03   |
| foldoc           | 0,95   | 1,51     | 2,47    | 2,08   |
| language         | 22,61  | 38,42    | 33,80   | 71,57  |
| eu-2005          | 175,93 | 182,80   | 254,06  | 390,66 |
| Times(s)         | OpenMP | OpenCilk | pThread | serial |
| wiki-topcats     | 1,45   | 1,31     | 2,09    | 4,56   |
| sx-stackoverflow | 0,77   | 0,72     | 1,02    | 1,14   |
| wb-edu           | 17,43  | 16,66    | 18,17   | 48,03  |
| indochina-2004   | 11,18  | 9,97     | 13,61   | 20,11  |
| uk-2002          | 16,60  | 15,67    | 17,65   | 40,67  |
| arabic-2005      | 12,16  | 12,08    | 15,04   | 30,46  |
| uk-2005          | 71,31  | 69,87    | 80,80   | 146,05 |
| twitter7         | 100,58 | 101,62   | 126,71  | 208,89 |

Figure 1: Times of the algorithm on the experimental setup, 12 threads. Times presented are in Seconds and Microseconds

## Chapter 1

### Part 1

#### 1.1 C++ Implementation

#### 1.1.1 Graph Representation

Due to the small amount of neighbors of each node, the matrix is extremely sparse. So we used a Compressed Sparse Column formatted matrix (CSC) to store it. This was implemented as the following struct:

```
struct Sparse_matrix {
    size_t n;
    size_t nnz;
    std::vector<size_t> ptr;
    std::vector<size_t> val;

enum CSC_CSR {CSC, CSR};
    CSC_CSR type;
};
```

This resulted in very easy and fast access to the neighbors of each vertex: the neighbors that point towards a vertex j are the rows with non-zero elements in column j, which is just val[ptr[j]], val[ptr[j]+1],..., val[ptr[j+1]].

If we compress the matrix according to its rows instead of its columns we get a CSR matrix, which we can use in the same way to get the neighbors that j points towards.

Using both of those methods means we have an O(1) way of finding both incoming and outgoing neighbors, that is very space efficient and since their ids are stored contiguously, we also minimize cache misses.

In practice, because only one Compressed Sparse Matrix could fit in the experiment's setup memory at a time, the algorithm was tweaked to only require one of them to function properly.

#### 1.1.2 SCC representation

Each vertex belongs to some SCC, so each vertex was given a variable <code>scc\_id</code> that recorded which SCC it belonged to. The id of an SCC is the id of the smallest vertex it contains.

#### 1.1.3 Trimming

A substantial part of the vertices in most graphs form trivial SCCs by themselves. We can detect such vertices by counting their incoming and outgoing neighbors and removing them if they have none. While this creates more potential trivial vertices, in practice one non-recursive run of this algorithm gives the best performance. In this implementation a non-recursive version was used.

Because outgoing neighbors sets are not readily available, a list of the vertices with no outgoing neighbors can be constructed by keeping track of the vertices that do not appear as any vertex's incoming neighbor. Then any vertex that either is in that list or is found to have no incoming neighbors is discarded. A check is required to determine if some neighbor has been removed or not.

Handling the special case of size one SCCs this way leads to a dramatic speedup both in the serial and the parallel implementation.

The time complexity of this step regardless of which of the versions is used is  $O(vertices\_left)$ , and what makes this improve the algorithm is that those vertices will not affect the coloring or propagation stages at all.

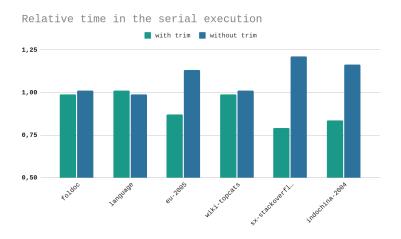


Figure 1.1: Execution time relative to the average between the two versions.

#### 1.1.4 Color Propagation

A vector of all vertices not yet added to some SCC, named <code>vleft</code> is kept in order to reduce lookups. In the start of each "coloring round" the algorithm goes through all vertices left, so by using <code>vleft</code> we avoid going through all vertices in every iteration. The performance gain of this however is questionable, since the overhead of removing entries might surpass the gains.

#### 1.1.5 BFS

After colors are stable, a BFS is initiated in every color group, starting from the vertex that gave the group its color. In the beginning this was implemented as a matrix multiplication as per [2], but this was later scrapped due to performance reasons, and instead BFS was implemented using a standard queue method. The assignment of  $scc_ids$  is handled inside the BFS function so that the value of  $scc_ids$  can serve as an indicator of removed/visited vertices, as well as avoiding the iteration that would be necessary for the assignment of the  $scc_ids$  was it to simply return the ids of visited vertices.

#### 1.1.6 Experimental Setup and Correctness testing

All tests were run on an AMD ${\mathbb R}$  Ryzen 5 5600h processor with 12 cores. The amount of unique  $scc\_ids$  in the returned list was compared with the known amount to verify the correctness of the algorithm.

#### 1.1.7 Serial Times

| Times(ms)        | serial |
|------------------|--------|
| celegansneural   | 0,03   |
| foldoc           | 2,08   |
| language         | 71,57  |
| eu-2005          | 390,66 |
| Times(s)         | serial |
| wiki-topcats     | 4,56   |
| sx-stackoverflow | 1,14   |
| wb-edu           | 48,03  |
| indochina-2004   | 20,11  |
| uk-2002          | 40,67  |
| arabic-2005      | 30,46  |
| uk-2005          | 146,05 |
| twitter7         | 208,89 |

Figure 1.2: Execution time for the various datasets in the sequential algorithm

#### 1.2 Parallelization

All three major subroutines (Trimming, Color Propagation, BFS) are parallelizable.

#### **Trimming**

Parallelizing this statistically leads to the same number of operations as the serial version, since all that changes is the examination order of the vertices, which was arbitrary to begin with. This however becomes non-deterministic, which leads to much harder debugging and harder to measure performance. That being said, simply changing the variables to std::atomic takes care of race conditions, and allows a trivial parallelization.

#### **Color Propagation**

The same is true for the colors, however due to some overwriting happening a 20-30% increase in loops was observed, which was counterbalanced by the speedup.

#### **BFS**

This part is embarrassingly parallel, as by design it acts on mutually exclusive sections of the graph for every iteration. The clean linear speed up this offered was apparent in the BFS-heavy dataset wiki-topcats

#### 1.2.1 OpenMP

Simple # pragma omp parallel for clauses where used. This was the easiest multithreading environment to integrate, and required the least amount of changes to function properly.

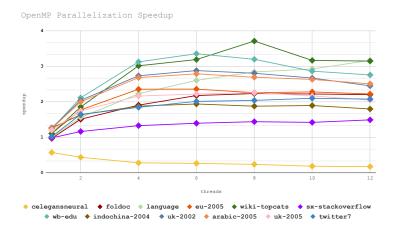


Figure 1.3: Speedup with OpenMP compared to the serial algorithm

#### 1.2.2 OpenCilk

Harder to integrate than OMP due to a lack of documentation. However substituting <code>cilk\_for</code> in the appropriate places worked. A weird bug that poped up was that a 'Flag' variable, that would only be accessed to write <code>true</code> into, sometimes kept a wrong value at the end of the loop. After investigation it was discovered that this is due to the way OpenCilk handles looping. Changing the type of the flag to atomic worked here.



Figure 1.4: Speedup with OpenCilk compared to the serial algorithm

#### 1.2.3 pThread

This was the hardest and messiest version to implement, perhaps due to a lack of time and planning. The entirety of the functions needed to be specially interfaced using intermediate functions to support multithreading. All loops needed to be scheduled in advance, and while this offered great flexibility it did not prove useful compared to the other versions performance-wise. The difficulty in implementation lead to messy code that was very hard to optimize.

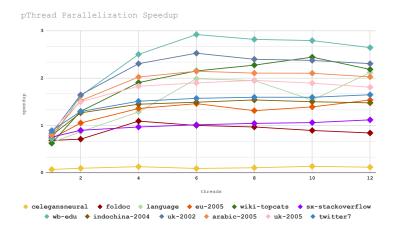


Figure 1.5: Speedup with pThread compared to the serial algorithm

#### 1.3 Conclusion

OpenCilk gave the best performance overall, and was easy enough to implement, however the combination of it's lack of documentation and non-intuitive design created a bug that was quite dificult to figure out. OpenMP was the best "out of the box" solution. Pthread required much longer than the other two to implement, and it's lack of abstraction left all scheduling on the programmer, which did not do a very good job compared to OpenMP and OpenCilk schedulers. The times overall where satisfactory, as I was able to run all datasets, with the largest (twitter7), at a little over 1.5 minutes in the fastest parallel programm.

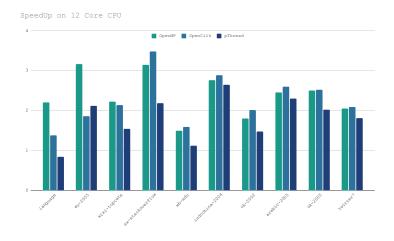


Figure 1.6: 12 Core Speedup using the different implementations

# **Bibliography**

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