(1) Given a 2-qubit state
$$|\Psi\rangle = \frac{1}{\sqrt{2}}|00\rangle + \frac{1}{\sqrt{2}}|11\rangle$$
, show that it is not possible to find $|\Psi_1\rangle \& |\Psi_2\rangle$ such that $|\Psi\rangle = |\Psi_1\rangle \otimes |\Psi_2\rangle$

Ang: let if possible
$$|\Psi_1\rangle = \alpha_1 |0\rangle + \beta_1 |1\rangle$$
 & $|\Psi_2\rangle = \alpha_2 |0\rangle + \beta_2 |1\rangle$ such that $|\Psi_1\rangle = |\Psi_1\rangle \otimes |\Psi_2\rangle$ for some α_1,α_2 , $\beta_1,\beta_2 \in \mathcal{L}$ & $|\alpha_1|^2 + |\beta_1|^2 = |\alpha_2|^2 + |\beta_2|^2 = 1$ $|\Psi_1\rangle \otimes |\Psi_2\rangle$

$$= [\alpha_{1} | 0\rangle + \beta_{1} | 1\rangle) \otimes [\alpha_{2} | 0\rangle + \beta_{2} | 1\rangle]$$

$$= [\alpha_{1} | 0\rangle + \beta_{1} | 1\rangle) \otimes [\alpha_{2} | 0\rangle \otimes | 1\rangle) + \beta_{1} \alpha_{2} (| 1\rangle \otimes | 0\rangle)$$

$$= \alpha_{1} \alpha_{2} (| 0\rangle \otimes | 0\rangle) + \alpha_{1} \beta_{2} (| 0\rangle \otimes | 1\rangle) + \beta_{1} \beta_{2} (| 1\rangle \otimes | 1\rangle)$$

$$+ \beta_{1} \beta_{2} (| 1\rangle \otimes | 1\rangle)$$

)
$$\alpha_{1}\alpha_{2}=\sqrt{2}$$
, $\alpha_{1}\beta_{2}=0$, $\beta_{1}\alpha_{2}=0$, $\beta_{1}\beta_{2}=\sqrt{2}$

Now,
$$|\alpha_1\alpha_2|^2 + |\alpha_1\beta_2|^2 = \frac{1}{2} + 0$$

=)
$$|\alpha_1|^2 |\alpha_2|^2 + |\alpha_1|^2 |\beta_2|^2 = \frac{1}{2}$$

$$= |\alpha_1|^2 = \frac{1}{2} \left[\frac{1}{2} |\alpha_2|^2 + |\beta_2|^2 = 1 \right]$$

Similarly,
$$|B_1 \alpha_2|^2 + |B_1 B_2|^2 = \frac{1}{2}$$

 $\Rightarrow |B_1|^2 = \frac{1}{2} \left[\frac{1}{3} \cdot |\alpha_2|^2 + |B_1|^2 = 1 \right]$

=)
$$|\alpha_1|^2 |\beta_2|^2 = 0$$

$$=\frac{1}{2} \left| \beta_2 \right|^2 = 0 \quad \left[-\frac{1}{2} \left| \alpha_1 \right|^2 = \frac{1}{2} \right]$$

=)
$$\frac{1}{2} |\alpha_2|^2 = 0$$
 [-: $|\beta_1|^2 = \frac{1}{2}$]

$$=$$
 $|x_2|^2 = 0$

Thus $|\alpha_2|^2 + |\beta_2|^2 = 0 + 0 = 0$

which is a contradiction to our assumption

Mence not possible.

Find the matrix representation of CCNOT gate CSWAP goute. CCNOT Wates If the 1st & 2nd bits are 1, then only the 3rd bit will be flipped So, we have 1000> -> 1000> 1001> ->1001> 1010> -> 1010> 1011> -> 1011> 1100> -> 1100> 1101> -> 1101> 1110> > 111) 1111/ ->1110/

CSWAP Gate: