Assignment - 1

Reth Oct, 2022

1) Mathe of CC NOT gate

Sol": CC NOT gate has 3 bit input and output!

If the first two bits are 1, then it flips the third bit, else all the bits stay the same

1000> -> 1000> 1001> -> 1000> 1010> -> 1010> 1011> -> 1011> (100>H) (100> (101>H) (101> (111>H) (110>

Matrix CENOT =

2) Matrix of CSWAP gate

CSWAY does controlled swap.

If the first bit is 1, then it swaps

Second and third bit. Else, everything stays

the same.

 $\begin{array}{c|c}
1000 \rangle \longmapsto |000\rangle \\
1001 \rangle \longmapsto |000\rangle \\
1010 \rangle \longmapsto |010\rangle \\
1011 \rangle \longmapsto |010\rangle \\
1111 \rangle \longmapsto |1111\rangle
\end{array}$ 

Sol": We have to besitedly show that given
$$|\psi\rangle = \frac{1}{12}|00\rangle + \frac{1}{12}|01\rangle$$
We can't have  $|\psi\rangle > |\psi\rangle > |\psi\rangle > |\psi\rangle = |\psi\rangle |01\rangle$ 
We can't have  $|\psi\rangle > |\psi\rangle > |01\rangle > |01\rangle$ 
Then  $|\psi\rangle > |\omega\rangle + |\omega\rangle +$ 

We can't have 
$$(V_1)$$
,  $(V_2)$  S.t.  $(V_2) = |V_1| \otimes det$  us assume such  $(V_1)$ ,  $(V_2) = exist$ .  
Then  $(V_1) = \alpha'$ ,  $(0) + \beta_1 |1\rangle$   $|\alpha_1|^2 + |\beta_1|^2 = |V_2|^2 + |\alpha_2|^2 +$ 

$$|V_{1}\rangle \otimes |V_{2}\rangle = |X_{1}X_{2}| |00\rangle + |X_{1}B_{1}| |01\rangle + |X_{2}B_{1}| |10\rangle + |B_{1}B_{2}| |11\rangle + |X_{2}B_{1}| |10\rangle + |B_{1}B_{2}| |11\rangle$$

$$|X_{1}X_{2}| = |X_{1}| |X_{1}B_{2}| = |X_{1}| |X_{2}| = |X_{2}B_{1}| = 0$$

$$|X_{1}X_{2}| = |X_{2}| |X_{1}B_{2}| = |X_{1}| |X_{2}| = |X_{2}B_{1}| = 0$$

$$|X_{1}X_{2}| = |X_{2}| |X_{1}B_{2}| = |X_{1}| |X_{2}| = |X_{2}| = 0$$

$$|X_{1}X_{2}| = |X_{1}| |X_{2}| = |X_{1}| |X_{2}| = |X_{1}| = 0$$

Shutarly => | d1 d2 12+ | d1 | B2) = 1 +0 = 12 -> |x112 (|x2)2 + |\beta2|2) = = |x12=1 >> | x1 B2 | = 0 => |B2 | = 0 We get | \d'212 + |\beta21 = 0+0 =0 4 Contradiction : It's not possible to find such d2, \$2, d, \$4 : (4) is enbangled