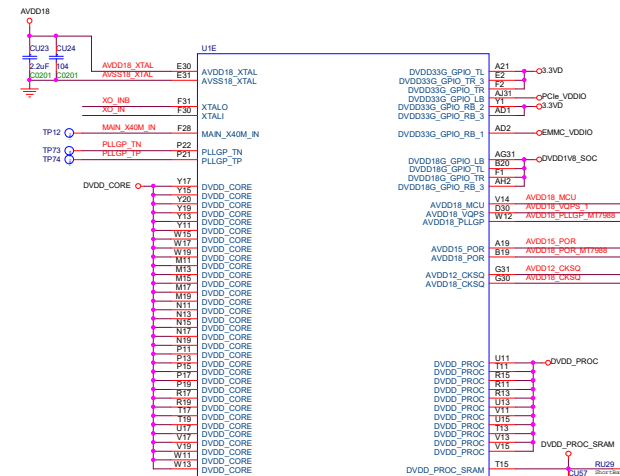


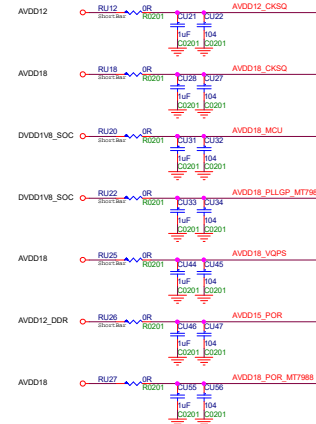
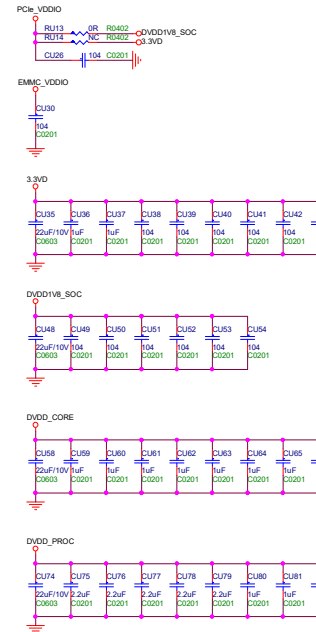
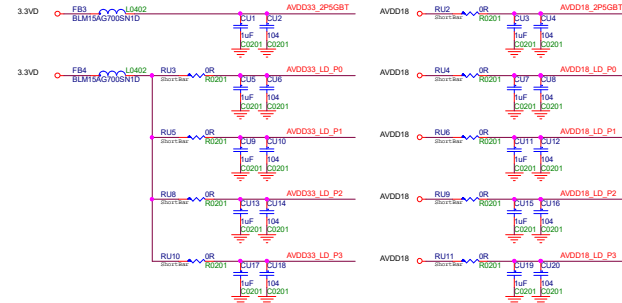
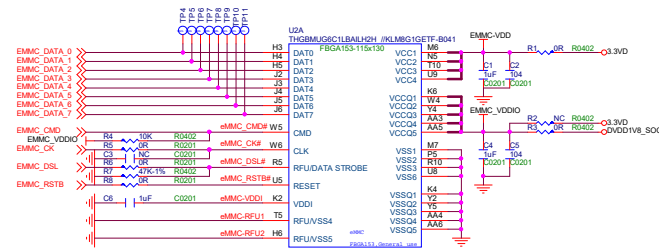
3.3V0
AVDD18
DVID18_S0C
DVID0_CORE
DVID0
DVID0_CORE_FB
DVID0_CORE_GND_FB
DVID0_PROC_FB

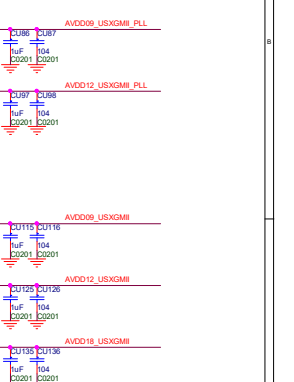
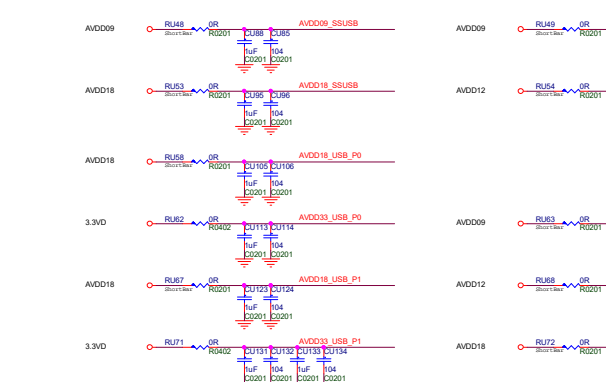
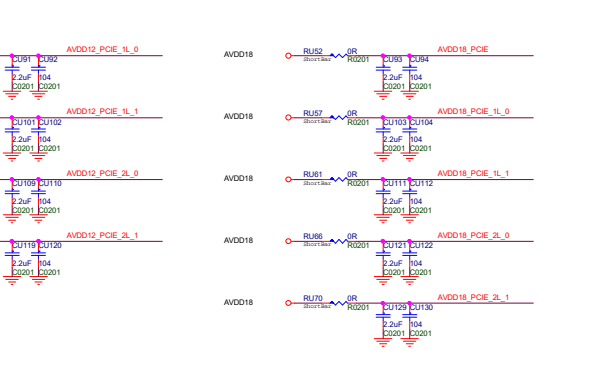
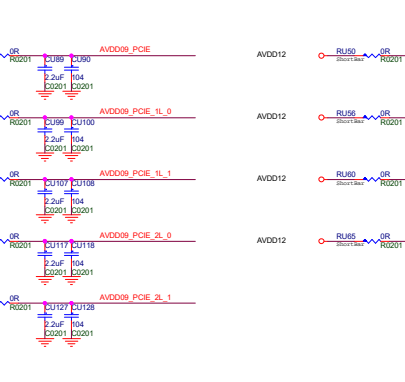
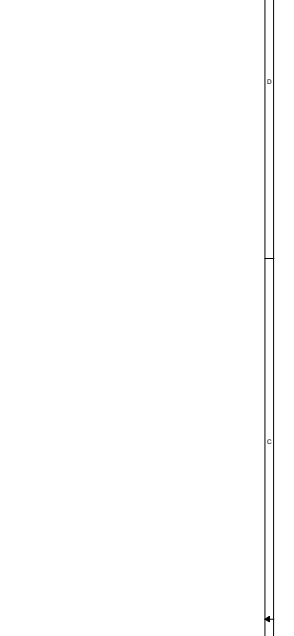
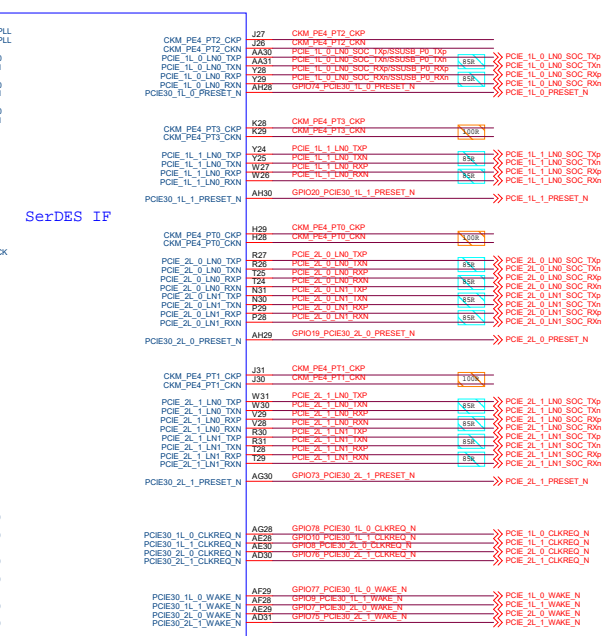


MT7988
MT7988_HFCMP819_P065_21X21

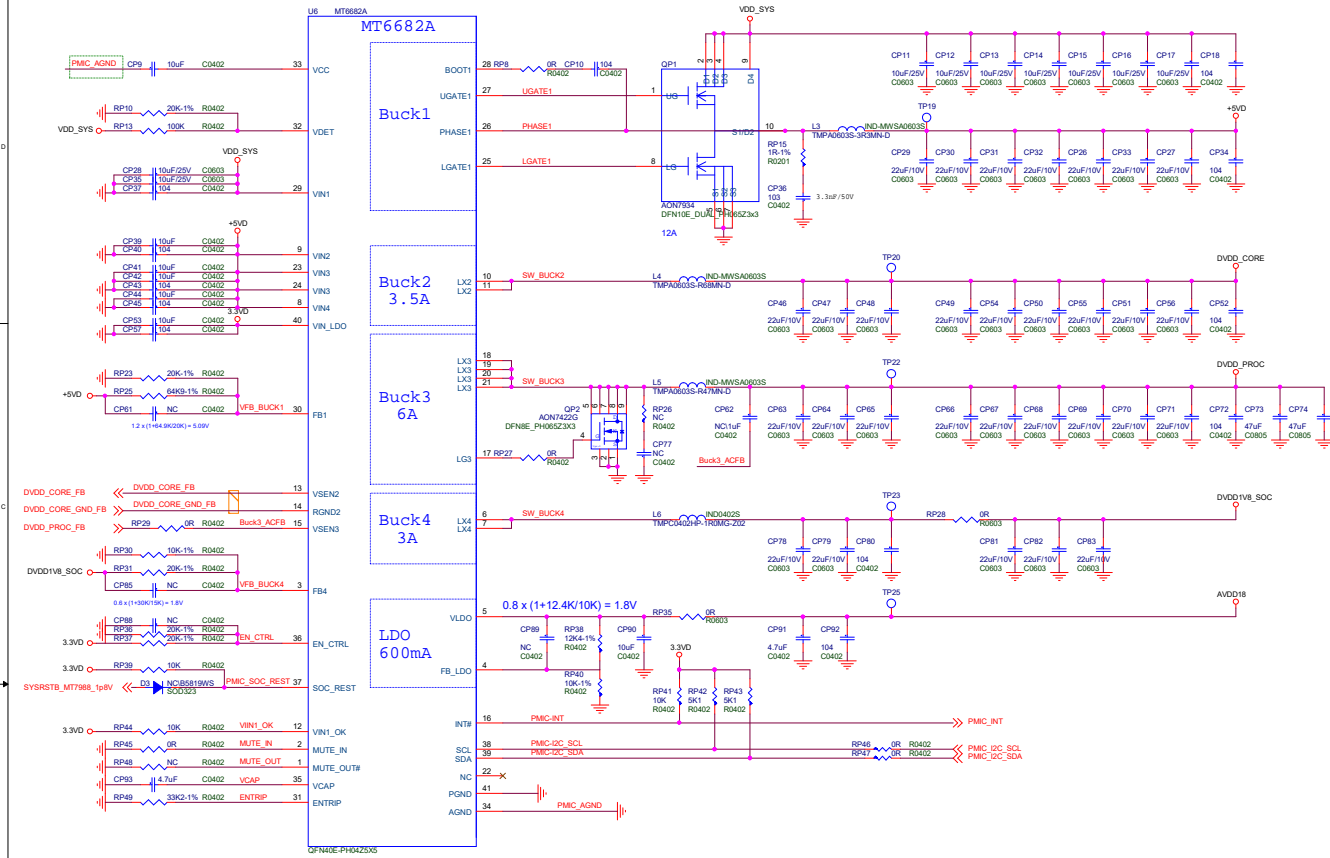


MT7988
MT7988_HFCMP819_P065_21X21

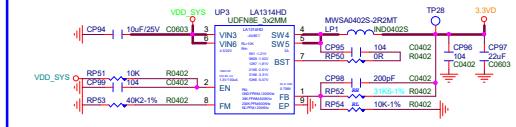




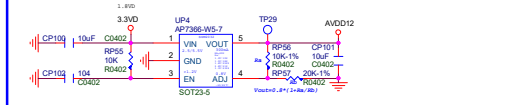
PMIC



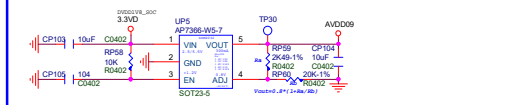
3.3V/3A



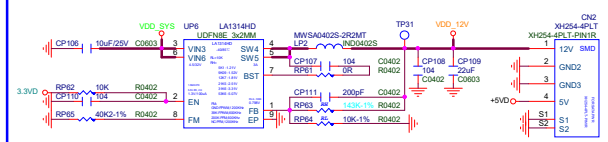
1.2V/0.6A



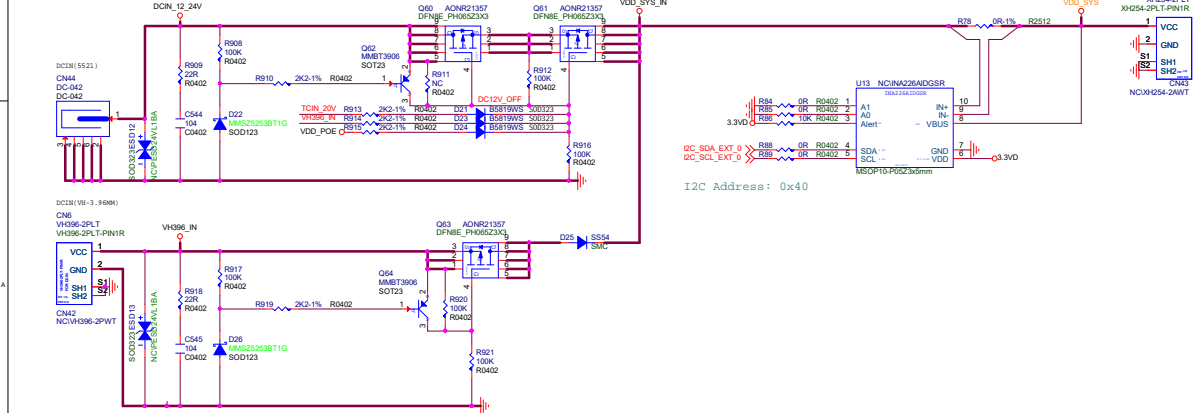
0.9V/0.6A



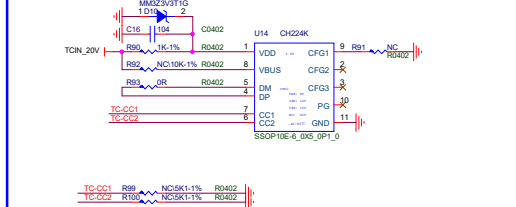
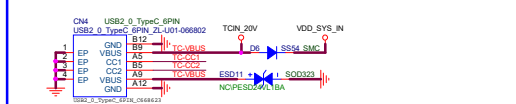
DCOUT 12V/5V



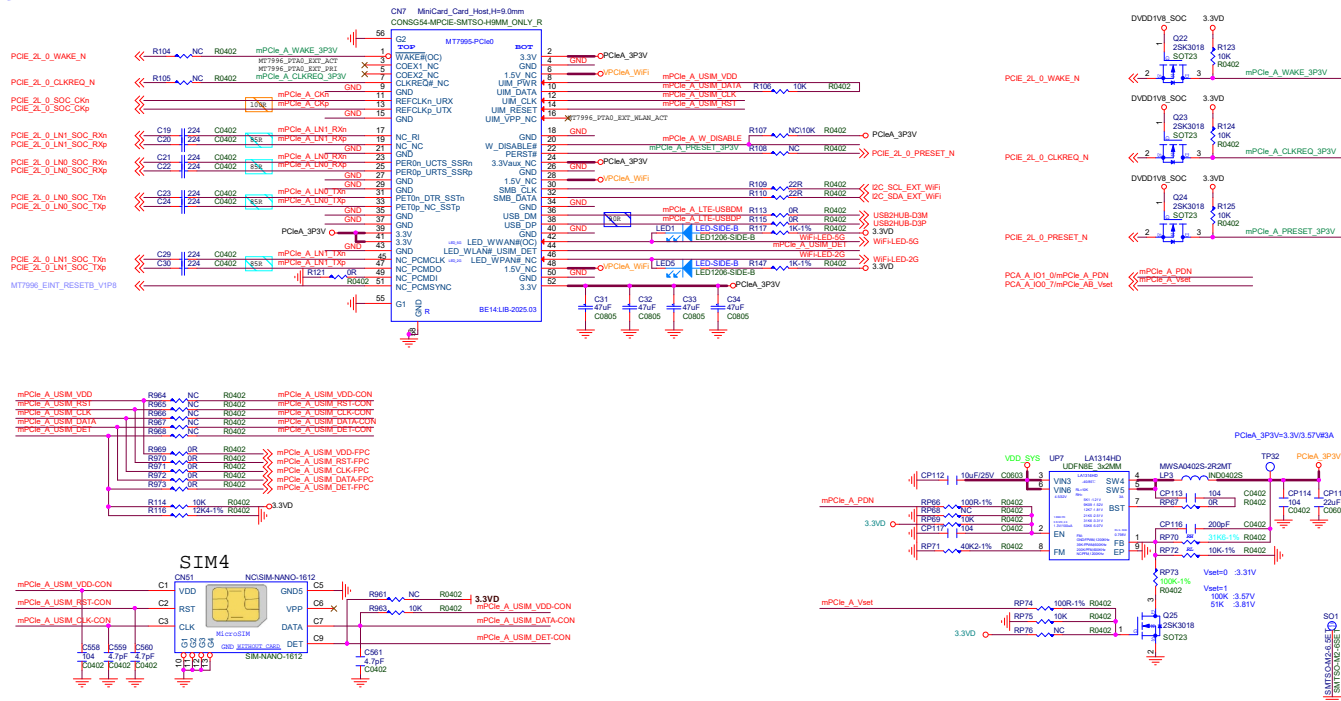
DCIN: 12~24V



TypeC PD

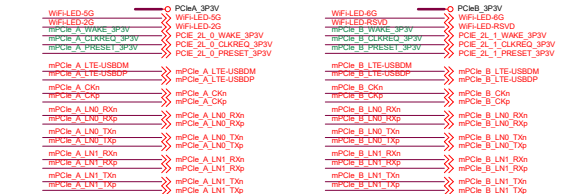
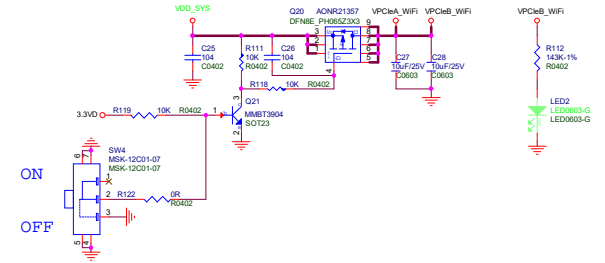


mPCIeA

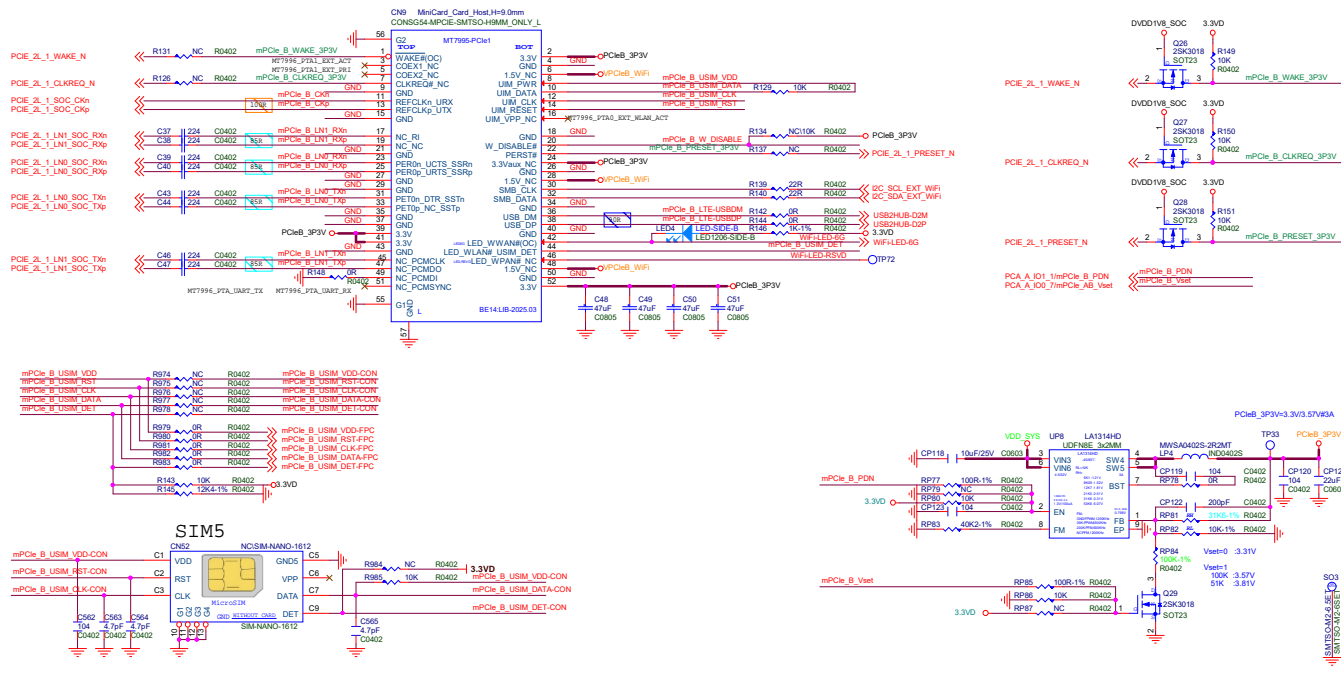


mPCIe Slot 12V(ONLY FOR BPI Wi-Fi NIC)

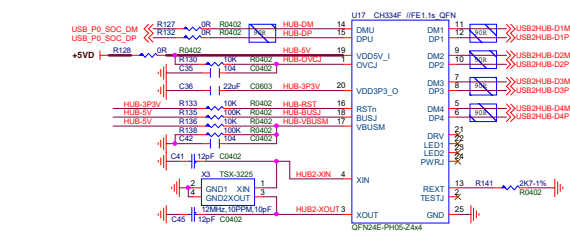
Confirm the PIN6, PIN28, PIN48 of the miniPCle module,
If it is not NC, DO NOT install and powered on! ! !



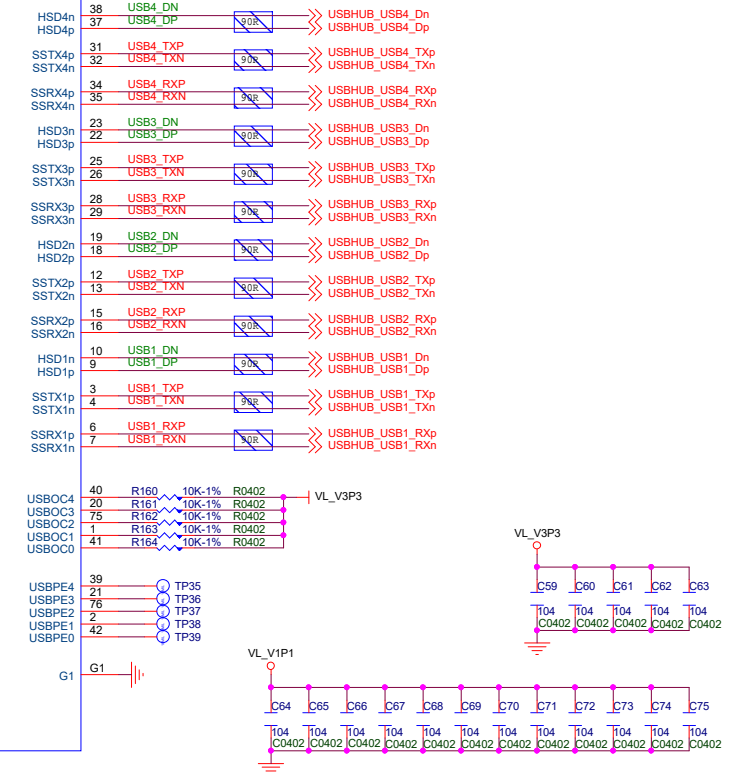
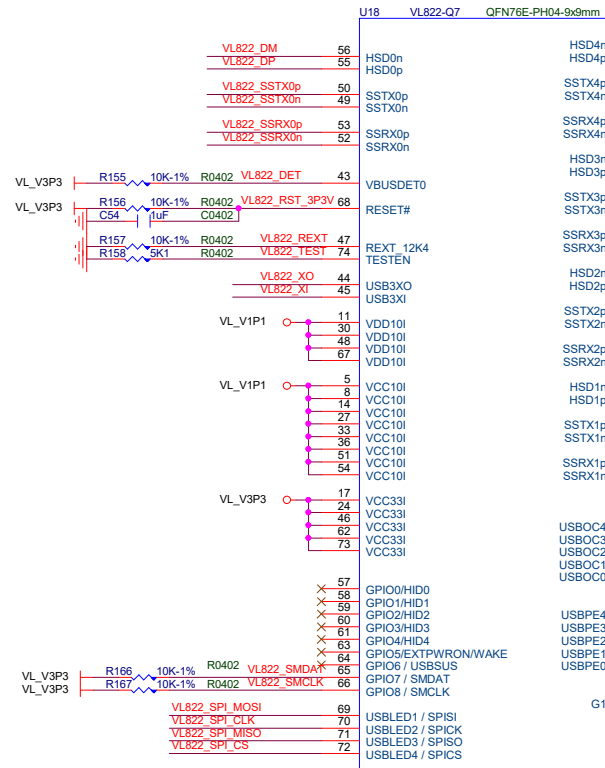
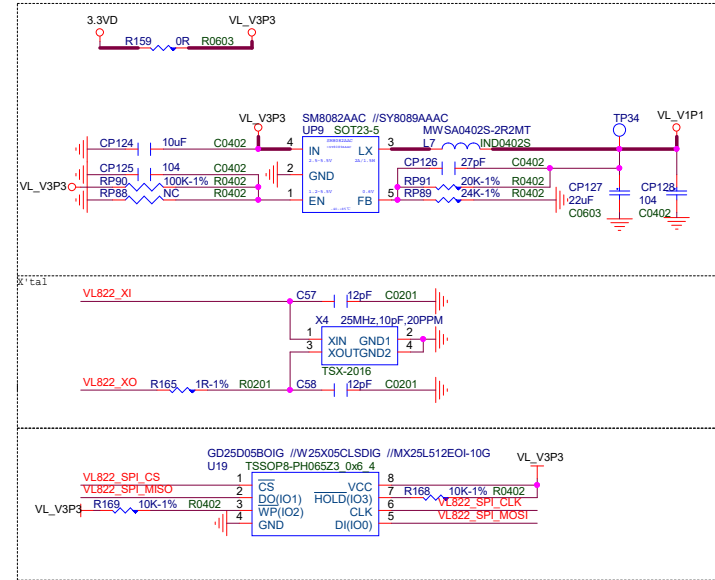
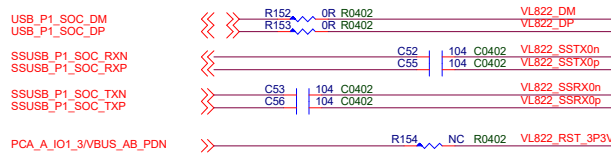
mPCIeB



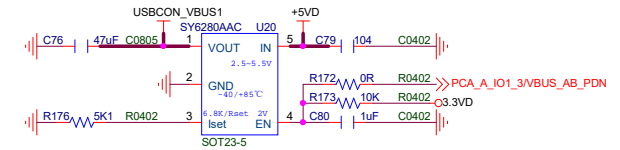
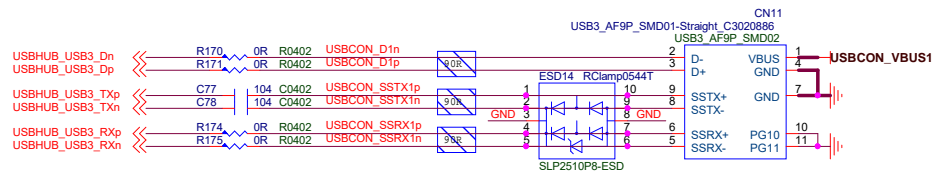
USB2.0 HUB



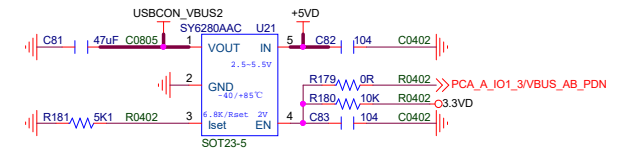
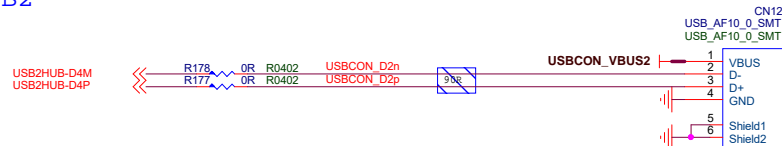
USB3.2 HUB



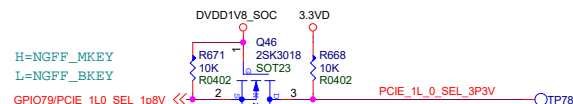
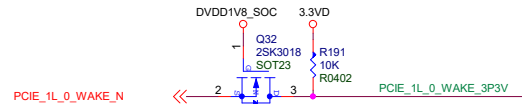
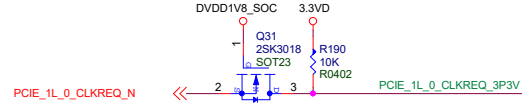
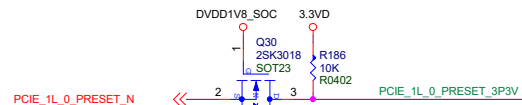
USB3



USB2



DVDD1V8_SOC NGFF_MKEY-A_1P8V
3.3VD NGFF_KEYM-A_3P3V
VDD_SYS

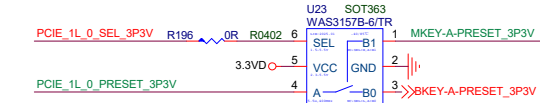


PCIE_1L_0_LN0_SOC_RxN
PCIE_1L_0_LN0_SOC_RxP

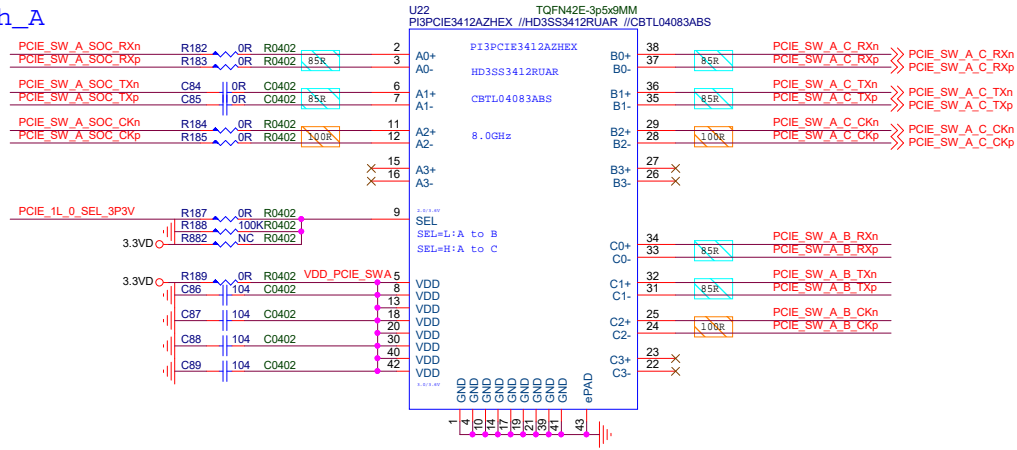
PCIE_1L_0_LN0_SOC_TxN
PCIE_1L_0_LN0_SOC_TxP

PCIE_1L_0_SOC_CkN
PCIE_1L_0_SOC_CkP

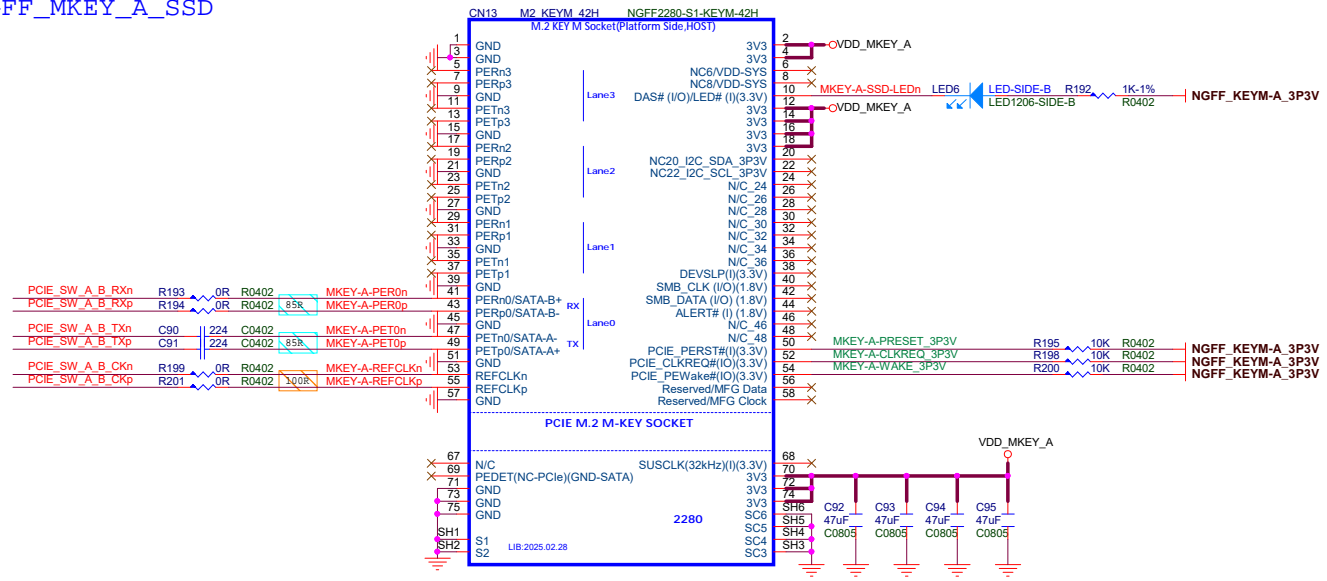
PCA_A_I01_5/BKEY-C-RST
MKEY-A-SSD-LEDn



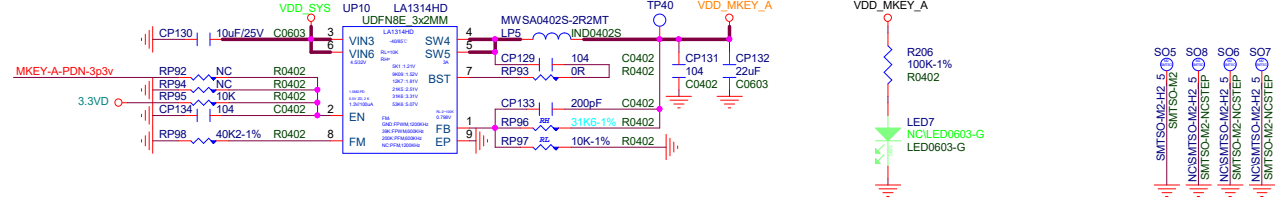
PCIE Switch_A



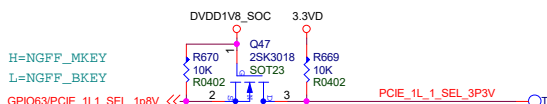
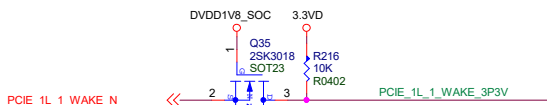
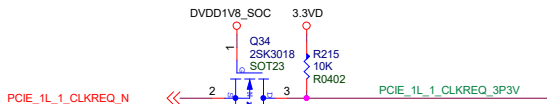
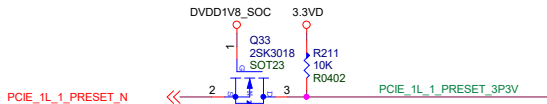
NGFF_MKEY_A_SSD



VDD_MKEY_A=3.3V#3A

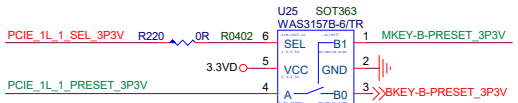


DVDD1V8_SOC NGFF_MKEY-B_1P8V
3.3VD NGFF_KEYM-B_3P3V
VDD_SYS

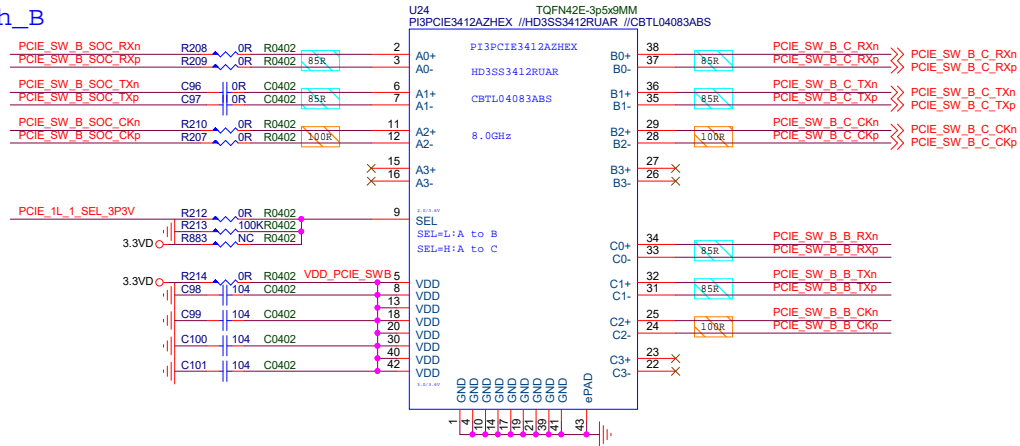


PCIE_1L_1_LN0_SOC_RXn << PCIE_SW_B SOC RXn
PCIE_1L_1_LN0_SOC_RXp << PCIE_SW_B SOC RXp
PCIE_1L_1_LN0_SOC_TXn << PCIE_SW_B SOC TXn
PCIE_1L_1_LN0_SOC_TXp << PCIE_SW_B SOC TXp
PCIE_1L_1_SOC_CkN << PCIE_SW_B SOC CkN
PCIE_1L_1_SOC_CkP << PCIE_SW_B SOC CkP

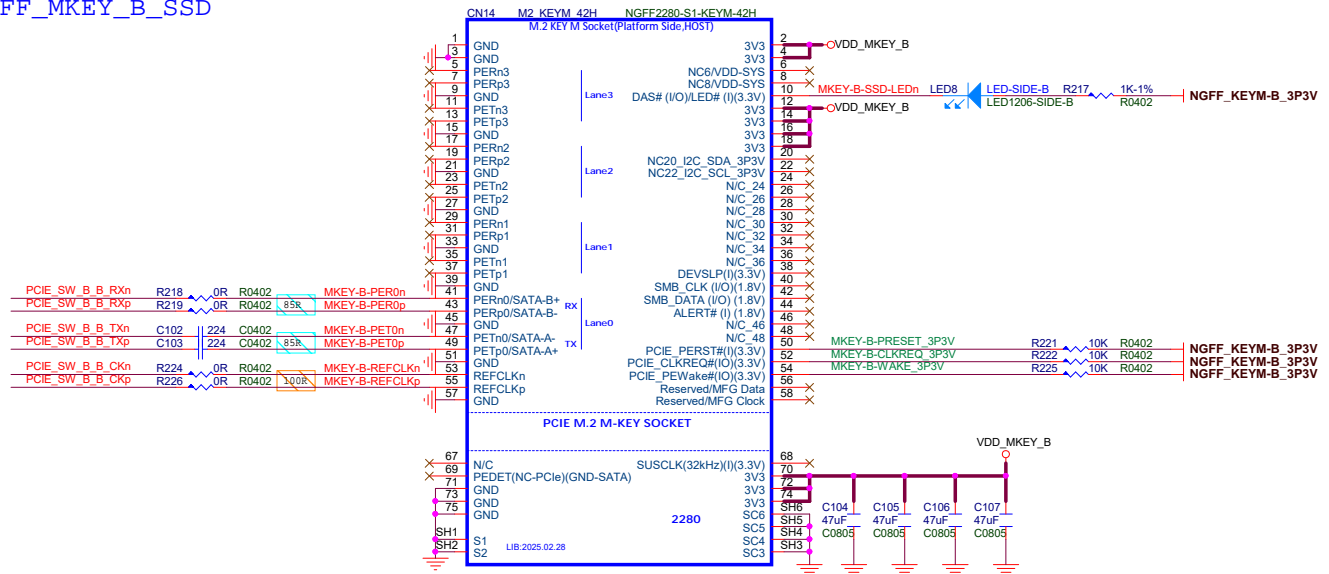
PCA_A_IO1_5/BKEY-C-RST << MKEY-B-PDN-3p3v
MKEY-B-SSD-LEDn << MKEY-B-SSD-LEDn



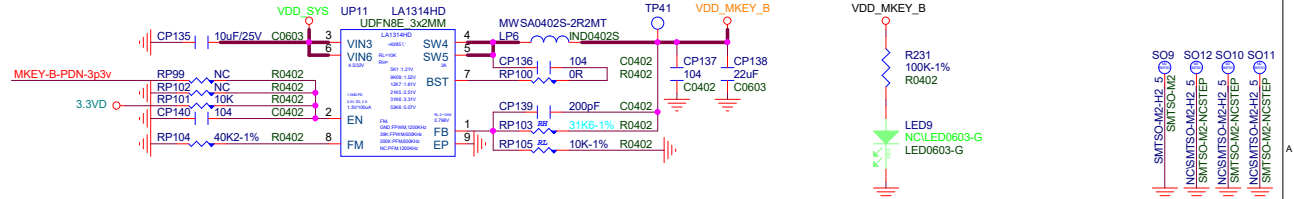
PCIE Switch_B



NGFF_MKEY_B_SSD



VDD_MKEY_B=3.3V#3A



NGFF_BKEY_A

DVDD1V8_SOC NGFF_BKEY-A_1P8V
3.3V NGFF_BKEY-A_3P3V
VDD_SYS

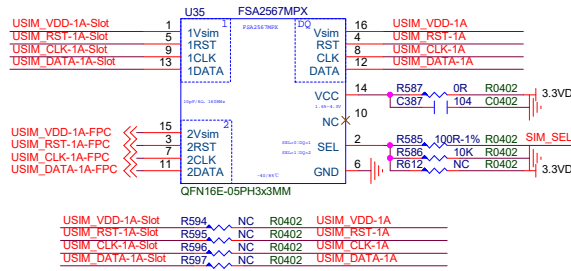
PCA_A_I00_0/BKEY-A-PDN
PCA_A_I00_1/BKEY-A-Vset
BKEY-A-NET-STUn
PCA_A_I01_3/BKEY-A-RST

USBHUB_USB1_Dp
USBHUB_USB1_Dn
USBHUB_USB1_RxN
USBHUB_USB1_RxP
USBHUB_USB1_TxN
USBHUB_USB1_TxP

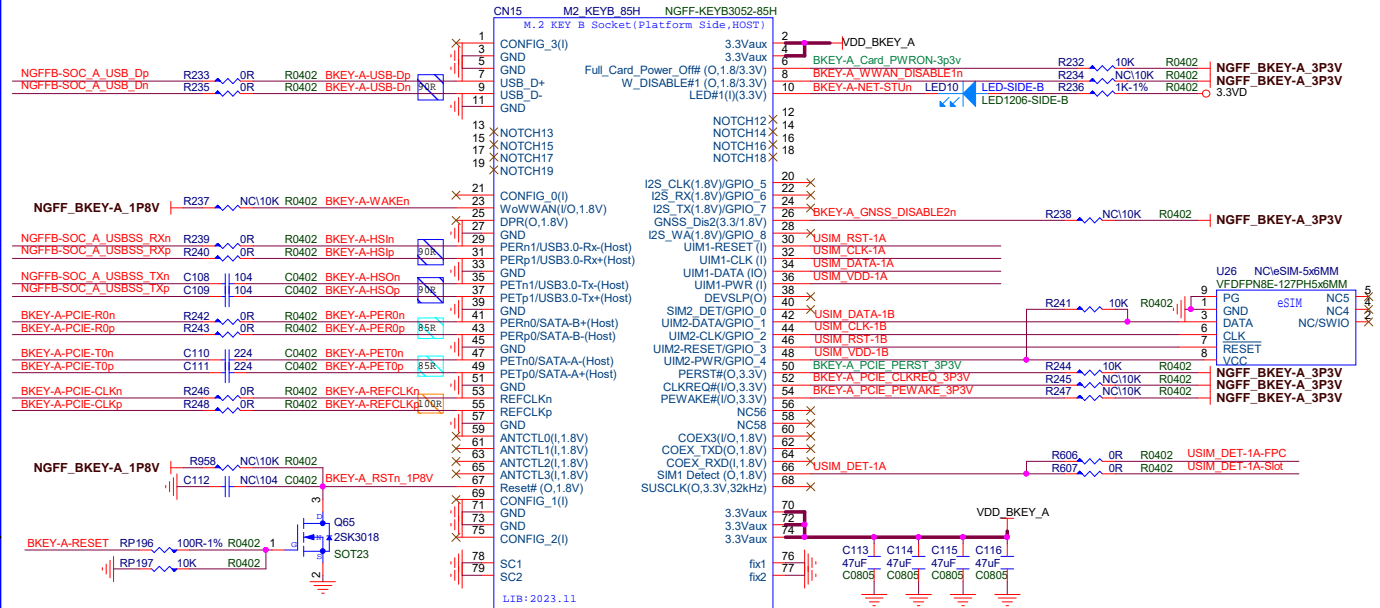
PCIE_SW_A_C_RxN
PCIE_SW_A_C_RxP
PCIE_SW_A_C_TxN
PCIE_SW_A_C_TxP
PCIE_SW_A_C_CkN
PCIE_SW_A_C_CkP

BKEY-A-PRESET_3P3V
BKEY-A-CLKREQ_3P3V
BKEY-A-WAKE_3P3V

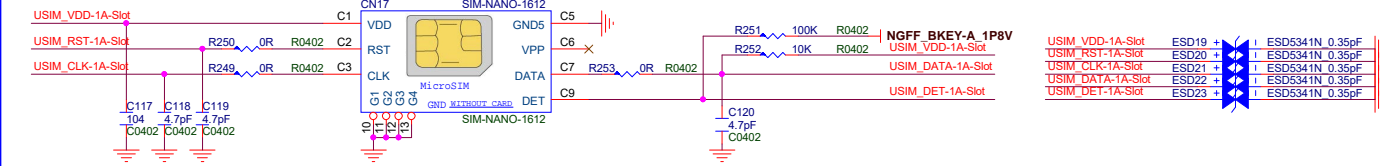
USIM_DET-1A-FPC
PCA_A_I00_6/SIM_SEL



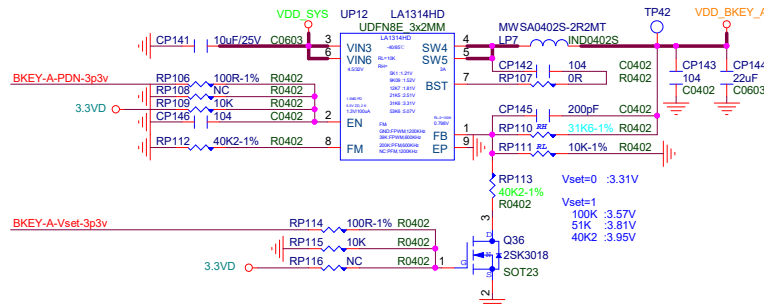
SIM1



SIM1



VDD_BKEY_A=3.3V/3.95V#3A



NGFF_BKEY_B

DVDD1V8_SOC NGFF_BKEY-B_1P8V
3.3VD NGFF_BKEY-B_3P3V
VDD_SYS

PCA_A_I00_2/BKEY-B-PDN
PCA_A_I00_3/BKEY-B-Vset
BKEY-B-NET-STUn
PCA_A_I01_4/BKEY-B-RST

USBHUB_USB2_Dp
USBHUB_USB2_Dn
USBHUB_USB2_RXn
USBHUB_USB2_RXp
USBHUB_USB2_TXn
USBHUB_USB2_TXp

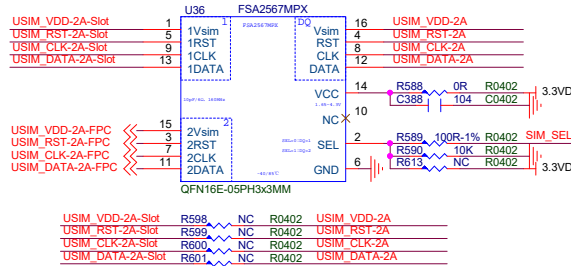
PCIE_SW_B_C_RXn
PCIE_SW_B_C_RXp

PCIE_SW_B_C_TXn
PCIE_SW_B_C_TXp

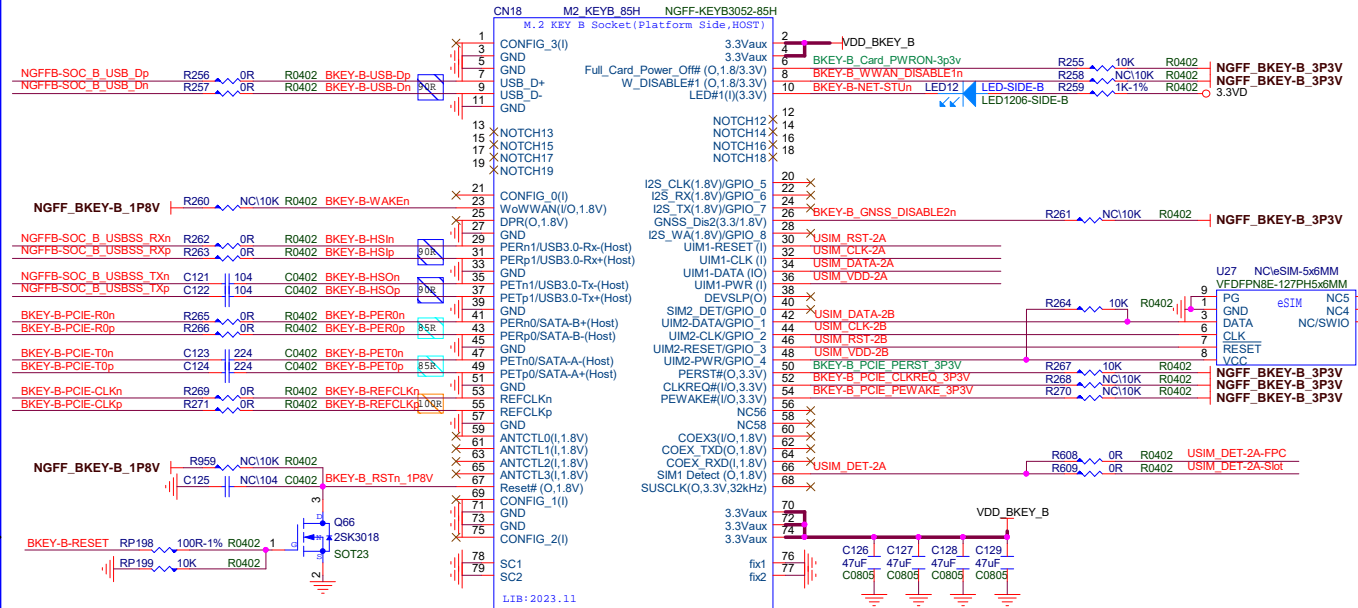
PCIE_SW_B_C_CKn
PCIE_SW_B_C_CKp

BKEY-B-PRESET_3P3V
BKEY-B-CLKREQ_3P3V
BKEY-B-WAKE_3P3V

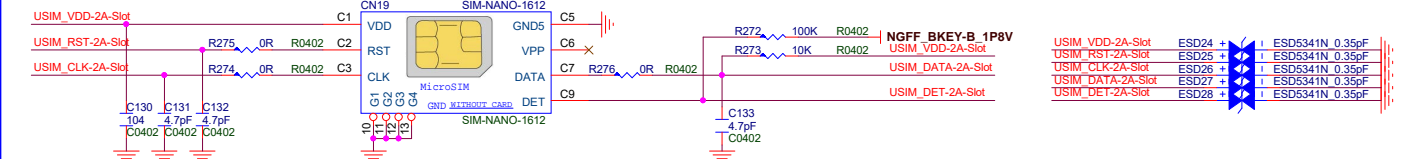
USIM_DET-2A-FPC
PCA_A_I00_6/SIM_SEL



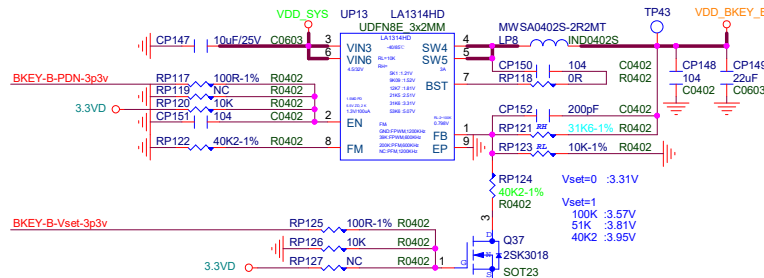
SIM2



SIM2



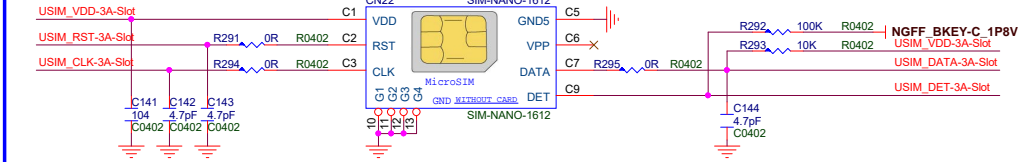
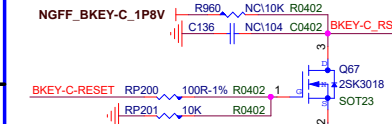
VDD_BKEY_B=3.3V/3.95V#3A



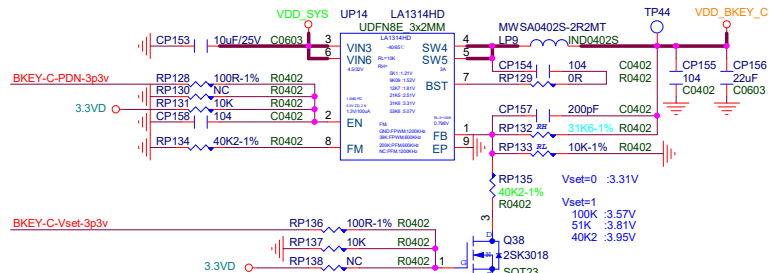
DVDD1V8_SOC NGFF_BKEY-C_1P8V
3.3VD NGFF_BKEY-C_3P3V
VDD_SYS



1



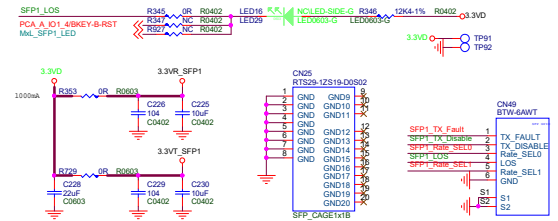
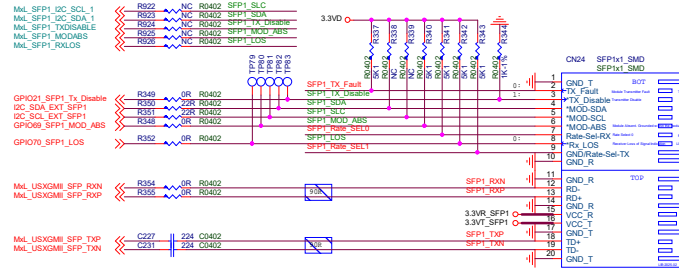
VDD_BKEY_C=3.3V/3.95V#3A



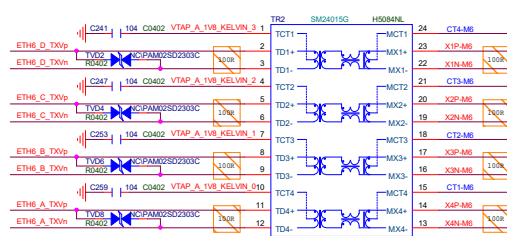
[illegible][illegible][illegible]

AS21010 NDI INTERFACE		AS21010P	UN
AEON_LAN_A_TxTx	AS A MDIO	15	TPAP
AEON_LAN_A_TxRx	AS A MDIO	16	TPAN
AEON_LAN_B_TxTx	AS A MDIO	17	TPBP
AEON_LAN_B_TxRx	AS A MDIO	18	TPBN
AEON_LAN_C_TxTx	AS A MDIO	19	TPCP
AEON_LAN_C_TxRx	AS A MDIO	20	TPCN
AEON_LAN_D_TxTx	AS A MDIO	21	TPDP
AEON_LAN_D_TxRx	AS A MDIO	22	TPDN

AS21010 MDI INTERFACE		AS21010P U45E
AEON_WAN_A_TXn	AS B MDIOp	J5
AEON_WAN_A_TXn	AS B MDIOn	H5
AEON_WAN_B_TXn	AS B MDIOp	J6
AEON_WAN_B_TXn	AS B MDIOn	H6
AEON_WAN_C_TXn	AS B MDIOp	J7
AEON_WAN_C_TXn	AS B MDIOn	H7
AEON_WAN_D_TXn	AS B MDIOp	J8
AEON_WAN_D_TXn	AS B MDIOn	H8
		TPAN TPBP TPCP TPCN TPDP TPDN

[illegible]

Genetic map of the *RL45_LED_C* region on chromosome 10. The map shows the arrangement of genes and their orientations. Genes are represented by colored boxes: blue for *RL45*, green for *LED_C*, and red for *LED_Yn*. The map includes the following genes: *RL45D1*, *RL45D2*, *RL45D3*, *RL45D4*, *RL45D5*, *RL45D6*, *RL45D7*, *RL45D8*, *RL45D9*, *RL45D10*, *RL45D11*, *RL45D12*, *RL45D13*, *RL45D14*, *RL45D15*, *RL45D16*, *RL45D17*, *RL45D18*, *RL45D19*, *RL45D20*, *RL45D21*, *RL45D22*, *RL45D23*, *RL45D24*, *RL45D25*, *RL45D26*, *RL45D27*, *RL45D28*, *RL45D29*, *RL45D30*, *RL45D31*, *RL45D32*, *RL45D33*, *RL45D34*, *RL45D35*, *RL45D36*, *RL45D37*, *RL45D38*, *RL45D39*, *RL45D40*, *RL45D41*, *RL45D42*, *RL45D43*, *RL45D44*, *RL45D45*, *RL45D46*, *RL45D47*, *RL45D48*, *RL45D49*, *RL45D50*, *RL45D51*, *RL45D52*, *RL45D53*, *RL45D54*, *RL45D55*, *RL45D56*, *RL45D57*, *RL45D58*, *RL45D59*, *RL45D60*, *RL45D61*, *RL45D62*, *RL45D63*, *RL45D64*, *RL45D65*, *RL45D66*, *RL45D67*, *RL45D68*, *RL45D69*, *RL45D70*, *RL45D71*, *RL45D72*, *RL45D73*, *RL45D74*, *RL45D75*, *RL45D76*, *RL45D77*, *RL45D78*, *RL45D79*, *RL45D80*, *RL45D81*, *RL45D82*, *RL45D83*, *RL45D84*, *RL45D85*, *RL45D86*, *RL45D87*, *RL45D88*, *RL45D89*, *RL45D90*, *RL45D91*, *RL45D92*, *RL45D93*, *RL45D94*, *RL45D95*, *RL45D96*, *RL45D97*, *RL45D98*, *RL45D99*, *RL45D100*. The map also shows the locations of the *RL45_LED_C* and *RL45_LED_Yn* genes. The map is oriented with the centromere (CEN) on the left and the telomere (TEL) on the right. The map is a schematic representation of the genetic structure of the *RL45_LED_C* region.



The top diagram shows the R445 module with pins 1-12 labeled. The bottom diagram shows the corresponding connections to the R445 module.

Pin Connections:

- Pin 1: 3.3V
- Pin 2: R741
- Pin 3: 240R-1%
- Pin 4: R4042
- Pin 5: ETH6 LEDP+
- Pin 6: ETH6 LEDY1
- Pin 7: 12
- Pin 8: V+
- Pin 9: V-
- Pin 10: G+
- Pin 11: G-
- Pin 12: SH1

Connections to R445:

- Pin 1: C14-M
- Pin 2: R2038
- Pin 3: 240R
- Pin 4: R20031
- Pin 5: C552
- Pin 6: I4F7K2V
- Pin 7: C1006
- Pin 8: C6531
- Pin 9: I4F7K2V
- Pin 10: C1006
- Pin 11: C11-M
- Pin 12: R80

Other Connections:

- Pin 1: C14-M
- Pin 2: R2038
- Pin 3: 240R
- Pin 4: R20031
- Pin 5: C555
- Pin 6: I4F7K2V
- Pin 7: C1006
- Pin 8: C6531
- Pin 9: I4F7K2V
- Pin 10: C1006
- Pin 11: C11-M
- Pin 12: R80

Ground Connections:

- Pin 1: PGND
- Pin 2: R742
- Pin 3: 240R-1%
- Pin 4: R4042
- Pin 5: ETH6 LEDP+
- Pin 6: ETH6 LEDP+
- Pin 7: 10
- Pin 8: G+
- Pin 9: G-
- Pin 10: SH1
- Pin 11: SH2
- Pin 12: SH2

Other Connections:

- Pin 1: C14-M
- Pin 2: R2038
- Pin 3: 240R
- Pin 4: R20031
- Pin 5: C555
- Pin 6: I4F7K2V
- Pin 7: C1006
- Pin 8: C6531
- Pin 9: I4F7K2V
- Pin 10: C1006
- Pin 11: C11-M
- Pin 12: R80

Ground Connections:

- Pin 1: PGND
- Pin 2: R742
- Pin 3: 240R-1%
- Pin 4: R4042
- Pin 5: ETH6 LEDP+
- Pin 6: ETH6 LEDP+
- Pin 7: 10
- Pin 8: G+
- Pin 9: G-
- Pin 10: SH1
- Pin 11: SH2
- Pin 12: SH2

Figure 10 shows the pin connections for the U930 module. The diagram is divided into two sections, each showing a 10-pin header. The top section shows the U930 module connected to the U930M1_1_SFP_PHY_TXN and U930M1_1_SFP_PHY_RXN pins of the U930 module. The bottom section shows the U930 module connected to the U930M1_1_SFP_PHY_TXN and U930M1_1_SFP_PHY_RXN pins of the U930 module. The connections are as follows:

- U930M1_1_SFP_PHY_TXN (Pin 2) to U930M1_1_SFP_PHY_TXN (Pin 2)
- U930M1_1_SFP_PHY_RXN (Pin 3) to U930M1_1_SFP_PHY_RXN (Pin 3)
- U930M1_1_SFP_PHY_RXP (Pin 4) to U930M1_1_SFP_PHY_RXP (Pin 4)
- U930M1_1_SFP_PHY_TXP (Pin 5) to U930M1_1_SFP_PHY_TXP (Pin 5)
- VCC (Pin 6) to VCC (Pin 6)
- VCC (Pin 7) to VCC (Pin 7)
- VCC (Pin 8) to VCC (Pin 8)
- VCC (Pin 9) to VCC (Pin 9)
- VCC (Pin 10) to VCC (Pin 10)

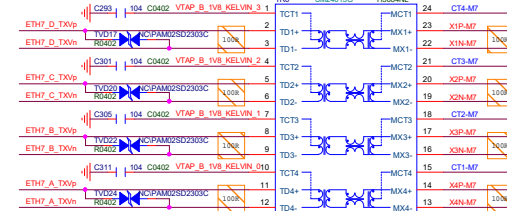
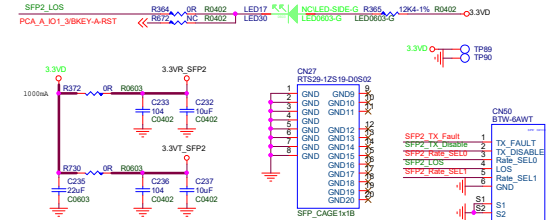
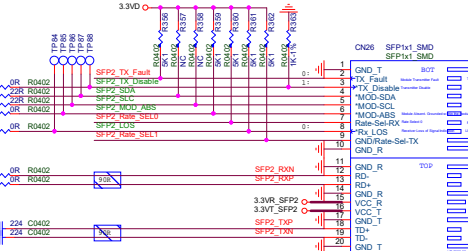
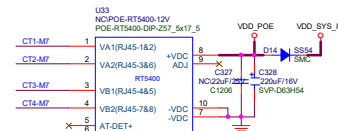


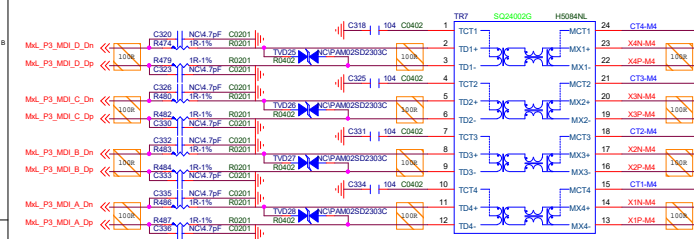
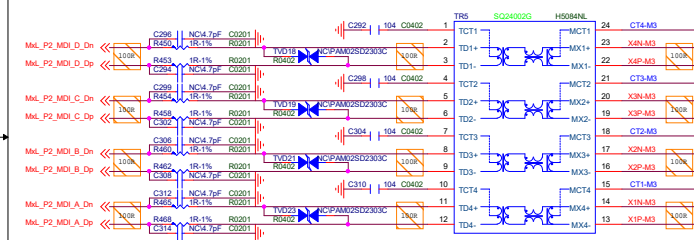
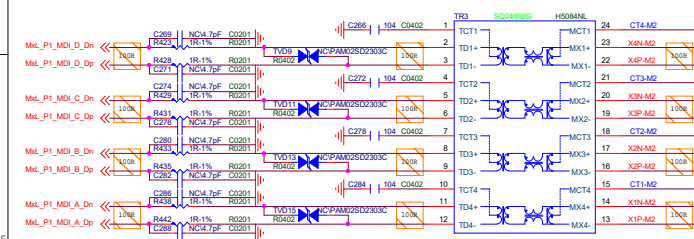
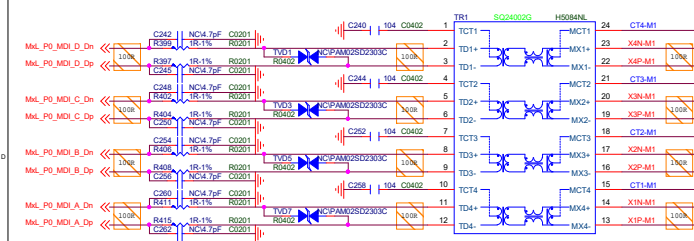
Figure 1 shows the pin connections of the R0503 C317 camera module. The module is a small, rectangular component with 12 pins. The pins are numbered 1 to 12. The connections are as follows:

- Pin 1: ETH7 LEDp, ETH7 LEDn
- Pin 2: X4N-M7, X4P-M7
- Pin 3: X2N-M7, X2P-M7
- Pin 4: X3N-M7, X3P-M7
- Pin 5: X5N-M7, X5P-M7
- Pin 6: X6N-M7, X6P-M7
- Pin 7: X7N-M7, X7P-M7
- Pin 8: X8N-M7, X8P-M7
- Pin 9: X9N-M7, X9P-M7
- Pin 10: ETH7 LEDp, ETH7 LEDn
- Pin 11: GND, GND
- Pin 12: GND, GND

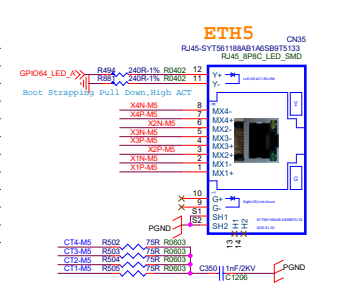
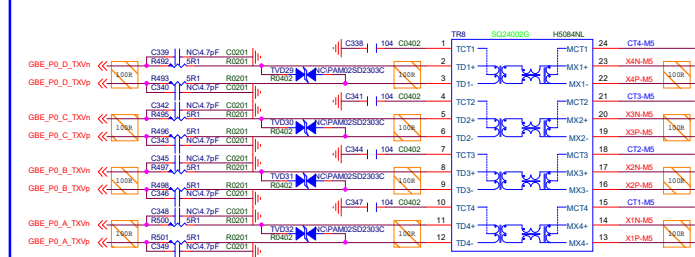
The module is labeled "R0503 C317" and "R0503 C317".



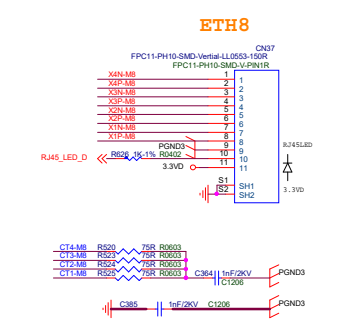
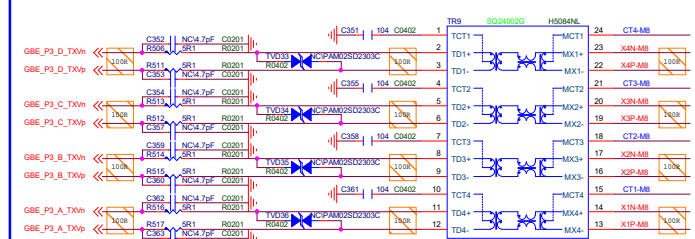
2.5G RJ45 x4(MxL86252)



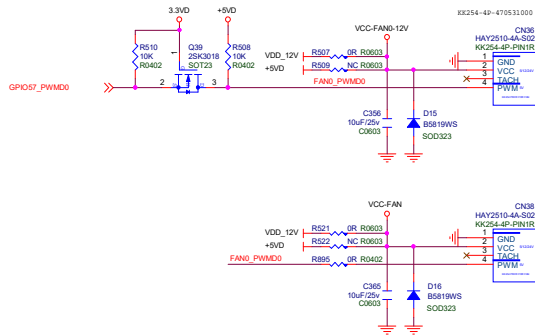
GbEx1 (MT7988)



GbE FPC Connector(MT7988)

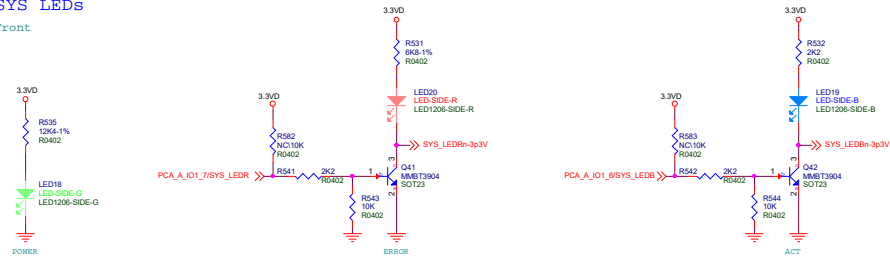


FAN

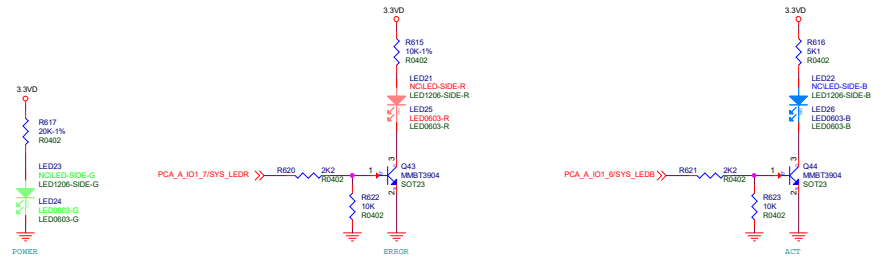


SYS LEDs

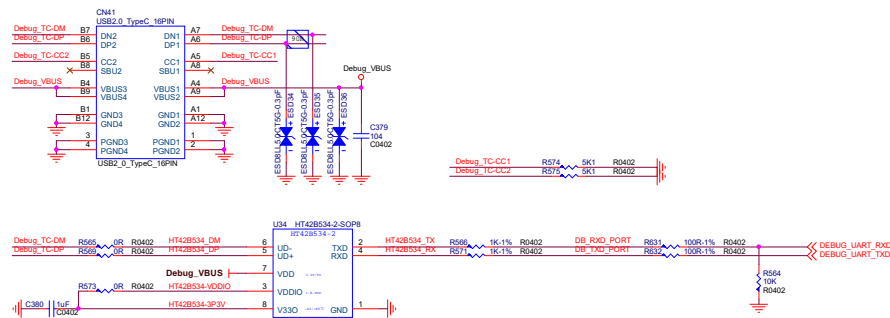
Front



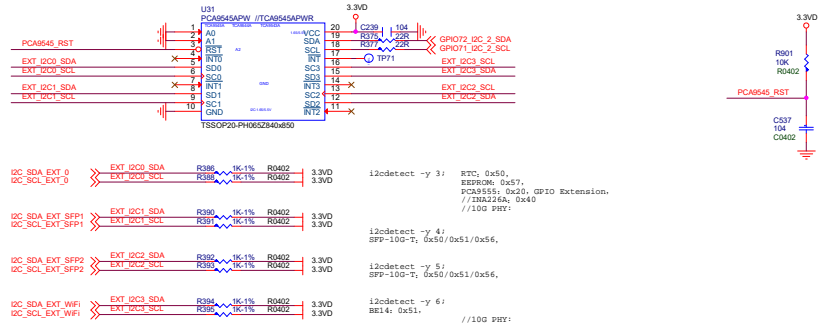
Rear



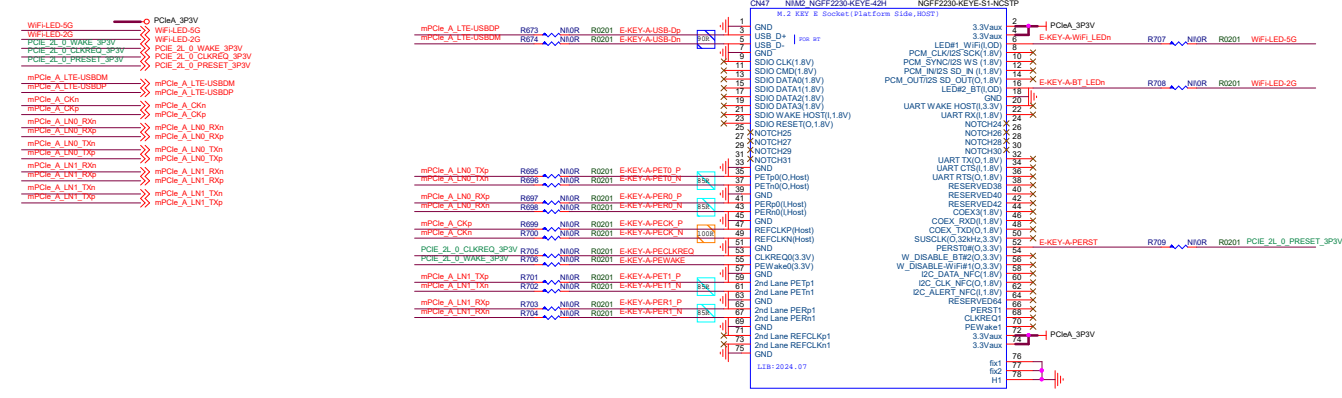
TypeC Console



I2C BUS Switch PCA9545: I2C Address: 0x70



NGFF_EKEY_A(RSVD)



NGFF_EKEY_B(RSVD)

