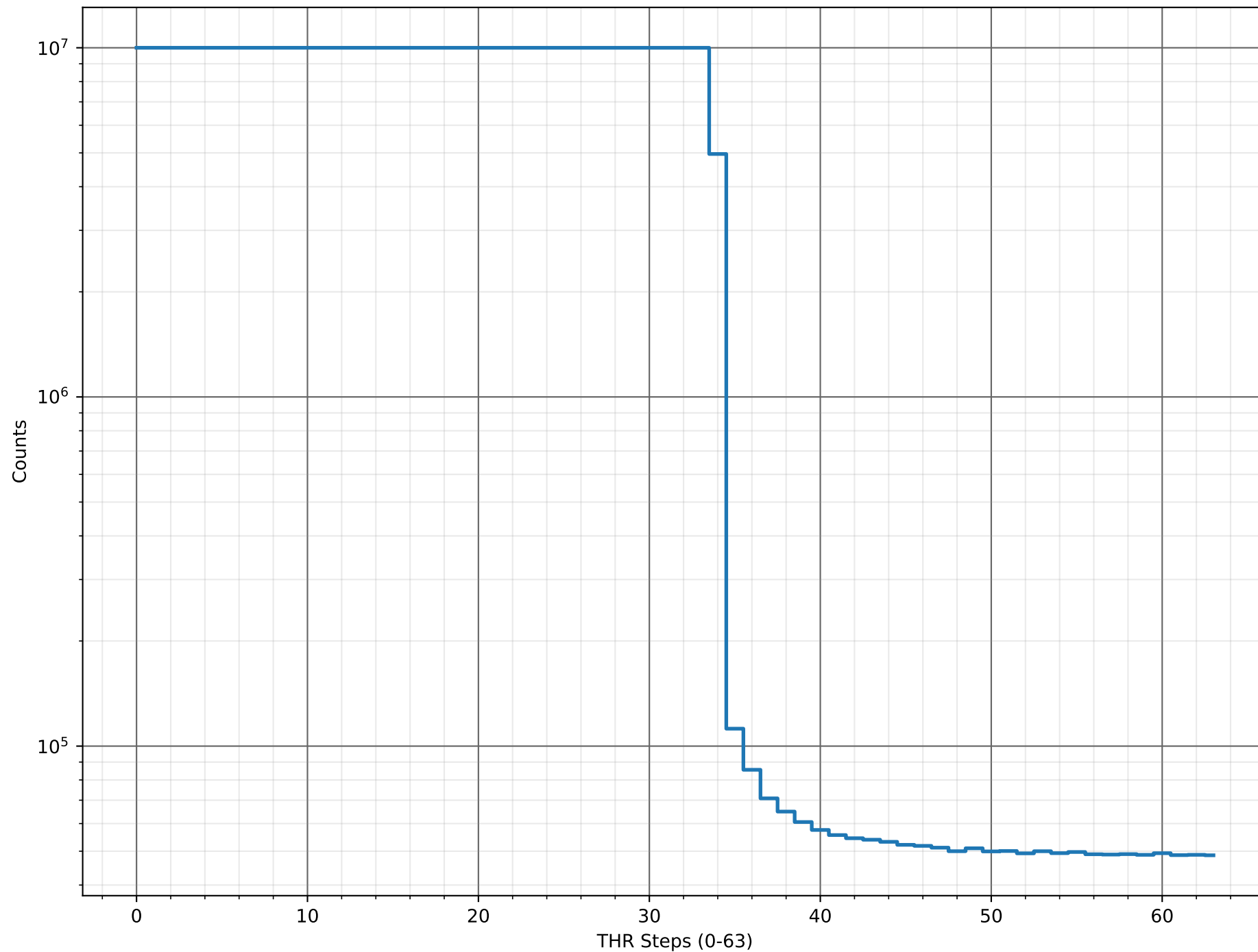
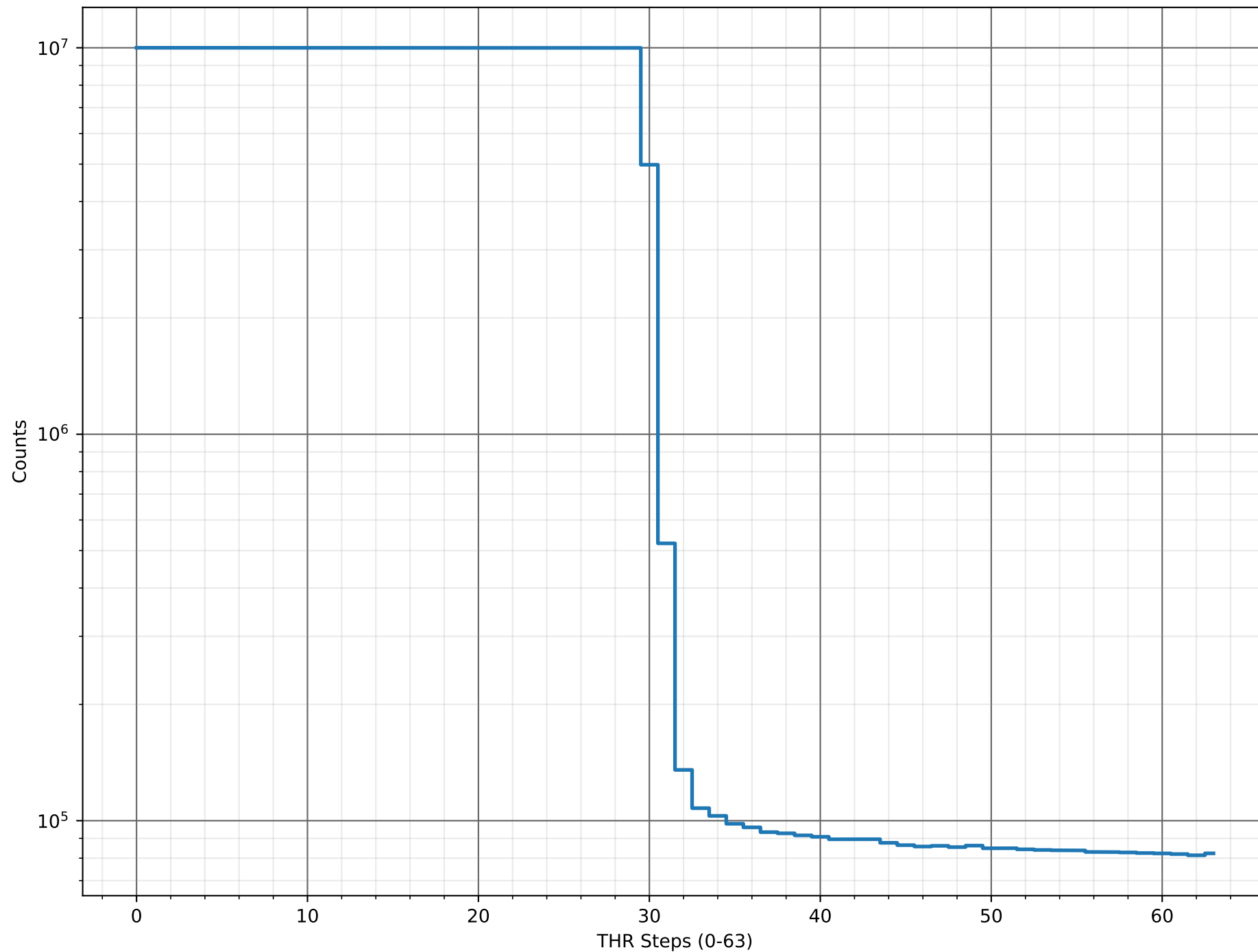


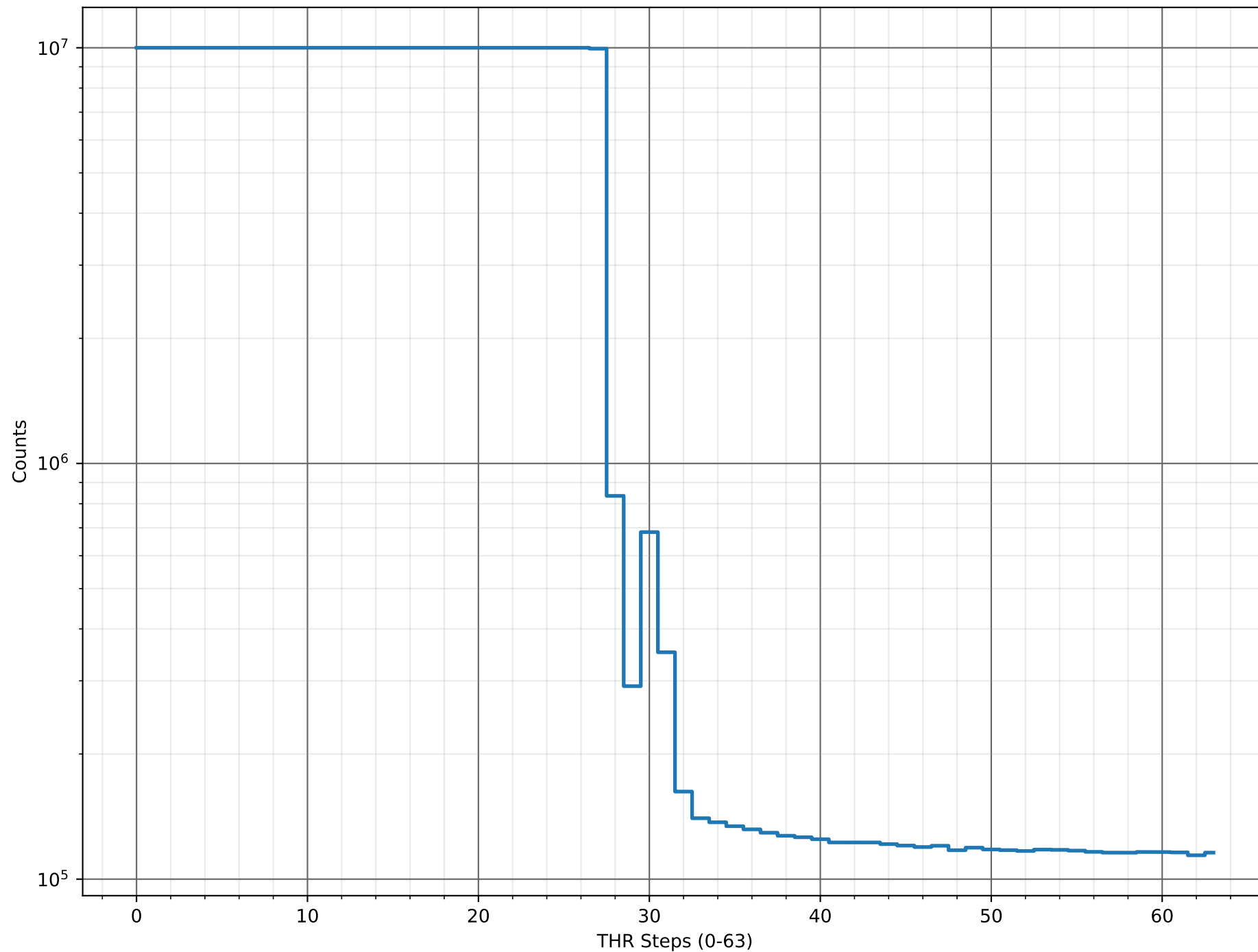
THR Distribution of PMT: A Anode: 1 (UART: 1 FEB: 1 CHIP: 4 CHANNEL: 7)



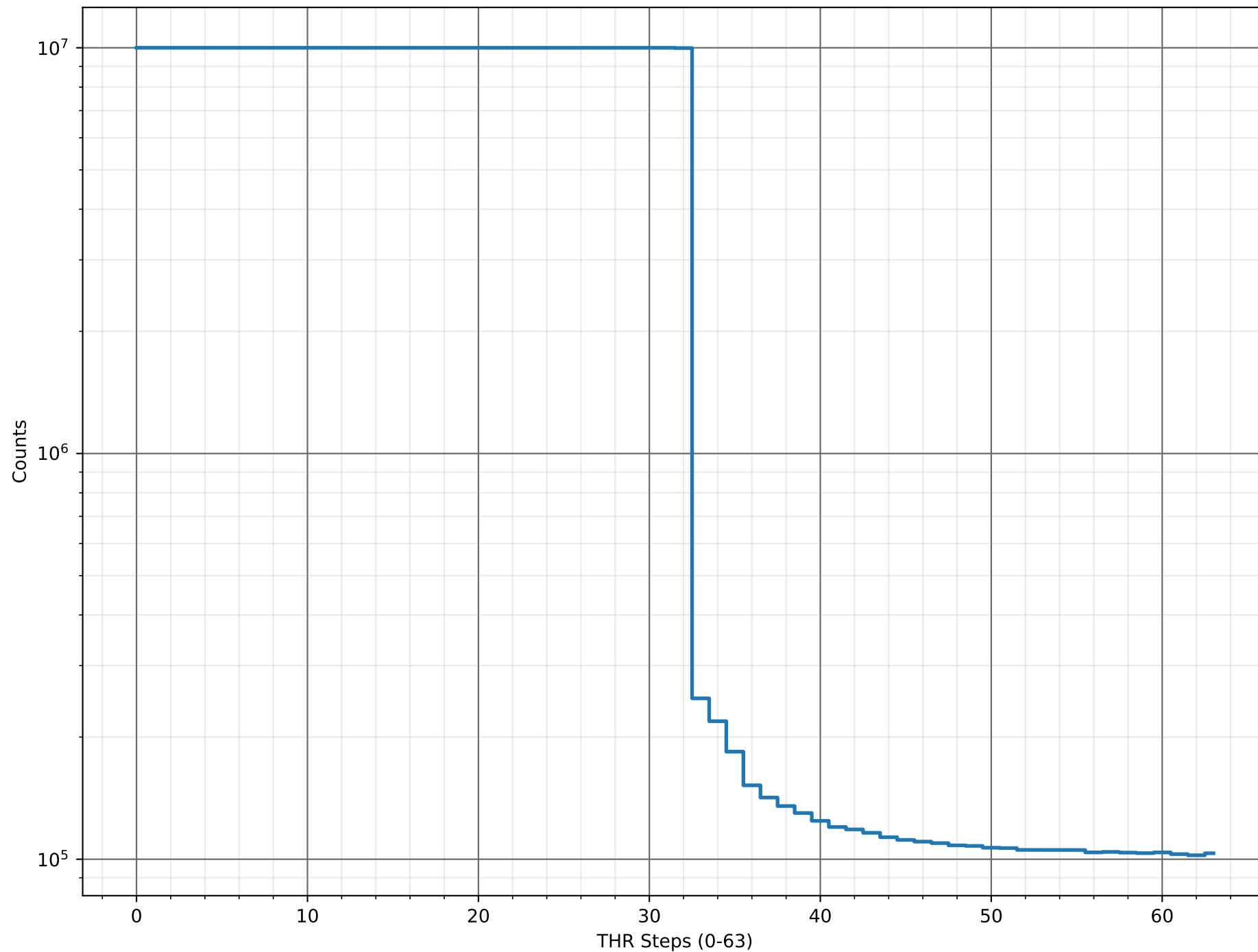
THR Distribution of PMT: A Anode: 2 (UART: 1 FEB: 1 CHIP: 4 CHANNEL: 6)



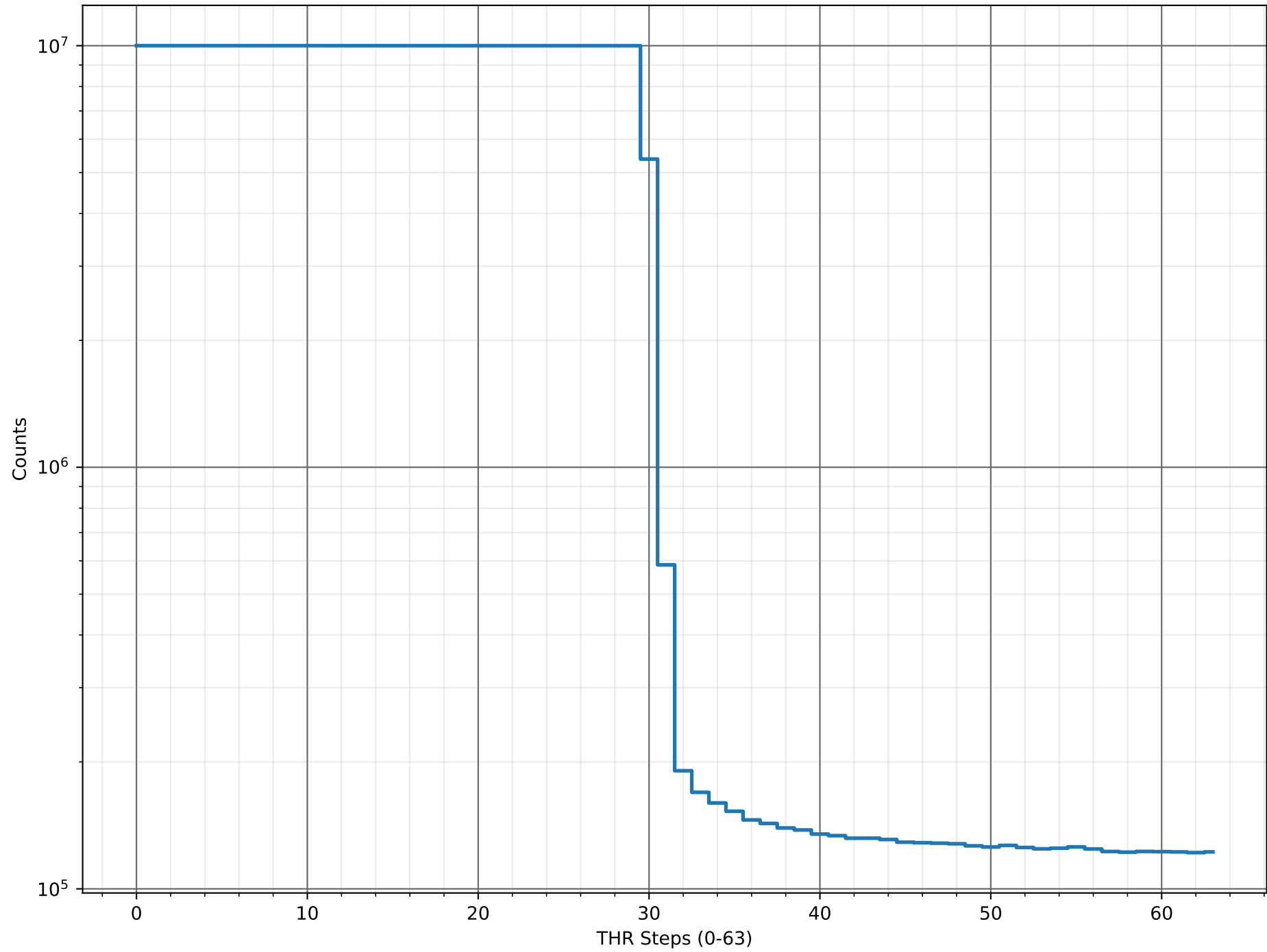
THR Distribution of PMT: A Anode: 3 (UART: 1 FEB: 1 CHIP: 5 CHANNEL: 0)



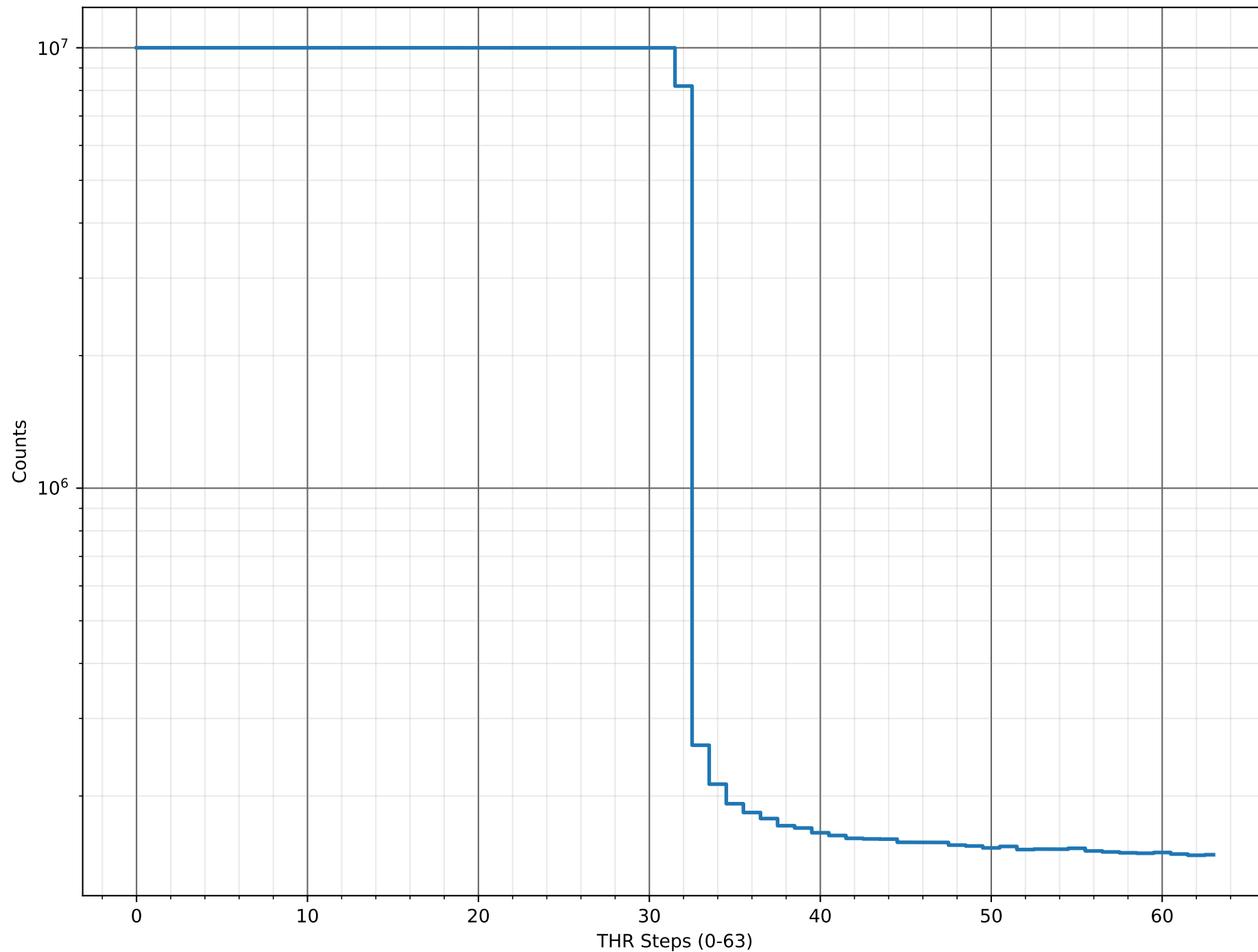
THR Distribution of PMT: A Anode: 4 (UART: 1 FEB: 1 CHIP: 5 CHANNEL: 1)



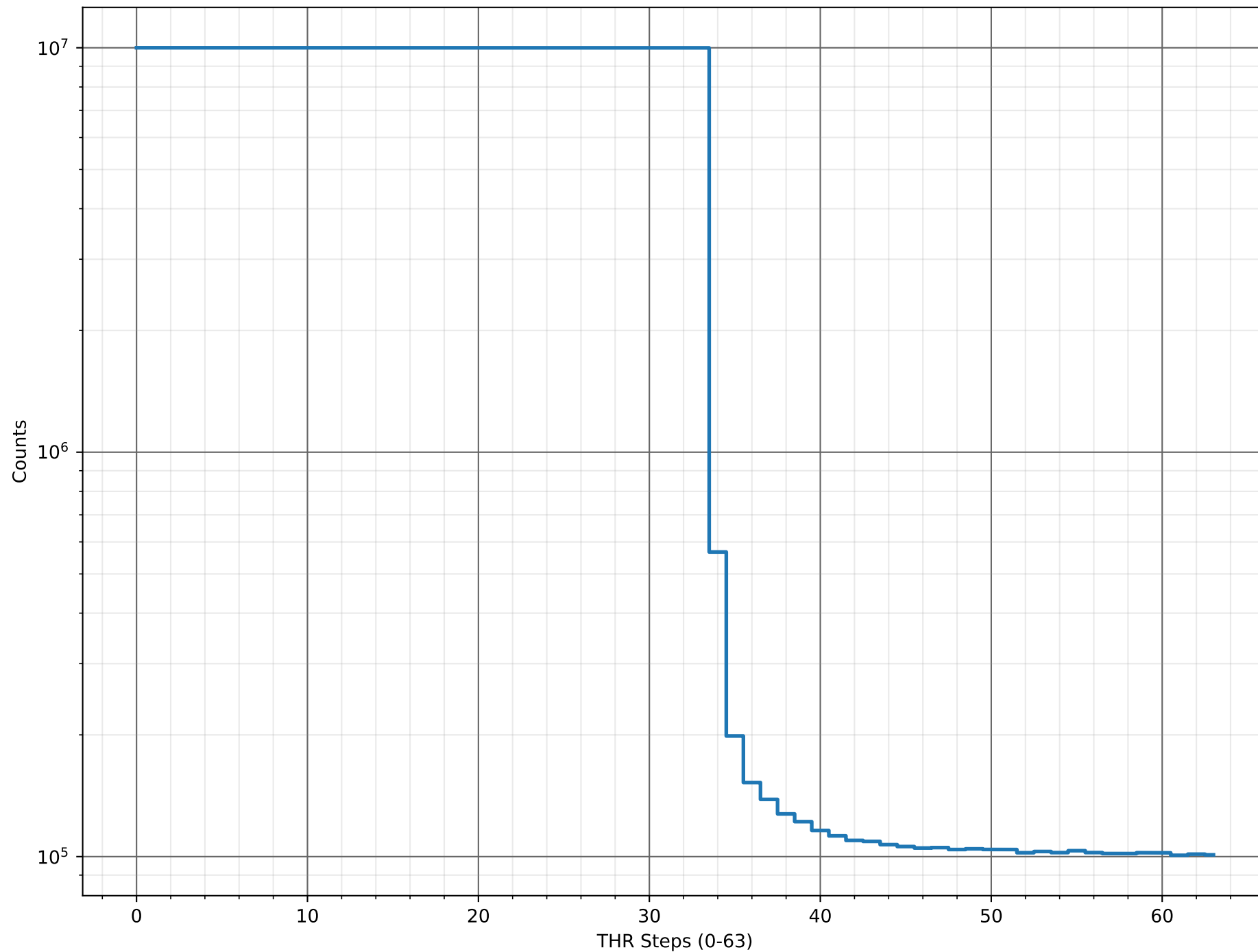
THR Distribution of PMT: A Anode: 5 (UART: 1 FEB: 0 CHIP: 4 CHANNEL: 6)



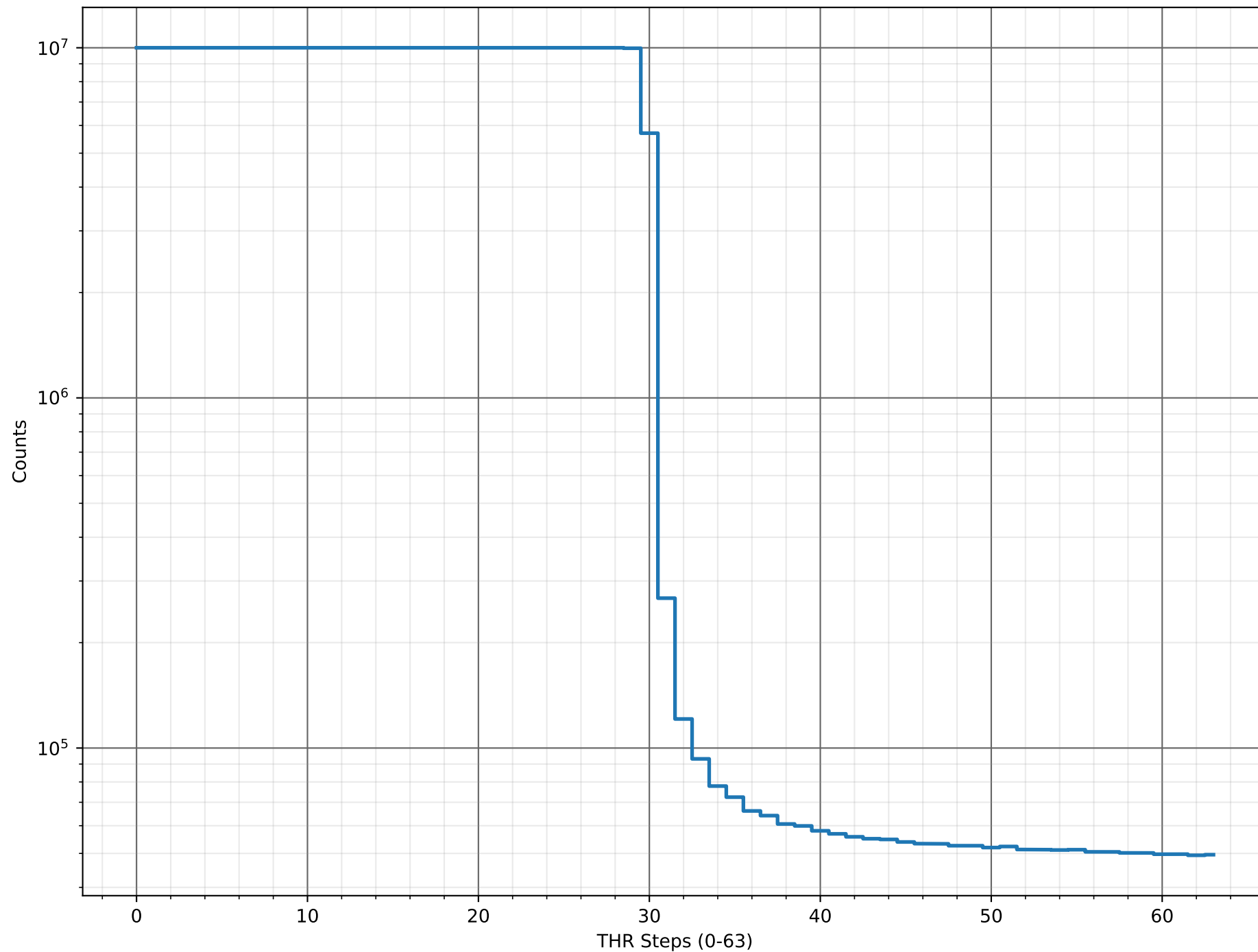
THR Distribution of PMT: A Anode: 6 (UART: 1 FEB: 0 CHIP: 4 CHANNEL: 7)



THR Distribution of PMT: A Anode: 7 (UART: 1 FEB: 0 CHIP: 5 CHANNEL: 1)

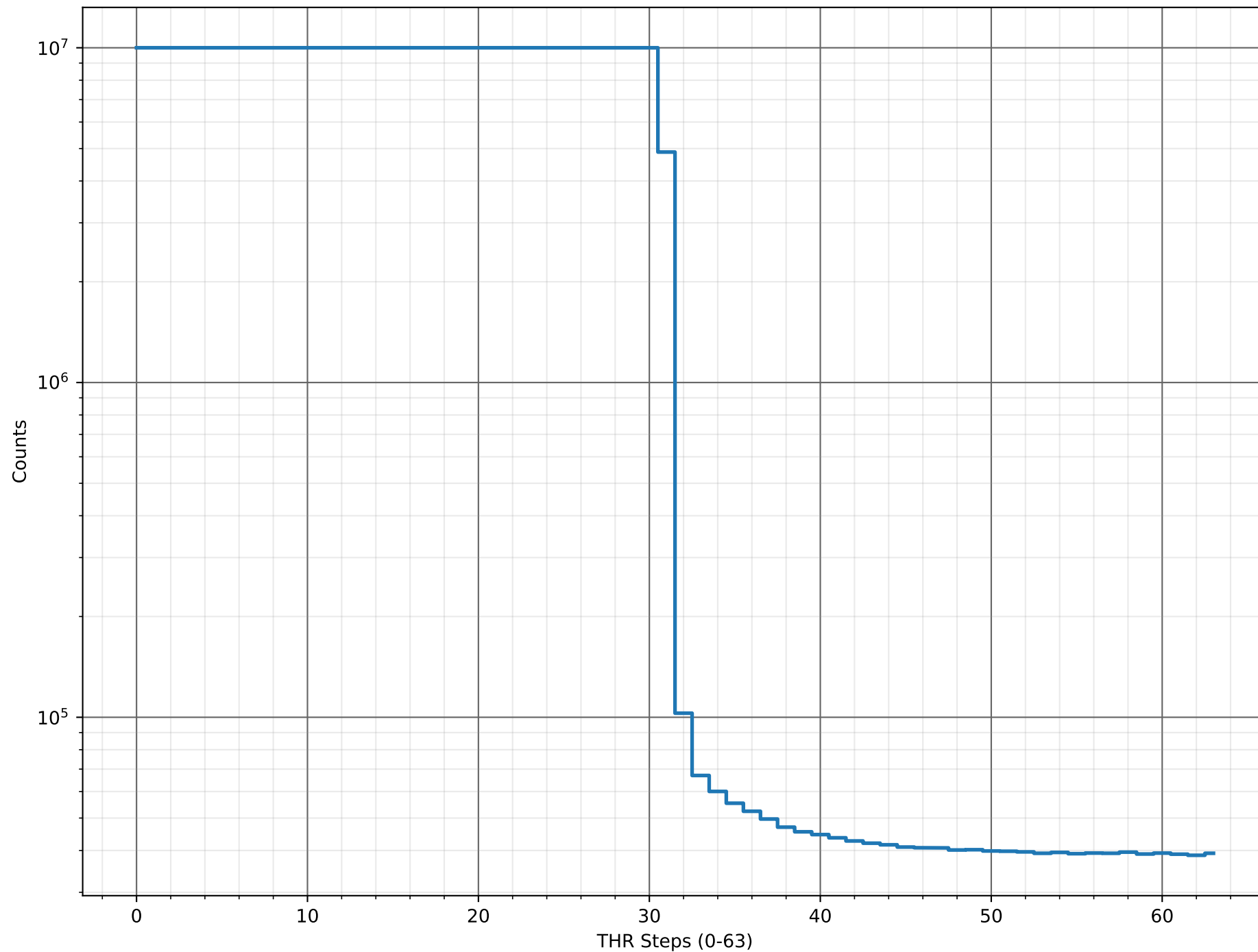


THR Distribution of PMT: A Anode: 8 (UART: 1 FEB: 0 CHIP: 5 CHANNEL: 0)

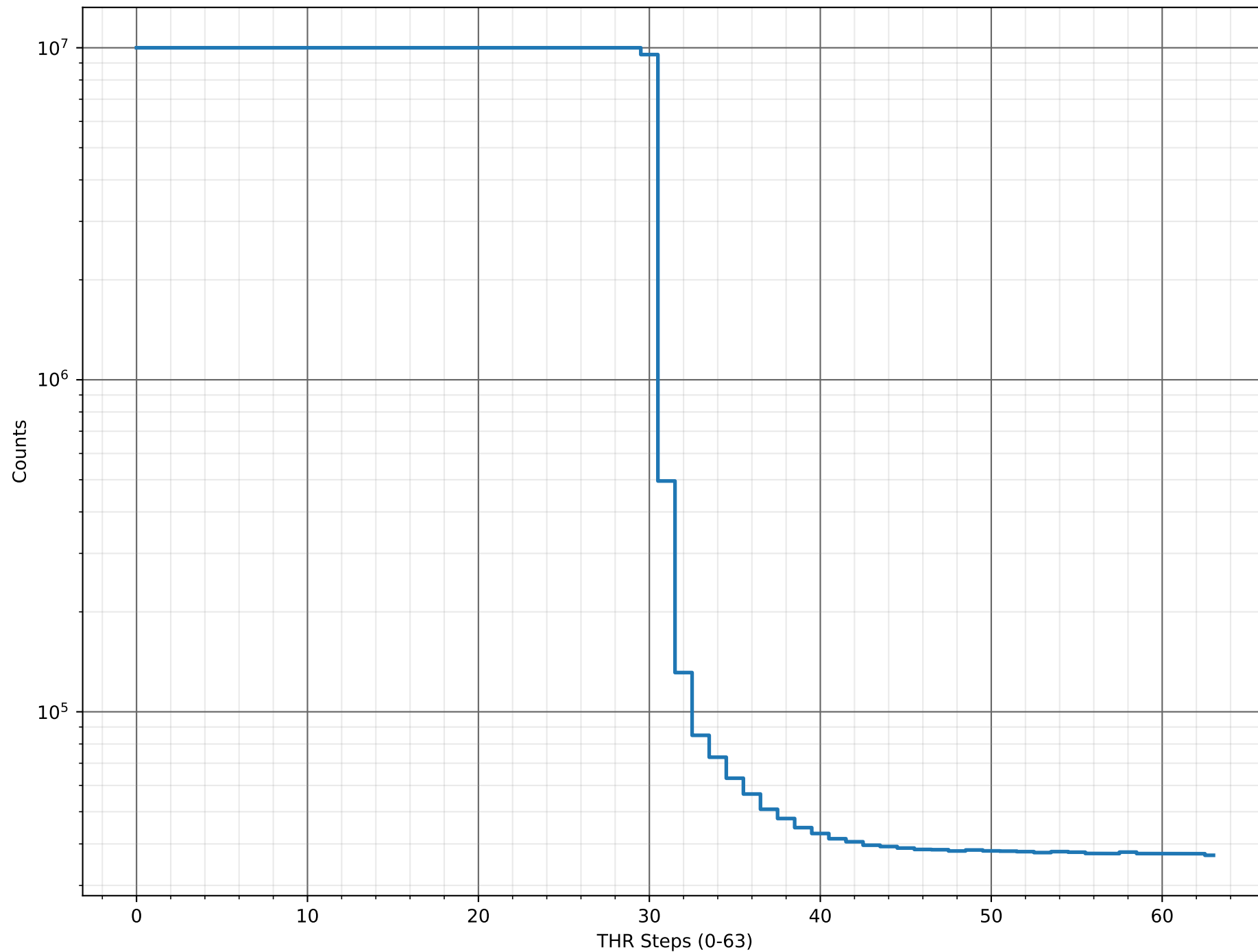




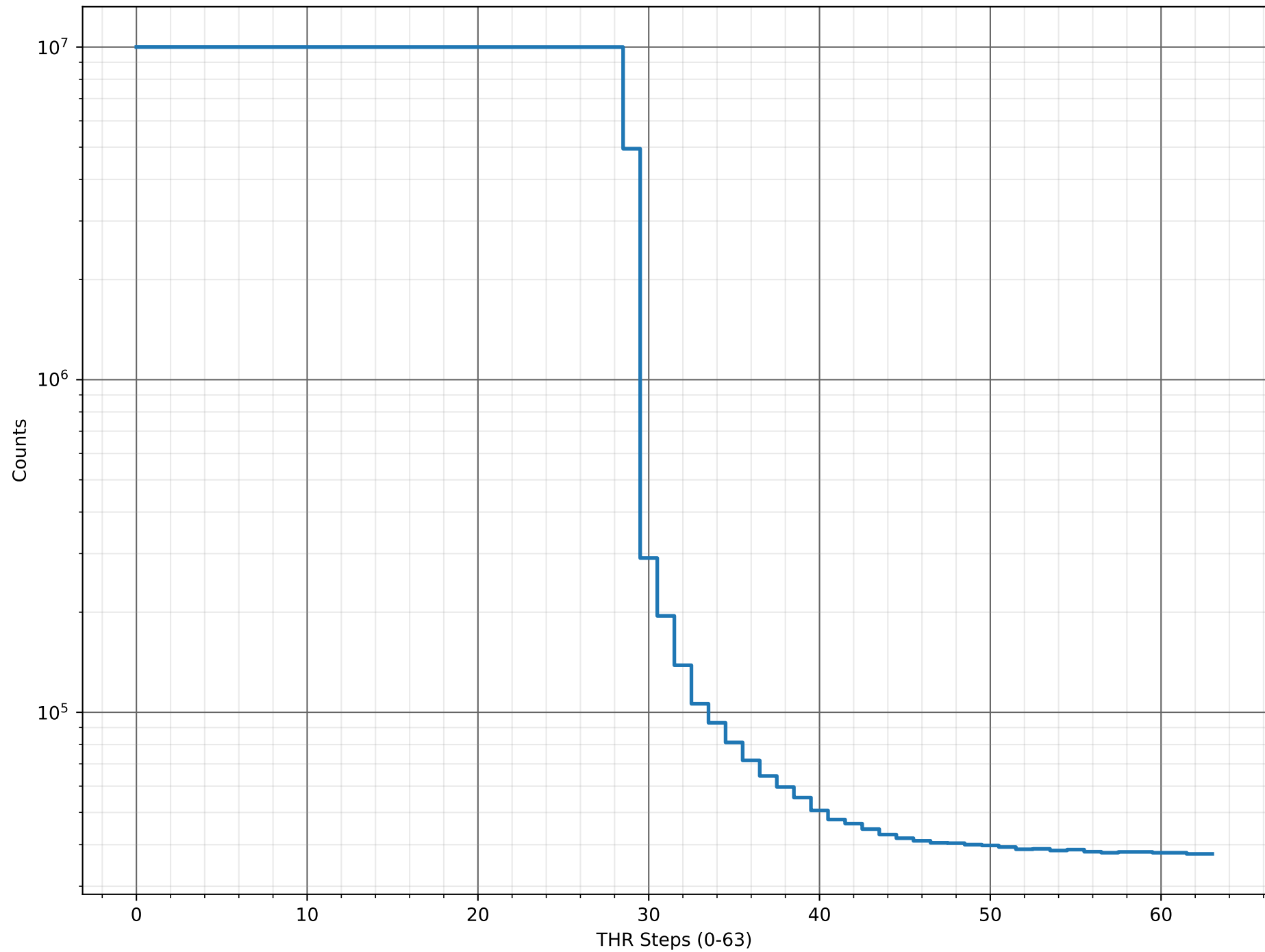
THR Distribution of PMT: A Anode: 9 (UART: 1 FEB: 1 CHIP: 4 CHANNEL: 5)



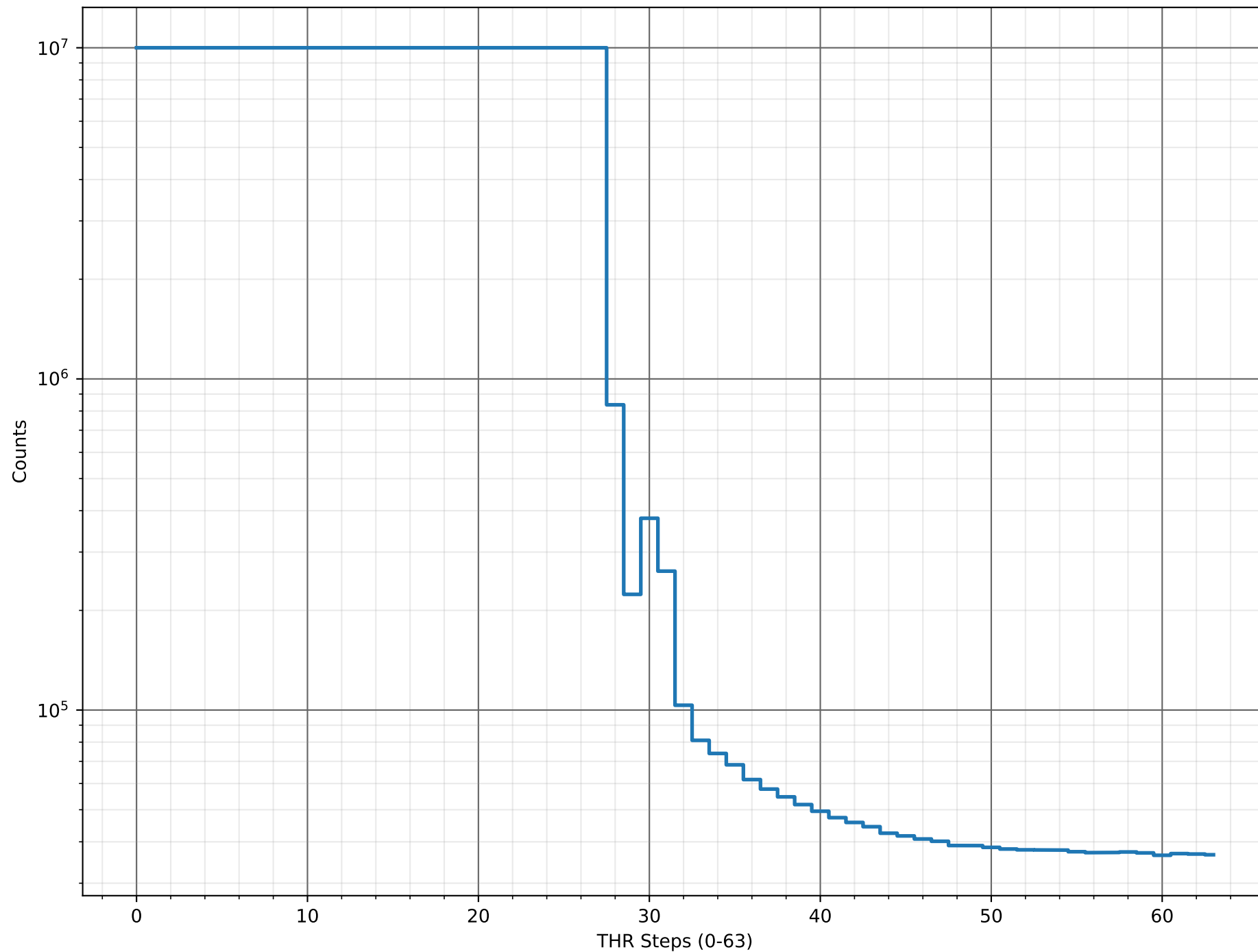
THR Distribution of PMT: A Anode: 10 (UART: 1 FEB: 1 CHIP: 4 CHANNEL: 4)



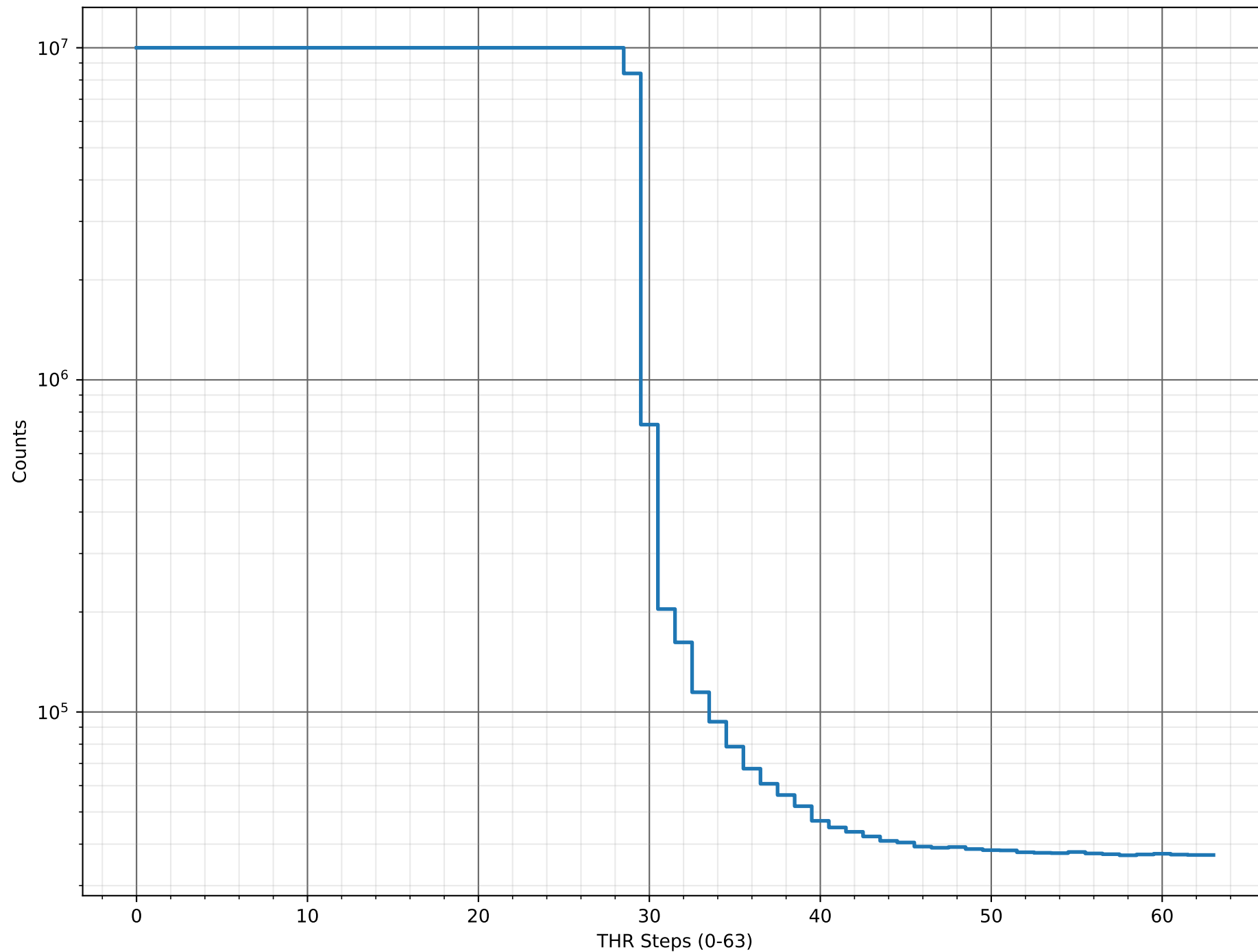
THR Distribution of PMT: A Anode: 11 (UART: 1 FEB: 1 CHIP: 5 CHANNEL: 2)



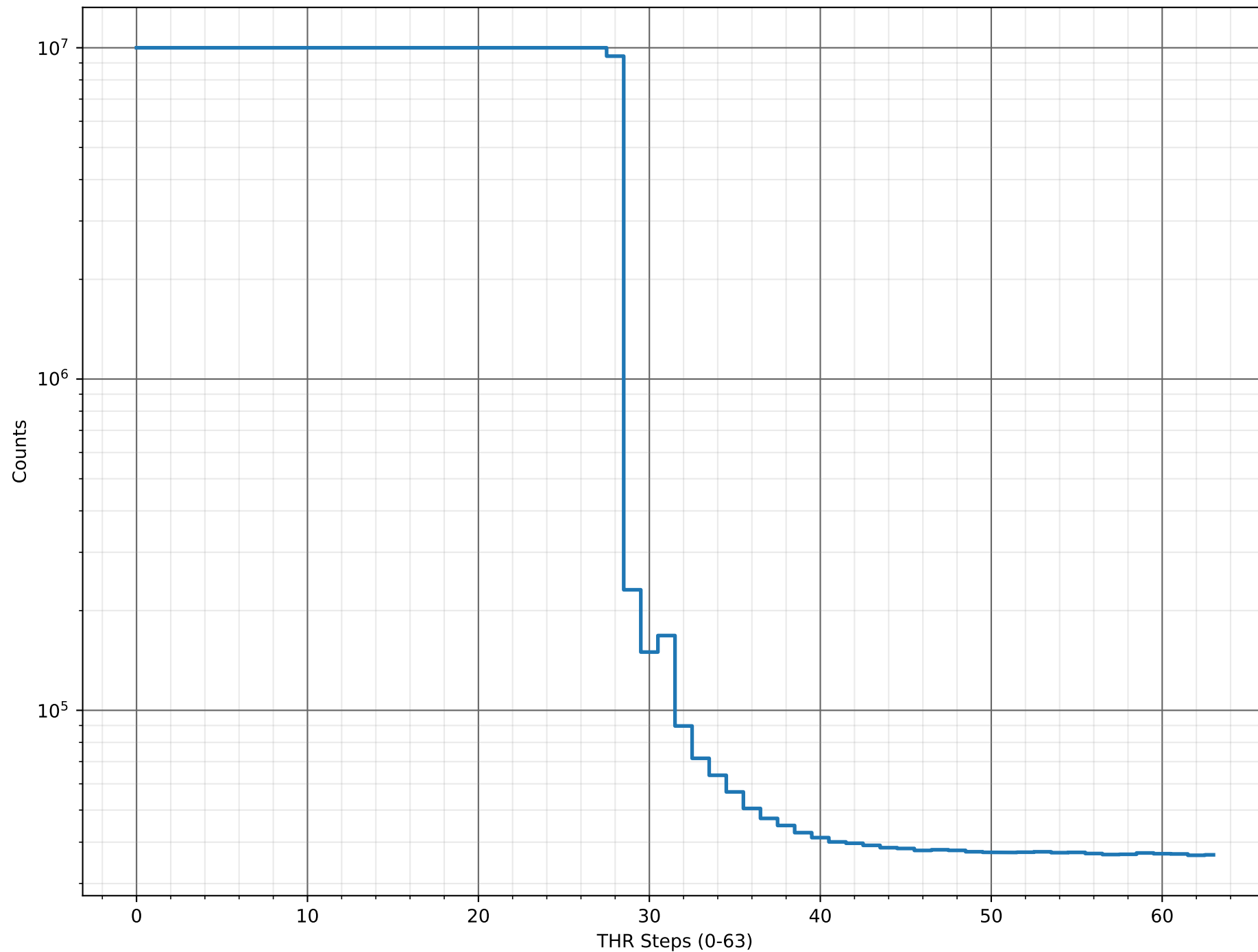
THR Distribution of PMT: A Anode: 12 (UART: 1 FEB: 1 CHIP: 5 CHANNEL: 3)



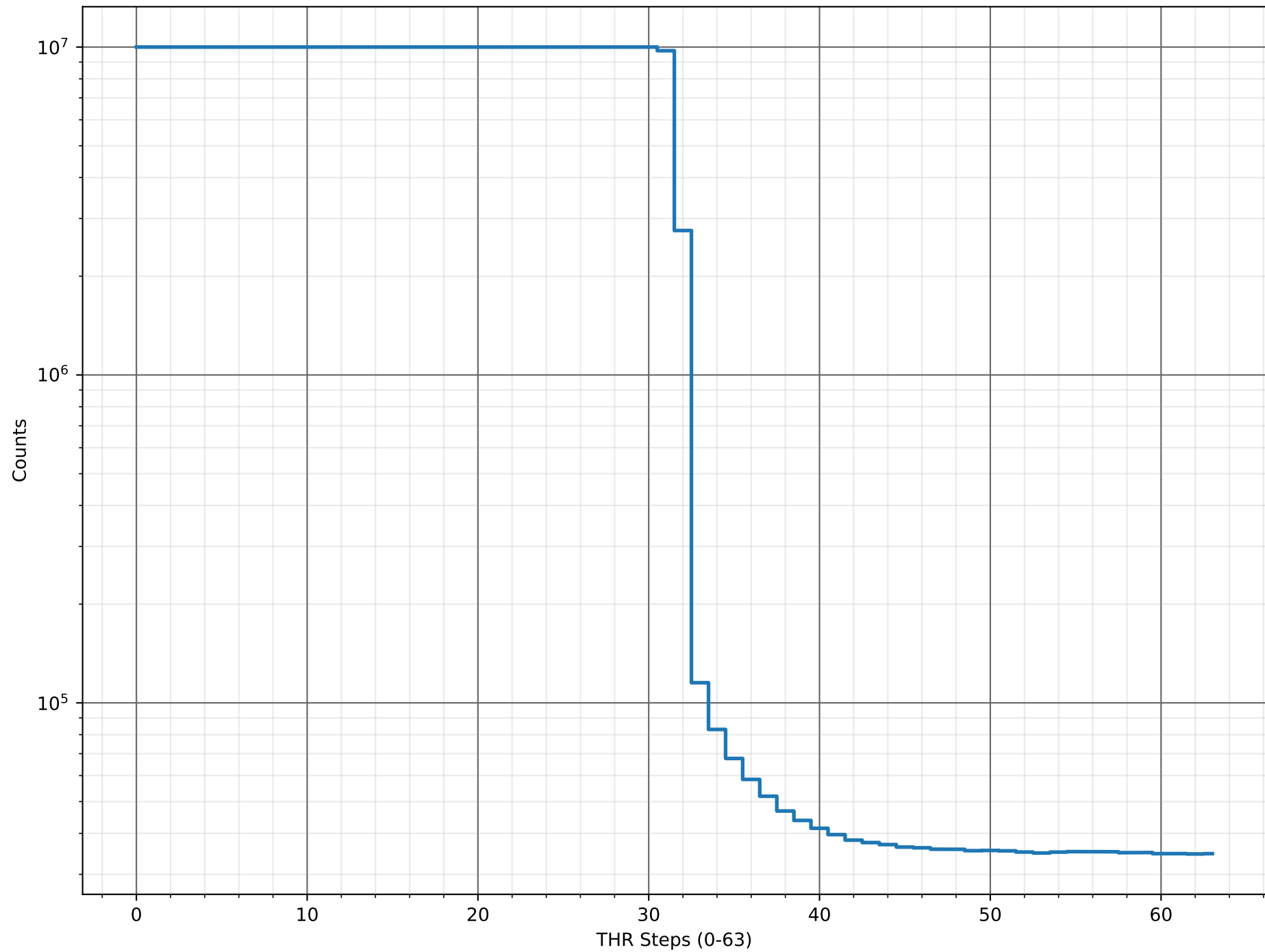
THR Distribution of PMT: A Anode: 13 (UART: 1 FEB: 0 CHIP: 4 CHANNEL: 4)



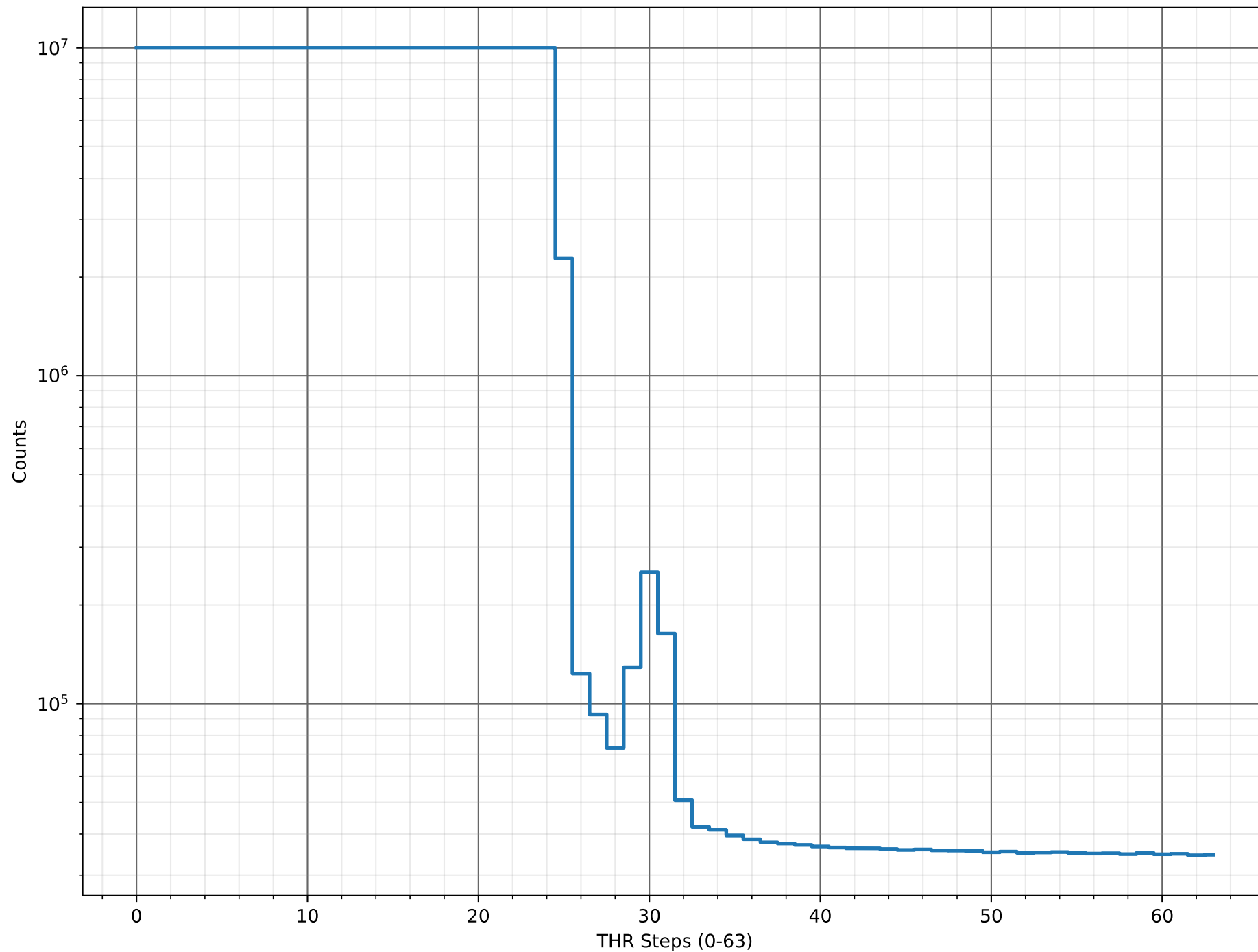
THR Distribution of PMT: A Anode: 14 (UART: 1 FEB: 0 CHIP: 4 CHANNEL: 5)



THR Distribution of PMT: A Anode: 15 (UART: 1 FEB: 0 CHIP: 5 CHANNEL: 3)

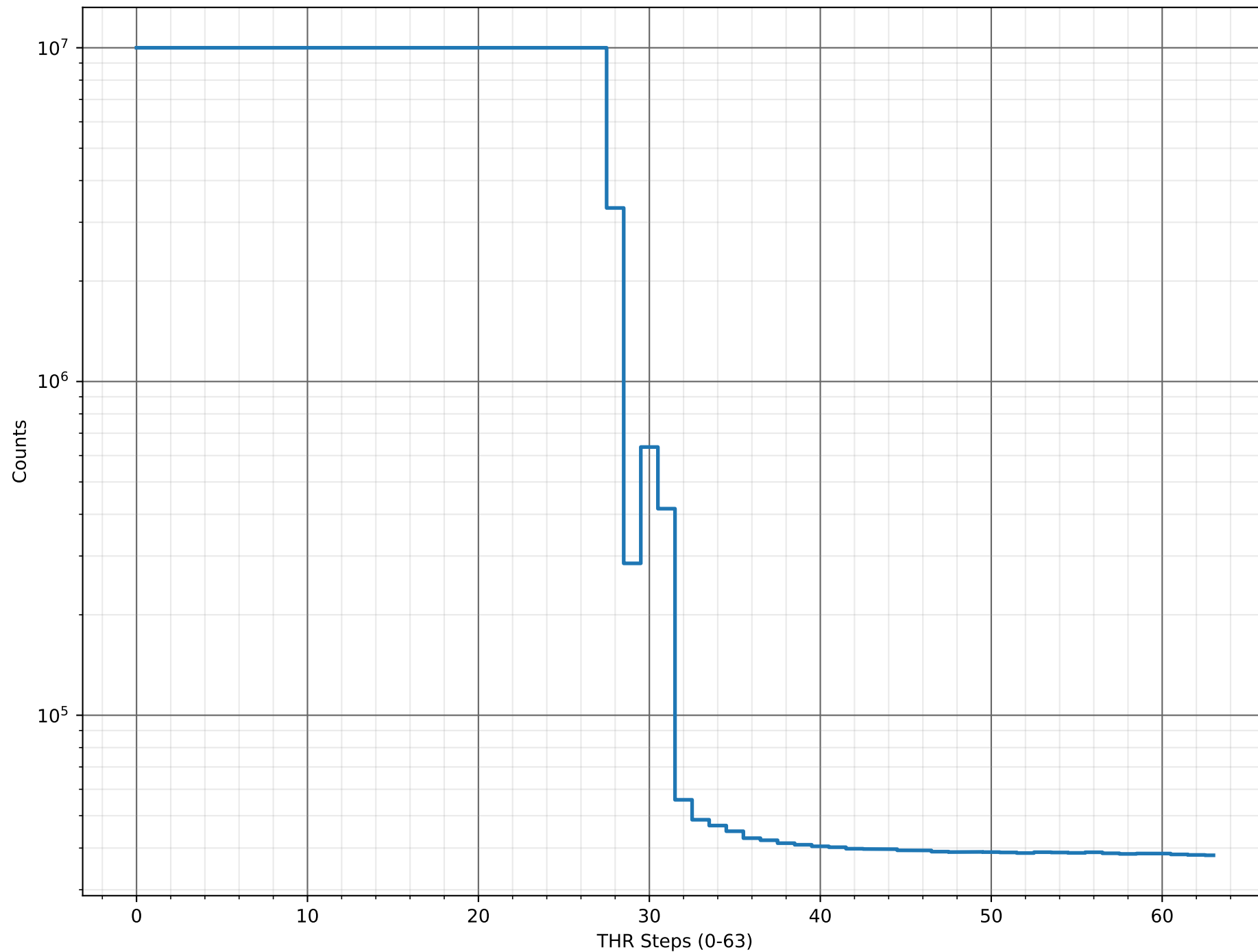


THR Distribution of PMT: A Anode: 16 (UART: 1 FEB: 0 CHIP: 5 CHANNEL: 2)

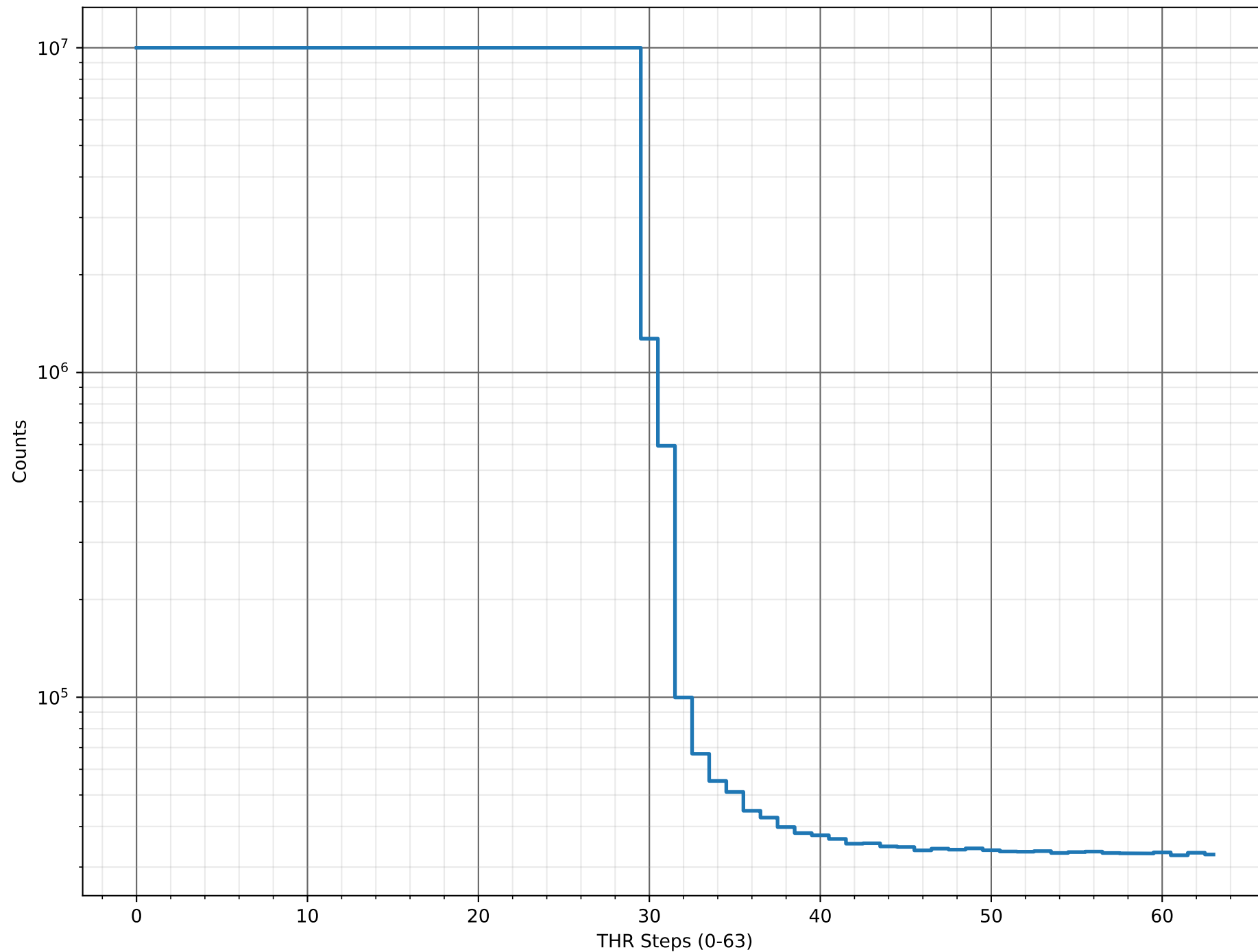




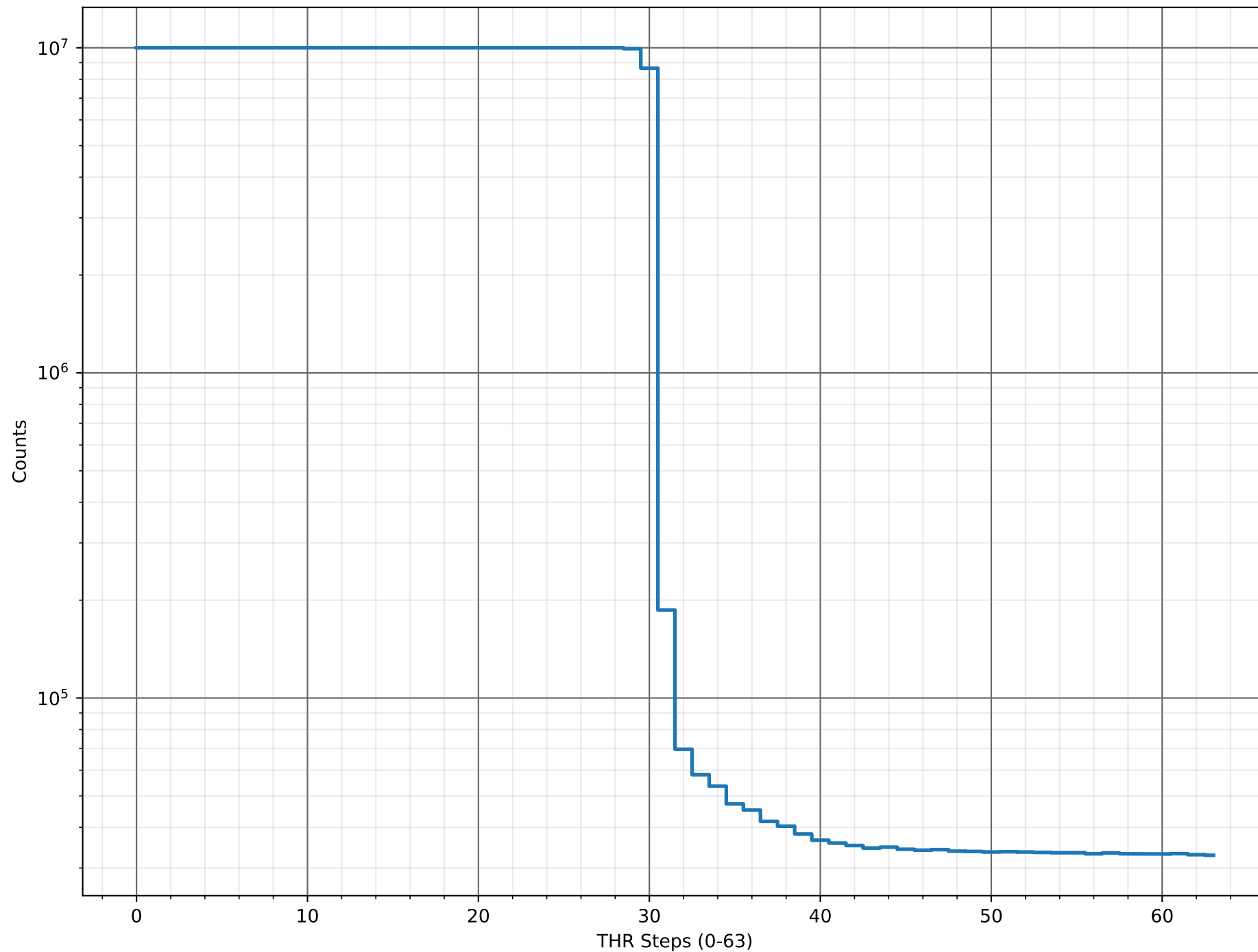
THR Distribution of PMT: A Anode: 17 (UART: 1 FEB: 1 CHIP: 4 CHANNEL: 3)



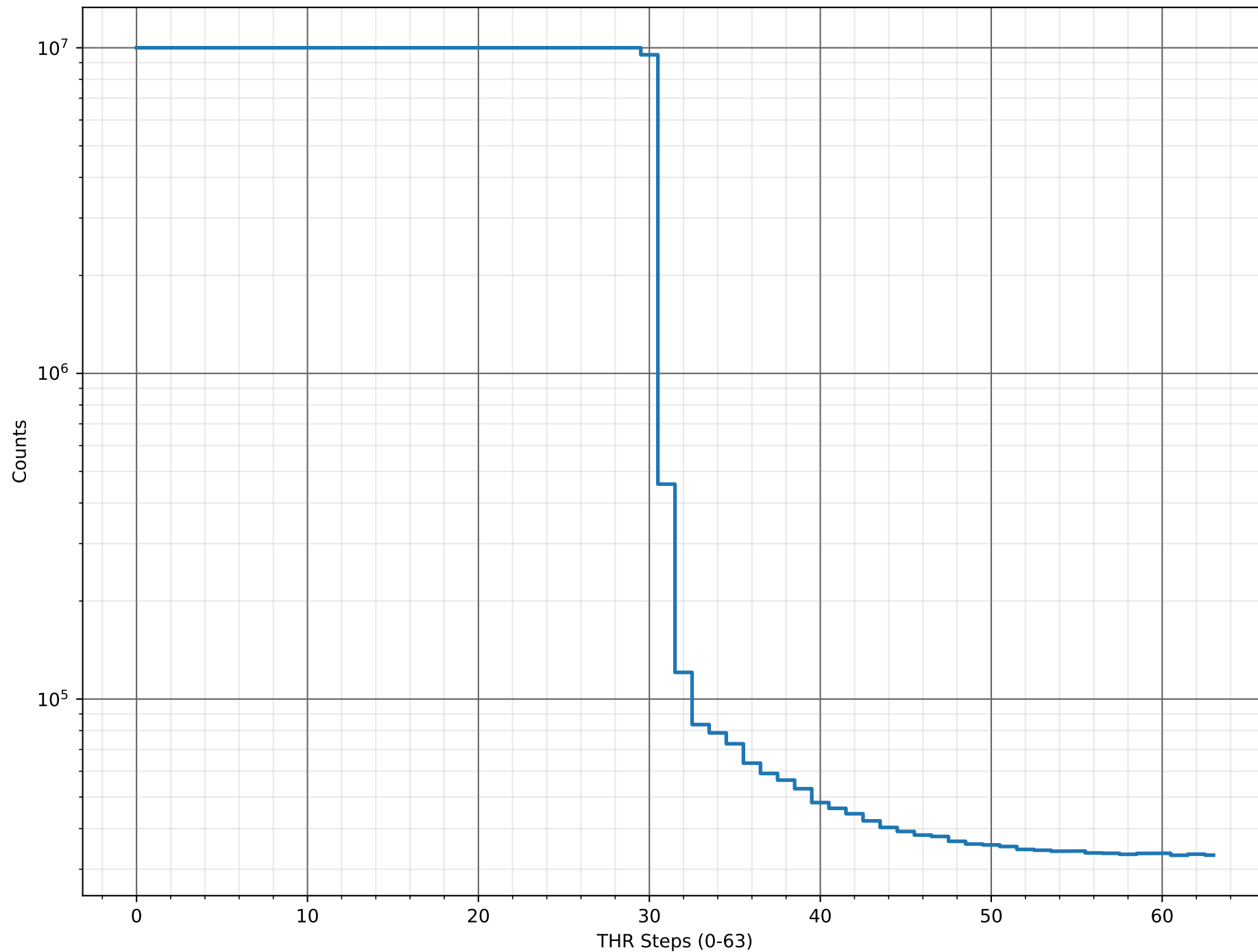
THR Distribution of PMT: A Anode: 18 (UART: 1 FEB: 1 CHIP: 4 CHANNEL: 2)



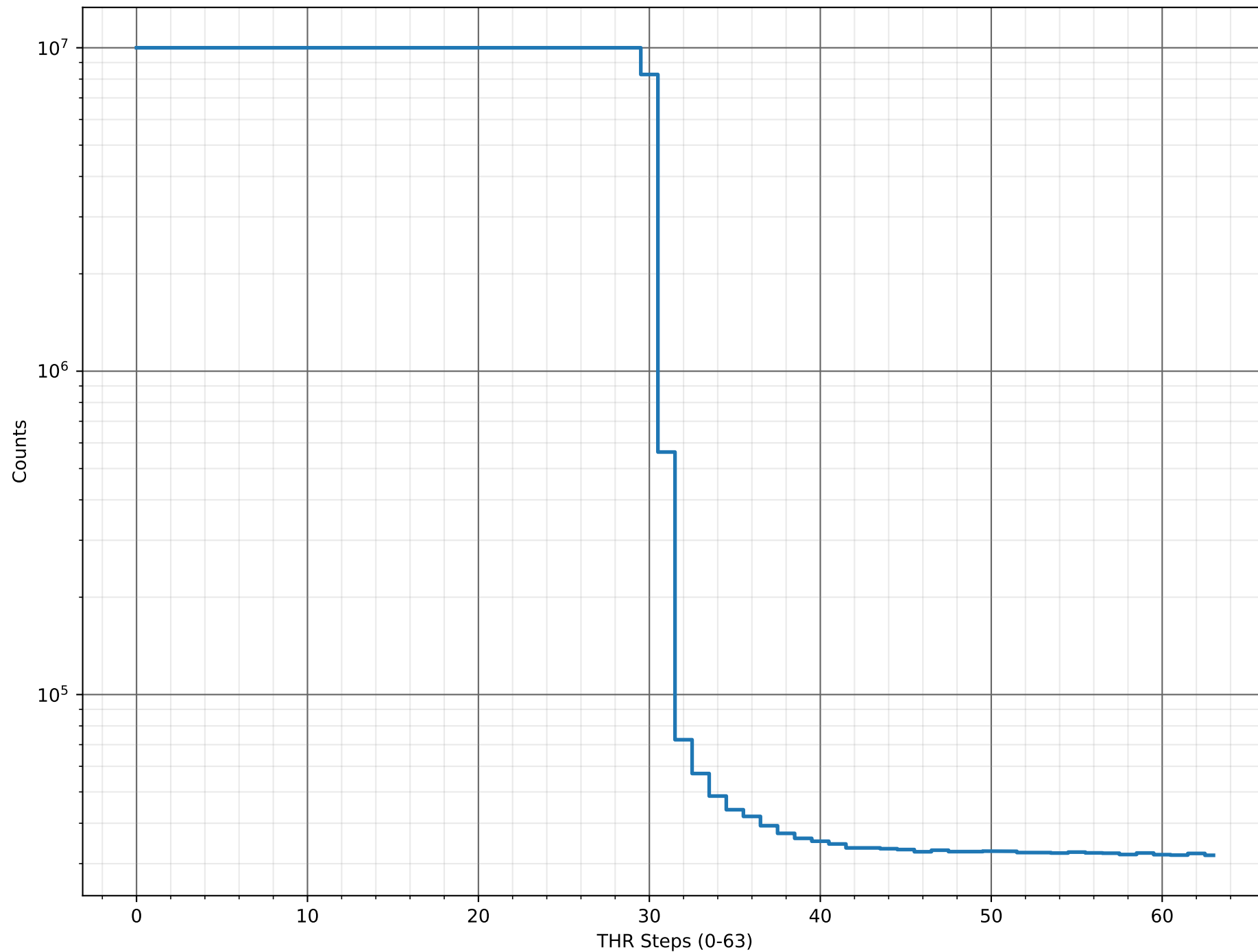
THR Distribution of PMT: A Anode: 19 (UART: 1 FEB: 1 CHIP: 5 CHANNEL: 4)



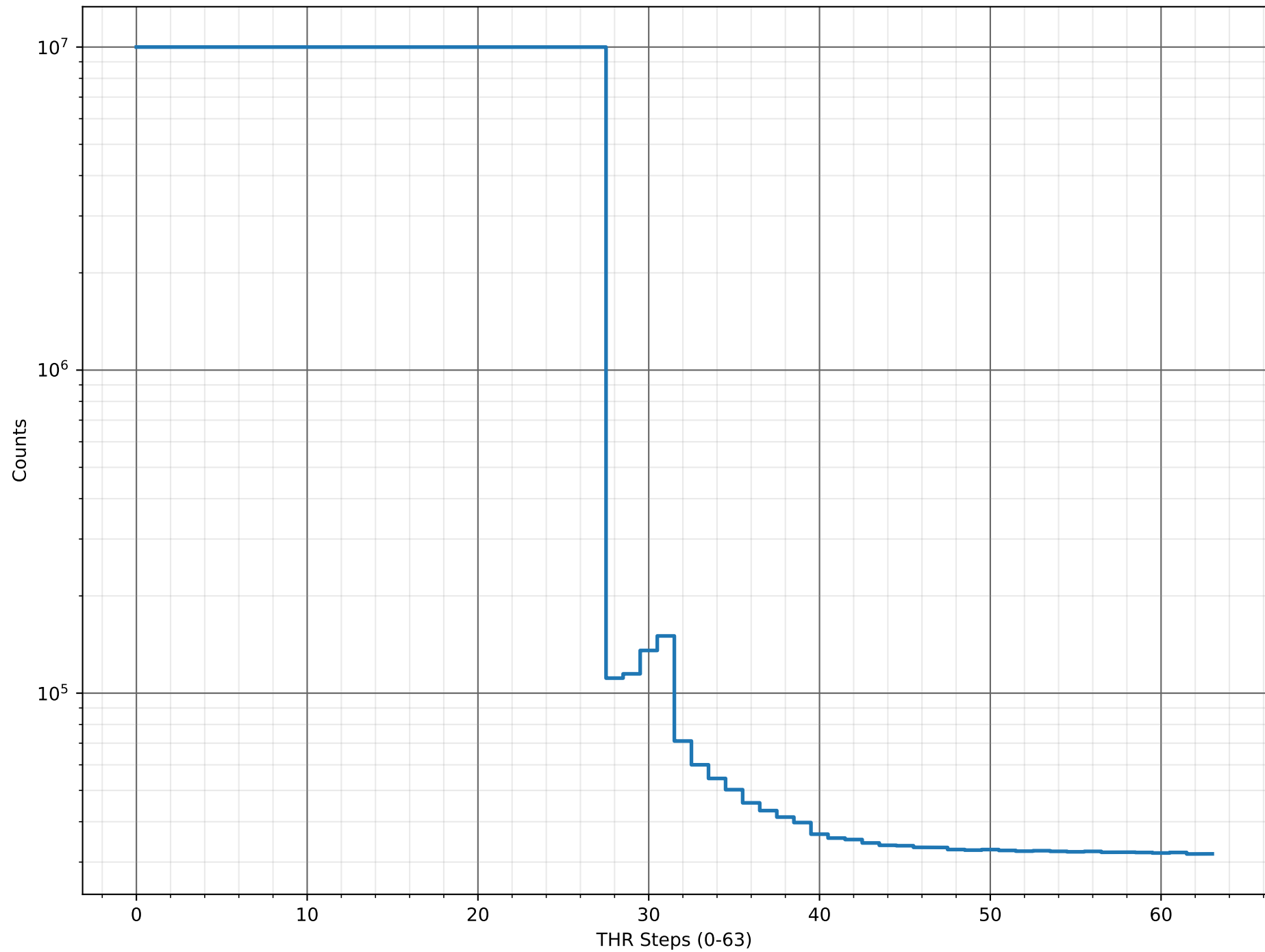
THR Distribution of PMT: A Anode: 20 (UART: 1 FEB: 1 CHIP: 5 CHANNEL: 5)



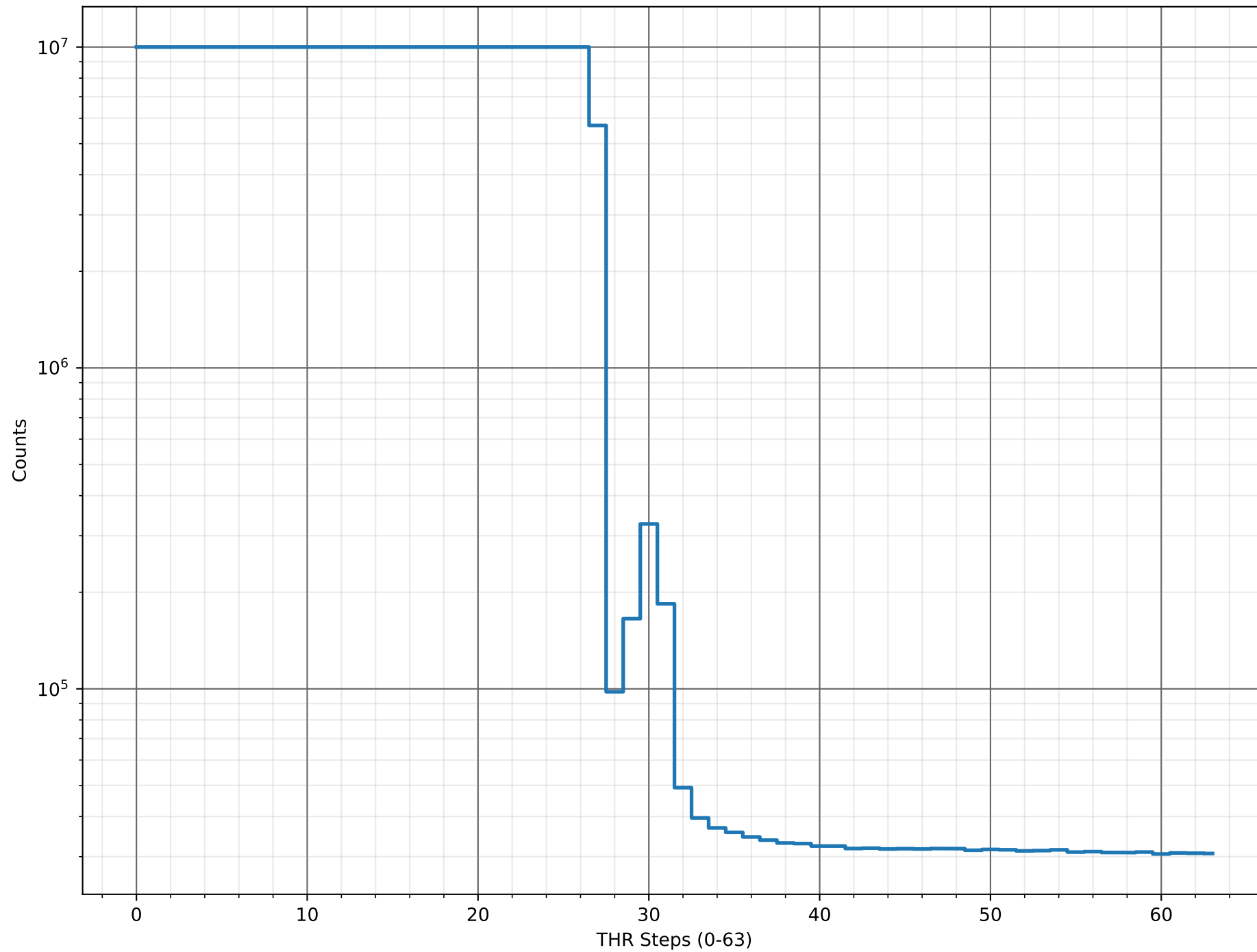
THR Distribution of PMT: A Anode: 21 (UART: 1 FEB: 0 CHIP: 4 CHANNEL: 2)



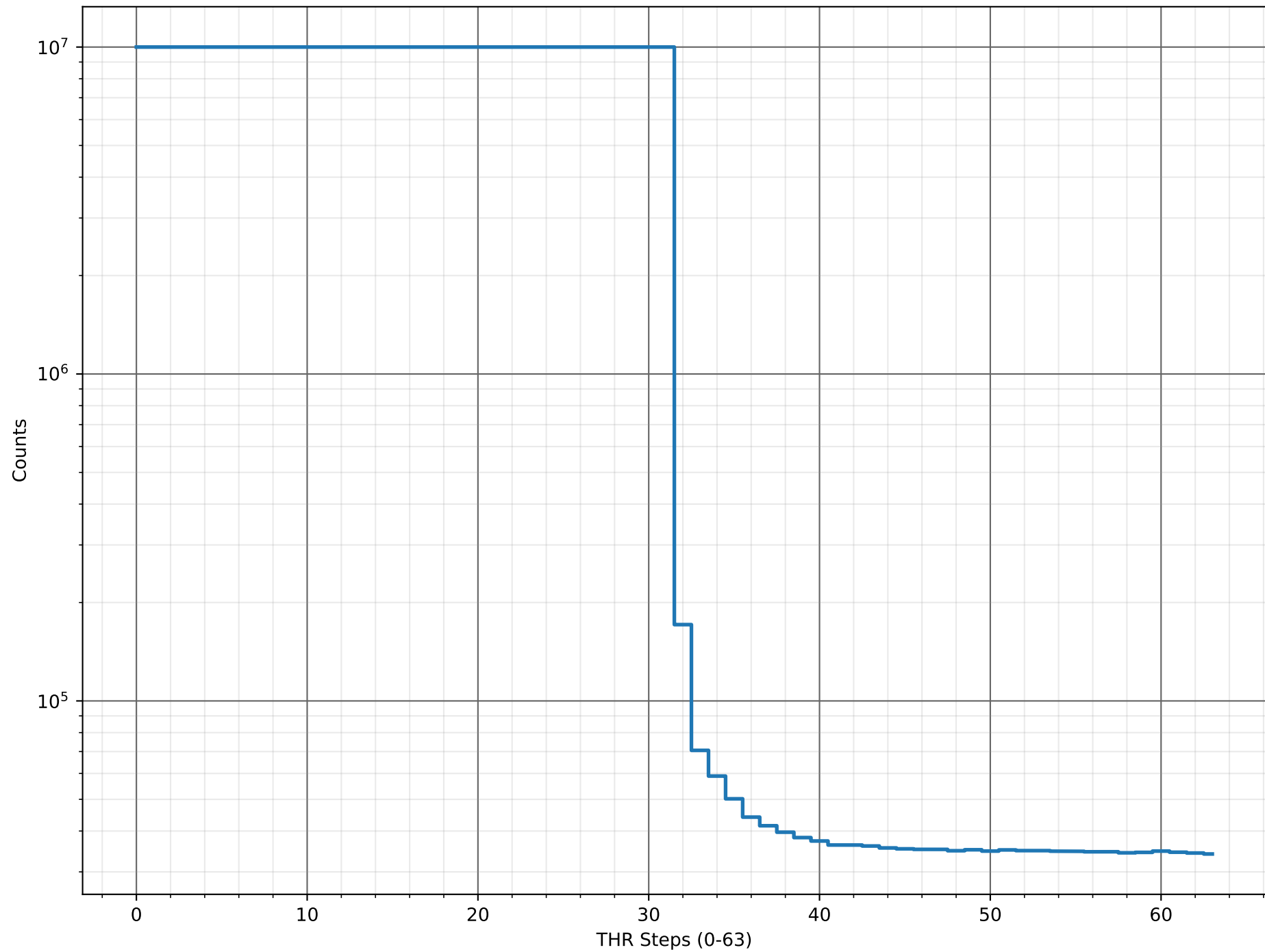
THR Distribution of PMT: A Anode: 22 (UART: 1 FEB: 0 CHIP: 4 CHANNEL: 3)



THR Distribution of PMT: A Anode: 23 (UART: 1 FEB: 0 CHIP: 5 CHANNEL: 5)

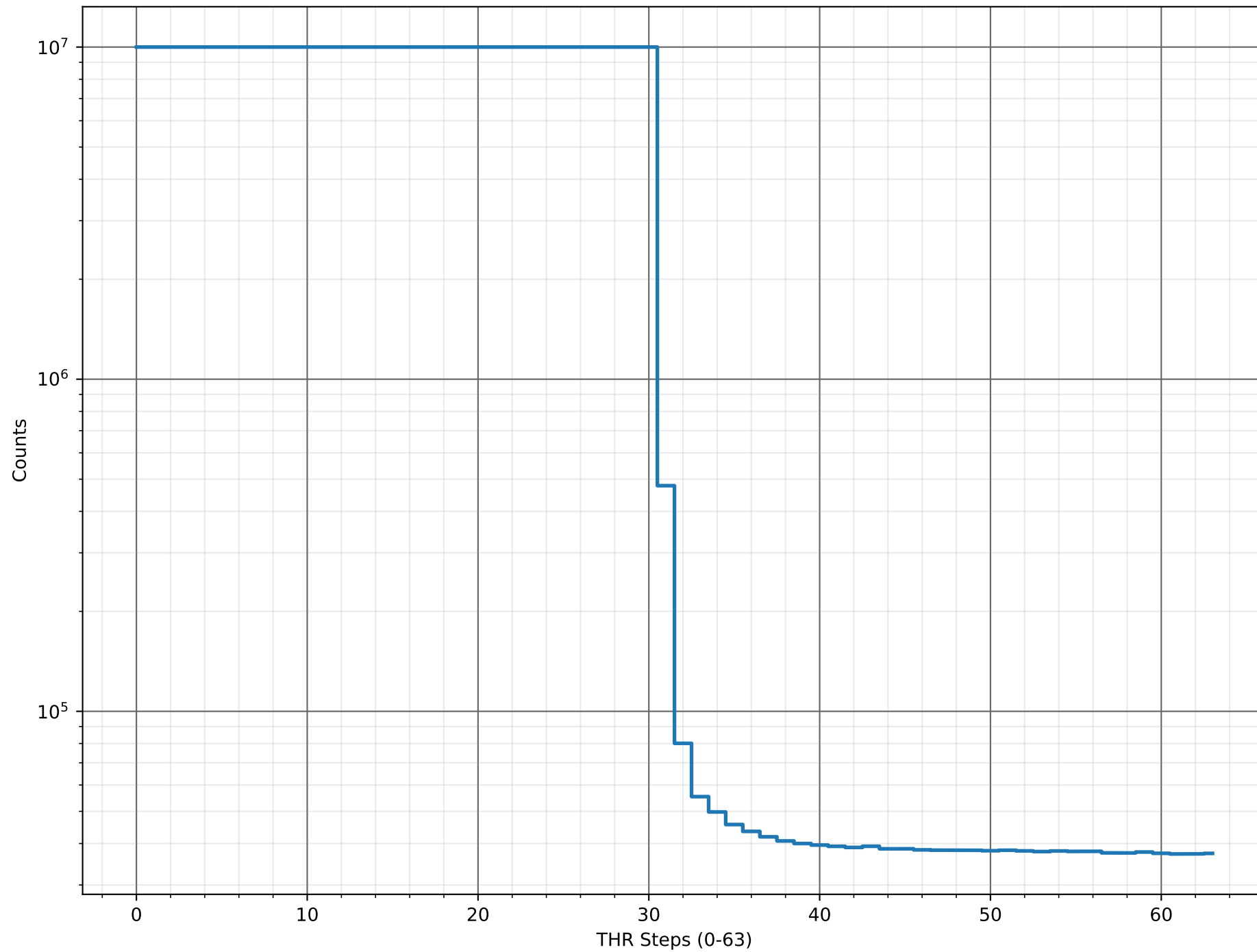


THR Distribution of PMT: A Anode: 24 (UART: 1 FEB: 0 CHIP: 5 CHANNEL: 4)

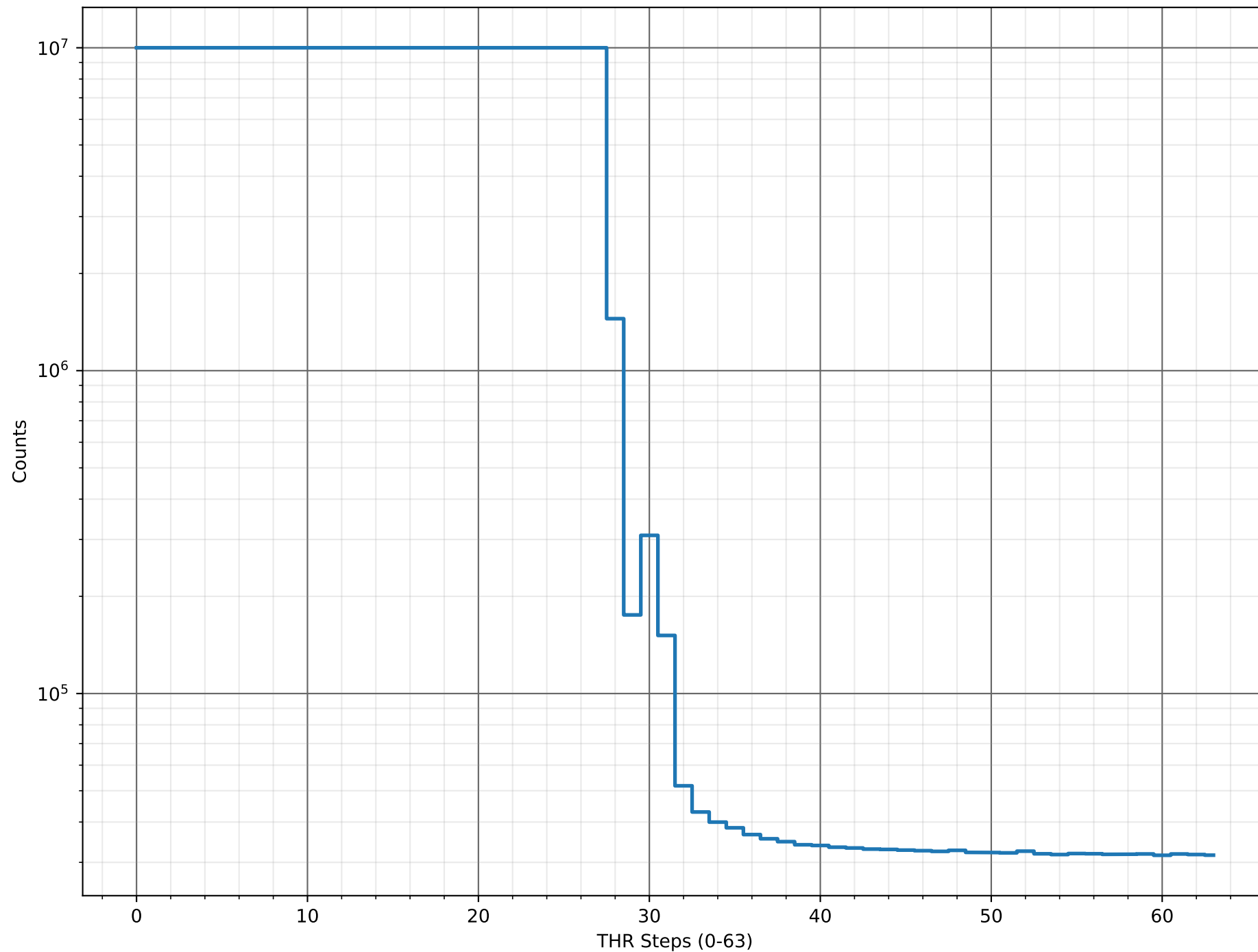




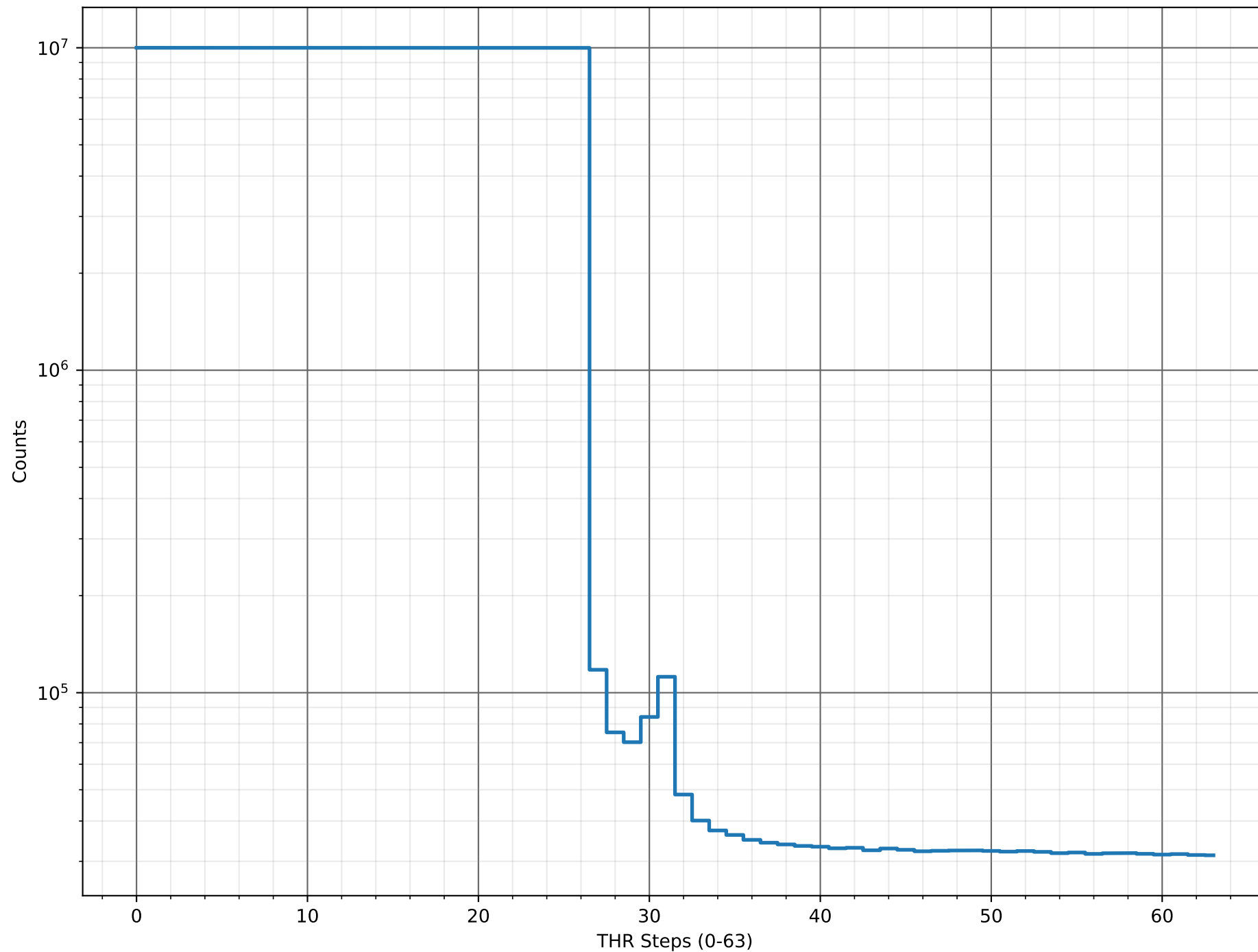
THR Distribution of PMT: A Anode: 25 (UART: 1 FEB: 1 CHIP: 4 CHANNEL: 1)



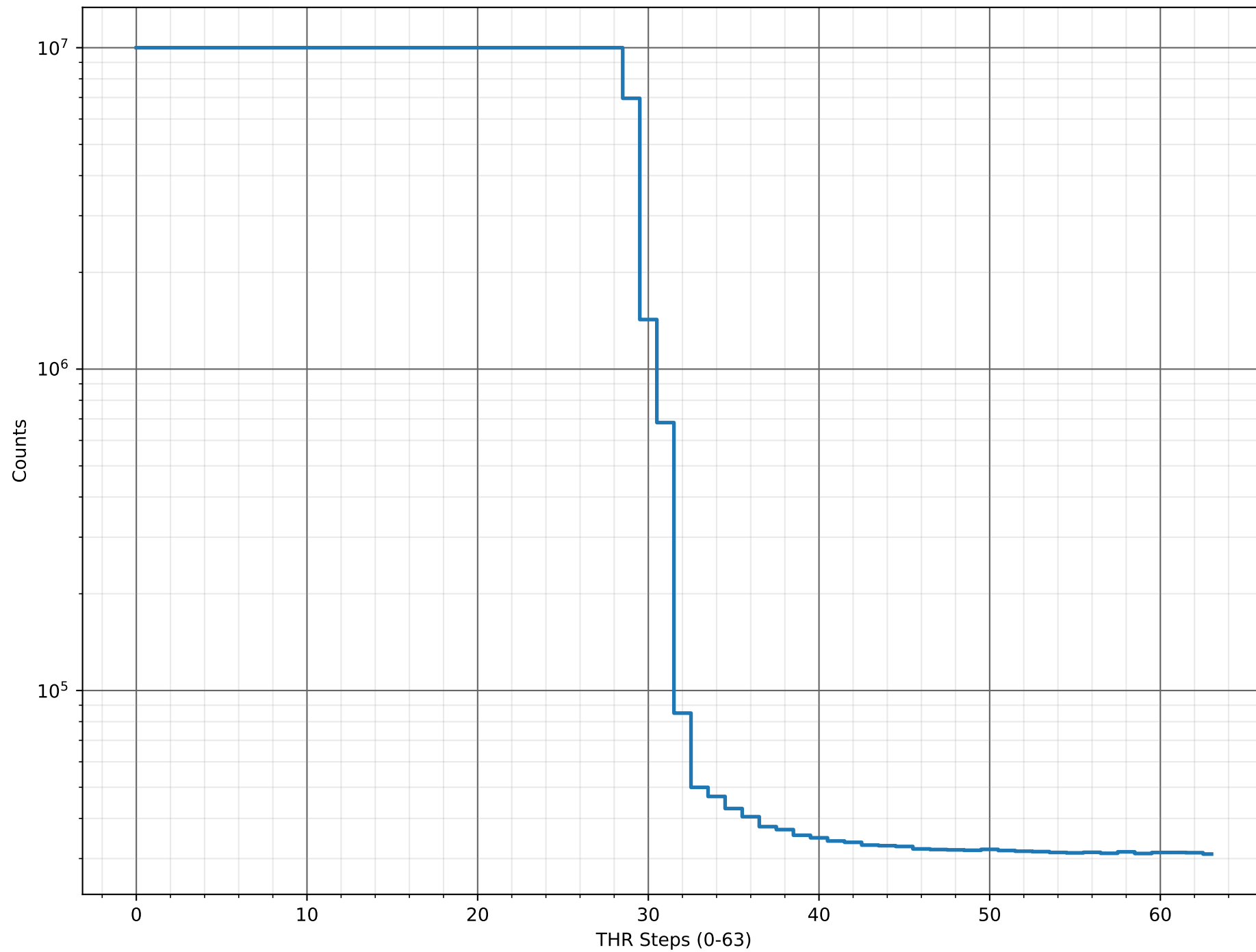
THR Distribution of PMT: A Anode: 26 (UART: 1 FEB: 1 CHIP: 4 CHANNEL: 0)



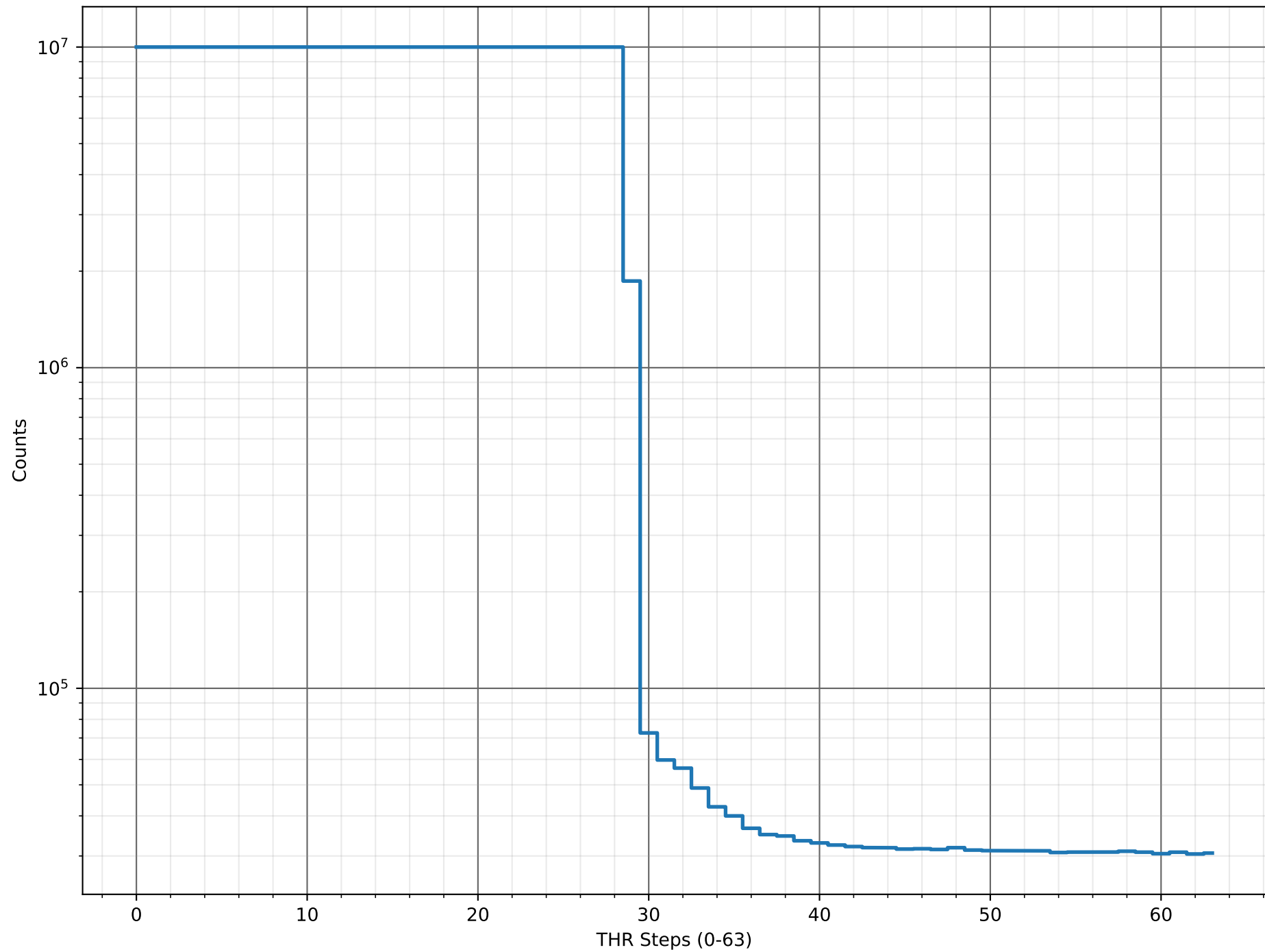
THR Distribution of PMT: A Anode: 27 (UART: 1 FEB: 1 CHIP: 5 CHANNEL: 7)



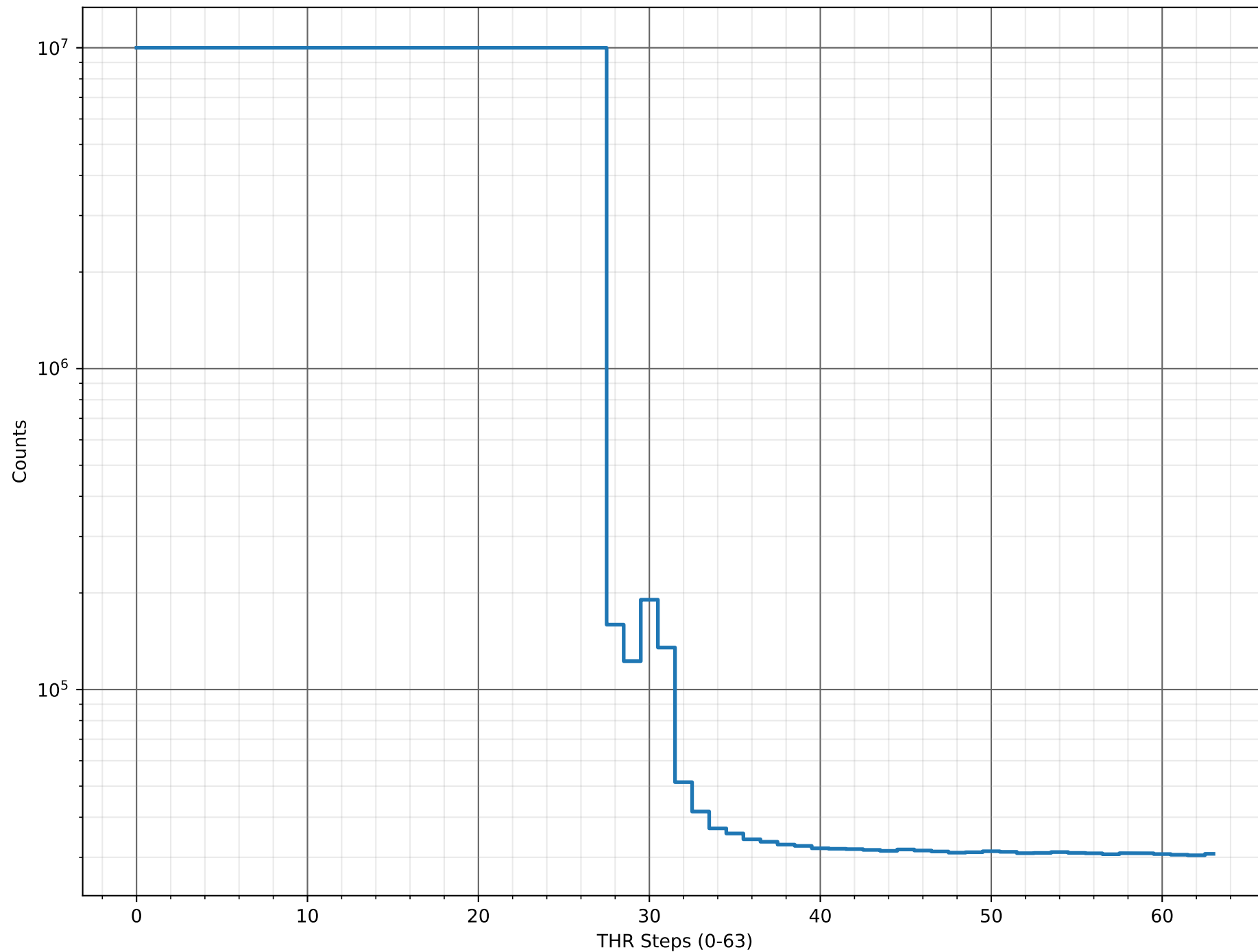
THR Distribution of PMT: A Anode: 28 (UART: 1 FEB: 1 CHIP: 5 CHANNEL: 6)



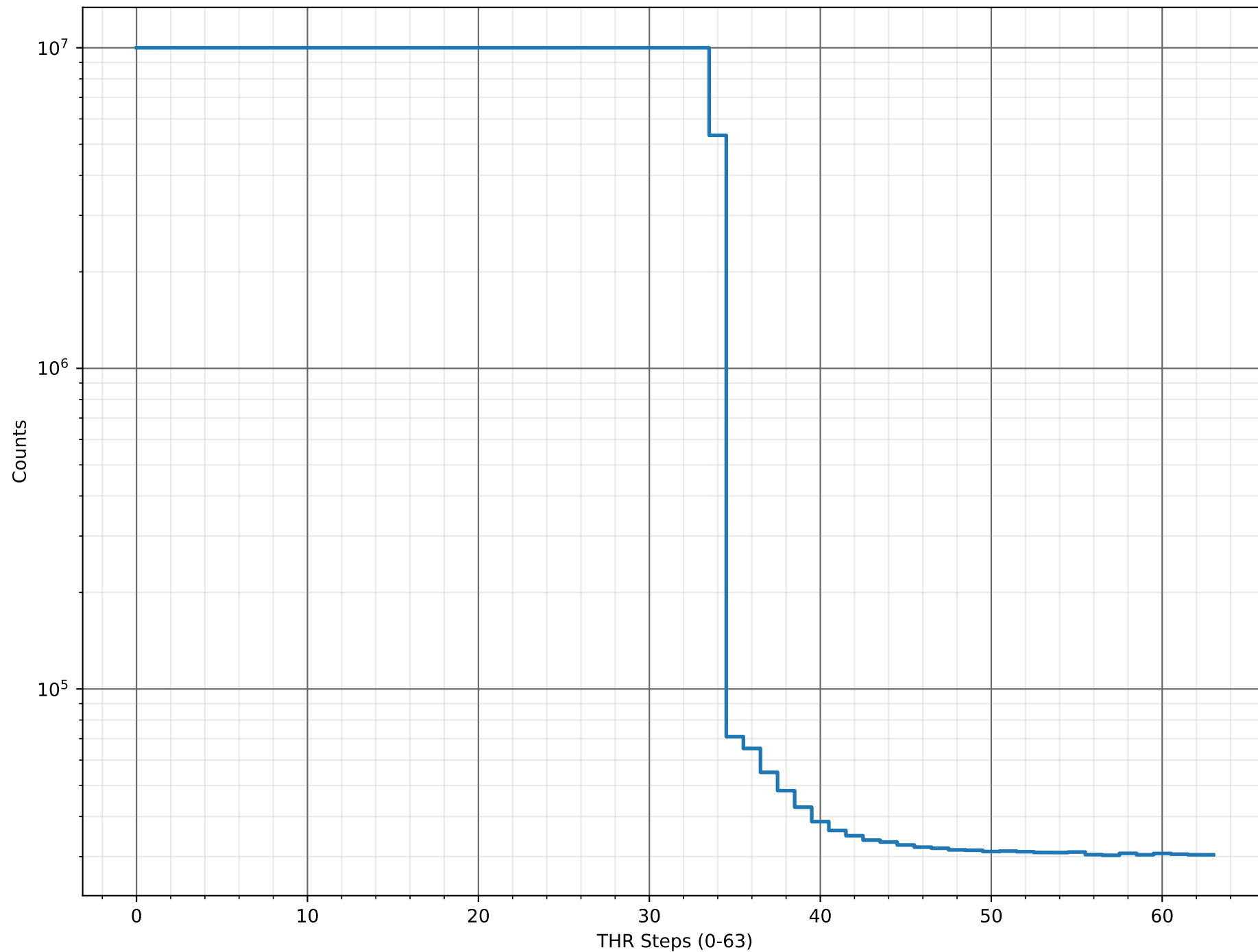
THR Distribution of PMT: A Anode: 29 (UART: 1 FEB: 0 CHIP: 4 CHANNEL: 1)



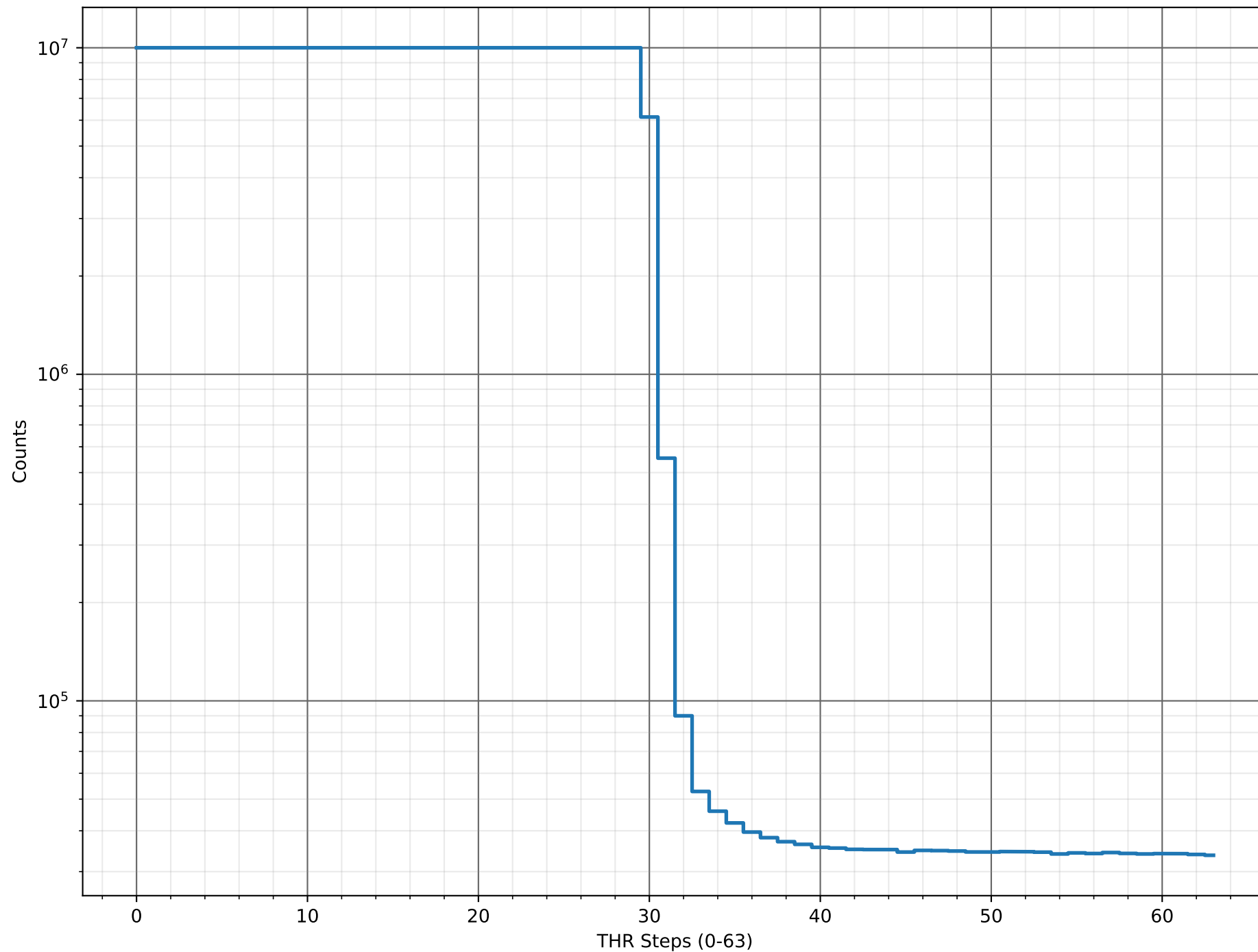
THR Distribution of PMT: A Anode: 30 (UART: 1 FEB: 0 CHIP: 4 CHANNEL: 0)



THR Distribution of PMT: A Anode: 31 (UART: 1 FEB: 0 CHIP: 5 CHANNEL: 7)

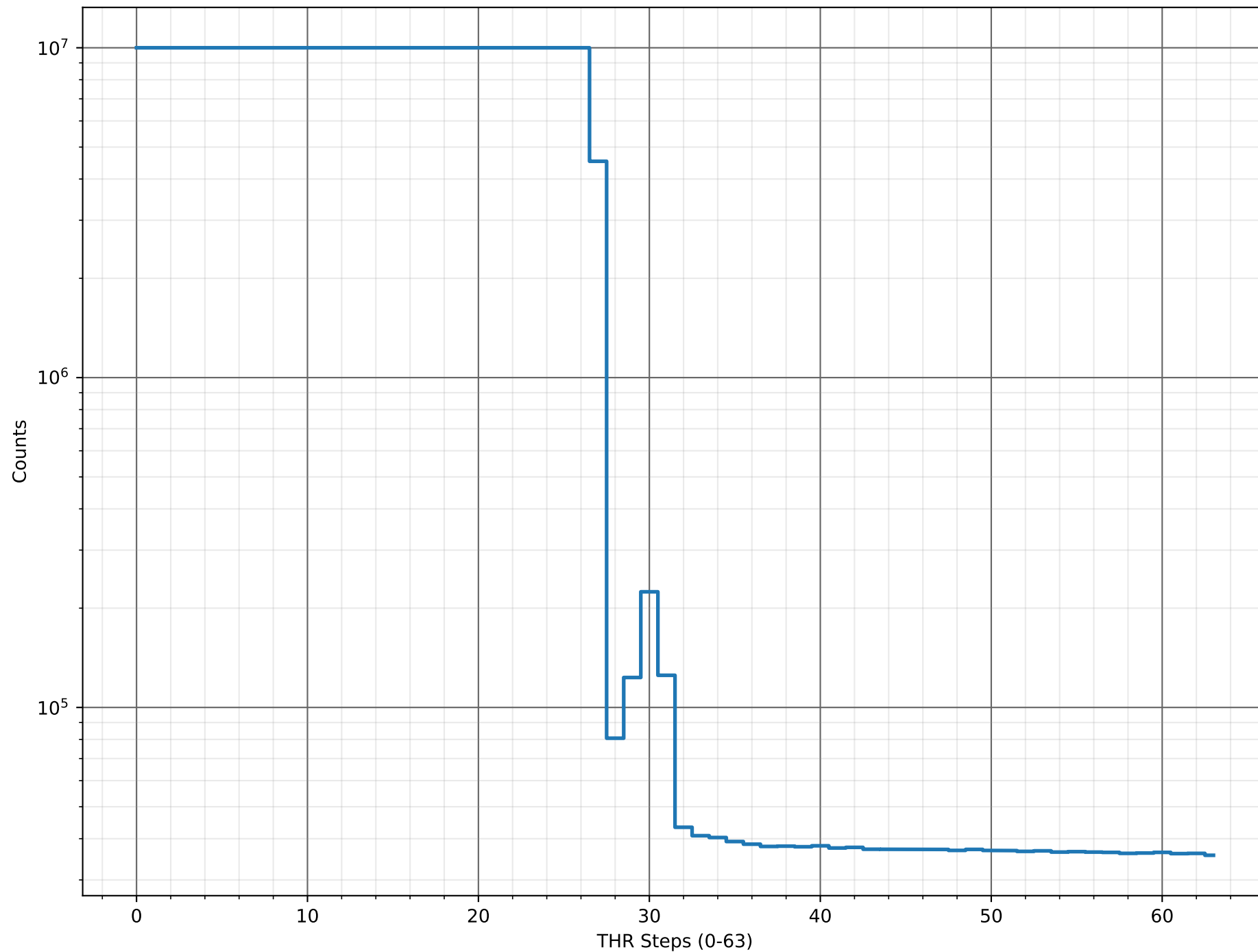


THR Distribution of PMT: A Anode: 32 (UART: 1 FEB: 0 CHIP: 5 CHANNEL: 6)

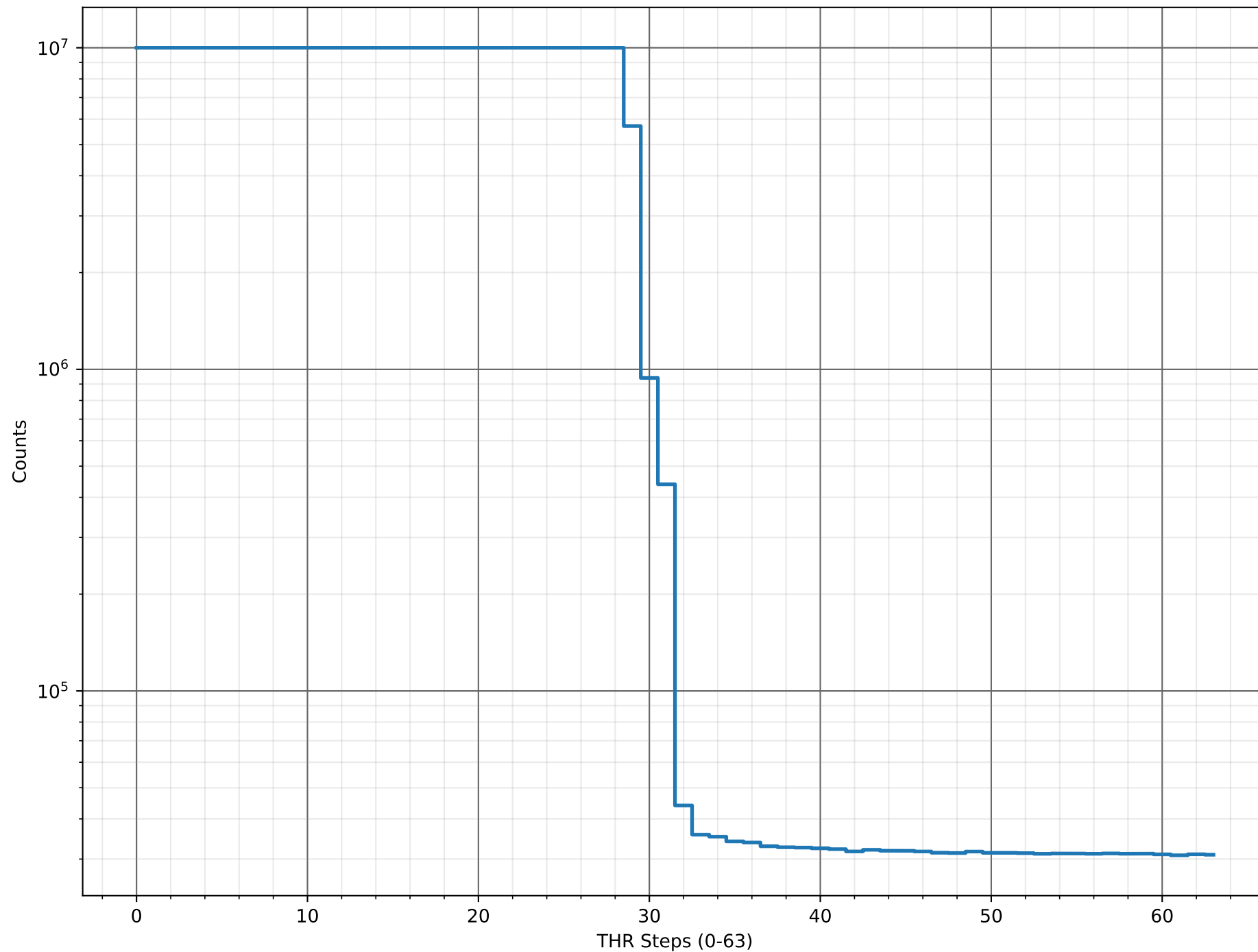




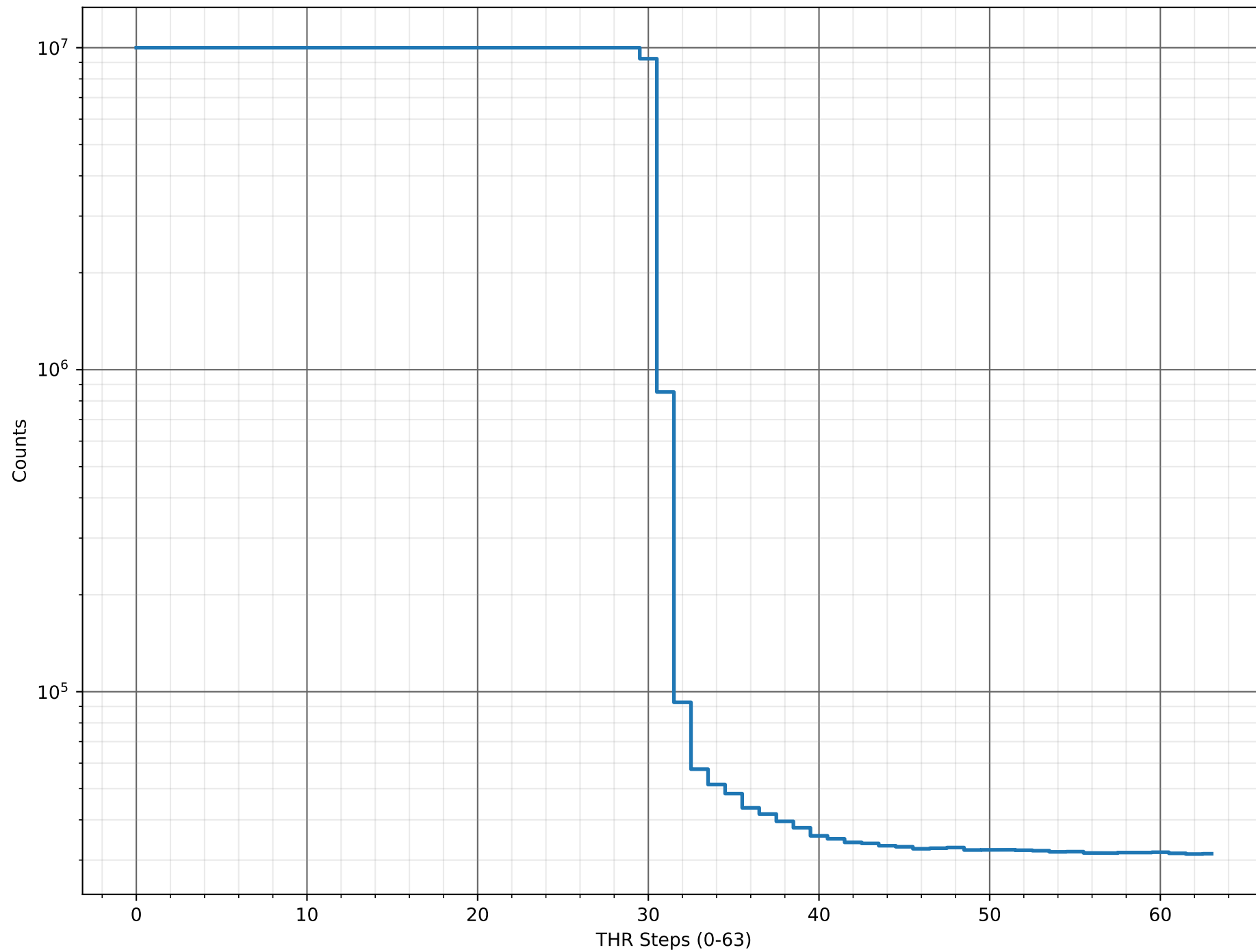
THR Distribution of PMT: A Anode: 33 (UART: 1 FEB: 1 CHIP: 6 CHANNEL: 6)



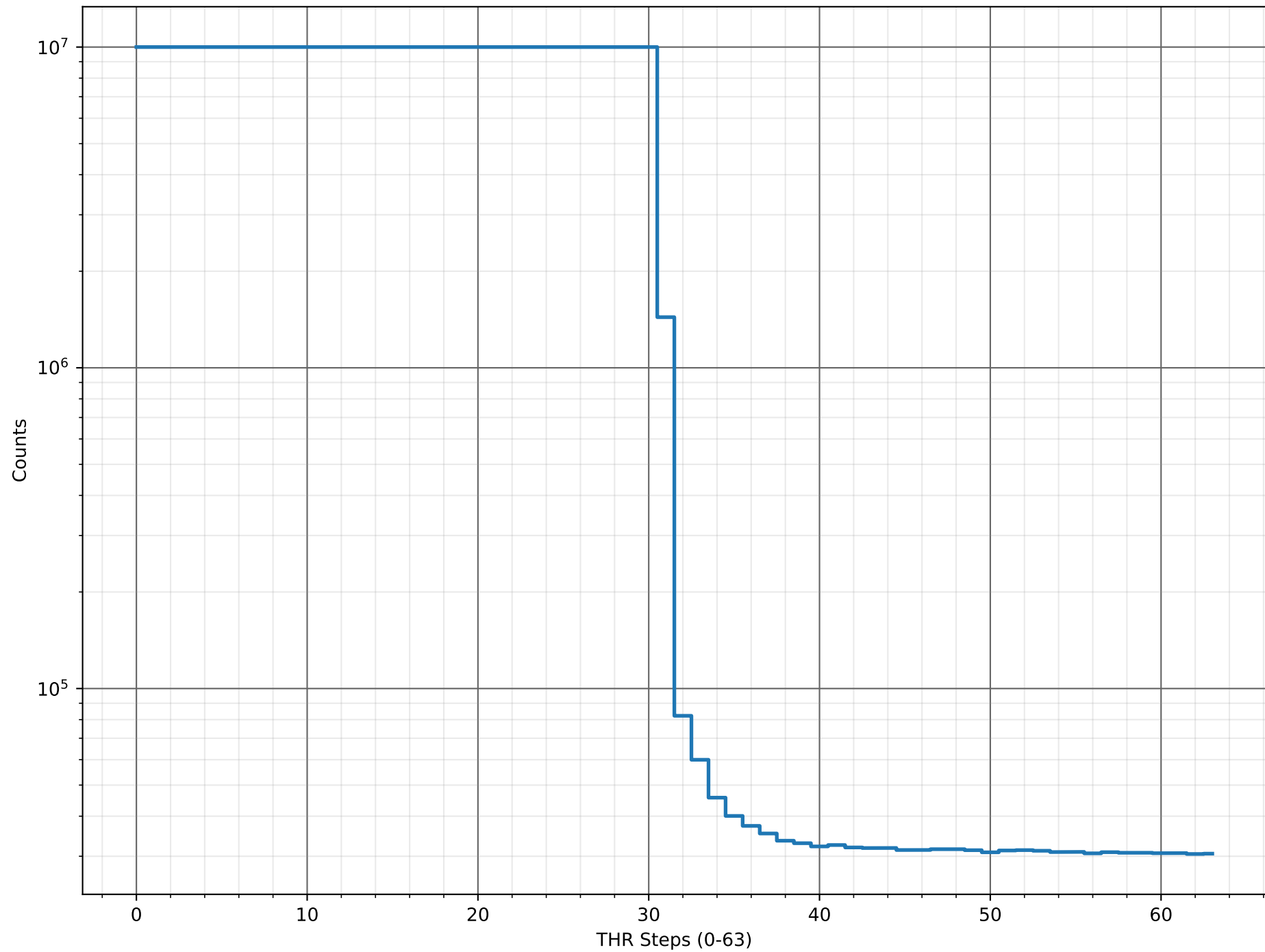
THR Distribution of PMT: A Anode: 34 (UART: 1 FEB: 1 CHIP: 6 CHANNEL: 7)



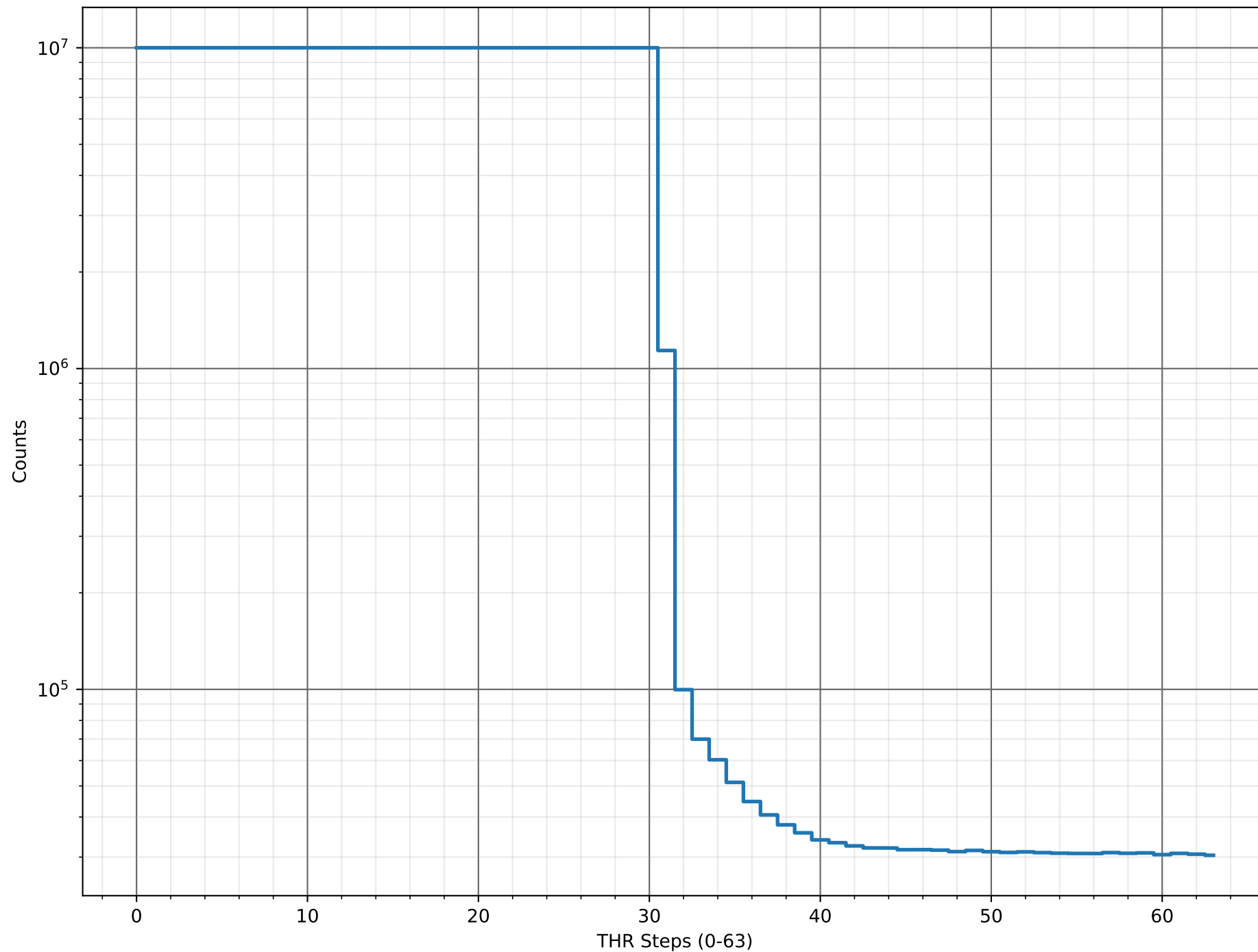
THR Distribution of PMT: A Anode: 35 (UART: 1 FEB: 1 CHIP: 7 CHANNEL: 1)



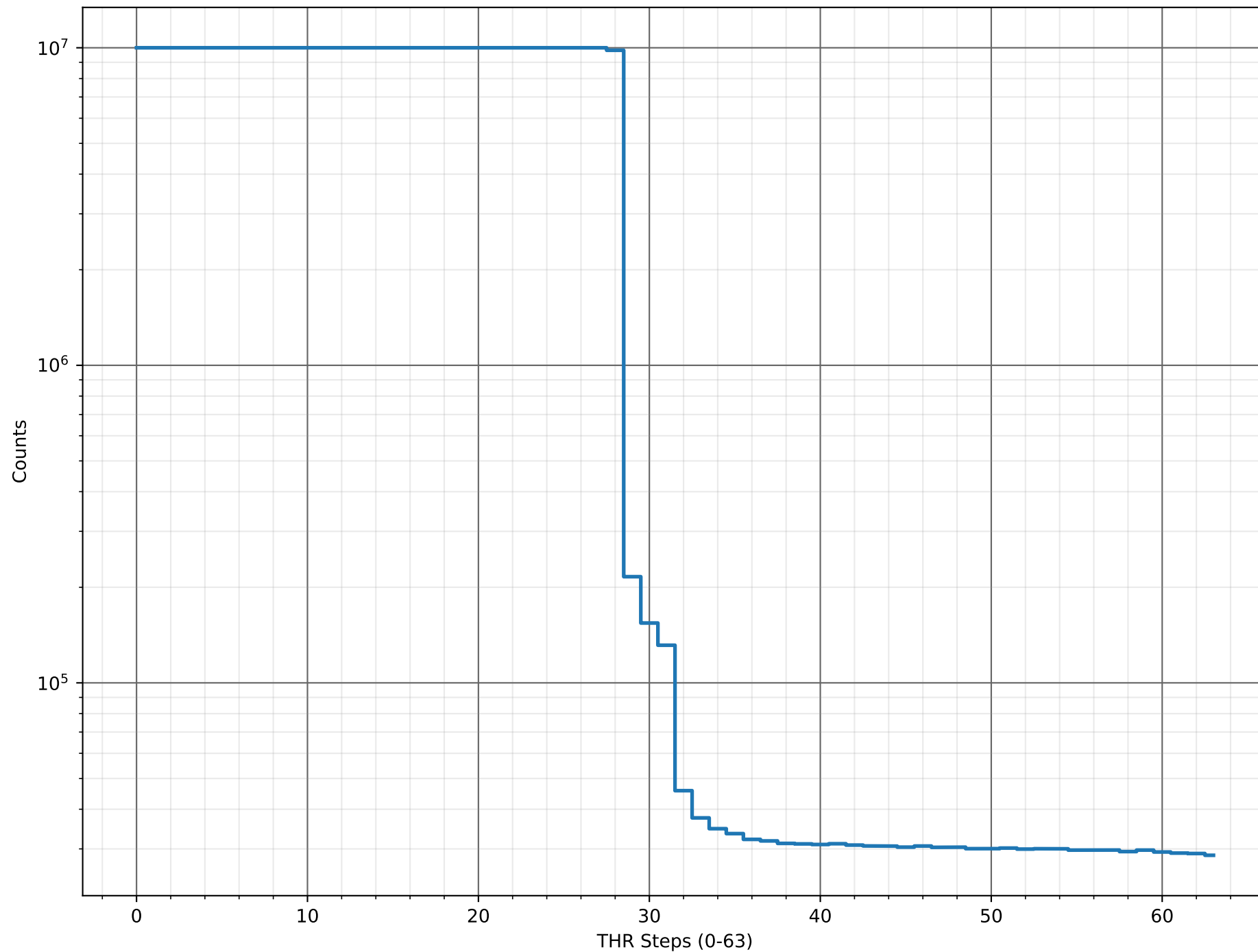
THR Distribution of PMT: A Anode: 36 (UART: 1 FEB: 1 CHIP: 7 CHANNEL: 0)



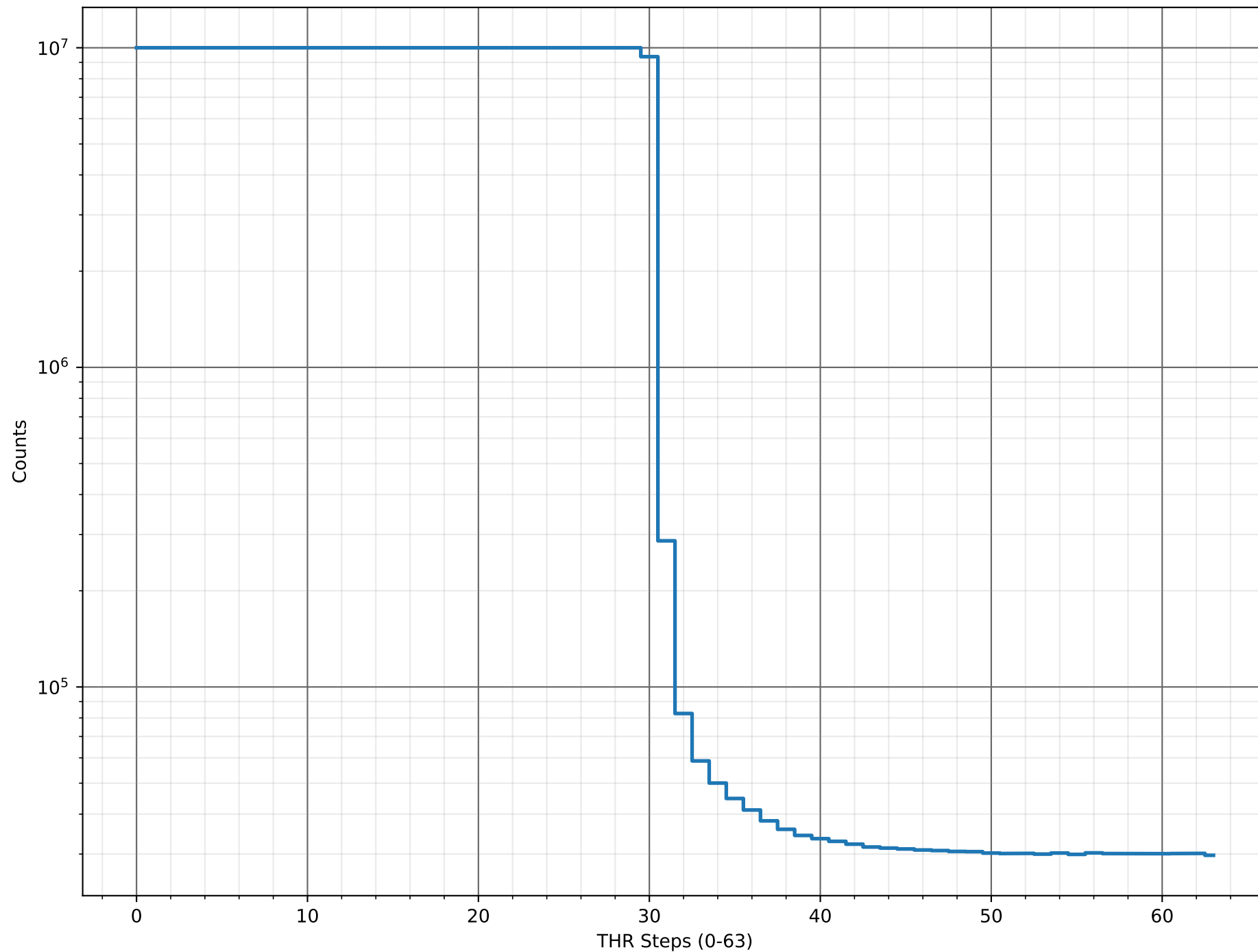
THR Distribution of PMT: A Anode: 37 (UART: 1 FEB: 0 CHIP: 6 CHANNEL: 7)



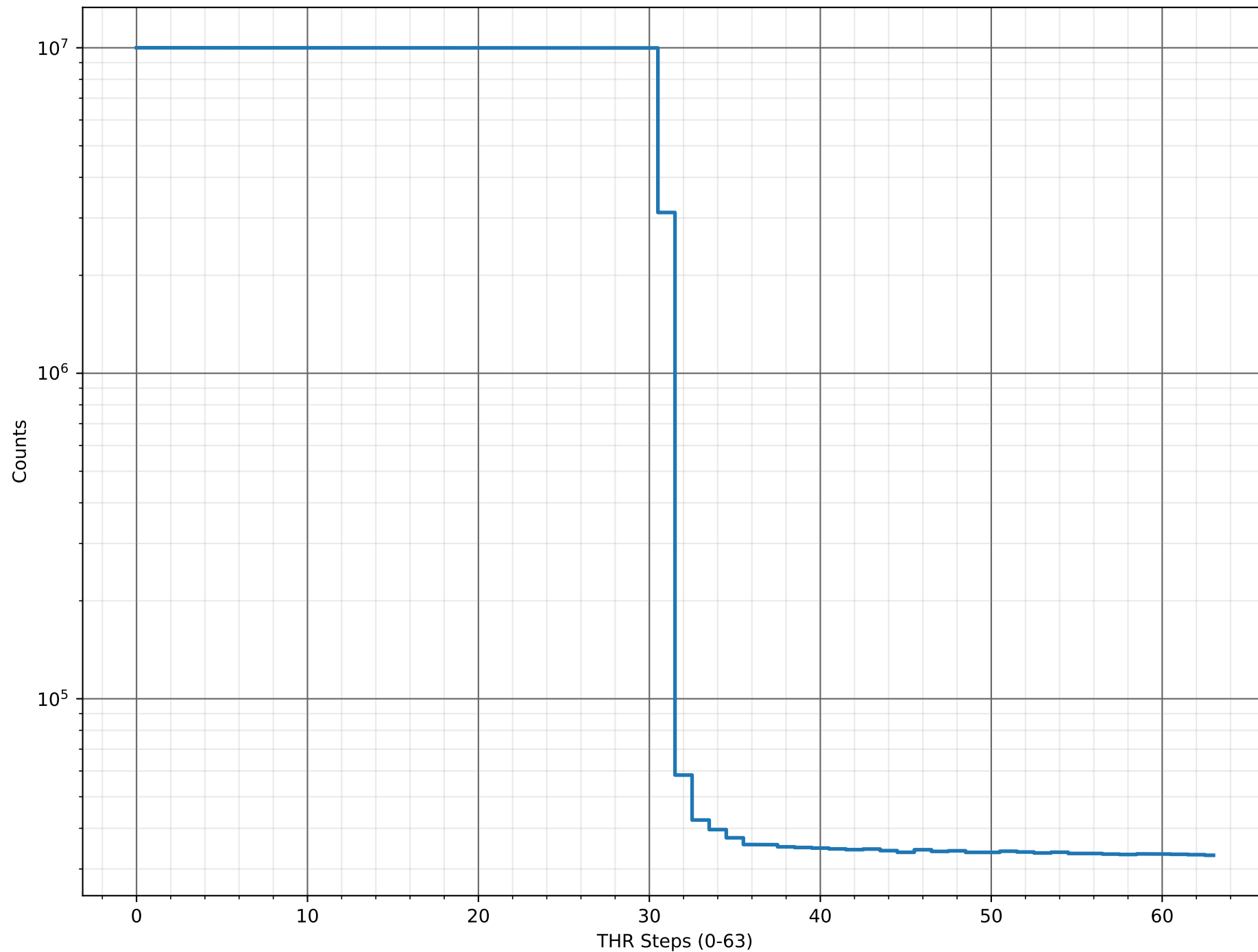
THR Distribution of PMT: A Anode: 38 (UART: 1 FEB: 0 CHIP: 6 CHANNEL: 6)



THR Distribution of PMT: A Anode: 39 (UART: 1 FEB: 0 CHIP: 7 CHANNEL: 0)

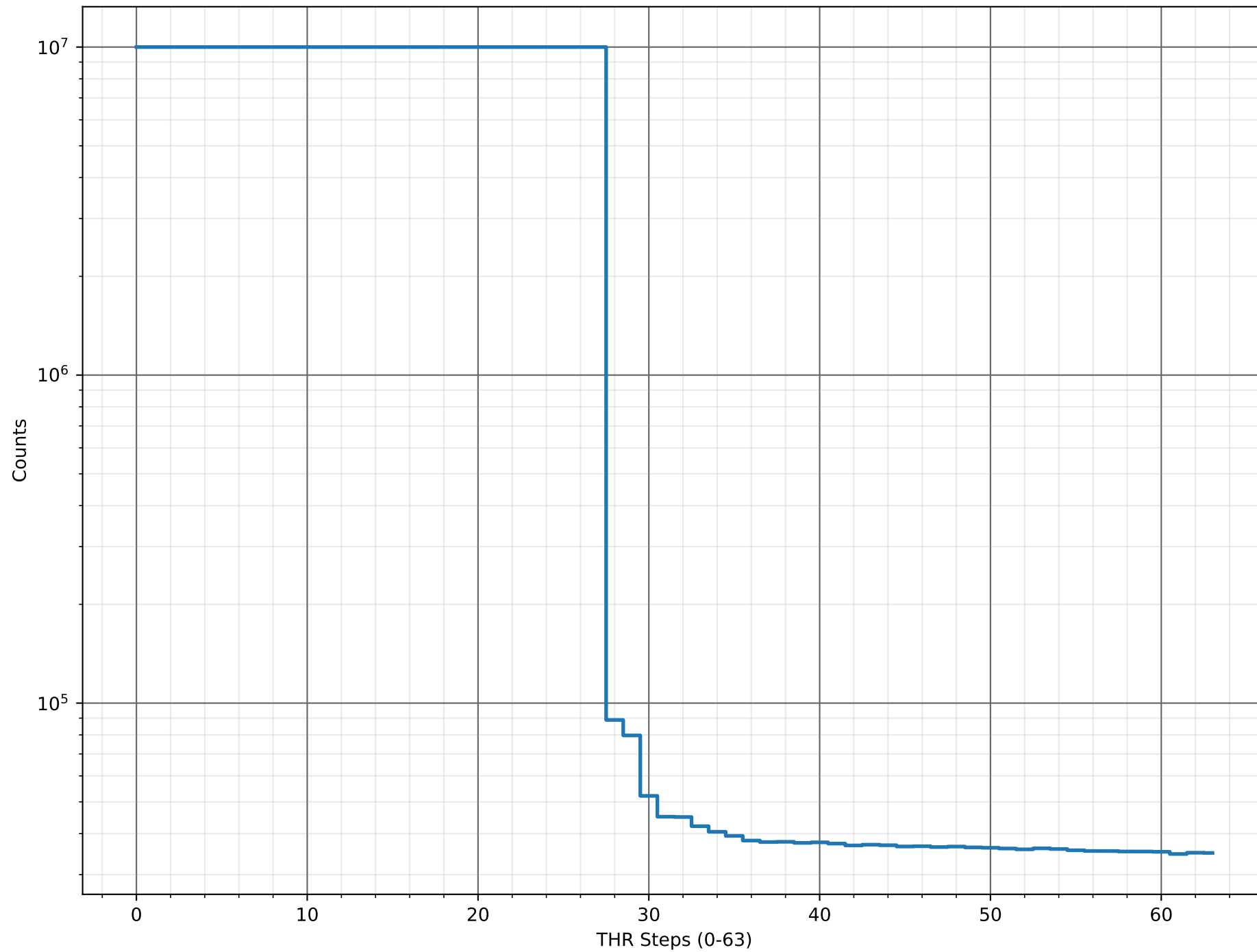


THR Distribution of PMT: A Anode: 40 (UART: 1 FEB: 0 CHIP: 7 CHANNEL: 1)

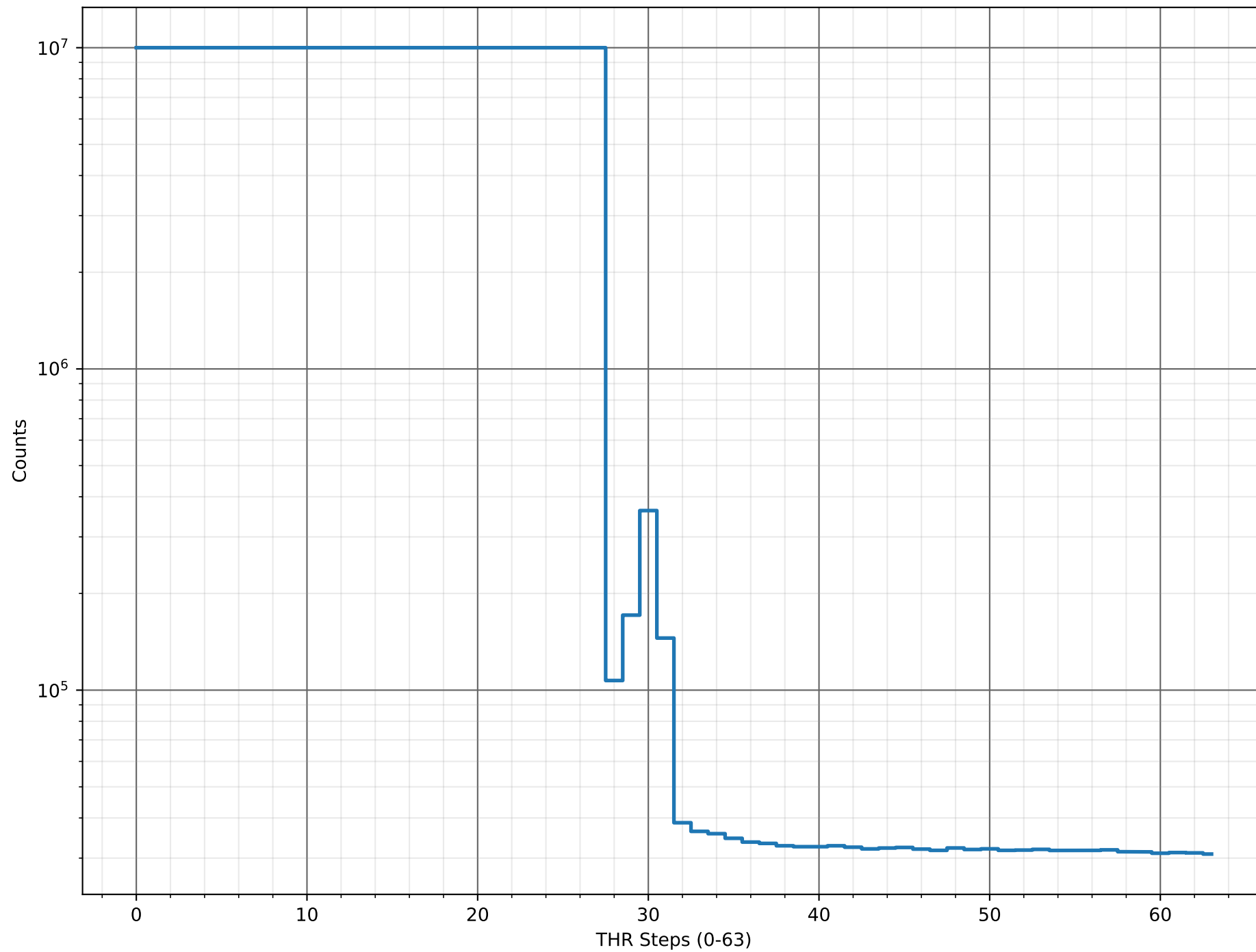




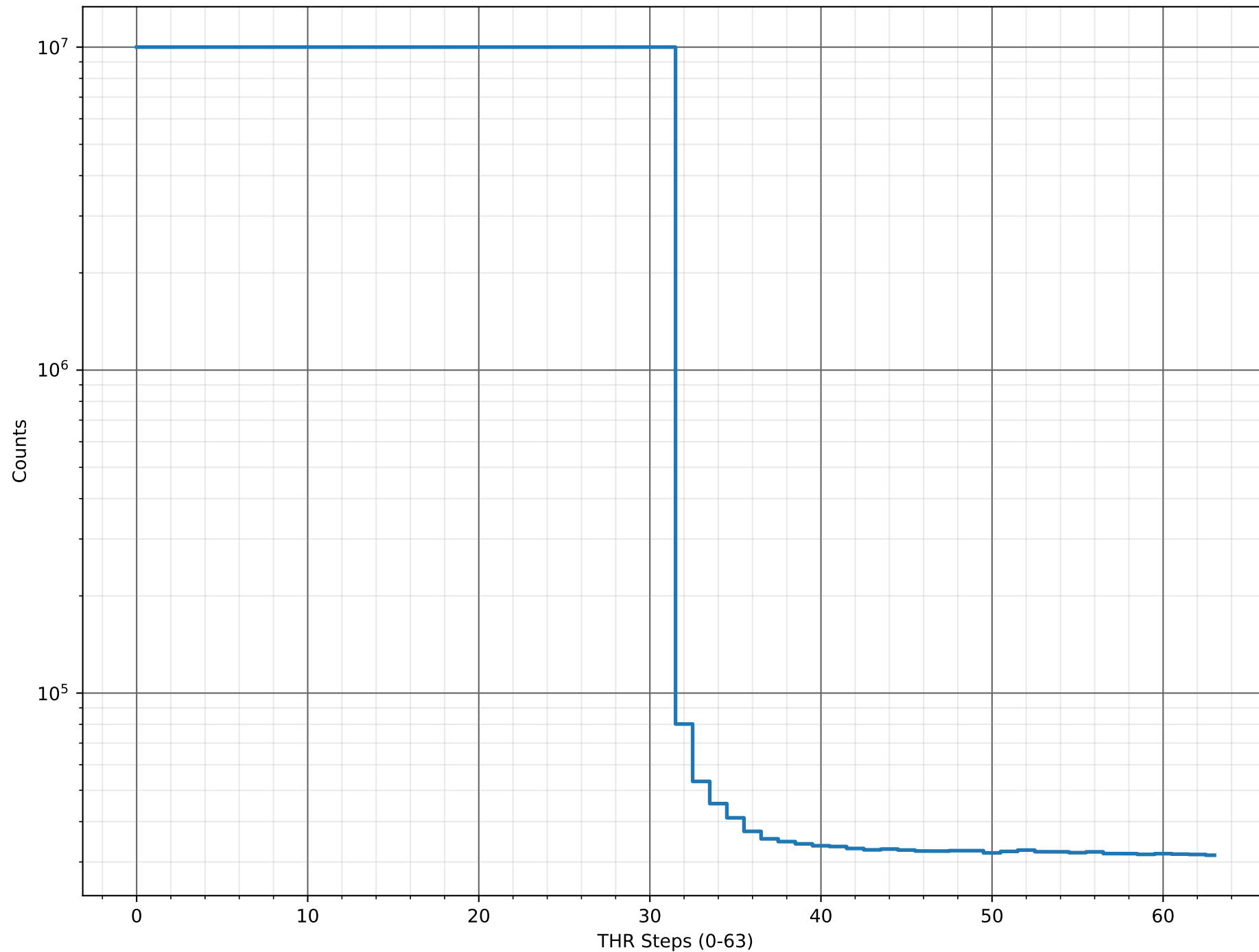
THR Distribution of PMT: A Anode: 41 (UART: 1 FEB: 1 CHIP: 6 CHANNEL: 4)



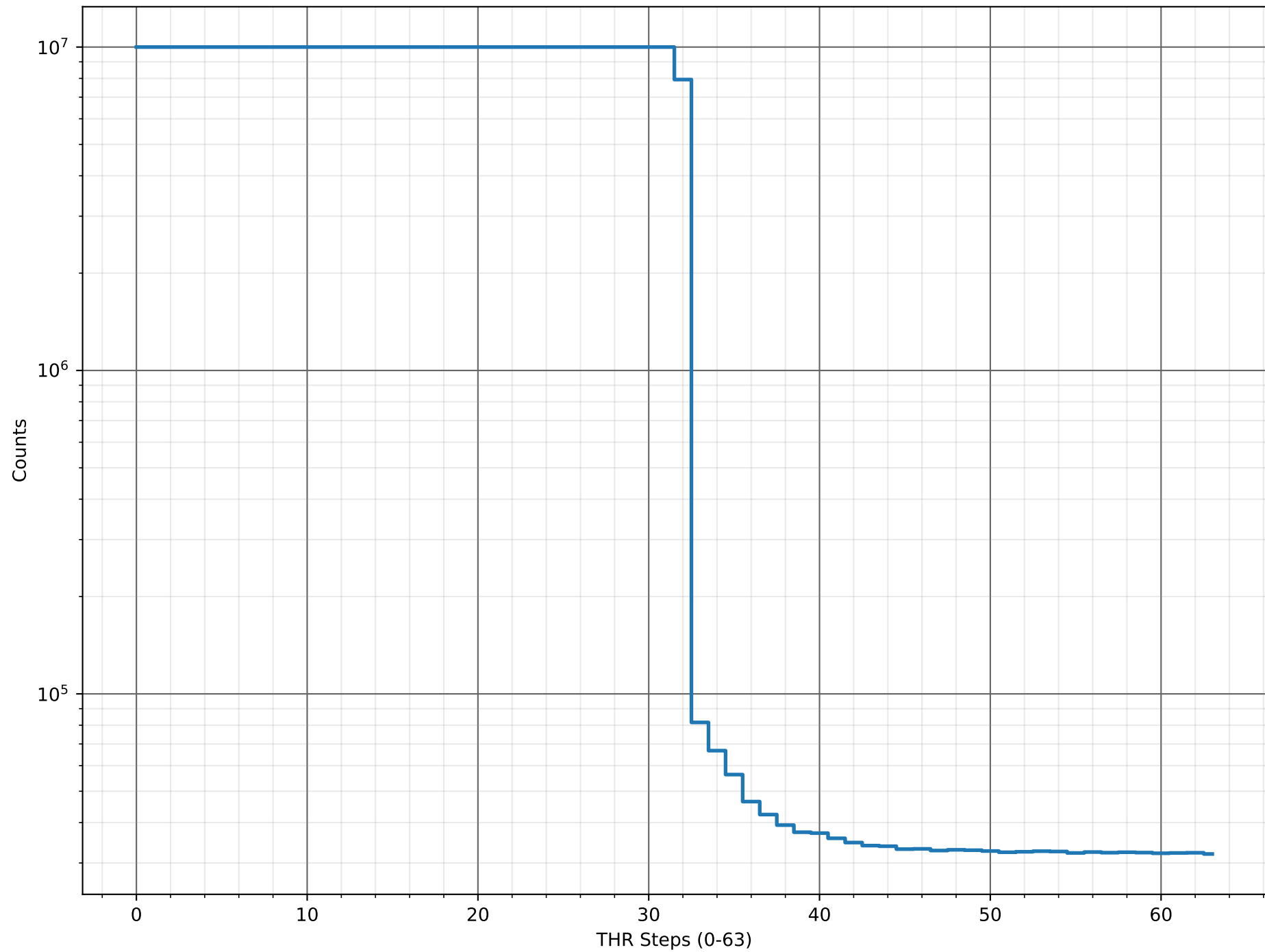
THR Distribution of PMT: A Anode: 42 (UART: 1 FEB: 1 CHIP: 6 CHANNEL: 5)



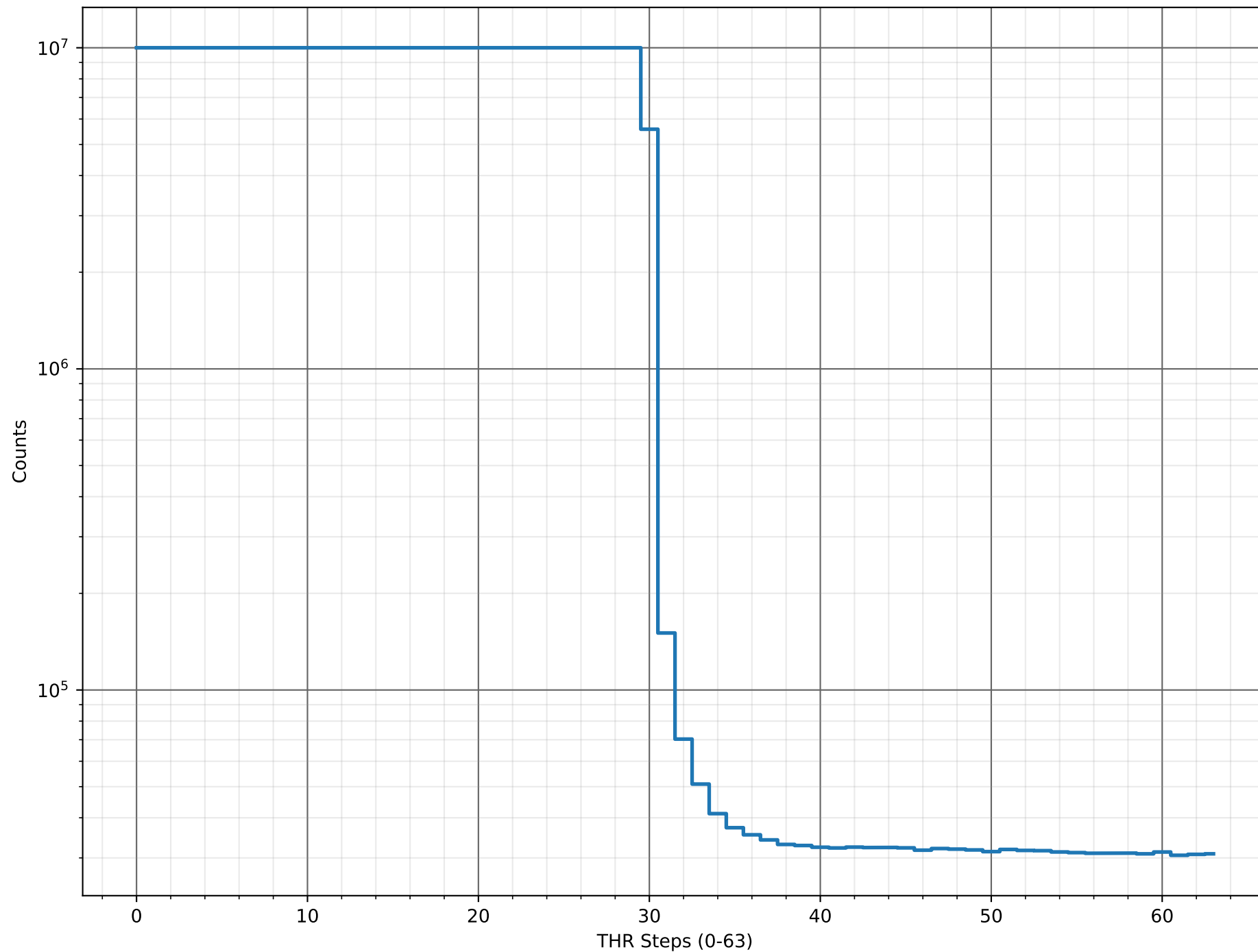
THR Distribution of PMT: A Anode: 43 (UART: 1 FEB: 1 CHIP: 7 CHANNEL: 3)



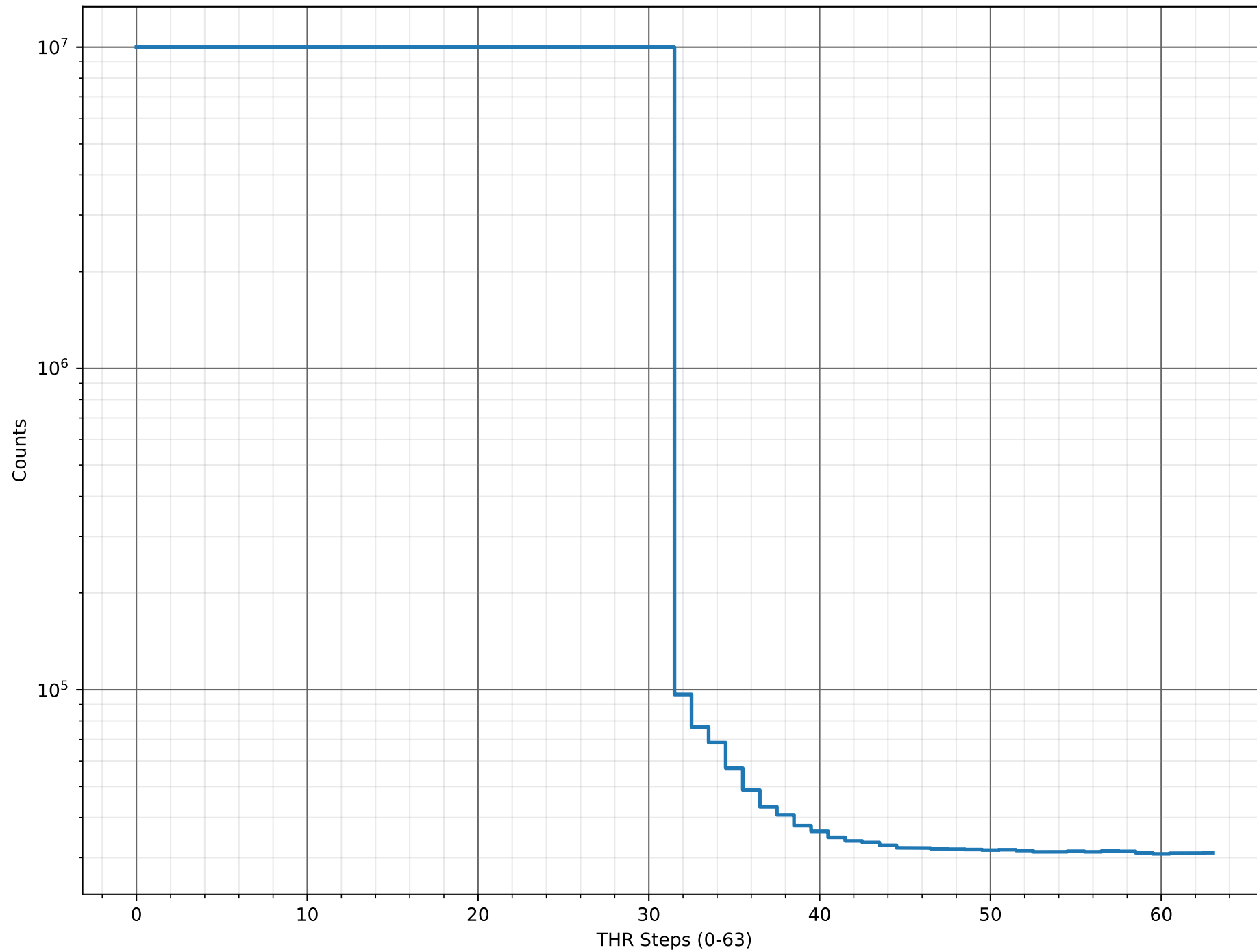
THR Distribution of PMT: A Anode: 44 (UART: 1 FEB: 1 CHIP: 7 CHANNEL: 2)



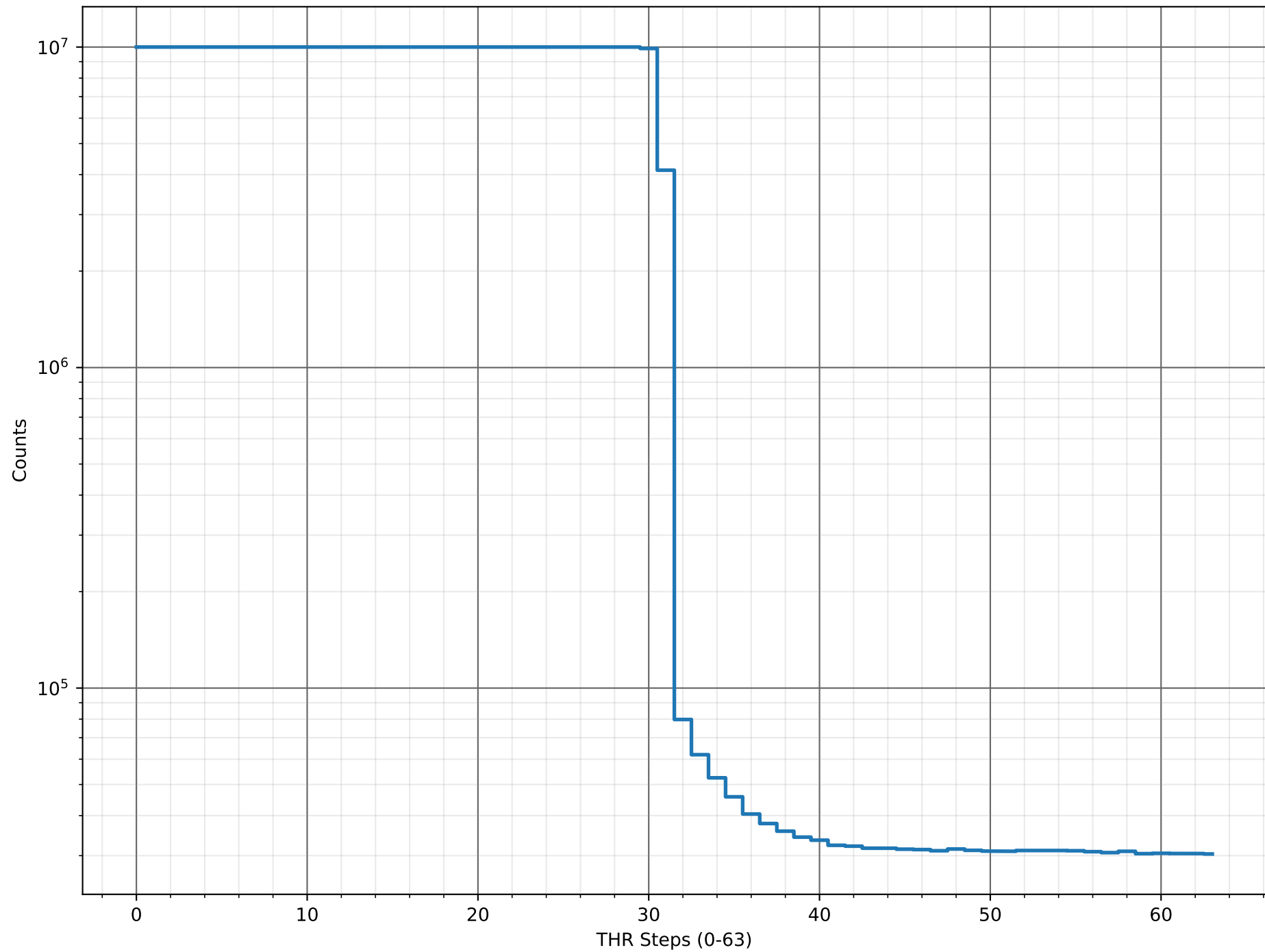
THR Distribution of PMT: A Anode: 45 (UART: 1 FEB: 0 CHIP: 6 CHANNEL: 5)



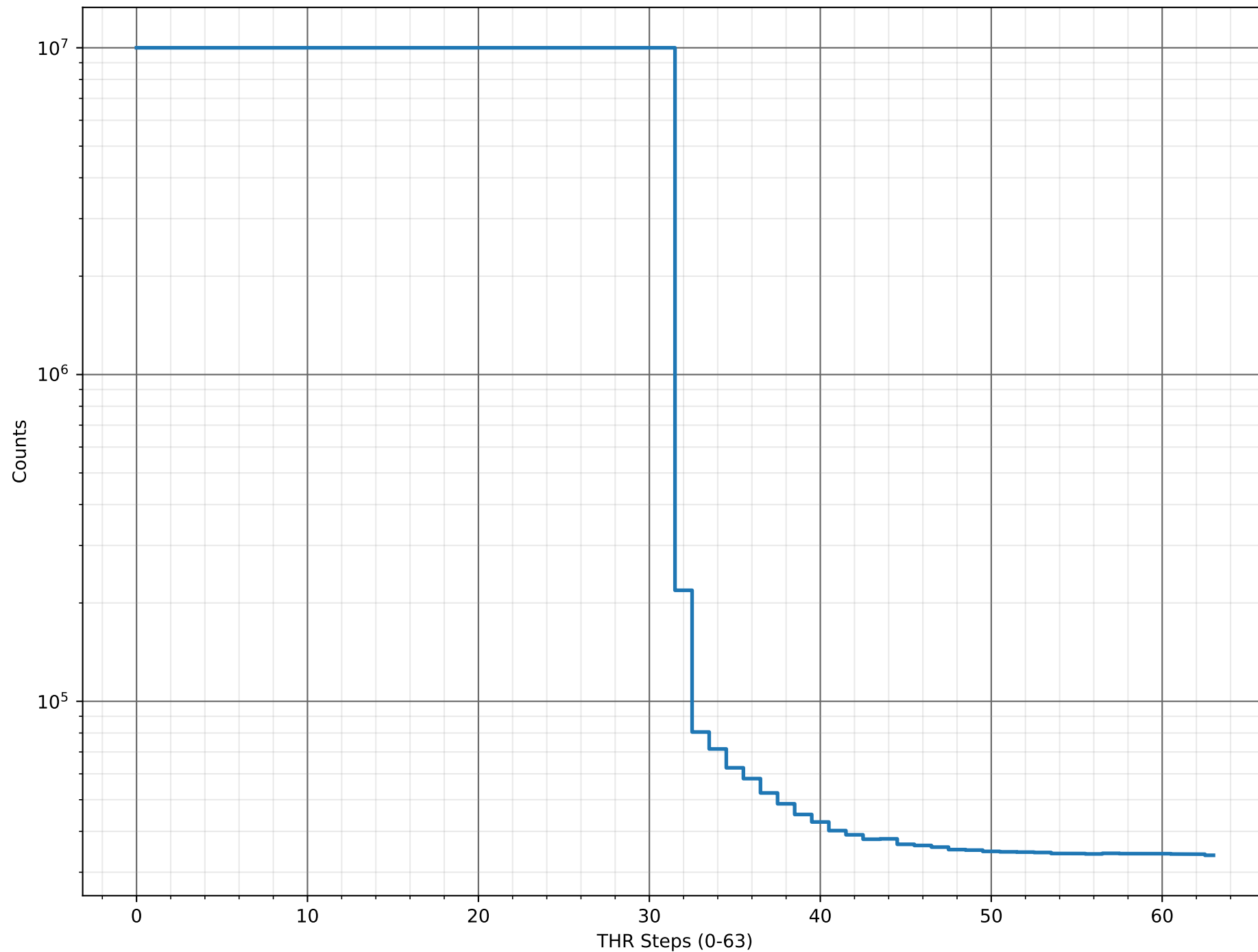
THR Distribution of PMT: A Anode: 46 (UART: 1 FEB: 0 CHIP: 6 CHANNEL: 4)



THR Distribution of PMT: A Anode: 47 (UART: 1 FEB: 0 CHIP: 7 CHANNEL: 2)

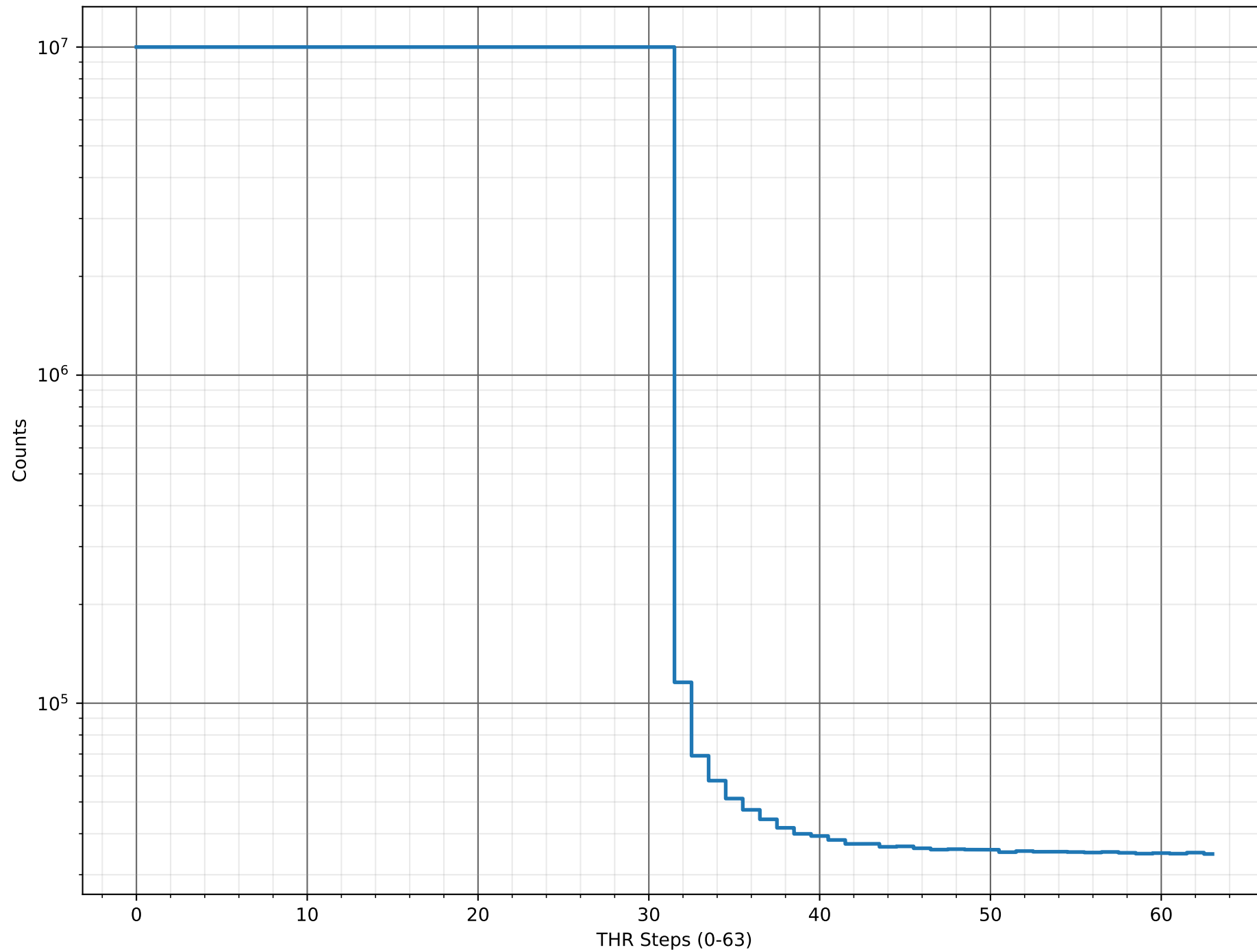


THR Distribution of PMT: A Anode: 48 (UART: 1 FEB: 0 CHIP: 7 CHANNEL: 3)

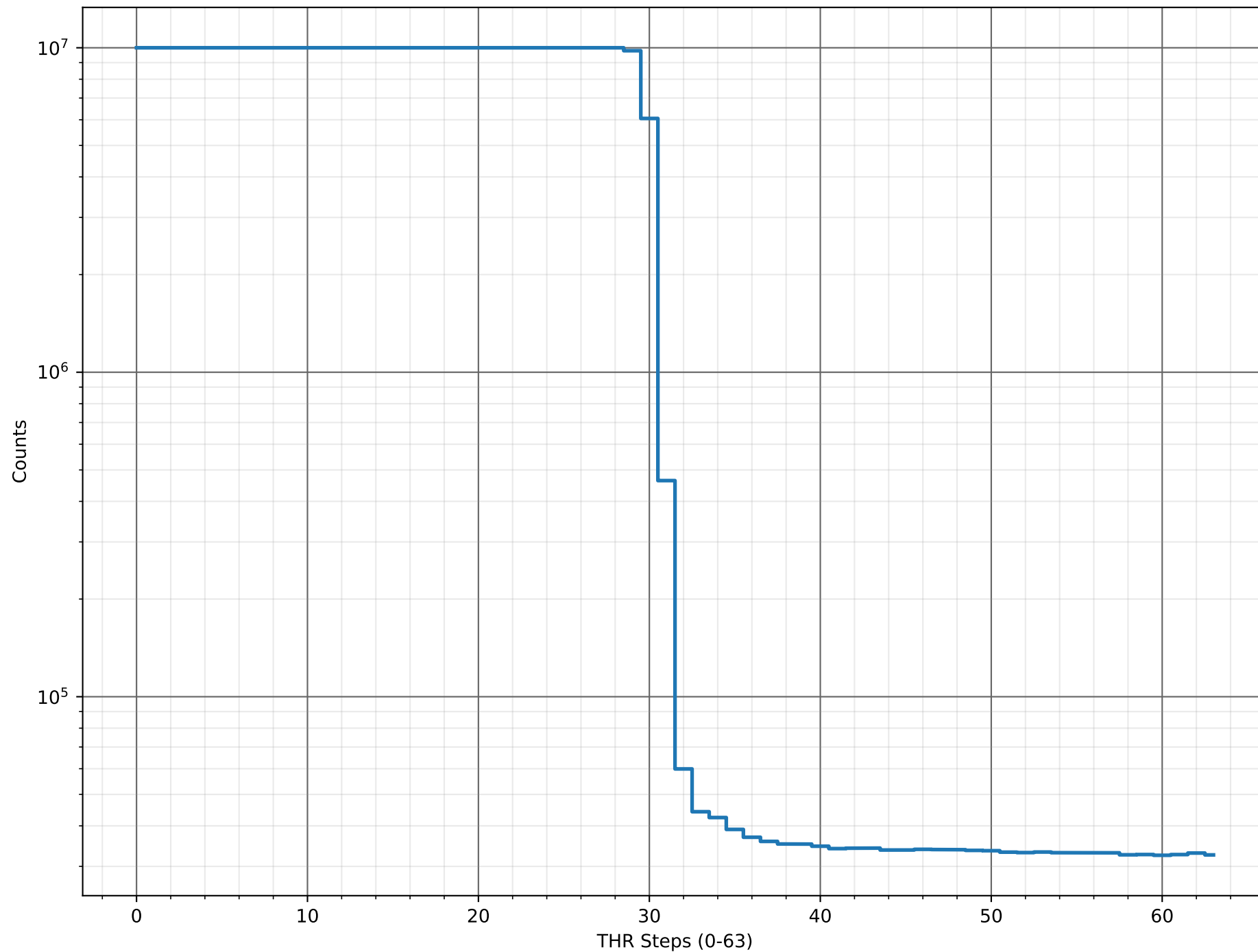




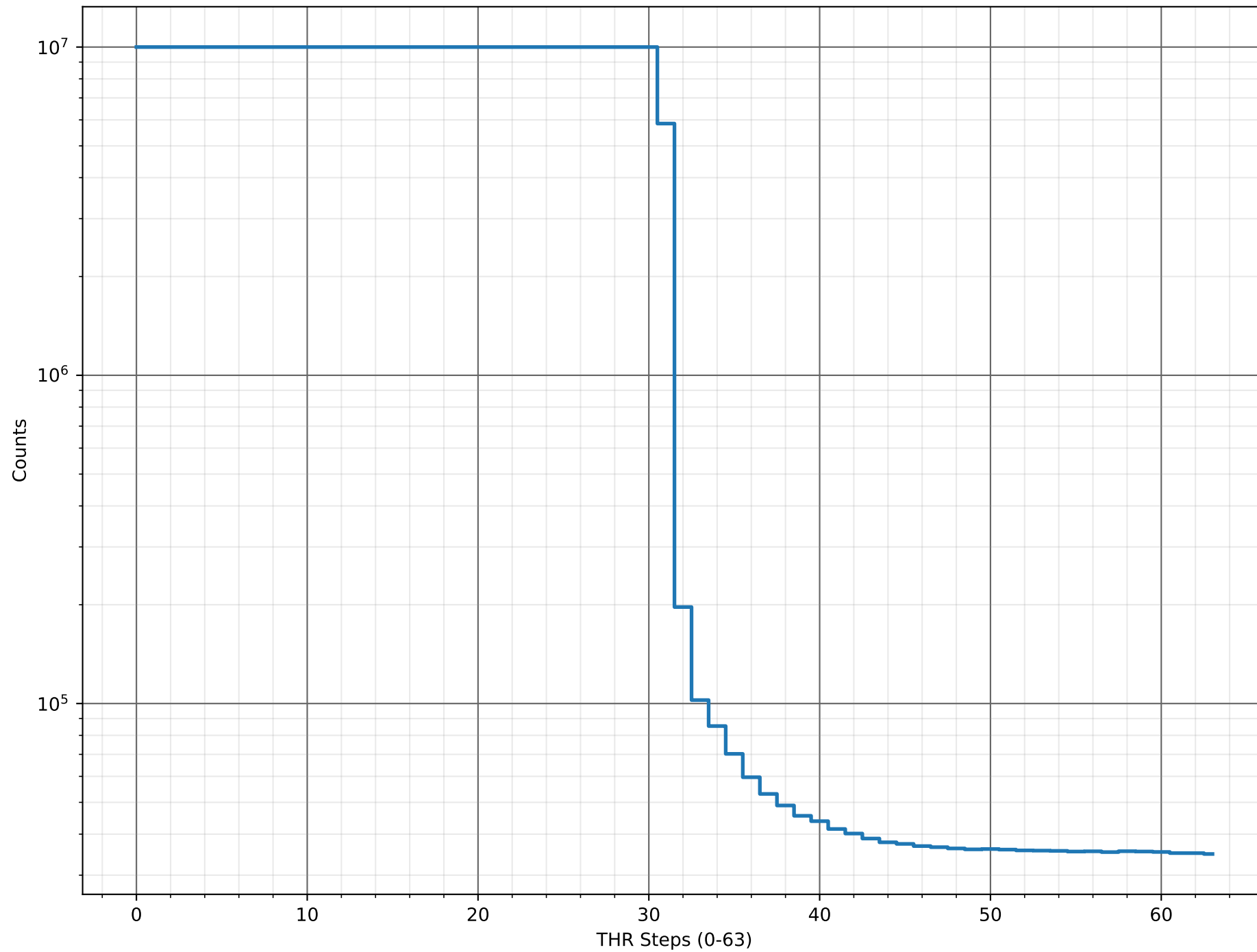
THR Distribution of PMT: A Anode: 49 (UART: 1 FEB: 1 CHIP: 6 CHANNEL: 2)



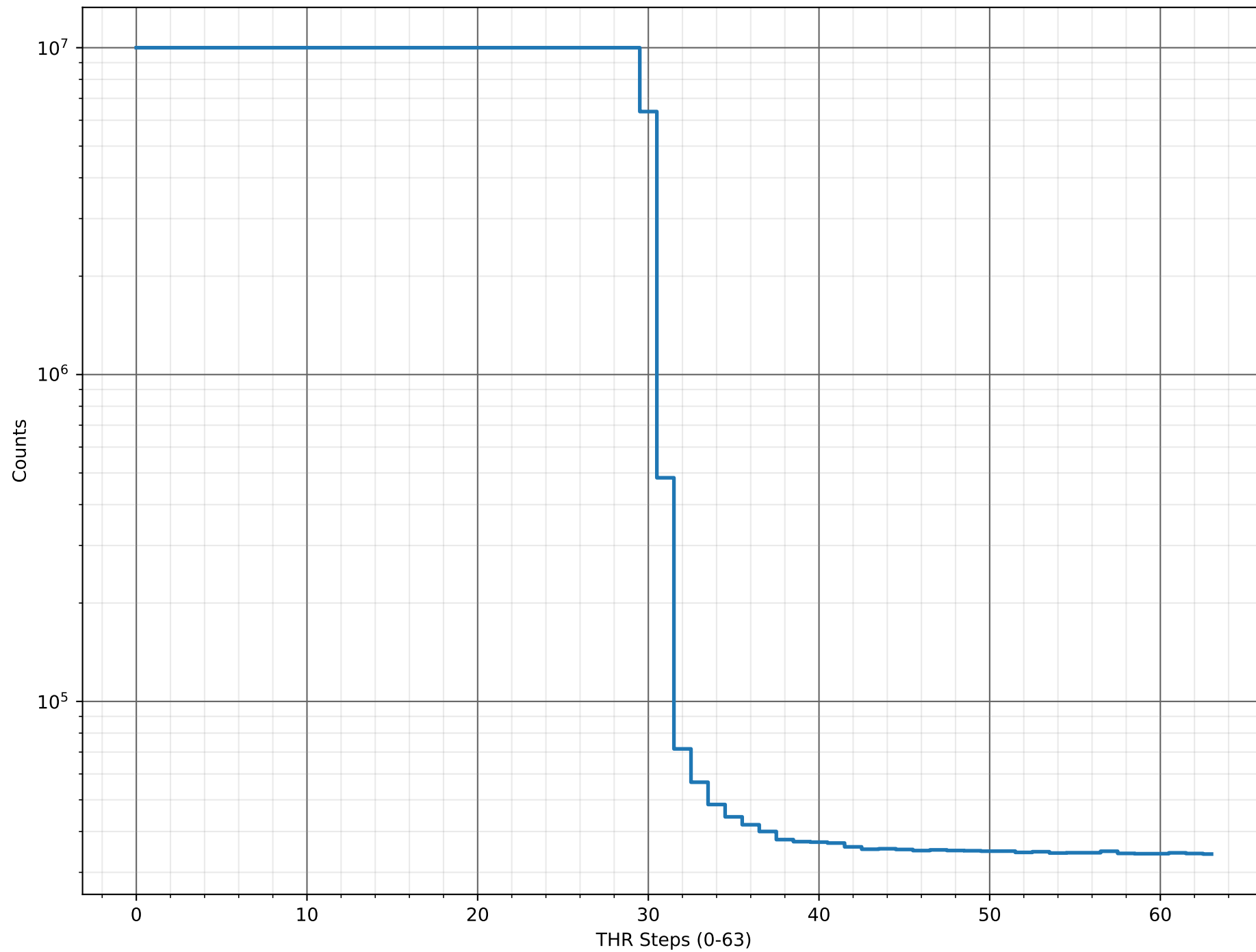
THR Distribution of PMT: A Anode: 50 (UART: 1 FEB: 1 CHIP: 6 CHANNEL: 3)



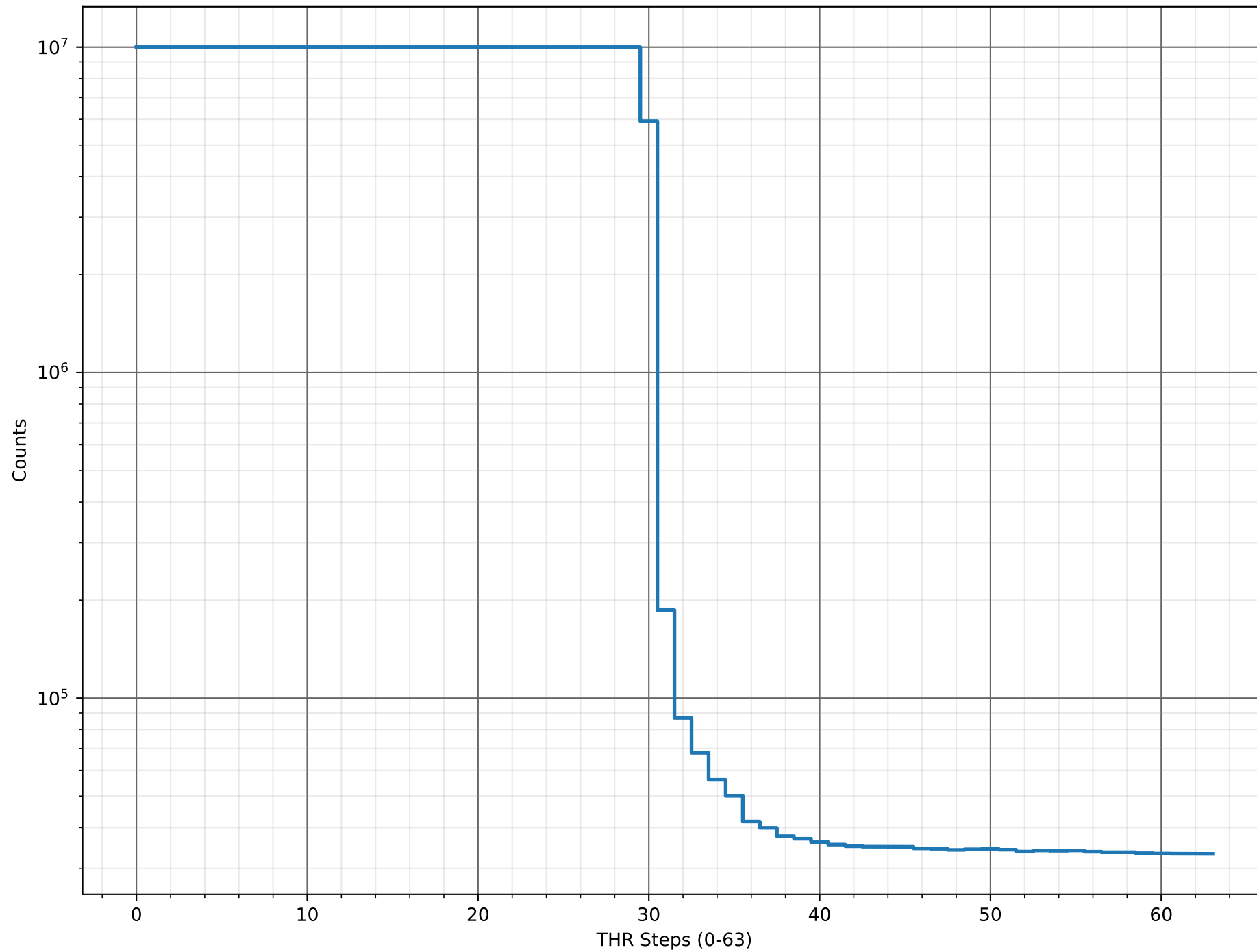
THR Distribution of PMT: A Anode: 51 (UART: 1 FEB: 1 CHIP: 7 CHANNEL: 5)



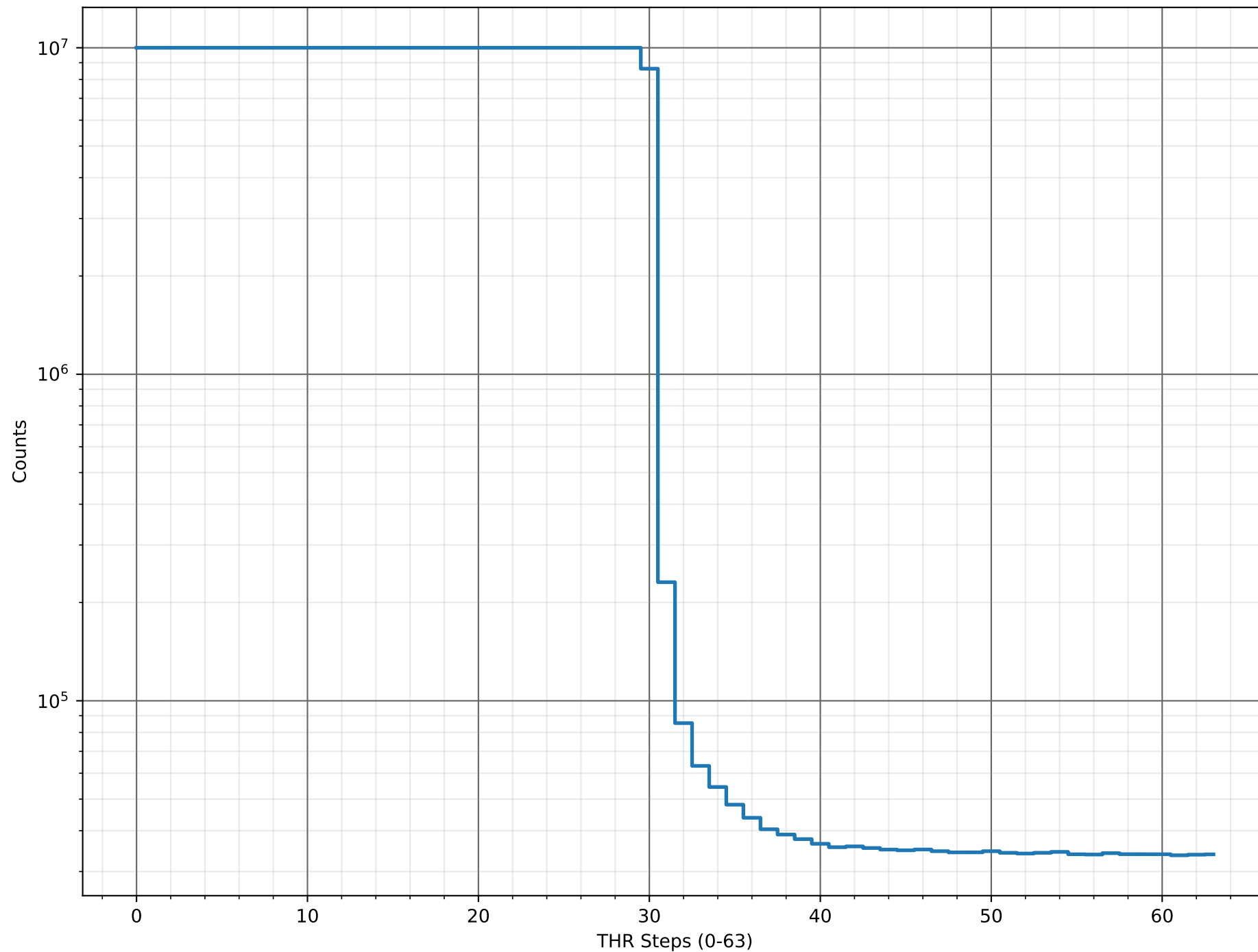
THR Distribution of PMT: A Anode: 52 (UART: 1 FEB: 1 CHIP: 7 CHANNEL: 4)



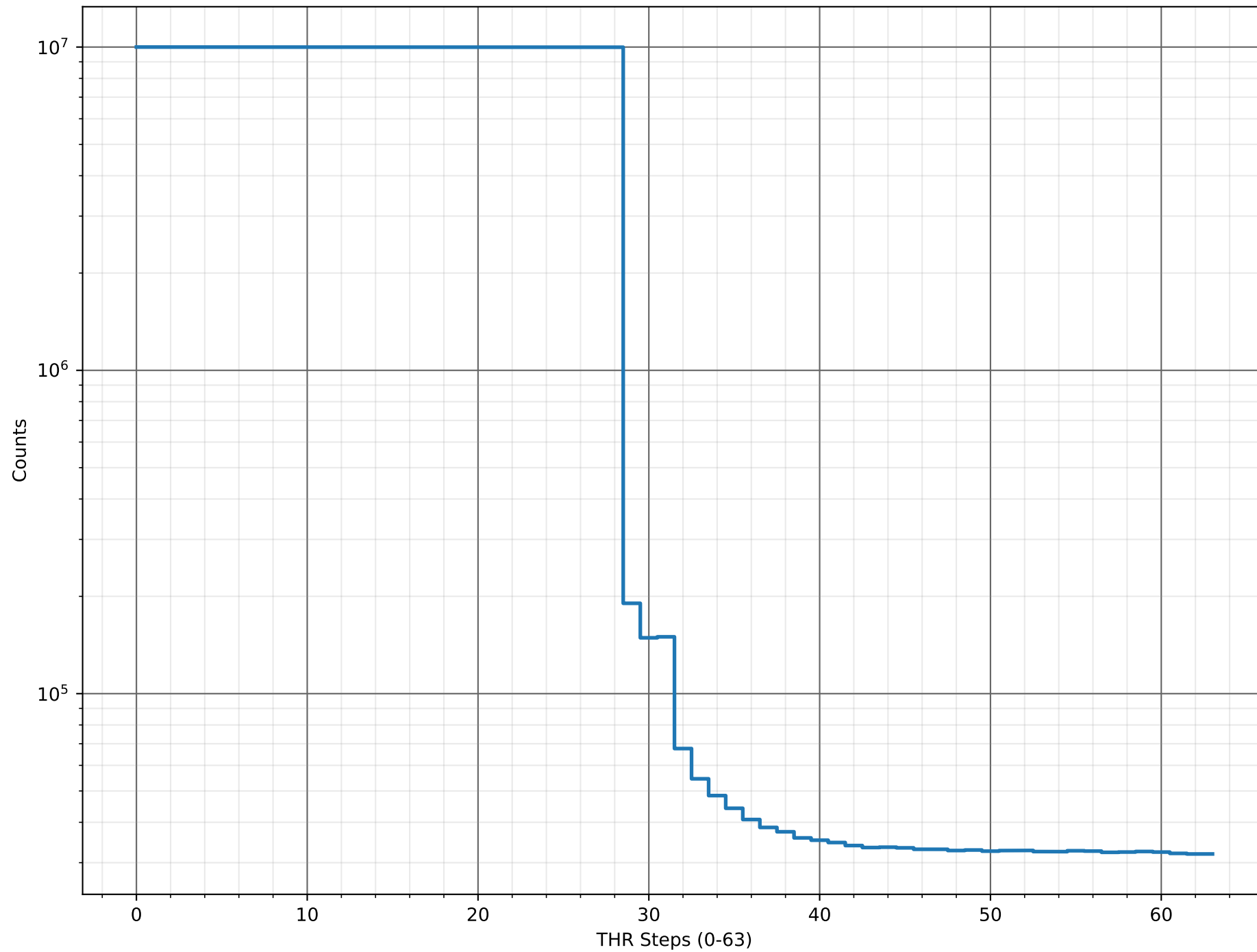
THR Distribution of PMT: A Anode: 53 (UART: 1 FEB: 0 CHIP: 6 CHANNEL: 3)



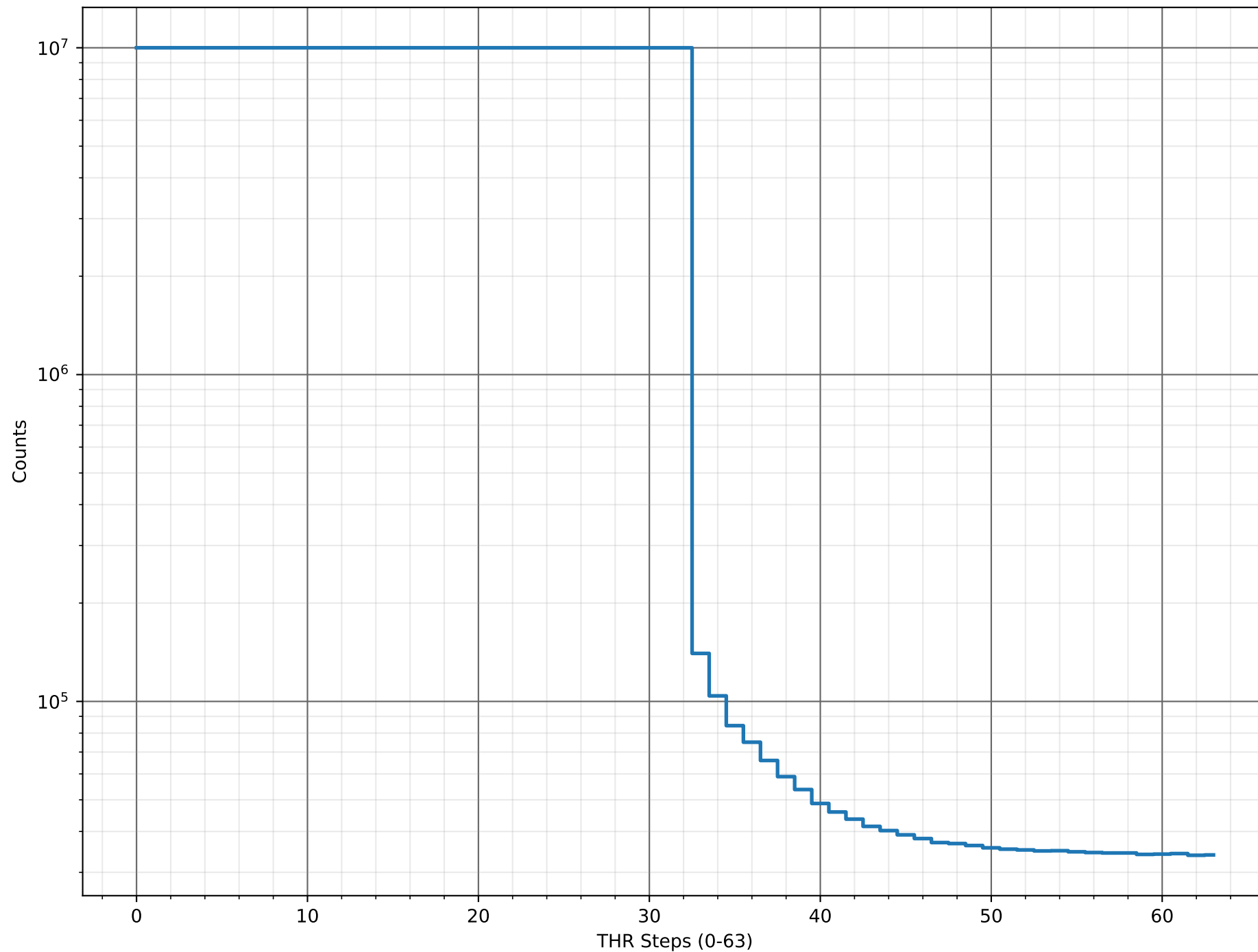
THR Distribution of PMT: A Anode: 54 (UART: 1 FEB: 0 CHIP: 6 CHANNEL: 2)



THR Distribution of PMT: A Anode: 55 (UART: 1 FEB: 0 CHIP: 7 CHANNEL: 4)

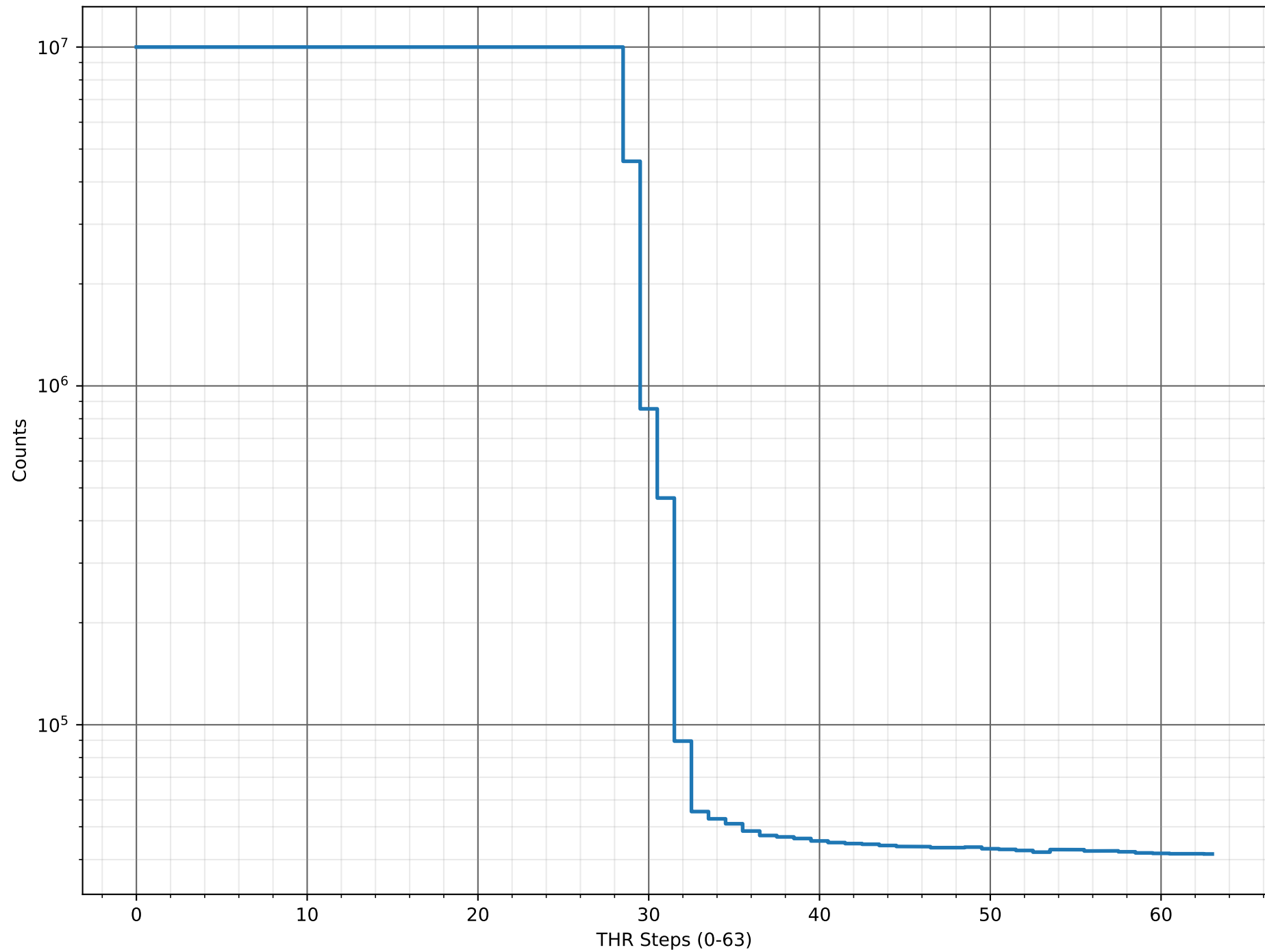


THR Distribution of PMT: A Anode: 56 (UART: 1 FEB: 0 CHIP: 7 CHANNEL: 5)

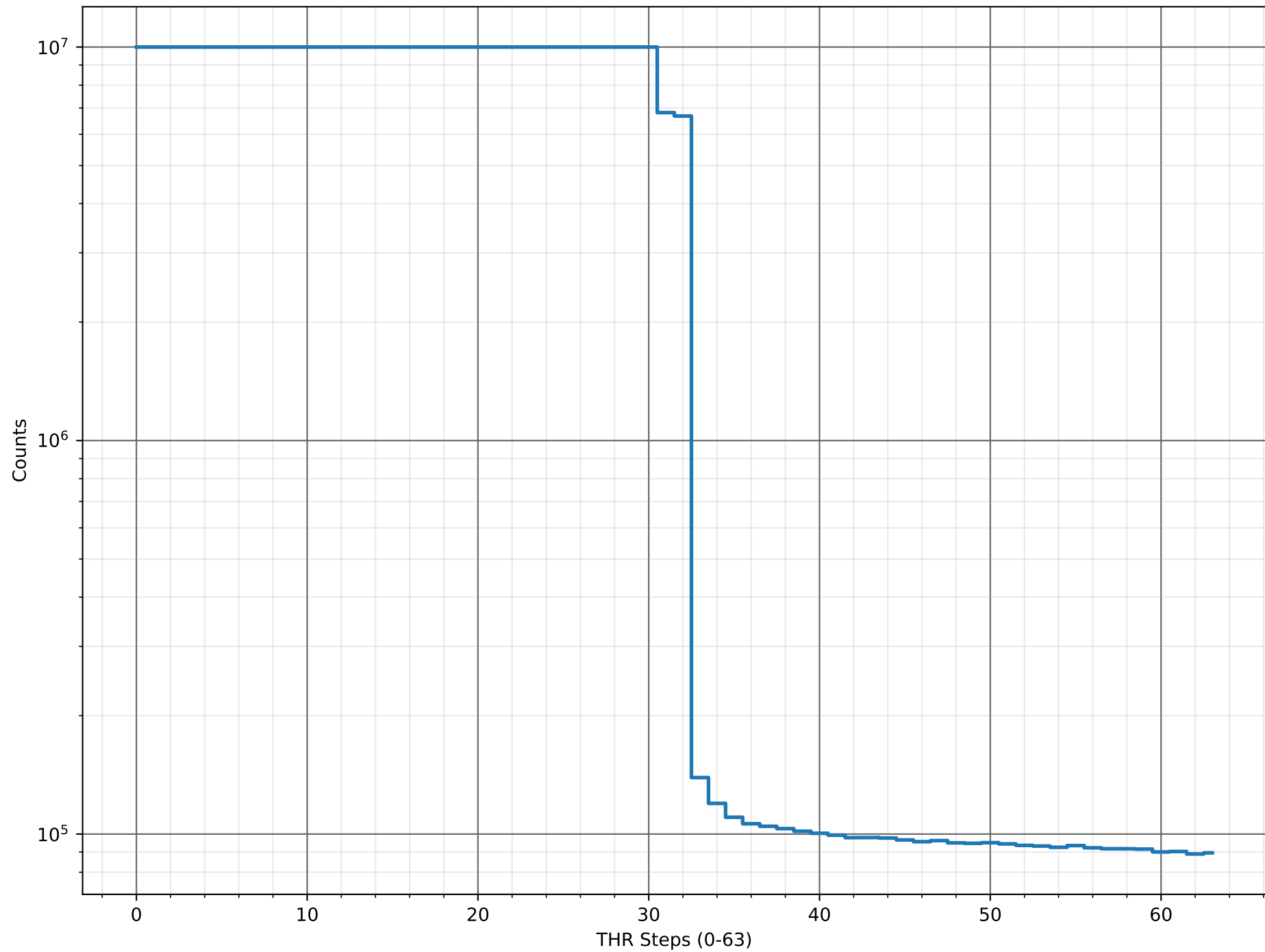




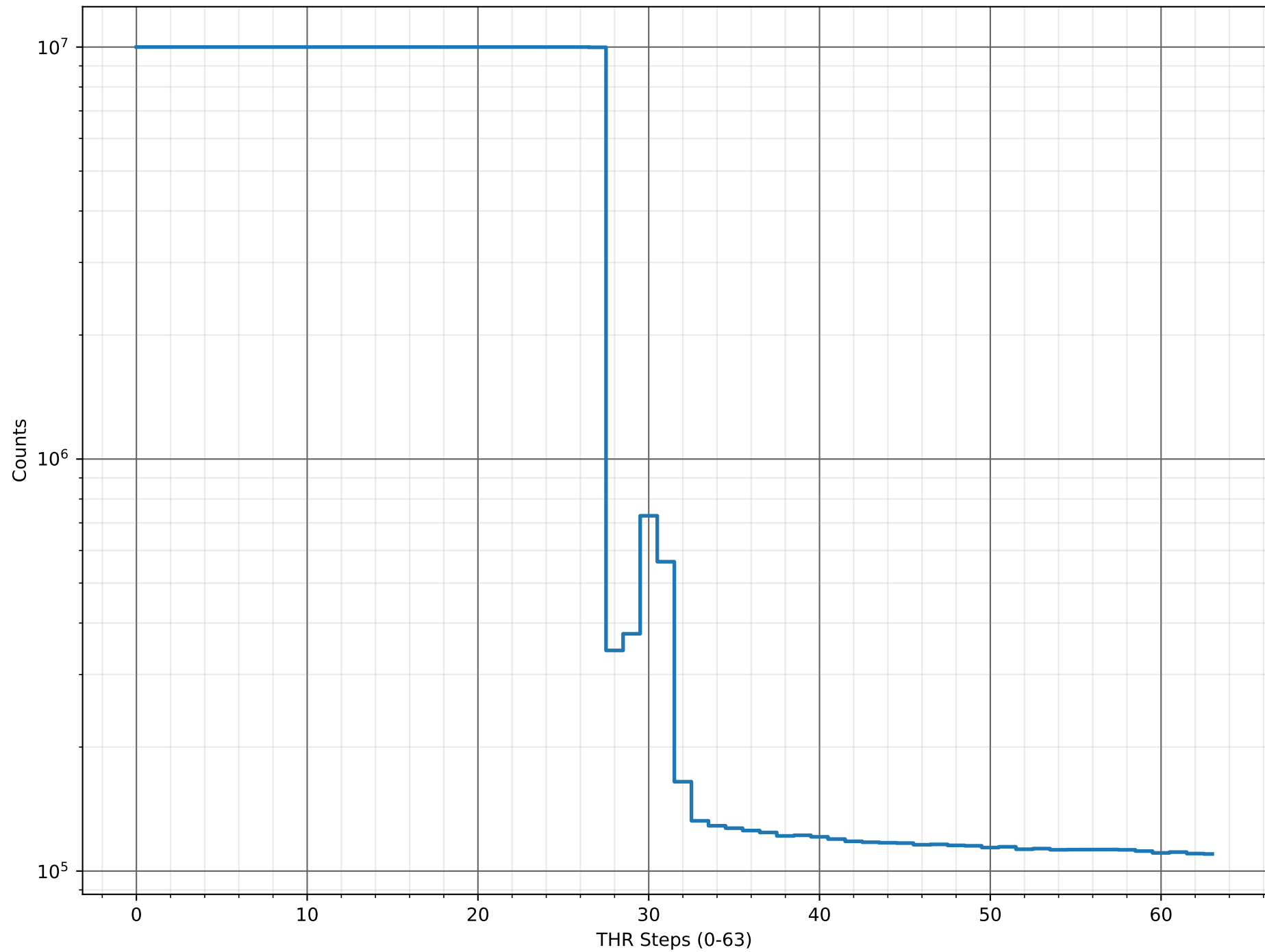
THR Distribution of PMT: A Anode: 57 (UART: 1 FEB: 1 CHIP: 6 CHANNEL: 0)



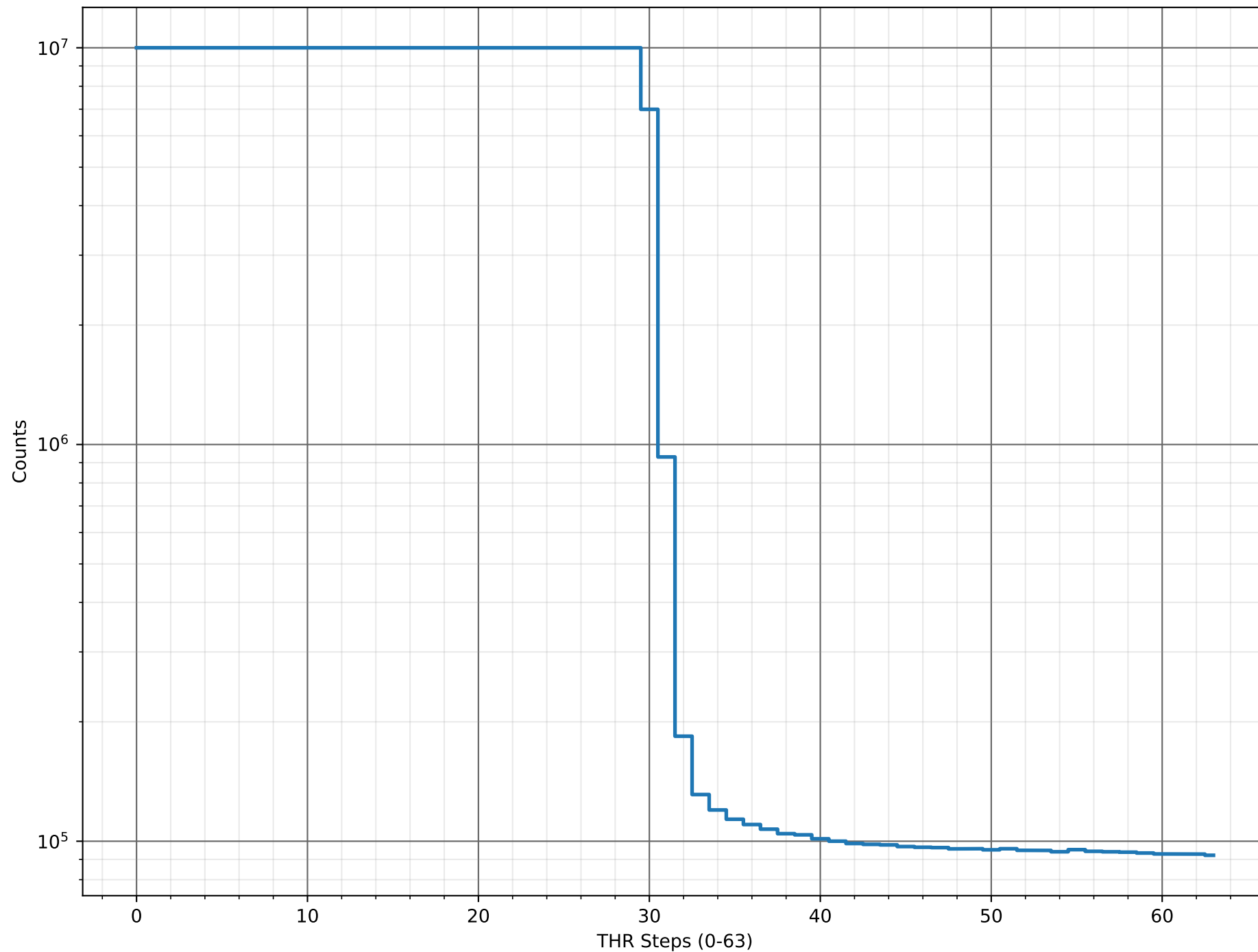
THR Distribution of PMT: A Anode: 58 (UART: 1 FEB: 1 CHIP: 6 CHANNEL: 1)



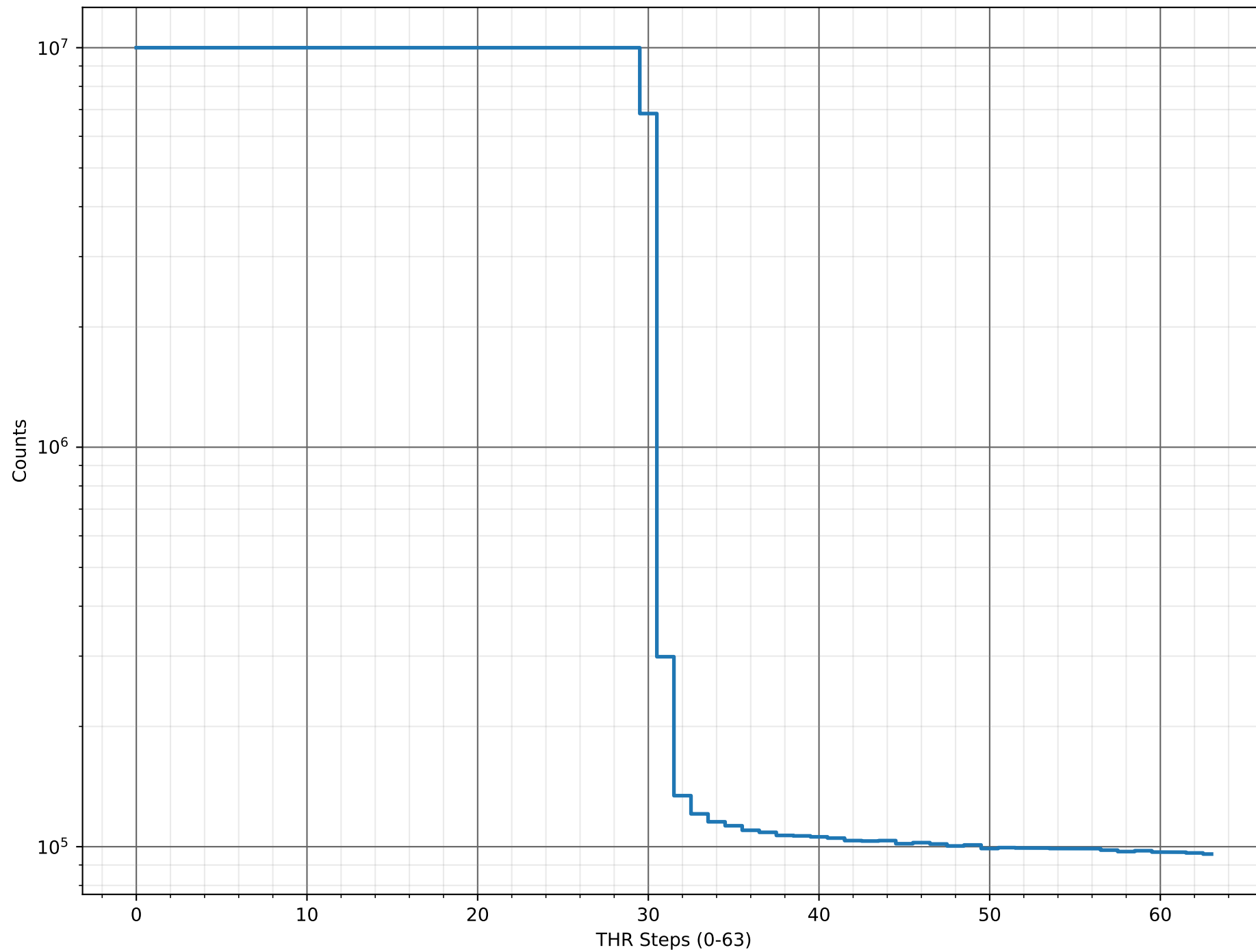
THR Distribution of PMT: A Anode: 59 (UART: 1 FEB: 1 CHIP: 7 CHANNEL: 7)



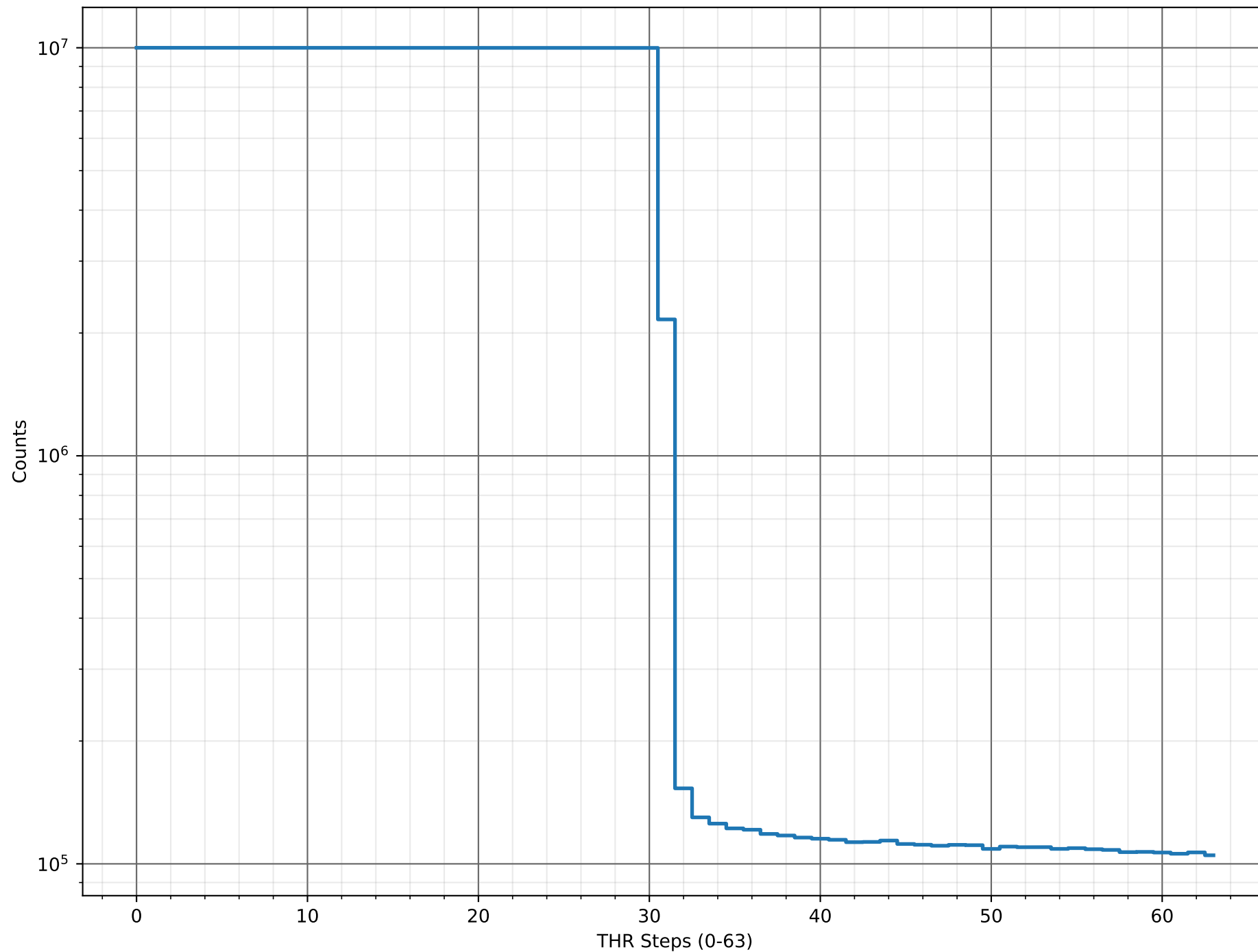
THR Distribution of PMT: A Anode: 60 (UART: 1 FEB: 1 CHIP: 7 CHANNEL: 6)



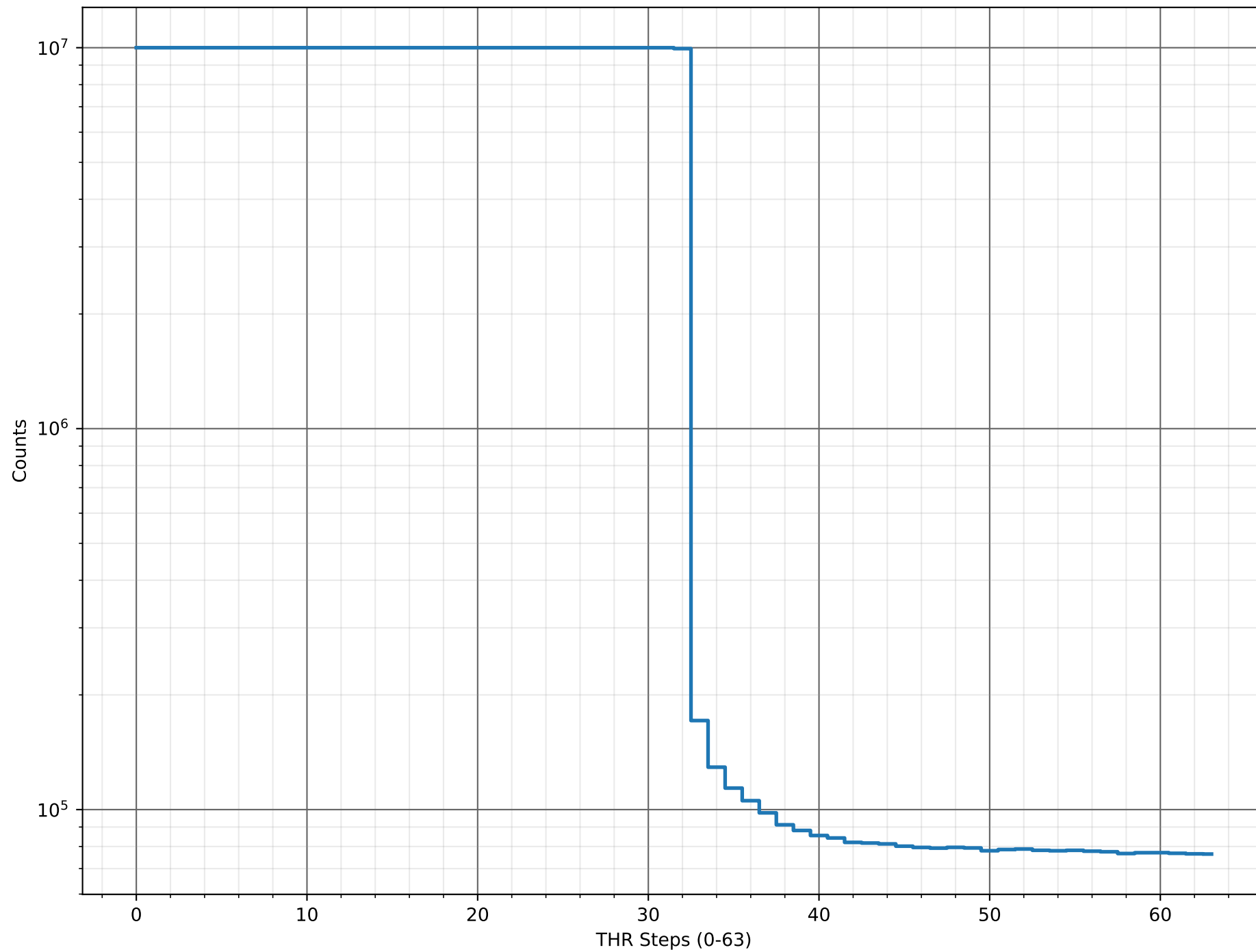
THR Distribution of PMT: A Anode: 61 (UART: 1 FEB: 0 CHIP: 6 CHANNEL: 1)



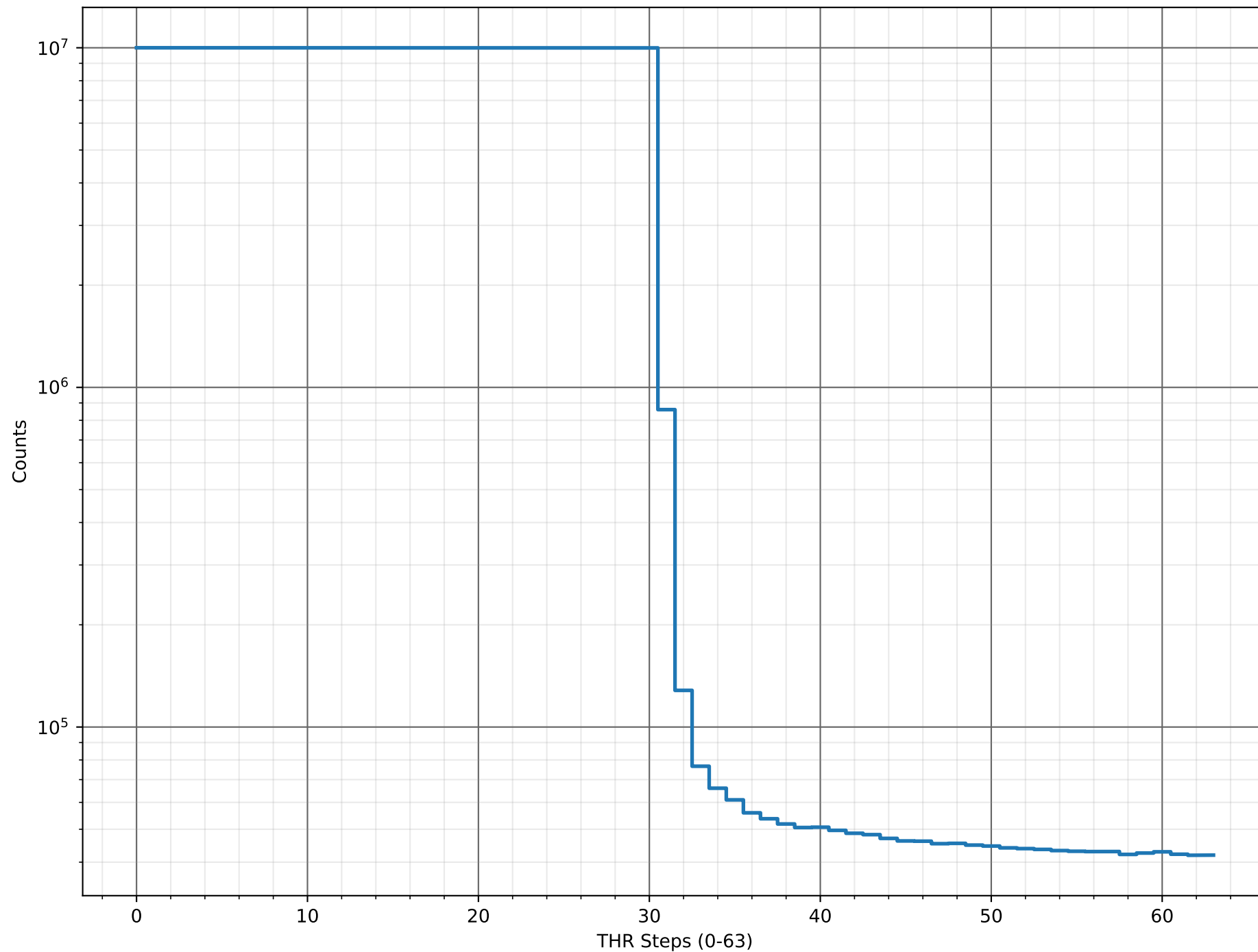
THR Distribution of PMT: A Anode: 62 (UART: 1 FEB: 0 CHIP: 6 CHANNEL: 0)



THR Distribution of PMT: A Anode: 63 (UART: 1 FEB: 0 CHIP: 7 CHANNEL: 6)

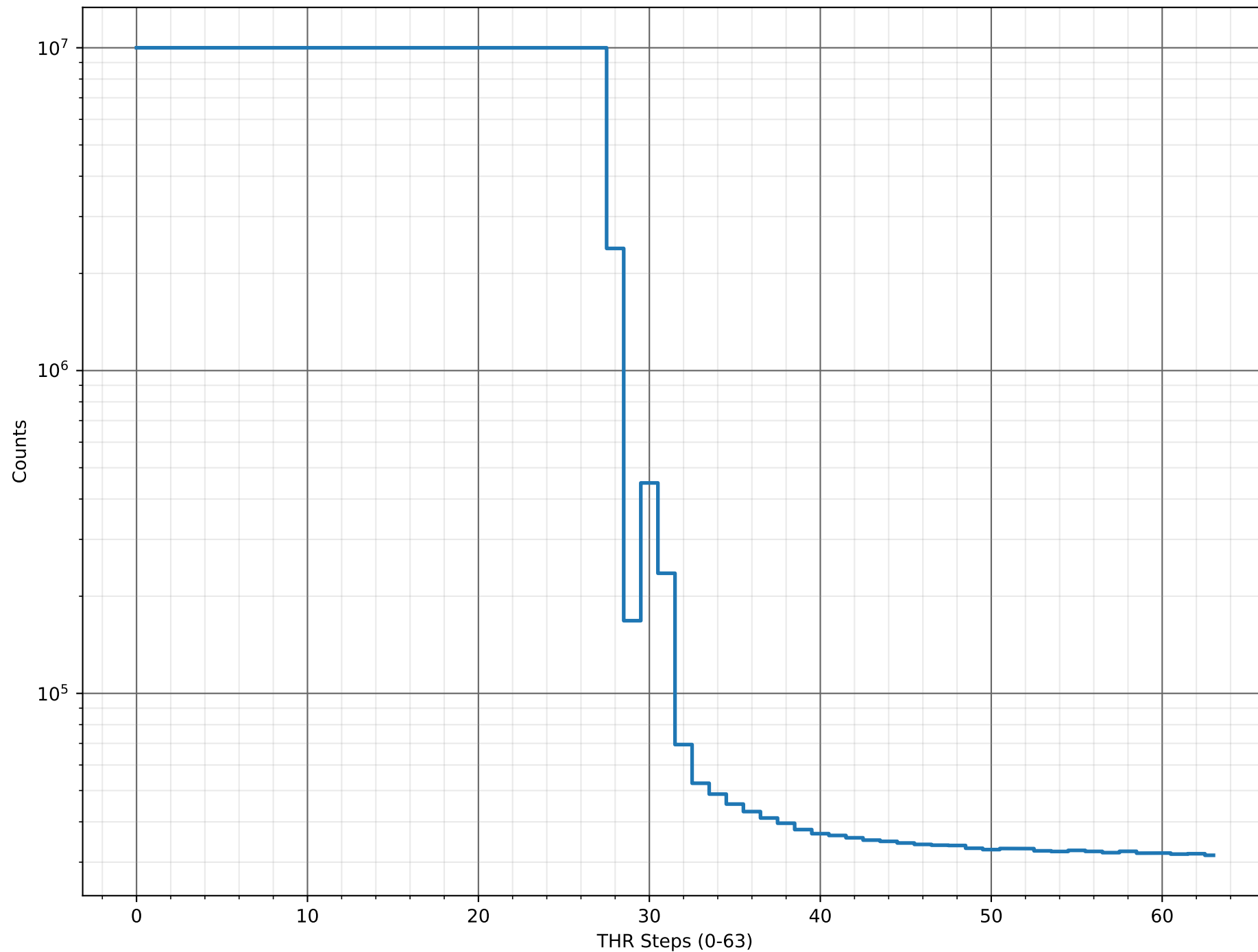


THR Distribution of PMT: A Anode: 64 (UART: 1 FEB: 0 CHIP: 7 CHANNEL: 7)

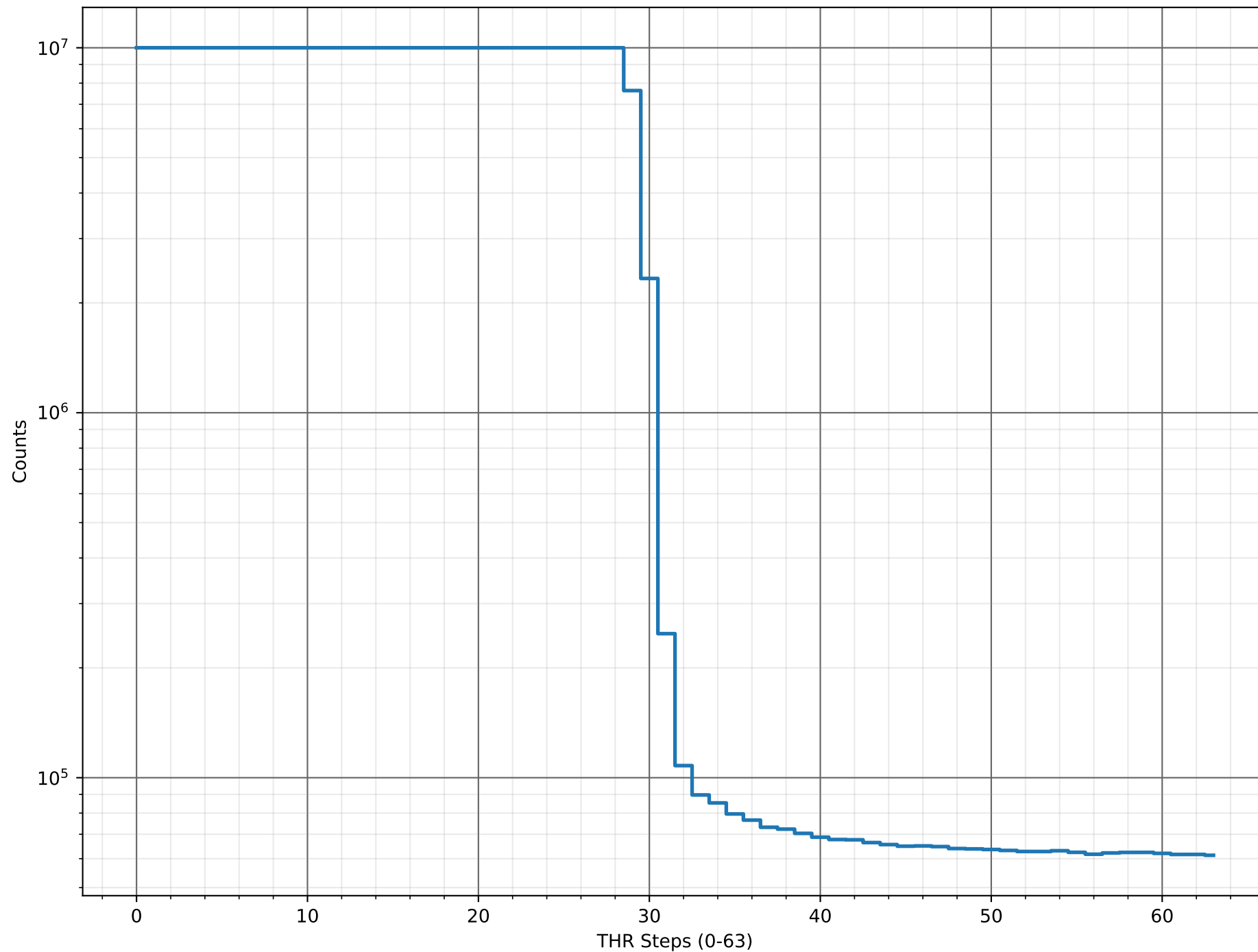




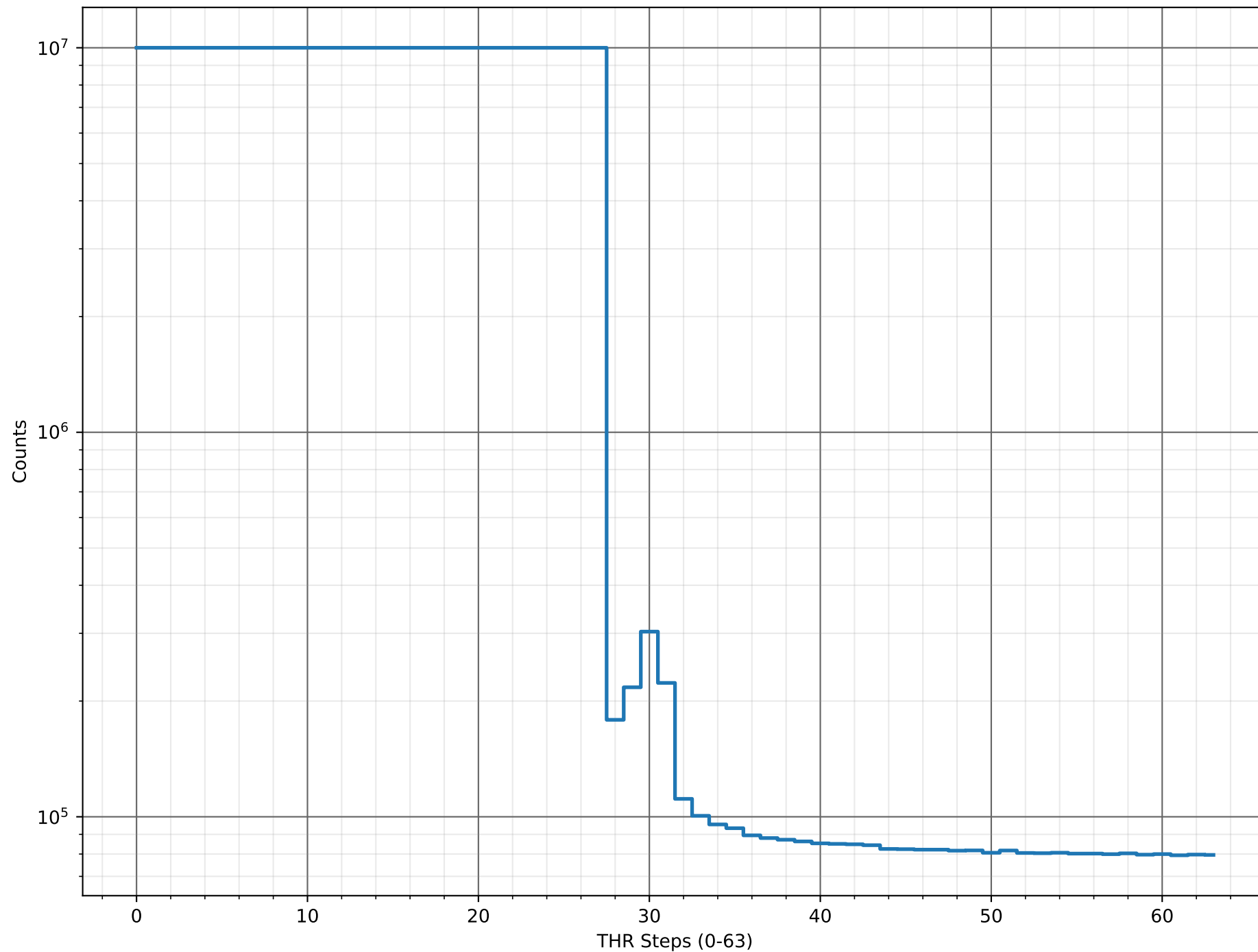
THR Distribution of PMT: B Anode: 1 (UART: 2 FEB: 1 CHIP: 5 CHANNEL: 0)



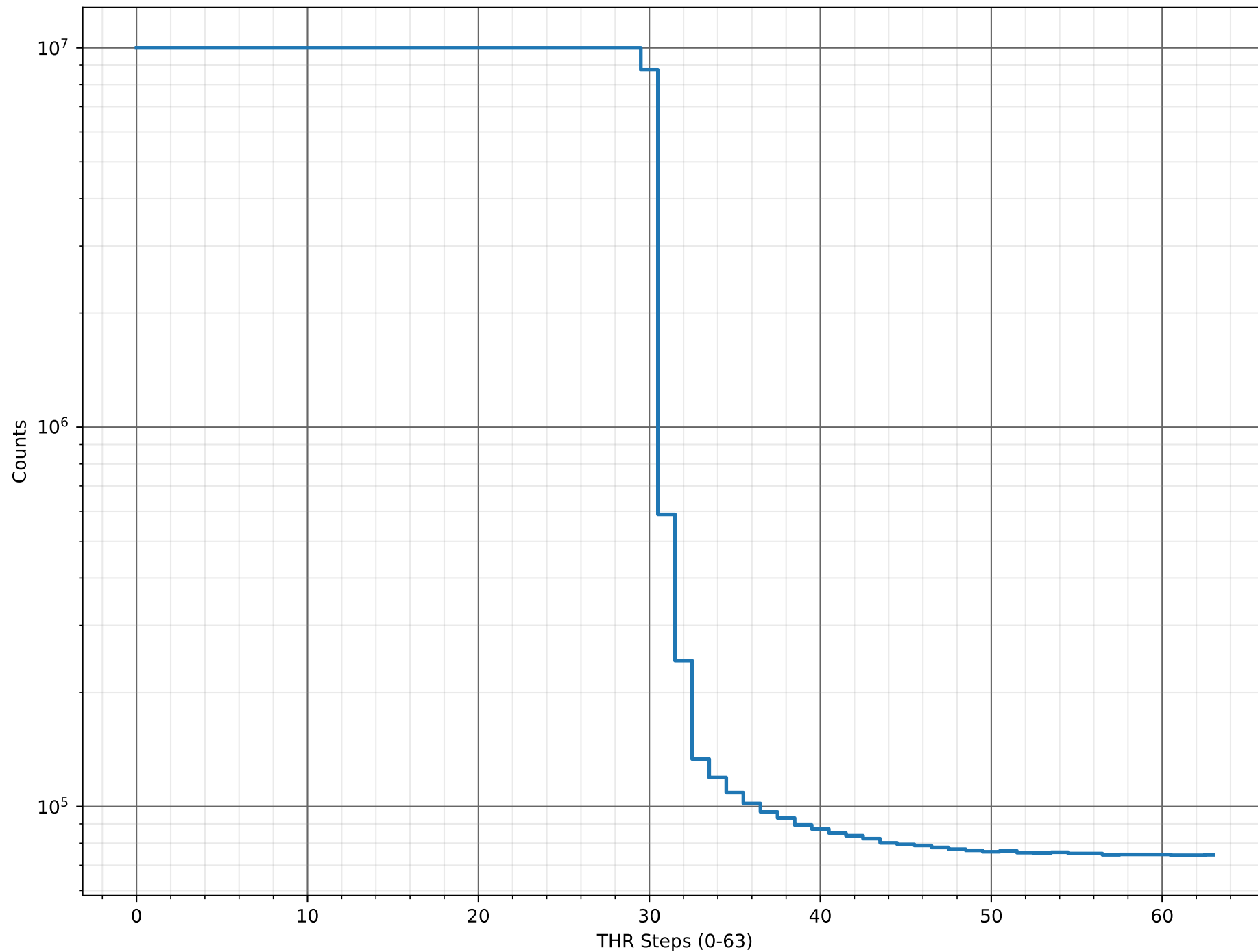
THR Distribution of PMT: B Anode: 2 (UART: 2 FEB: 1 CHIP: 5 CHANNEL: 2)



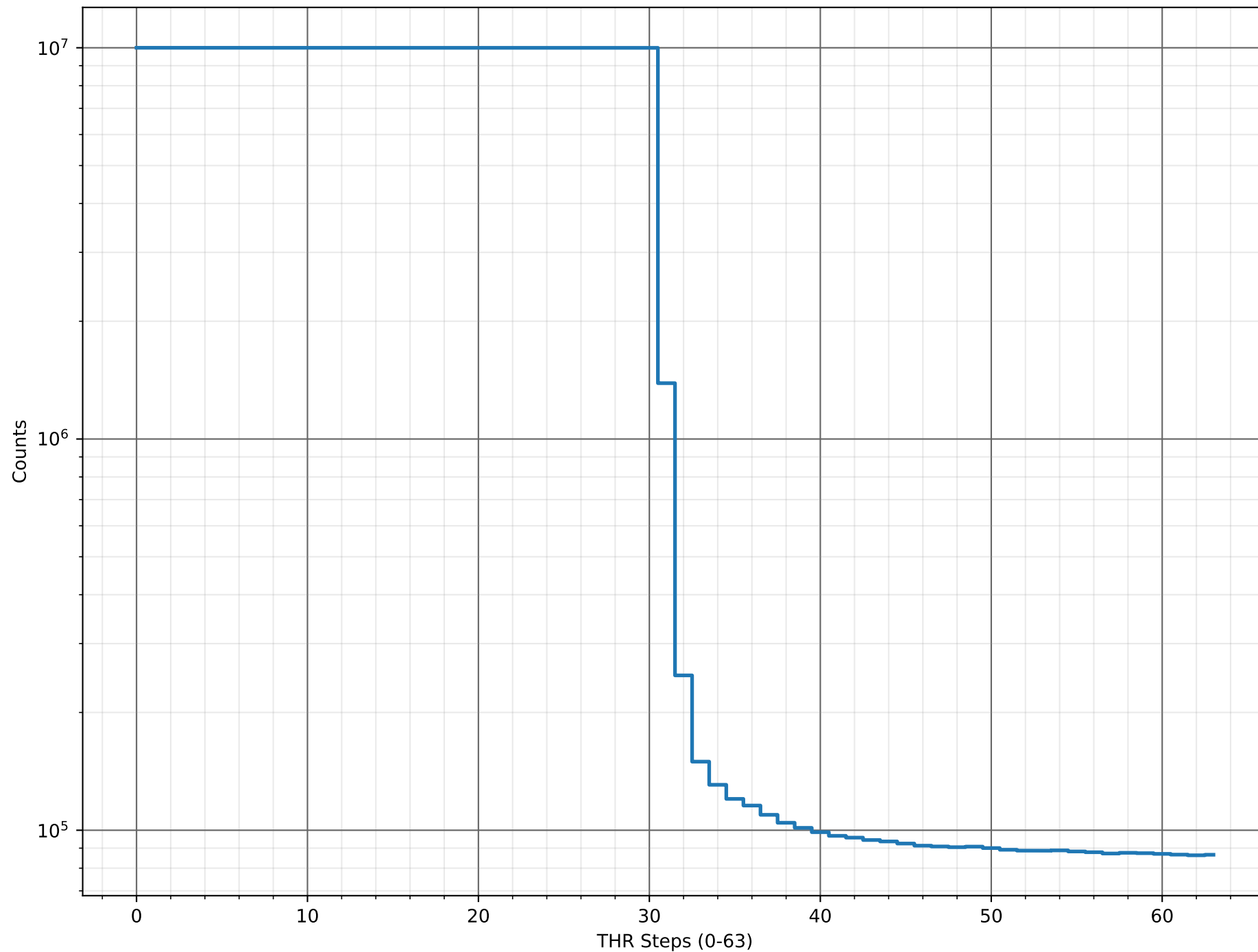
THR Distribution of PMT: B Anode: 3 (UART: 2 FEB: 1 CHIP: 5 CHANNEL: 4)



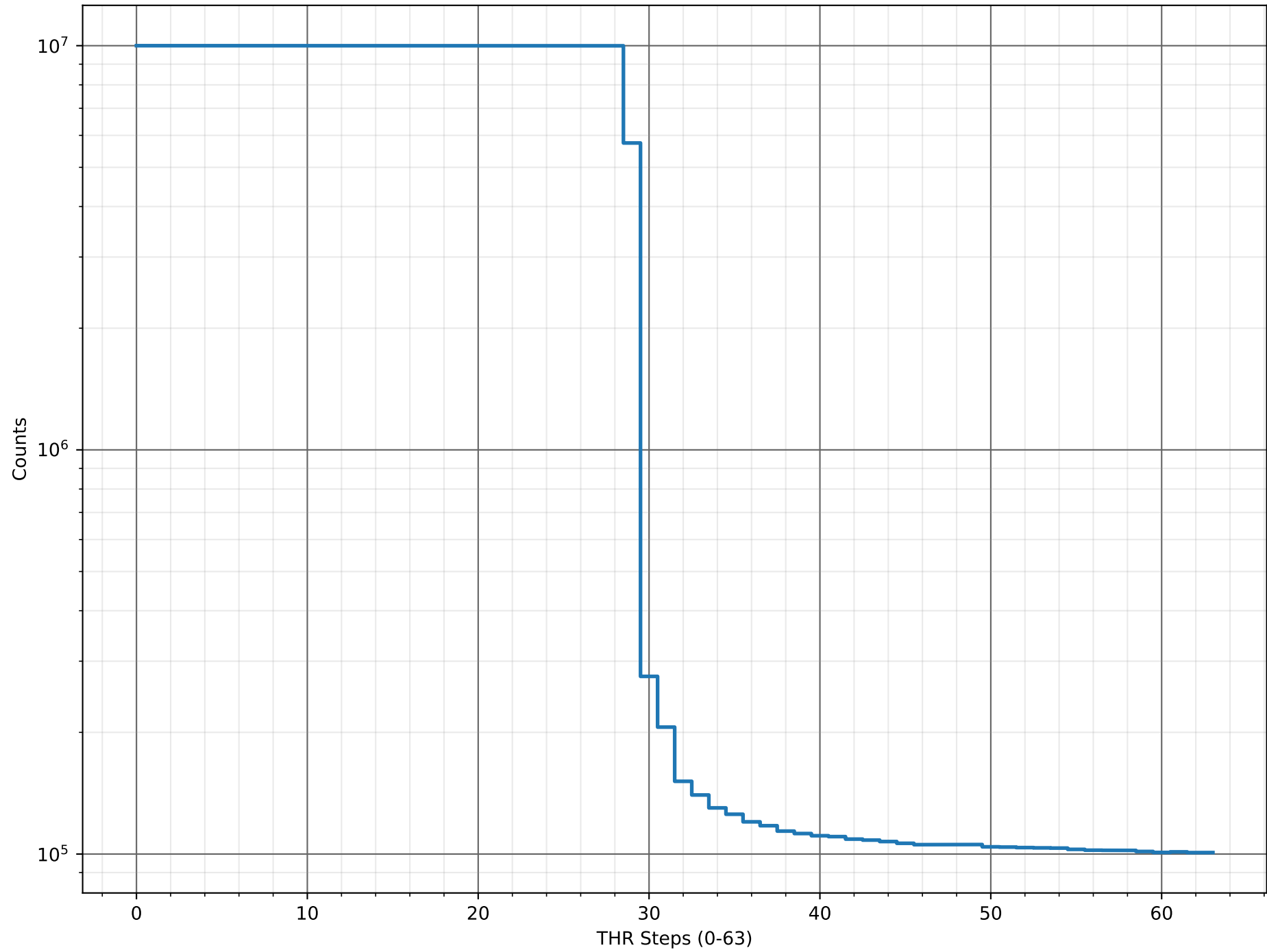
THR Distribution of PMT: B Anode: 4 (UART: 2 FEB: 1 CHIP: 5 CHANNEL: 6)



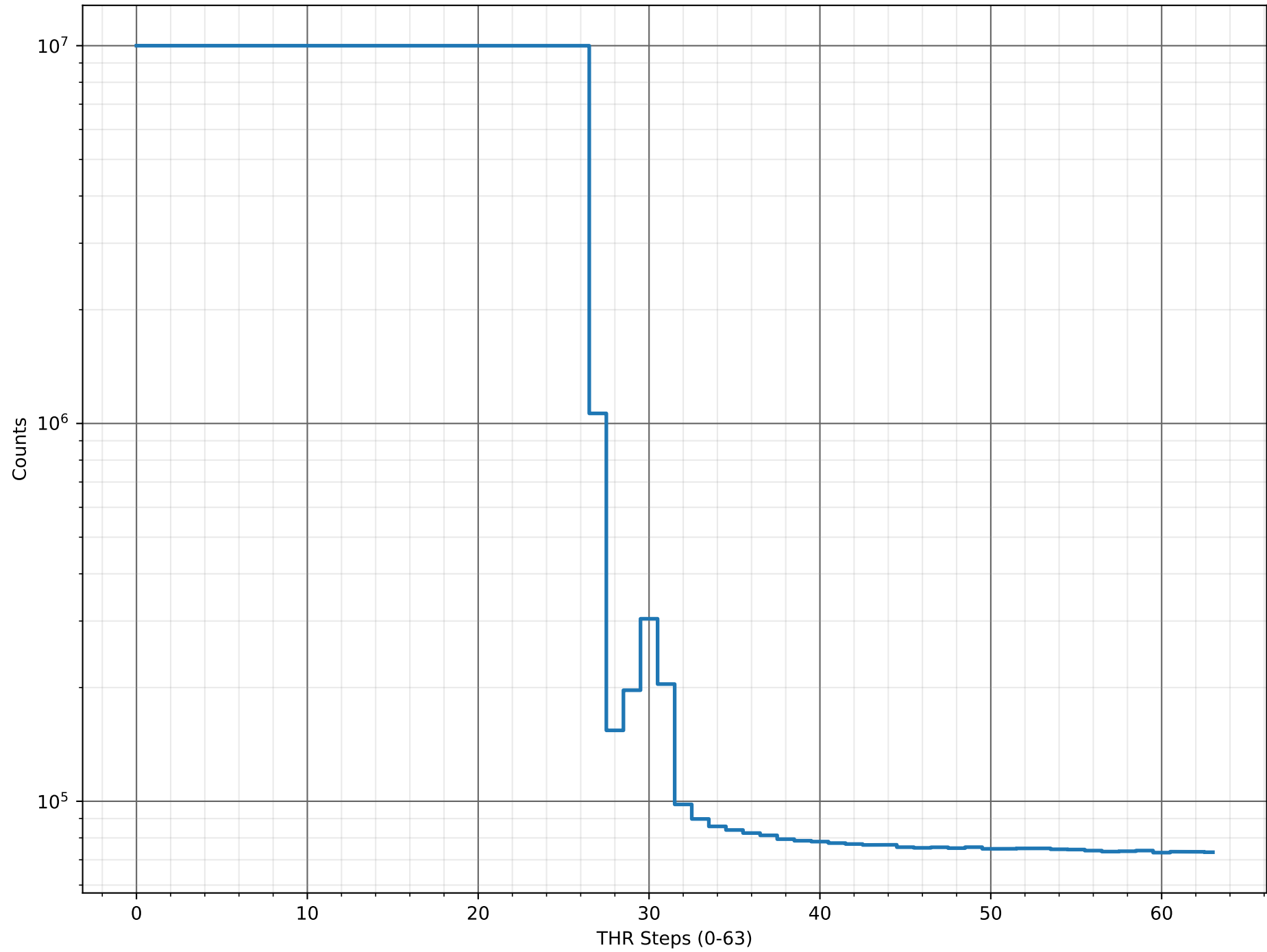
THR Distribution of PMT: B Anode: 5 (UART: 2 FEB: 1 CHIP: 7 CHANNEL: 1)



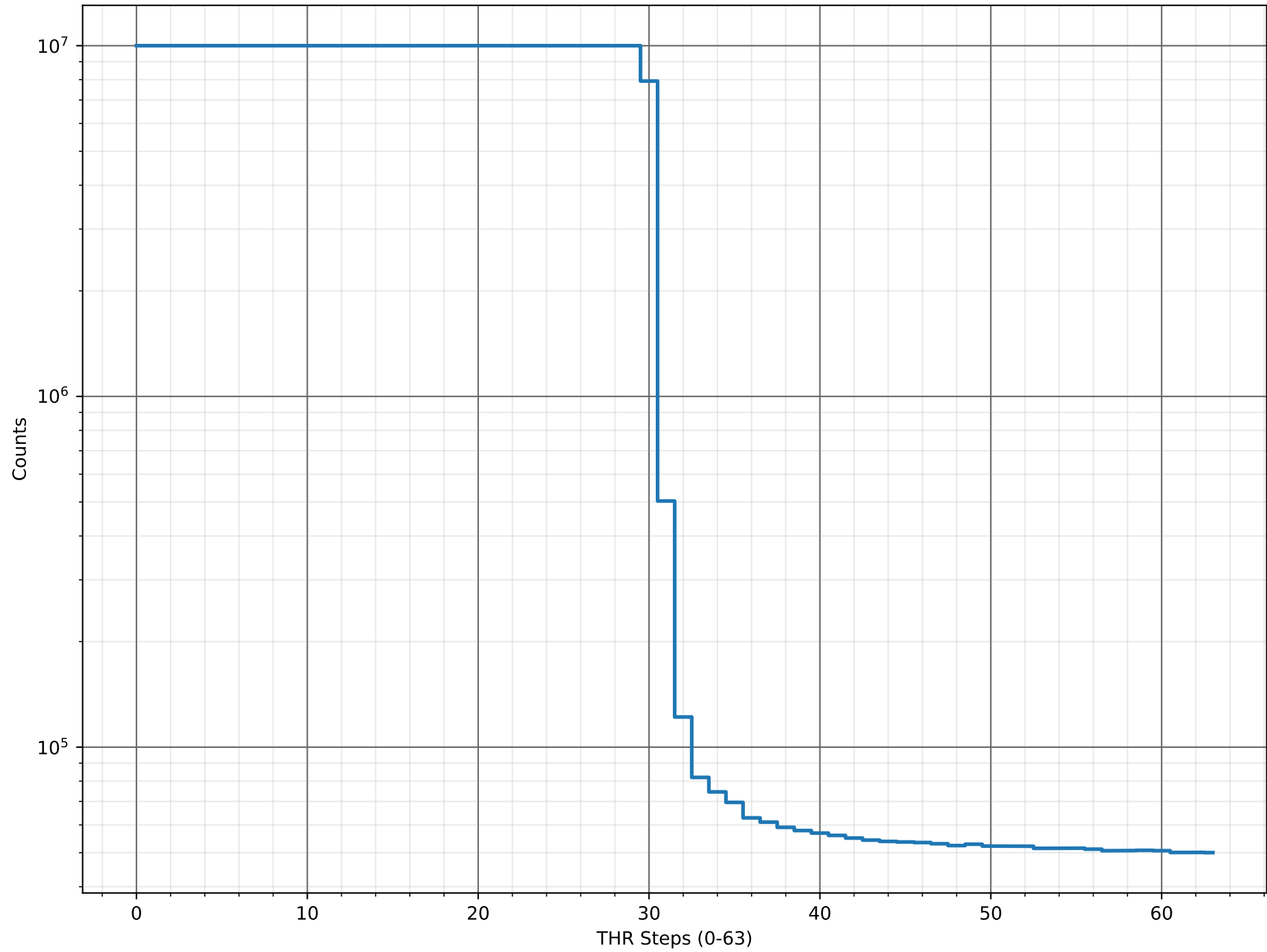
THR Distribution of PMT: B Anode: 6 (UART: 2 FEB: 1 CHIP: 7 CHANNEL: 3)



THR Distribution of PMT: B Anode: 7 (UART: 2 FEB: 1 CHIP: 7 CHANNEL: 5)

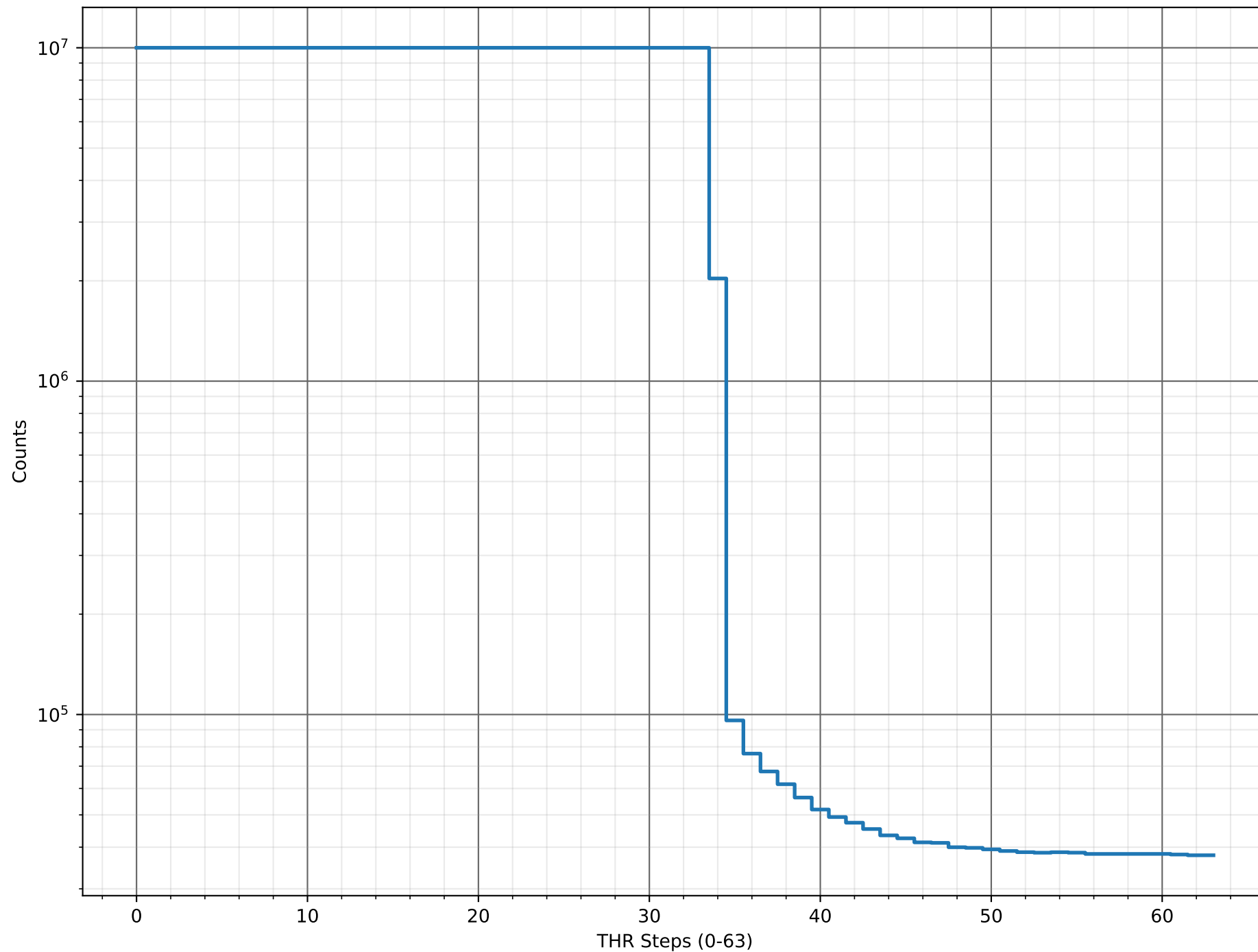


THR Distribution of PMT: B Anode: 8 (UART: 2 FEB: 1 CHIP: 7 CHANNEL: 7)

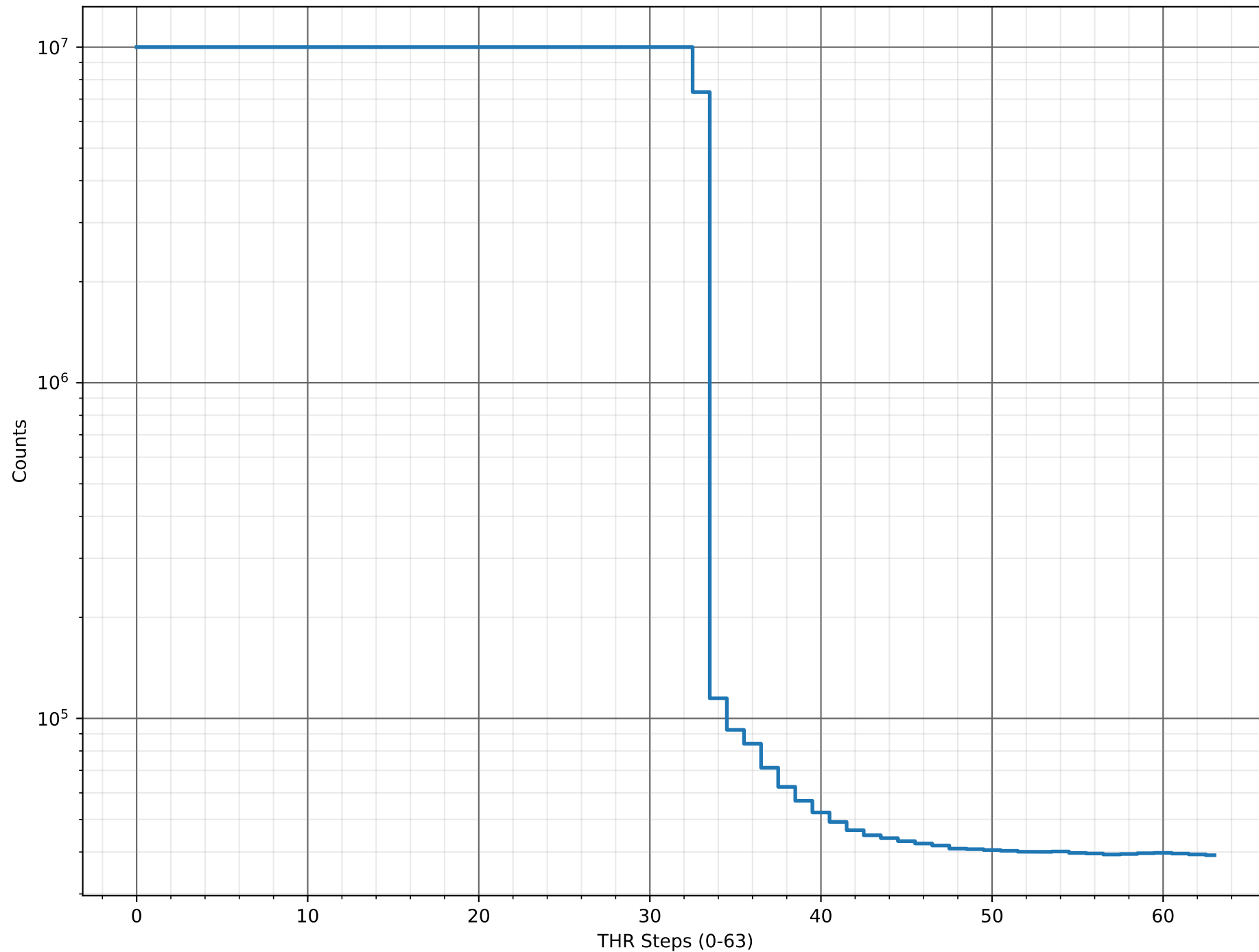




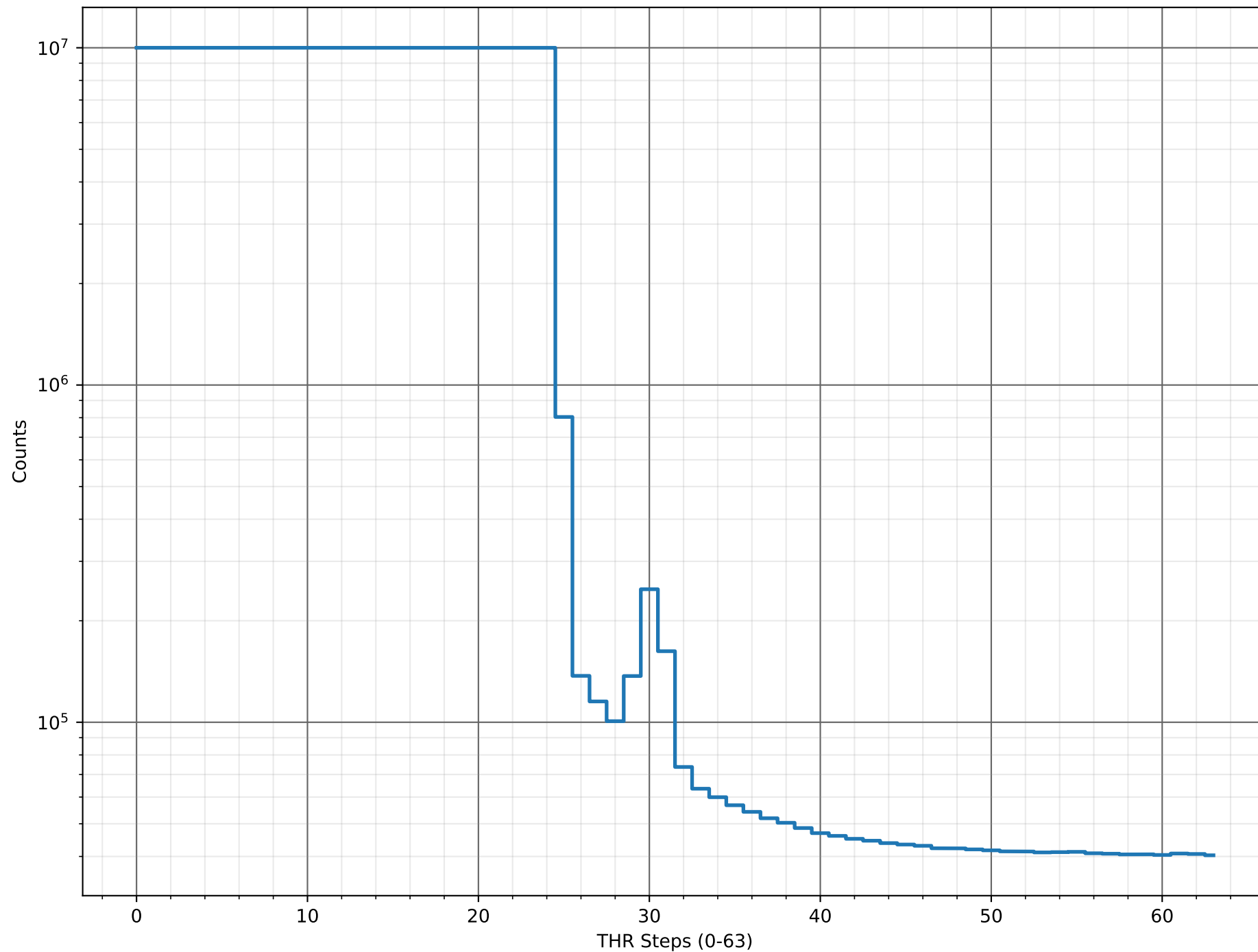
THR Distribution of PMT: B Anode: 9 (UART: 2 FEB: 1 CHIP: 5 CHANNEL: 1)



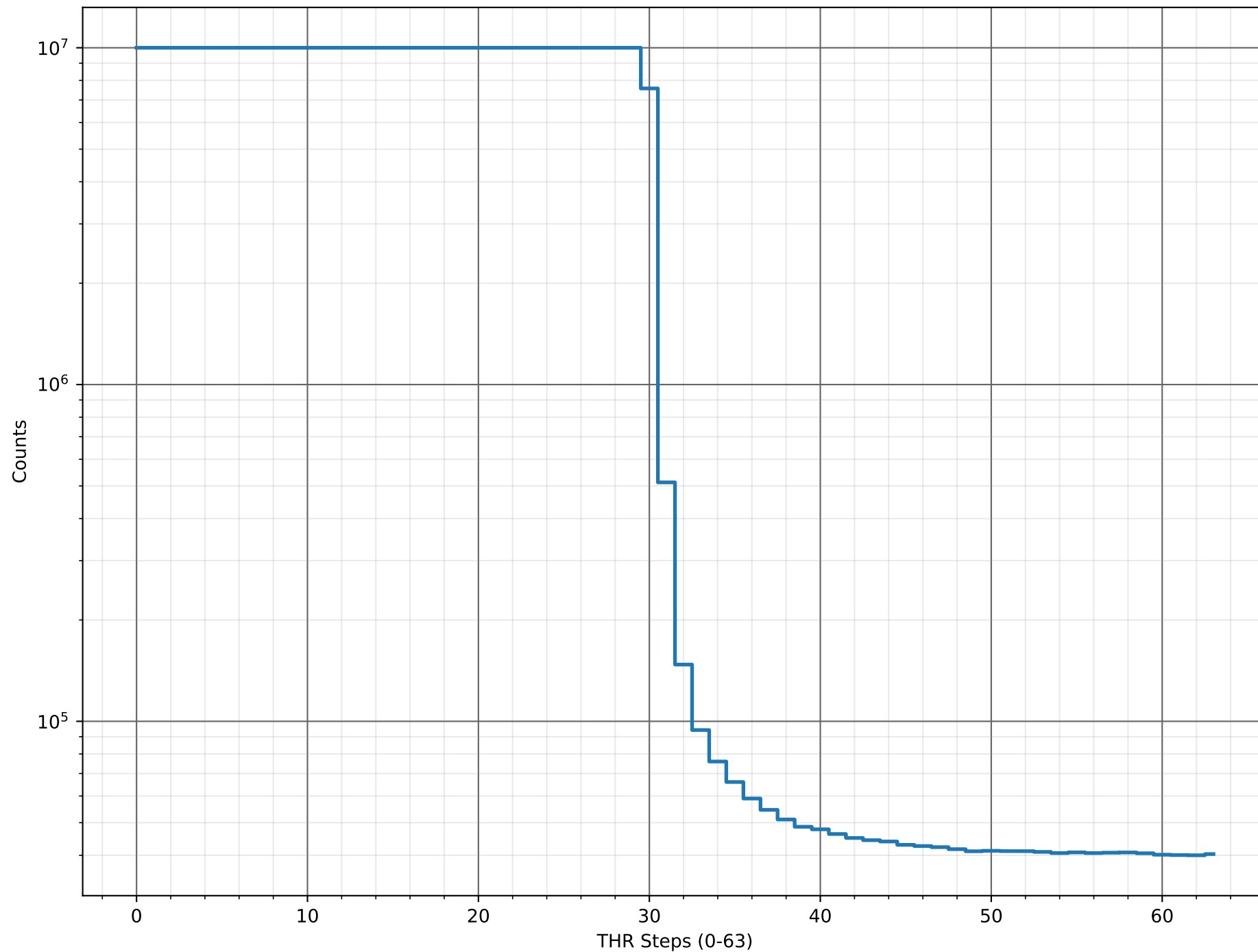
THR Distribution of PMT: B Anode: 10 (UART: 2 FEB: 1 CHIP: 5 CHANNEL: 3)



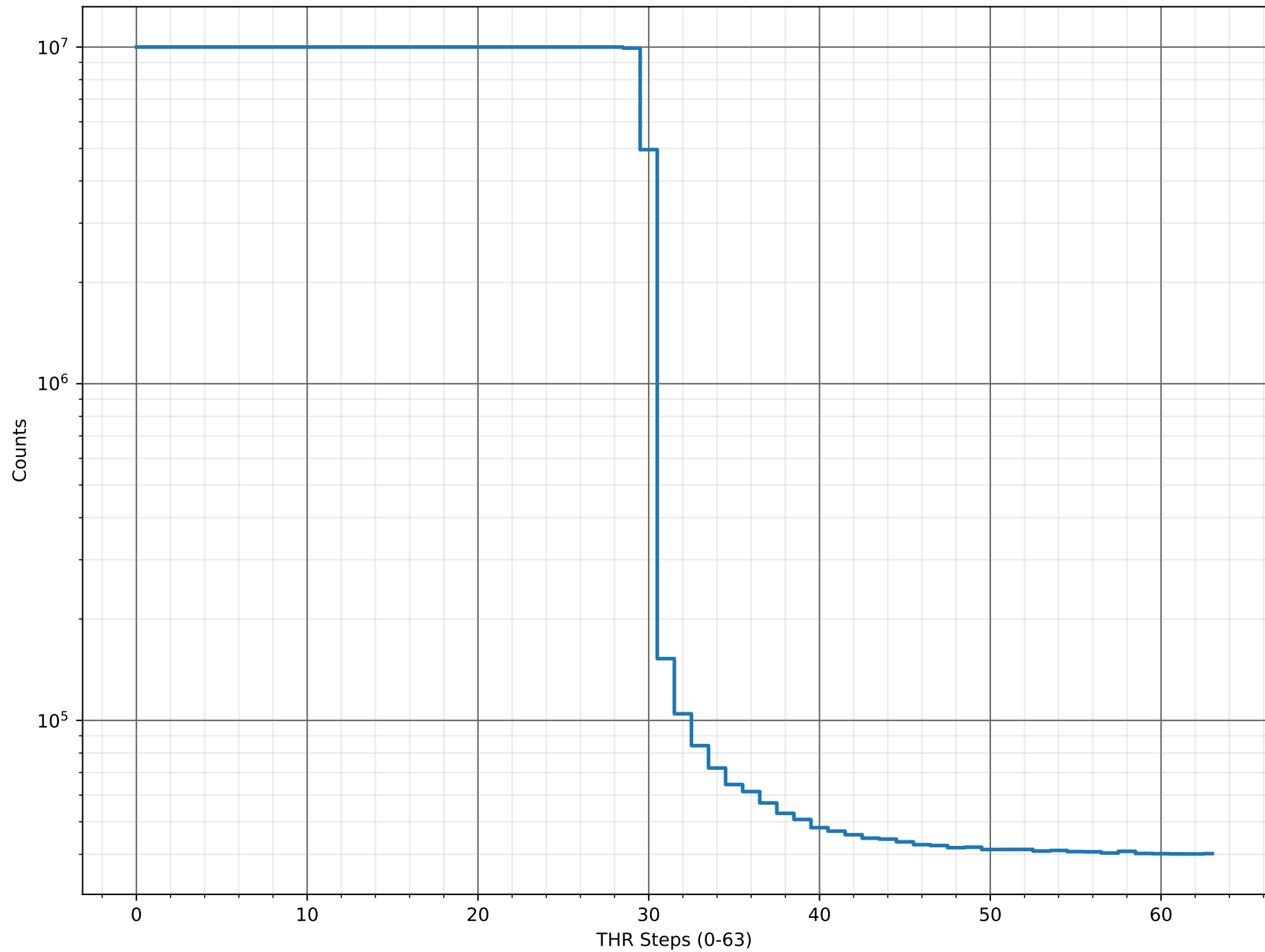
THR Distribution of PMT: B Anode: 11 (UART: 2 FEB: 1 CHIP: 5 CHANNEL: 5)



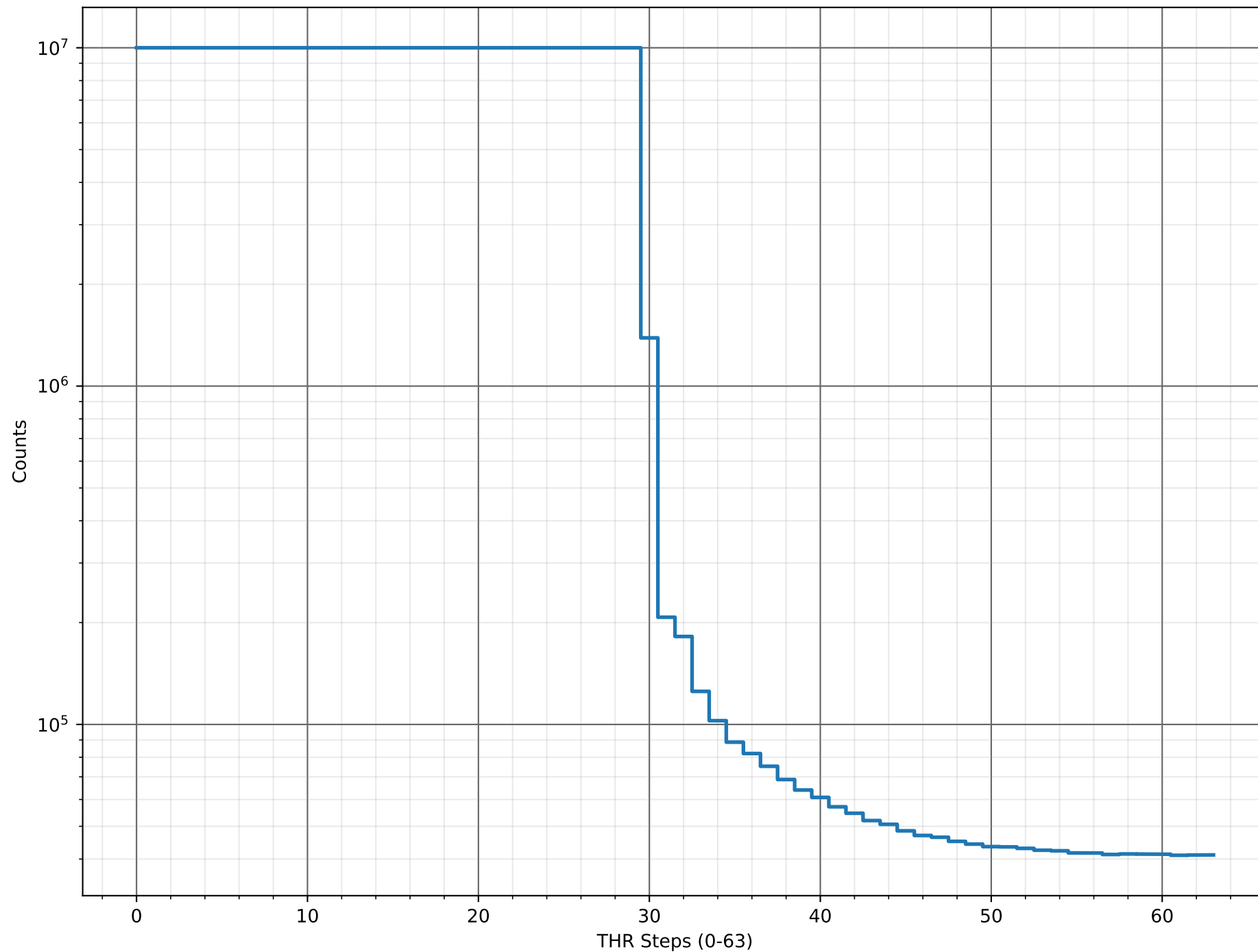
THR Distribution of PMT: B Anode: 12 (UART: 2 FEB: 1 CHIP: 5 CHANNEL: 7)



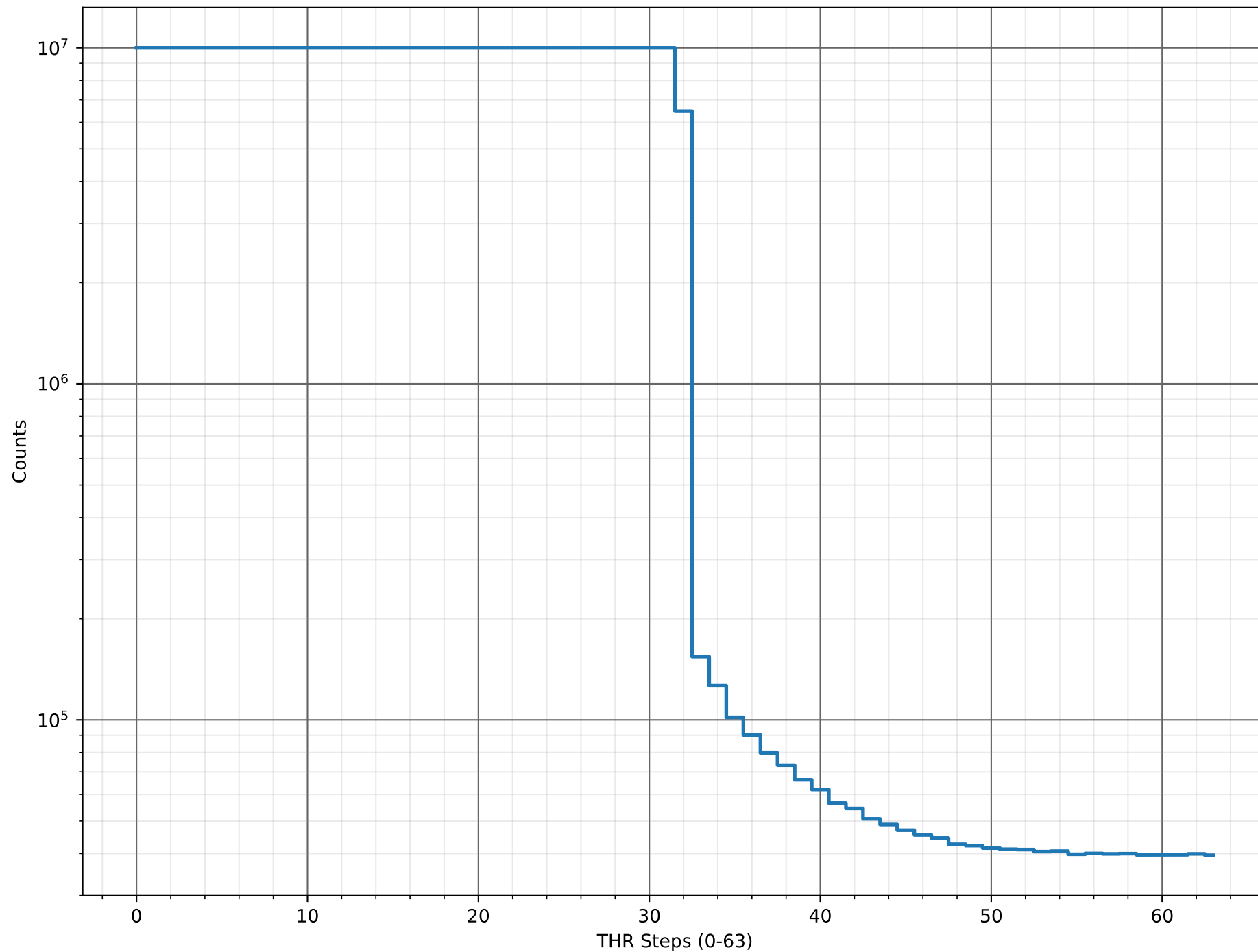
THR Distribution of PMT: B Anode: 13 (UART: 2 FEB: 1 CHIP: 7 CHANNEL: 0)



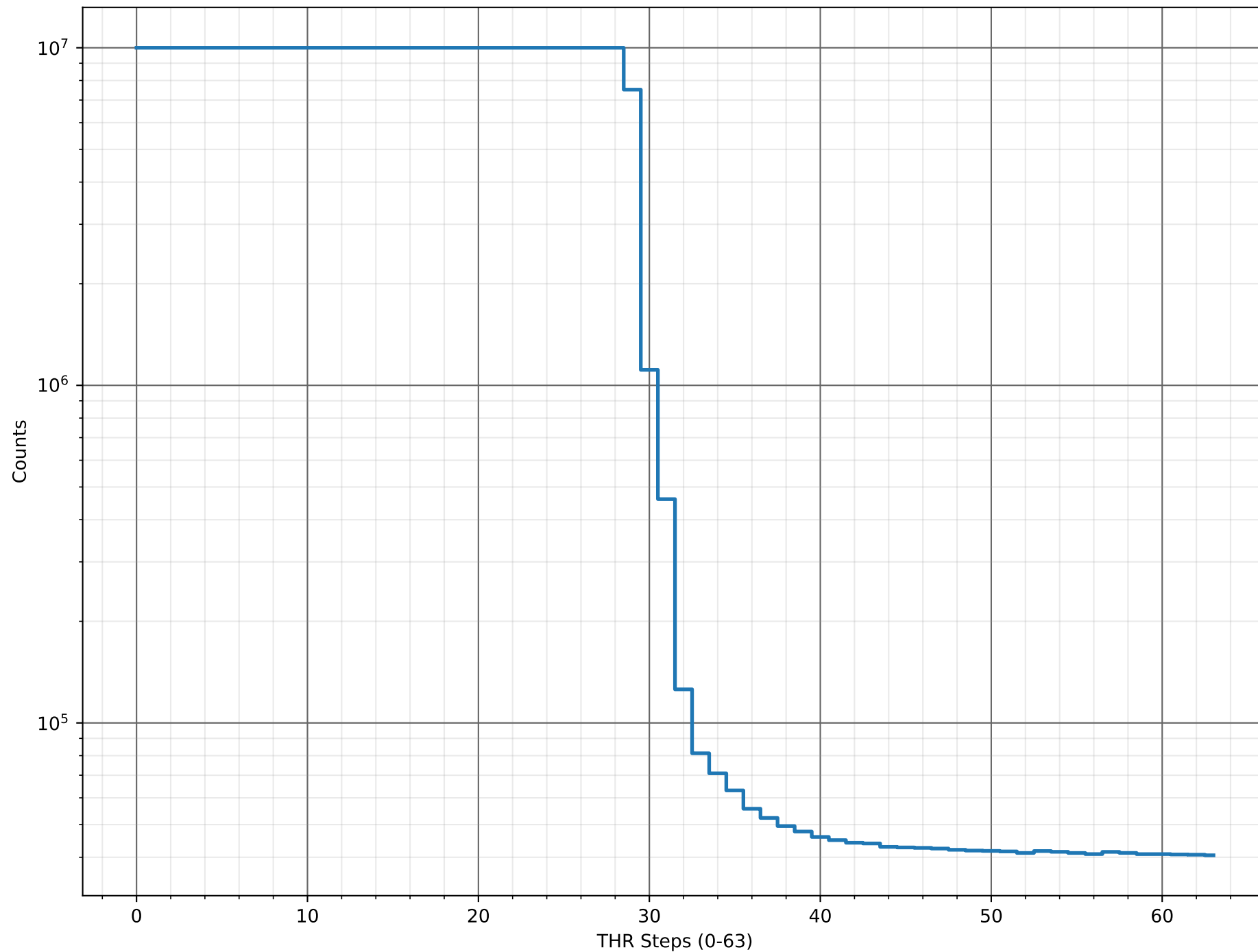
THR Distribution of PMT: B Anode: 14 (UART: 2 FEB: 1 CHIP: 7 CHANNEL: 2)



THR Distribution of PMT: B Anode: 15 (UART: 2 FEB: 1 CHIP: 7 CHANNEL: 4)

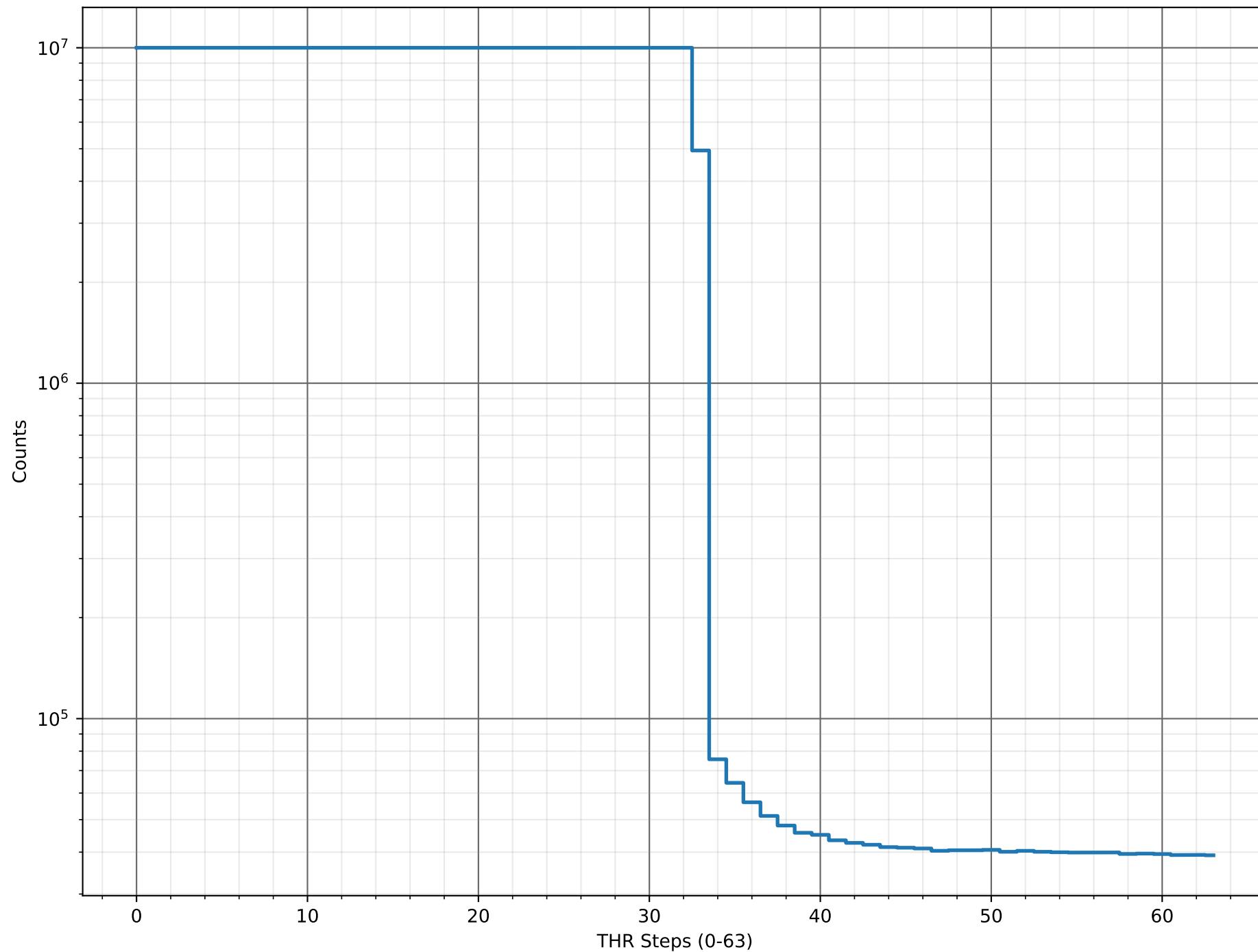


THR Distribution of PMT: B Anode: 16 (UART: 2 FEB: 1 CHIP: 7 CHANNEL: 6)

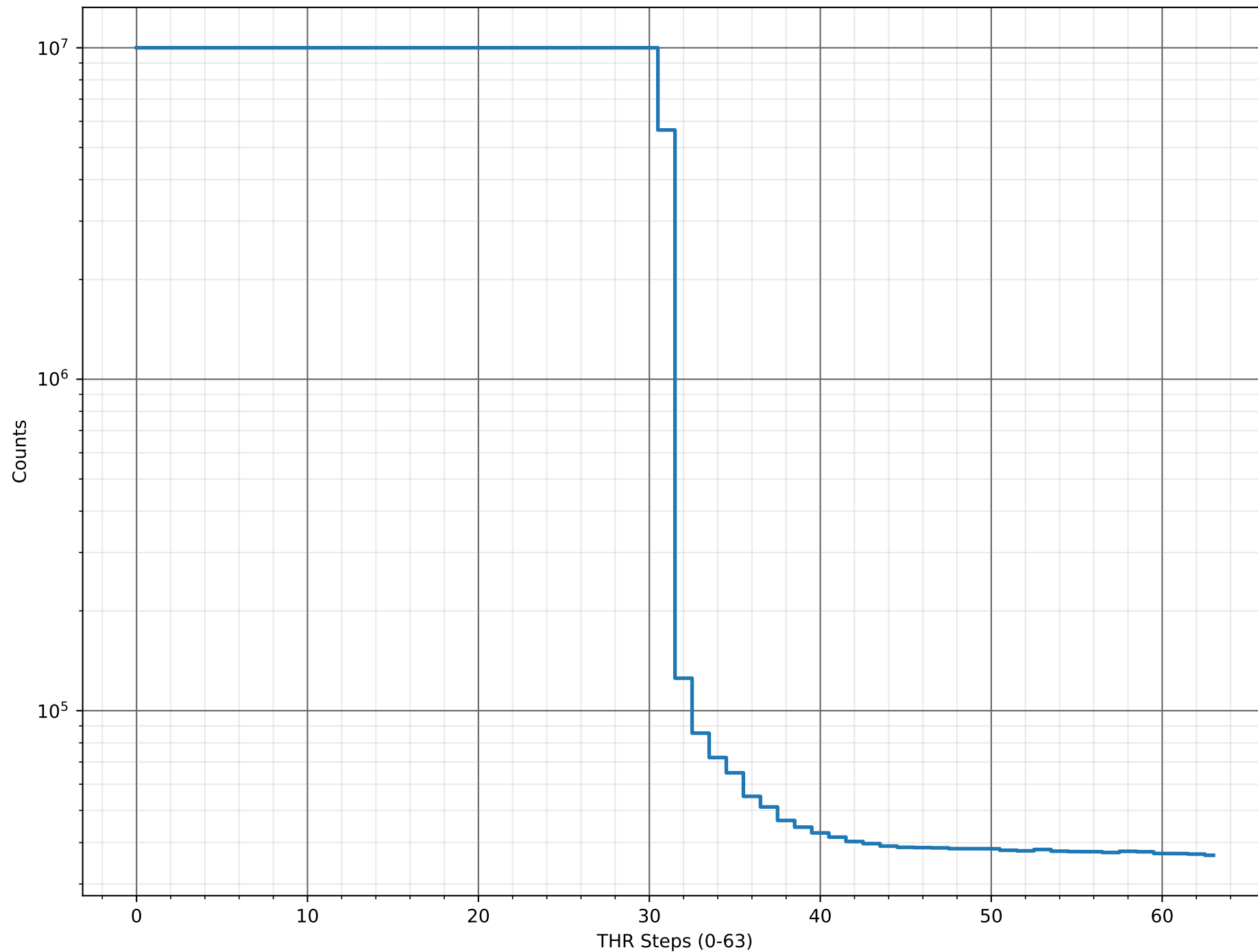




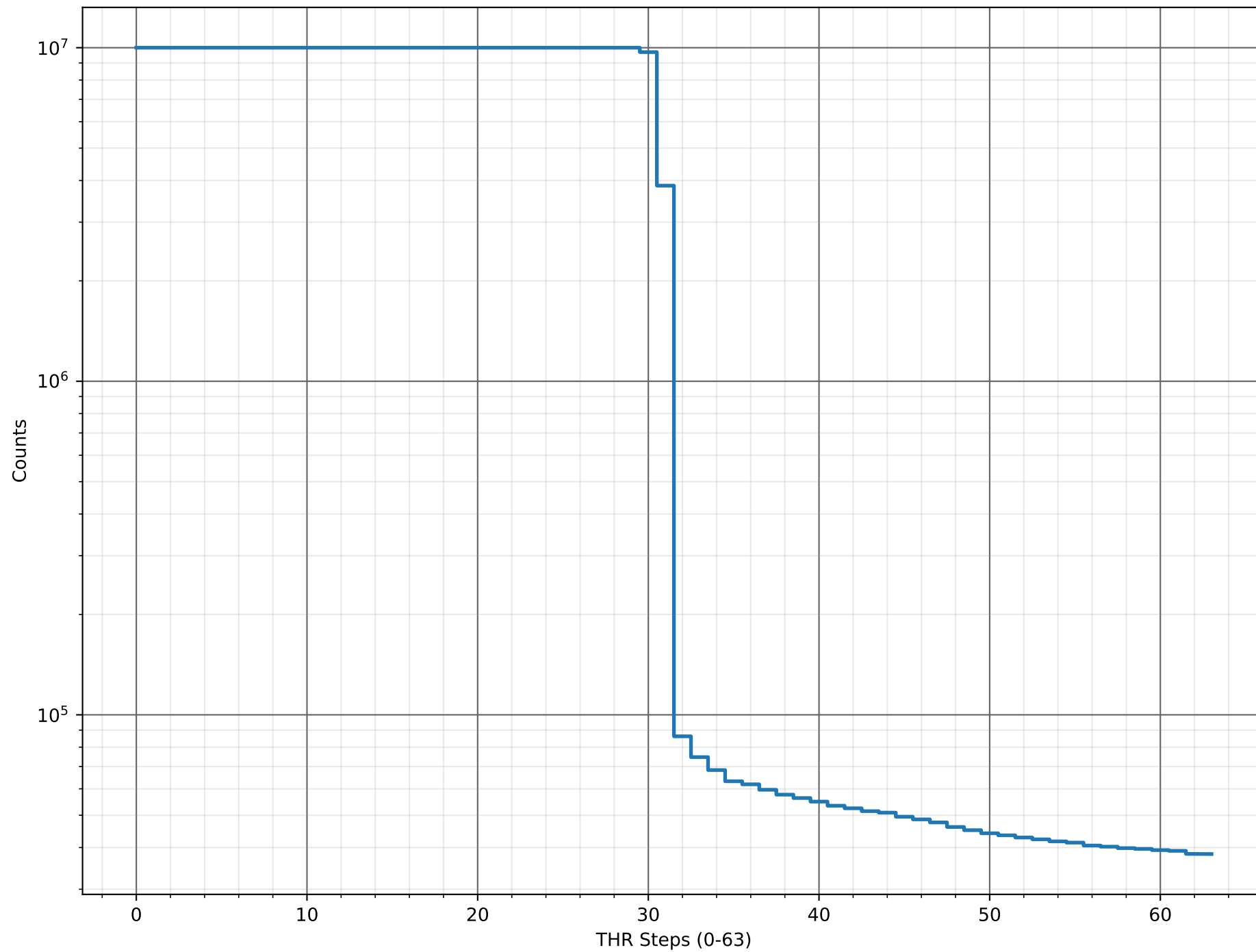
THR Distribution of PMT: B Anode: 17 (UART: 2 FEB: 1 CHIP: 4 CHANNEL: 7)



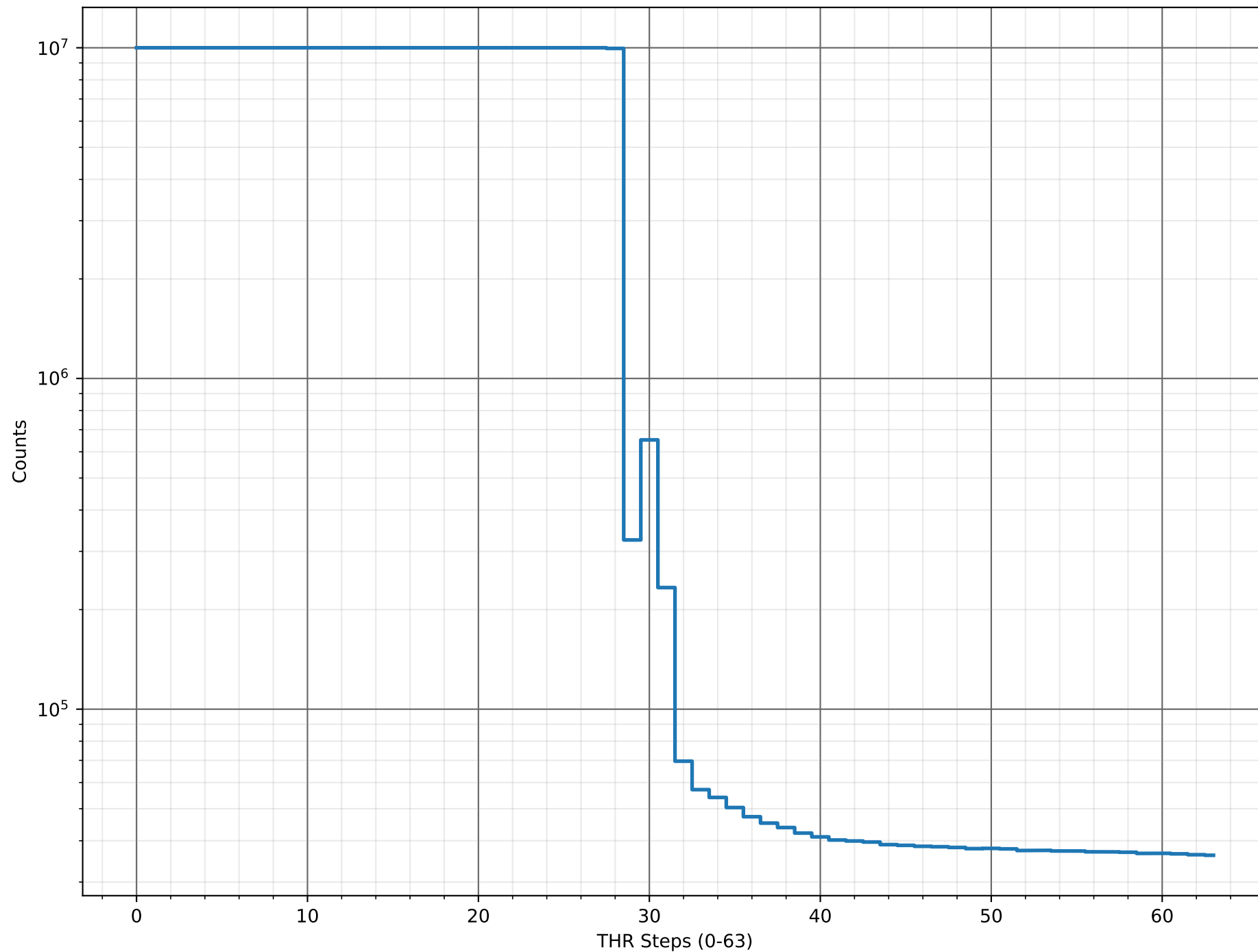
THR Distribution of PMT: B Anode: 18 (UART: 2 FEB: 1 CHIP: 4 CHANNEL: 5)



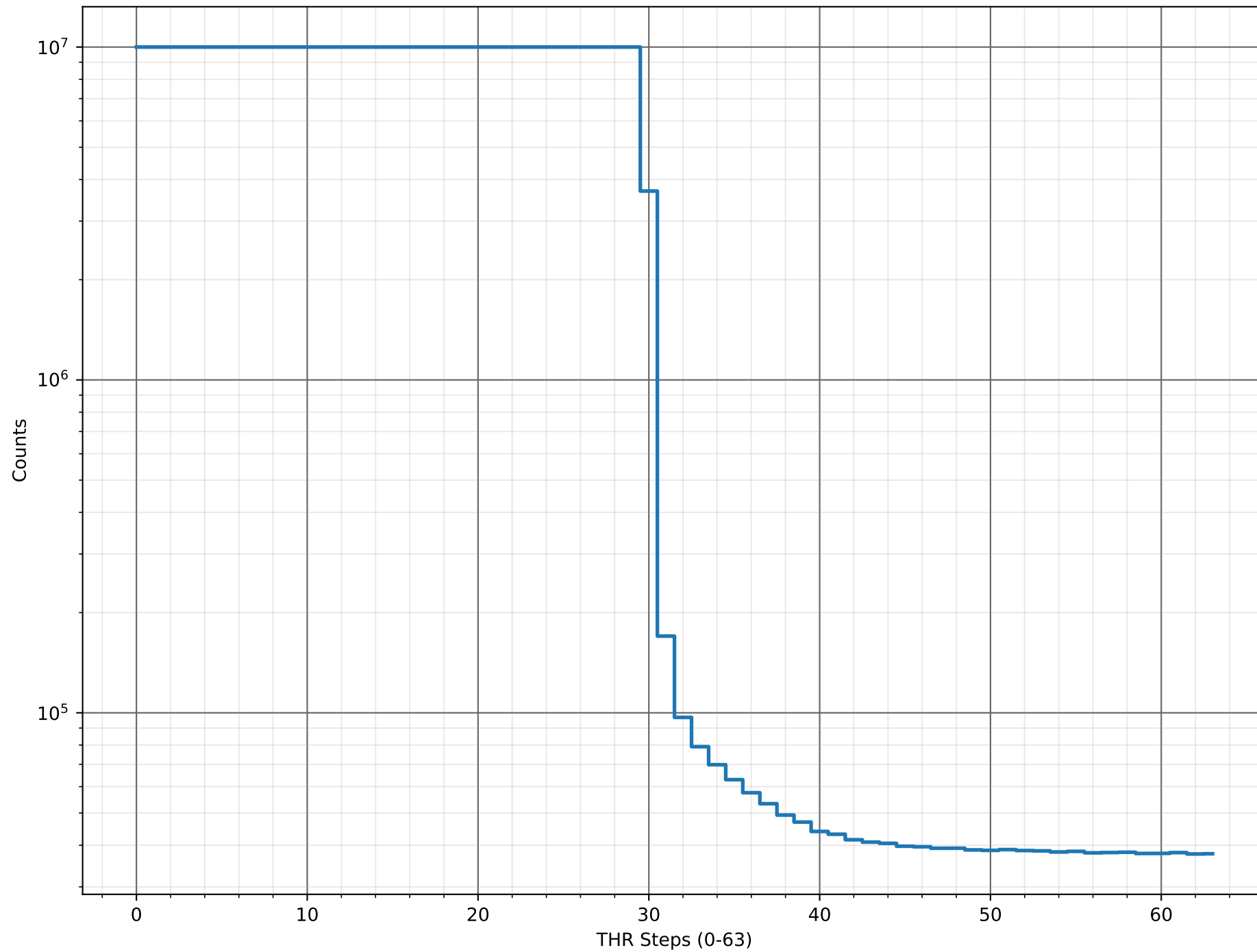
THR Distribution of PMT: B Anode: 19 (UART: 2 FEB: 1 CHIP: 4 CHANNEL: 3)



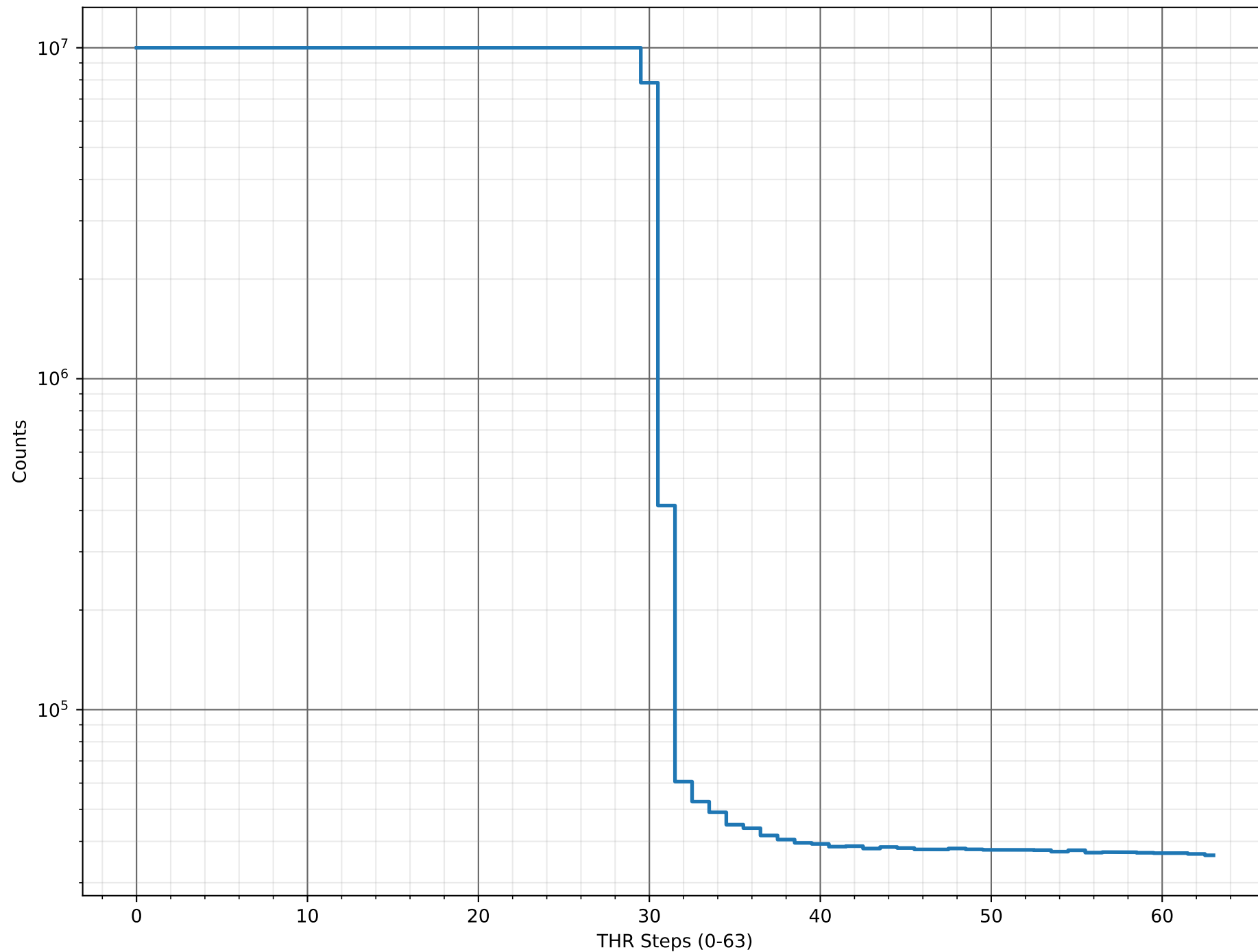
THR Distribution of PMT: B Anode: 20 (UART: 2 FEB: 1 CHIP: 4 CHANNEL: 0)



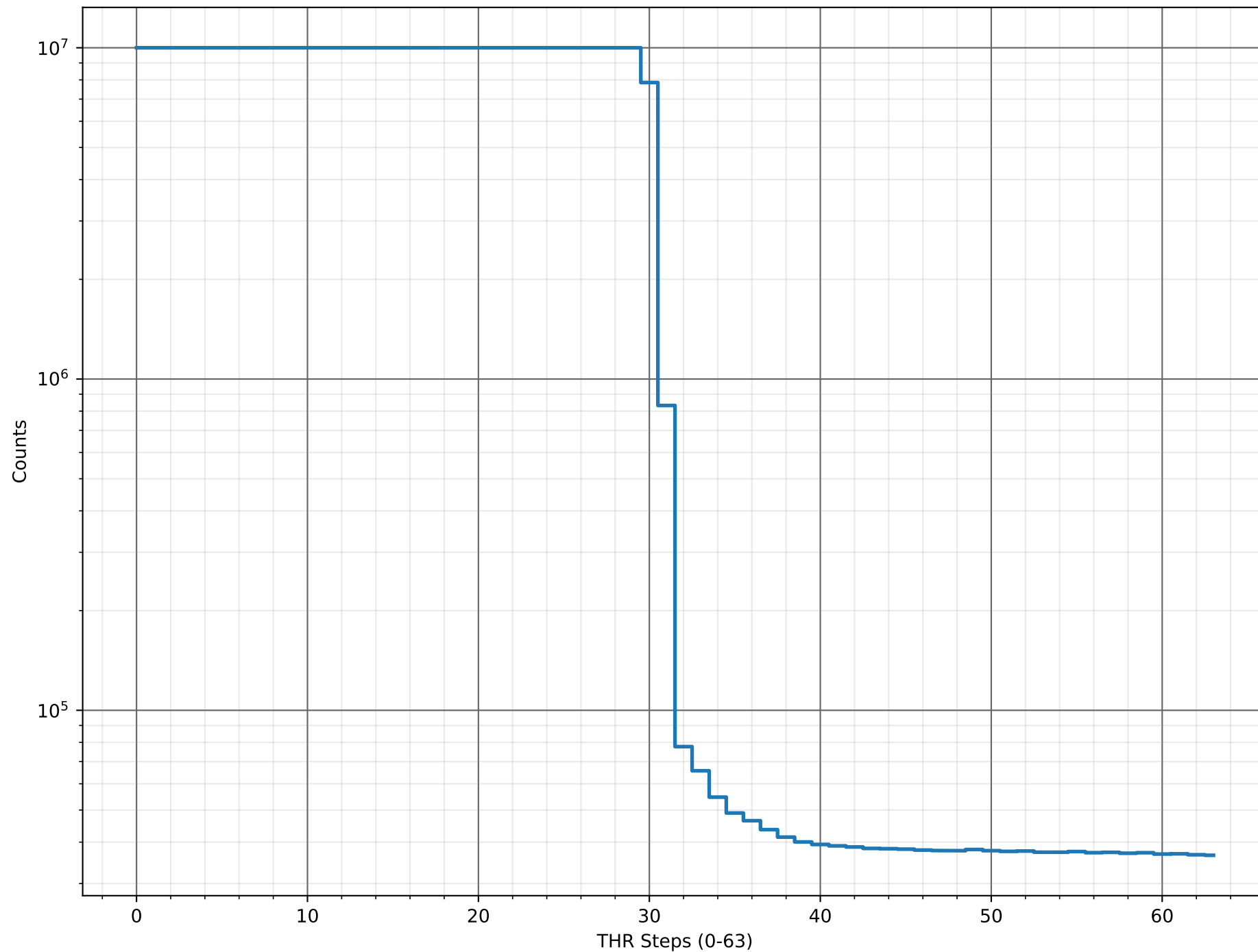
THR Distribution of PMT: B Anode: 21 (UART: 2 FEB: 1 CHIP: 6 CHANNEL: 6)



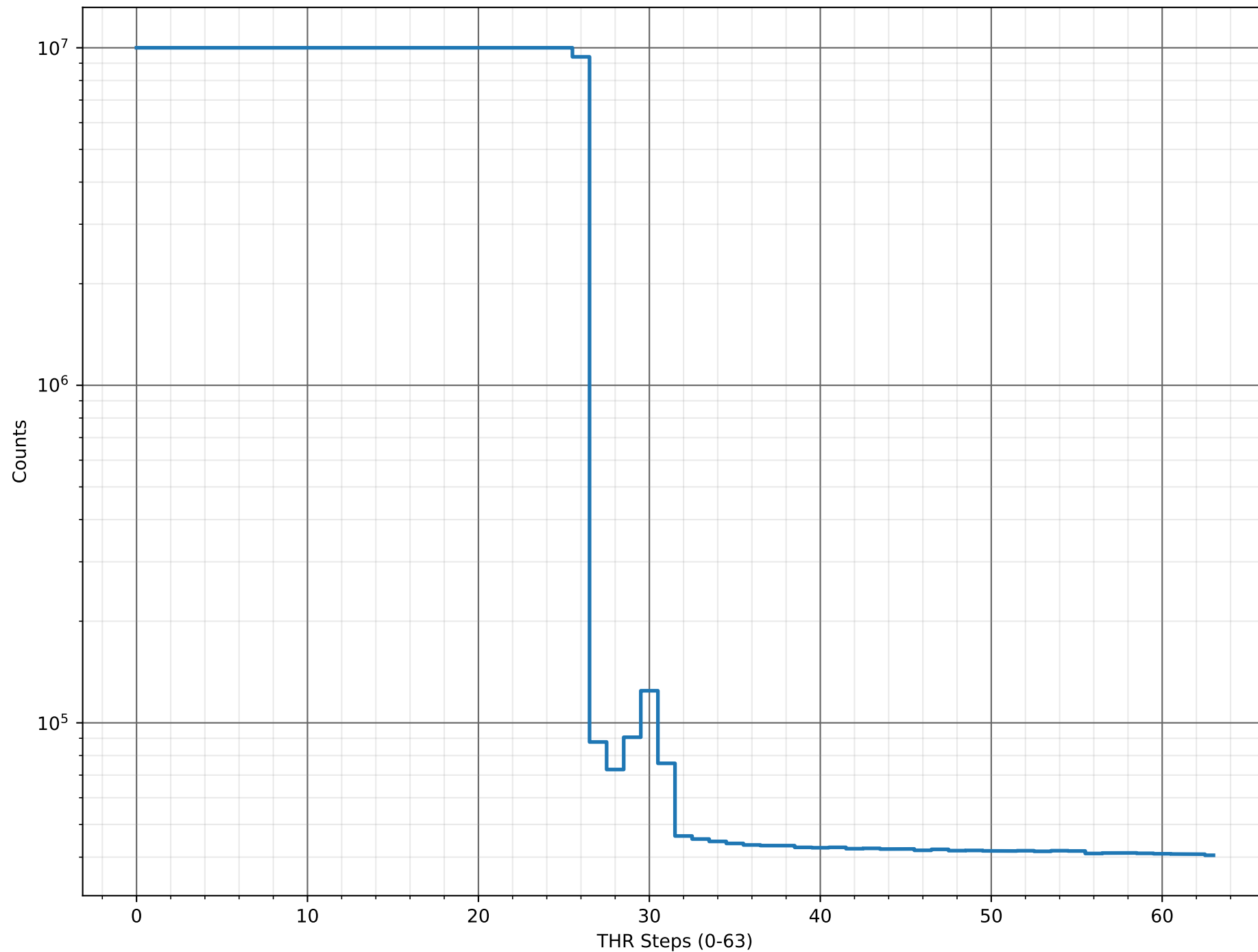
THR Distribution of PMT: B Anode: 22 (UART: 2 FEB: 1 CHIP: 6 CHANNEL: 4)



THR Distribution of PMT: B Anode: 23 (UART: 2 FEB: 1 CHIP: 6 CHANNEL: 2)

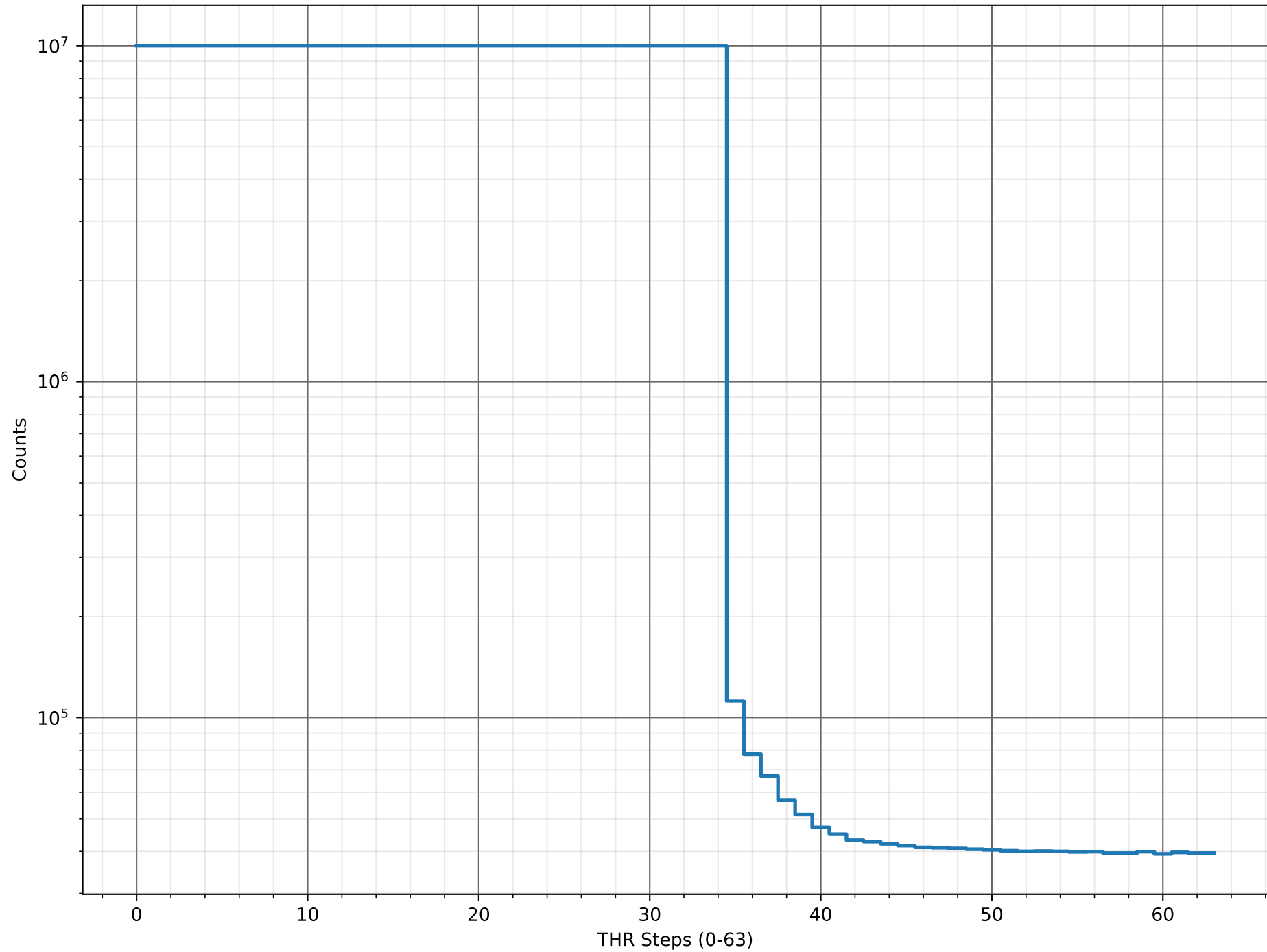


THR Distribution of PMT: B Anode: 24 (UART: 2 FEB: 1 CHIP: 6 CHANNEL: 0)

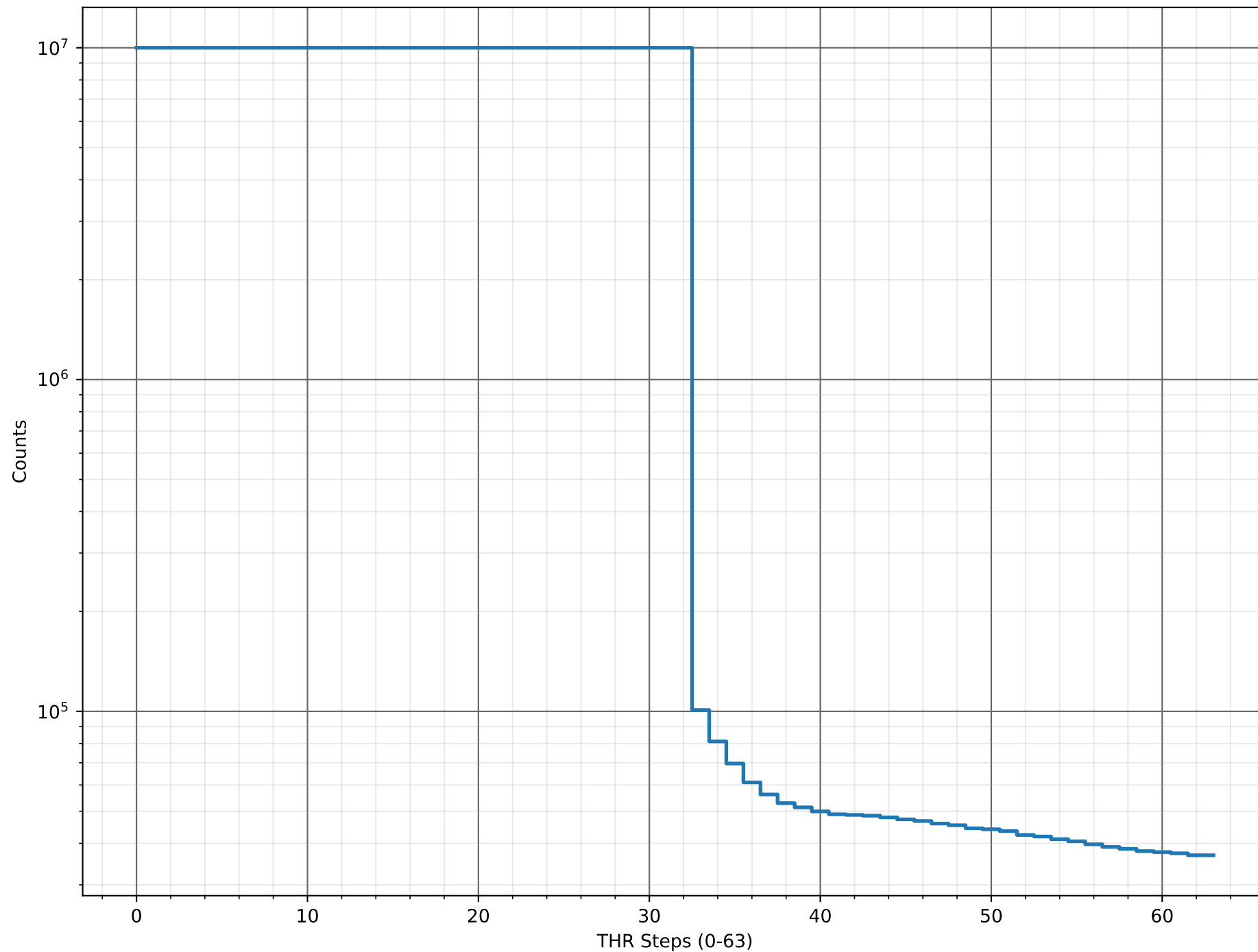




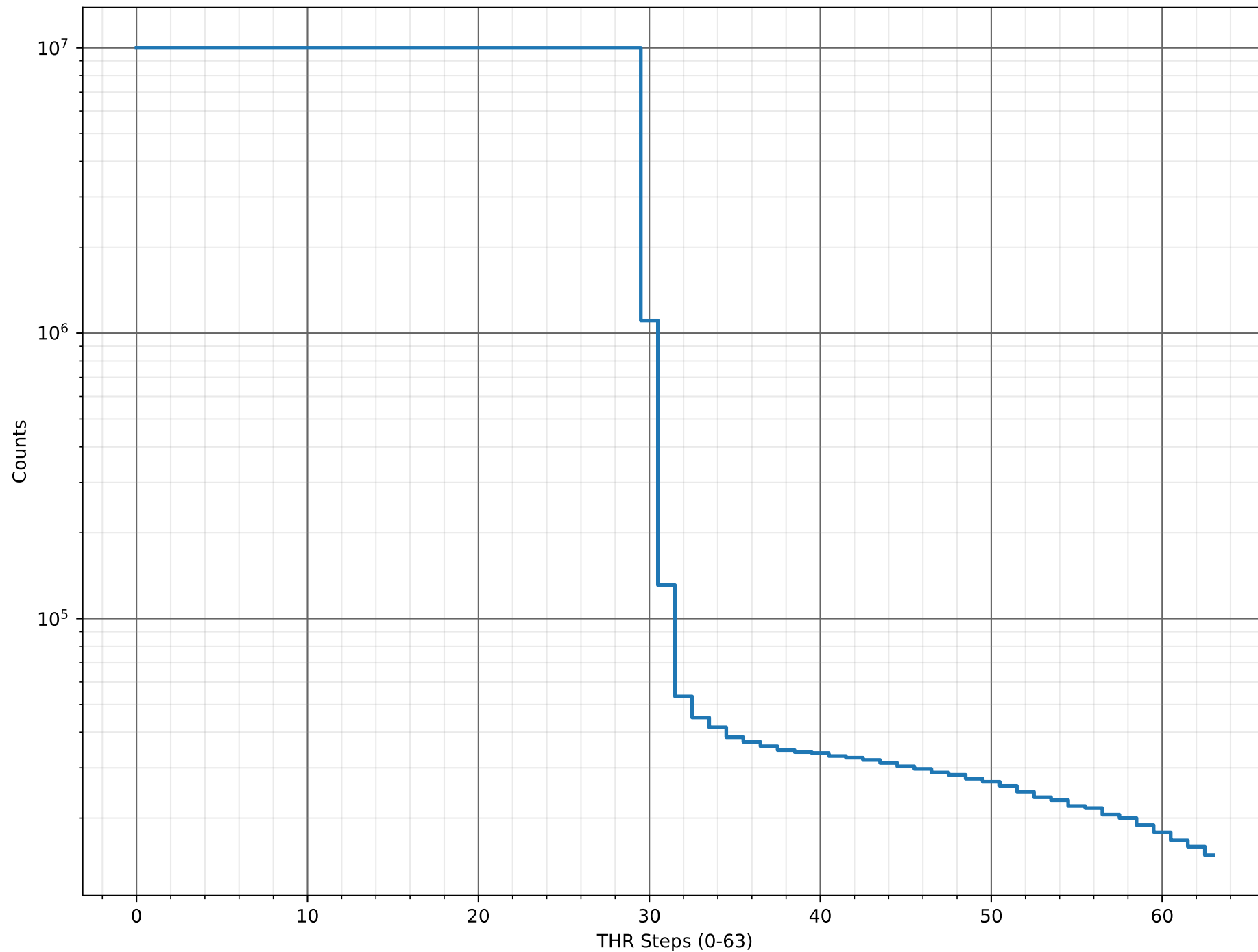
THR Distribution of PMT: B Anode: 25 (UART: 2 FEB: 1 CHIP: 4 CHANNEL: 6)



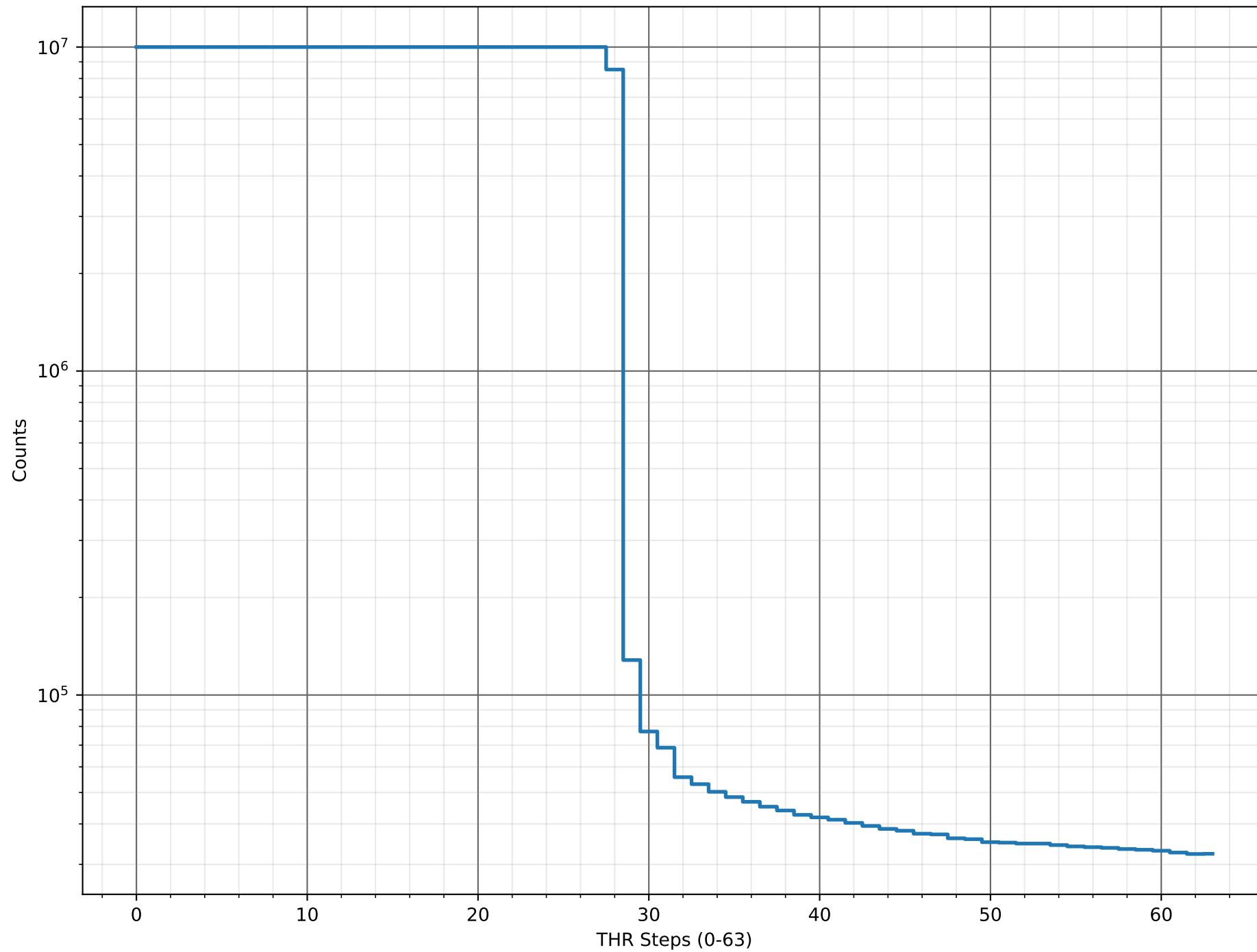
THR Distribution of PMT: B Anode: 26 (UART: 2 FEB: 1 CHIP: 4 CHANNEL: 4)



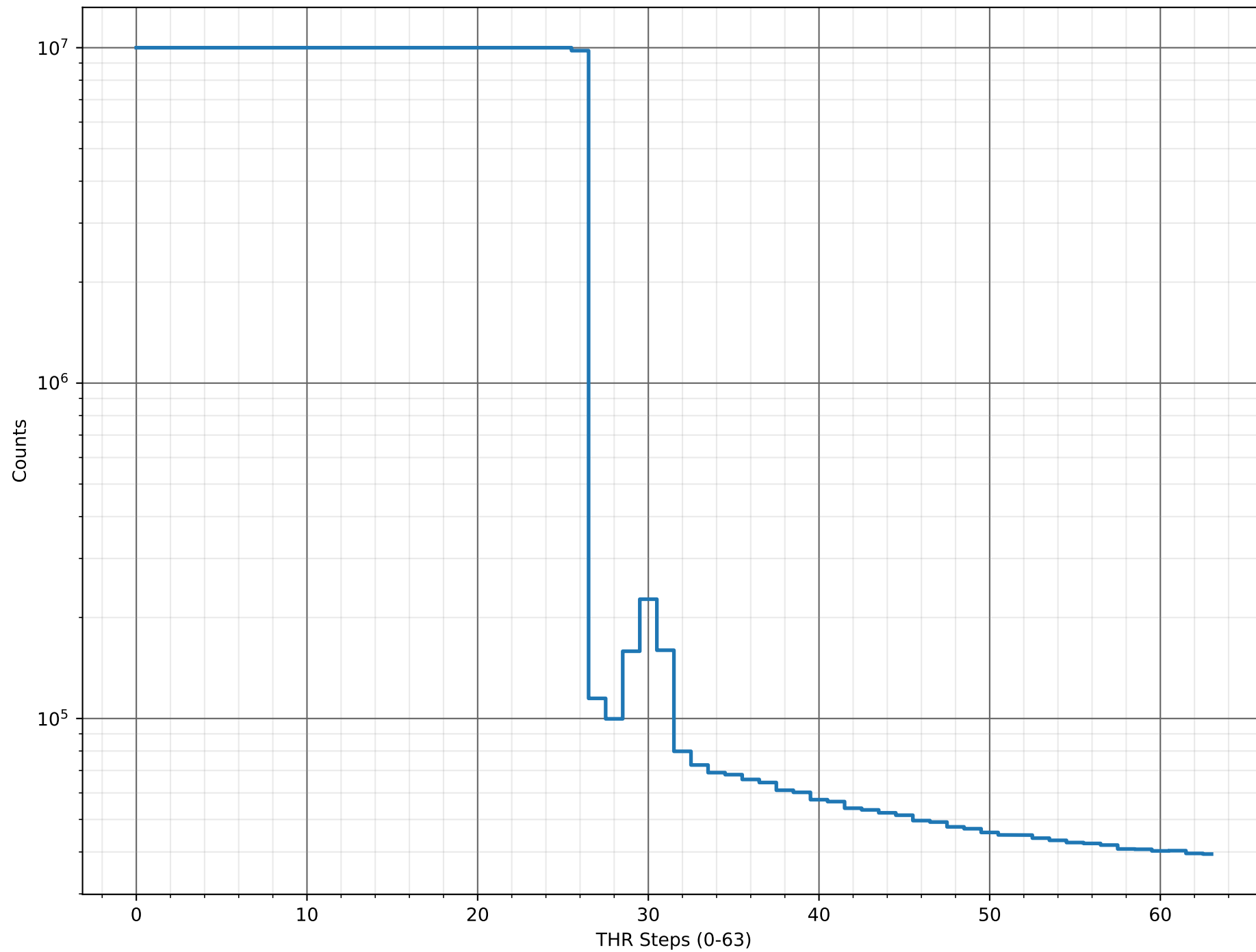
THR Distribution of PMT: B Anode: 27 (UART: 2 FEB: 1 CHIP: 4 CHANNEL: 2)



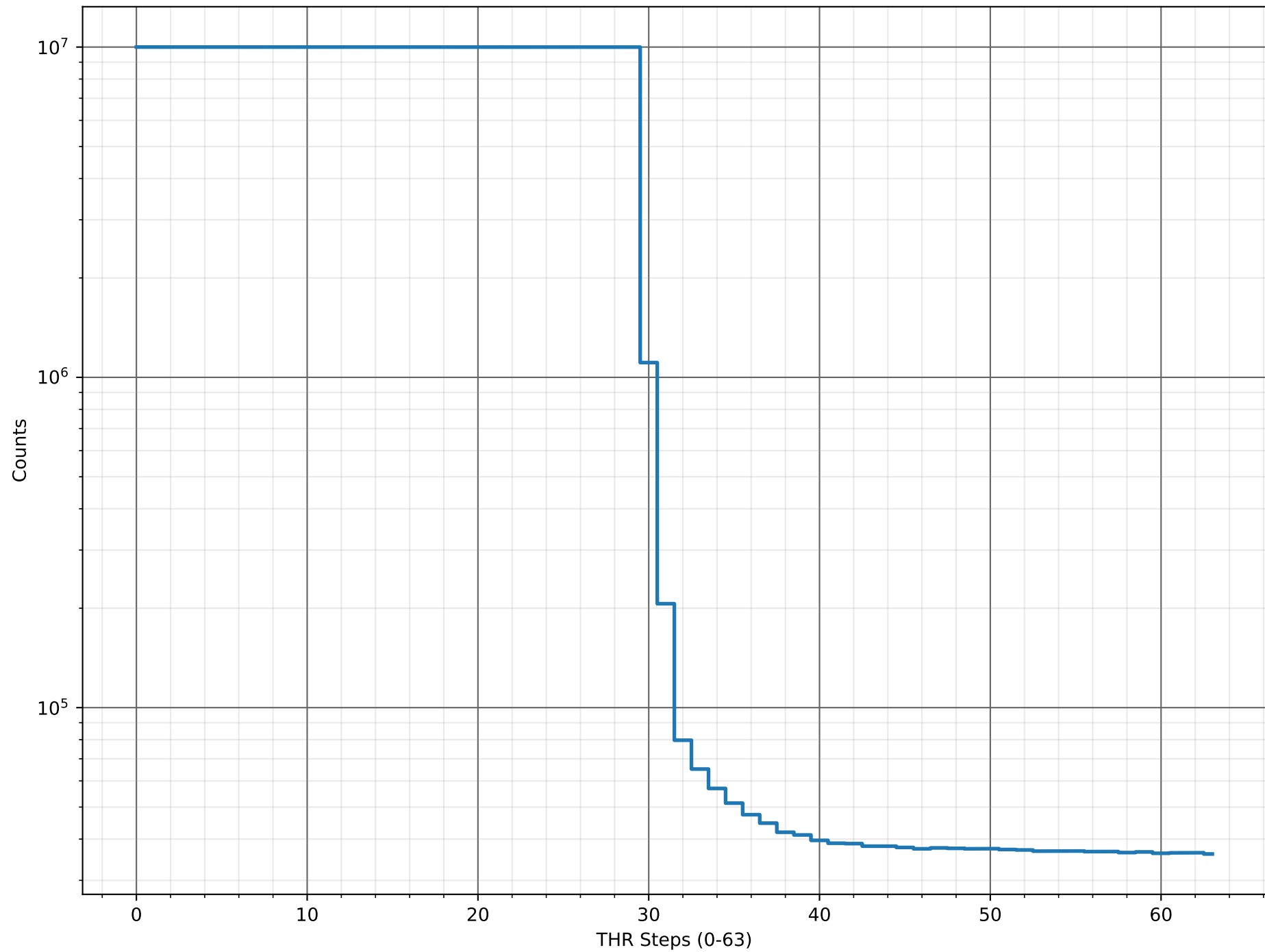
THR Distribution of PMT: B Anode: 28 (UART: 2 FEB: 1 CHIP: 4 CHANNEL: 1)



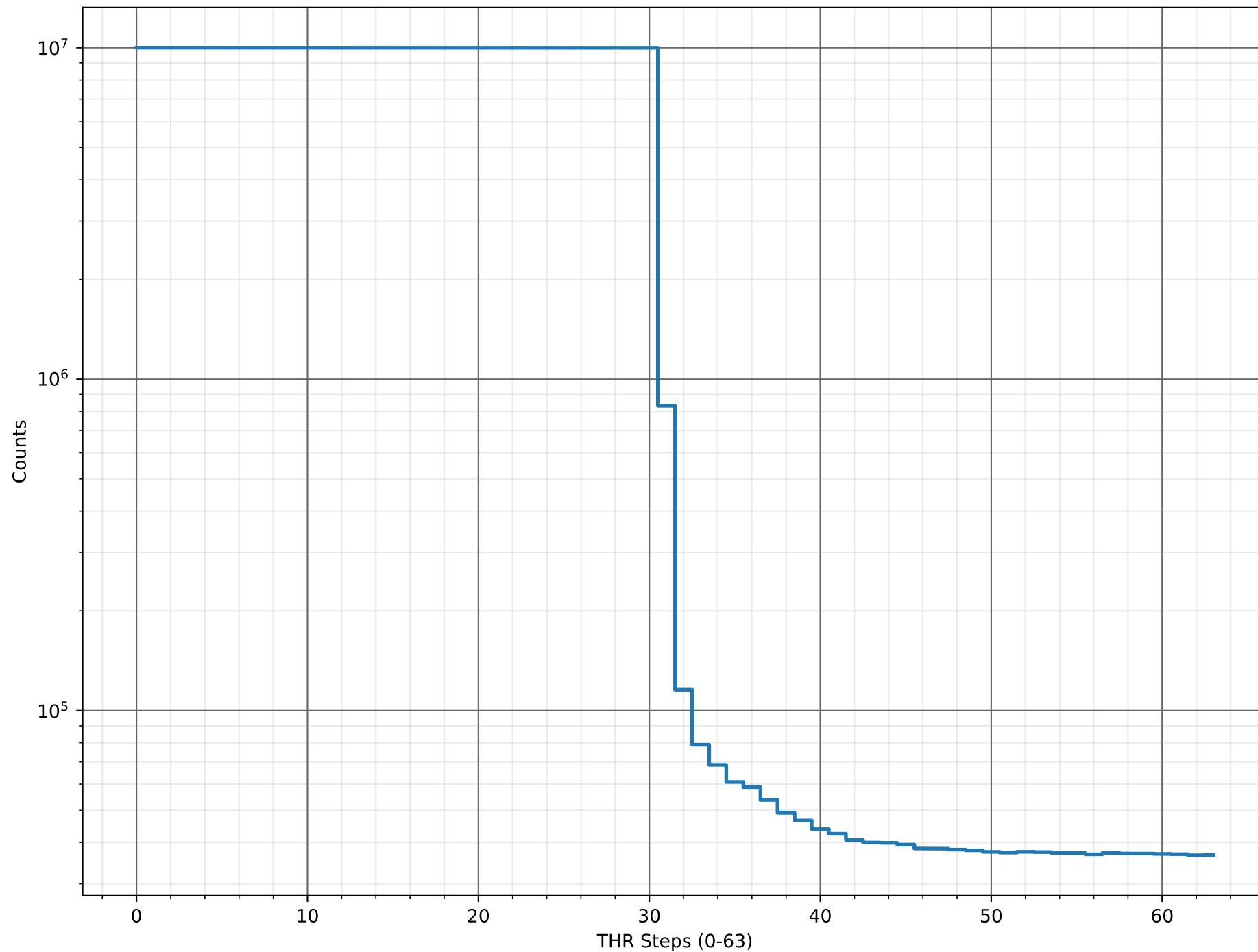
THR Distribution of PMT: B Anode: 29 (UART: 2 FEB: 1 CHIP: 6 CHANNEL: 7)



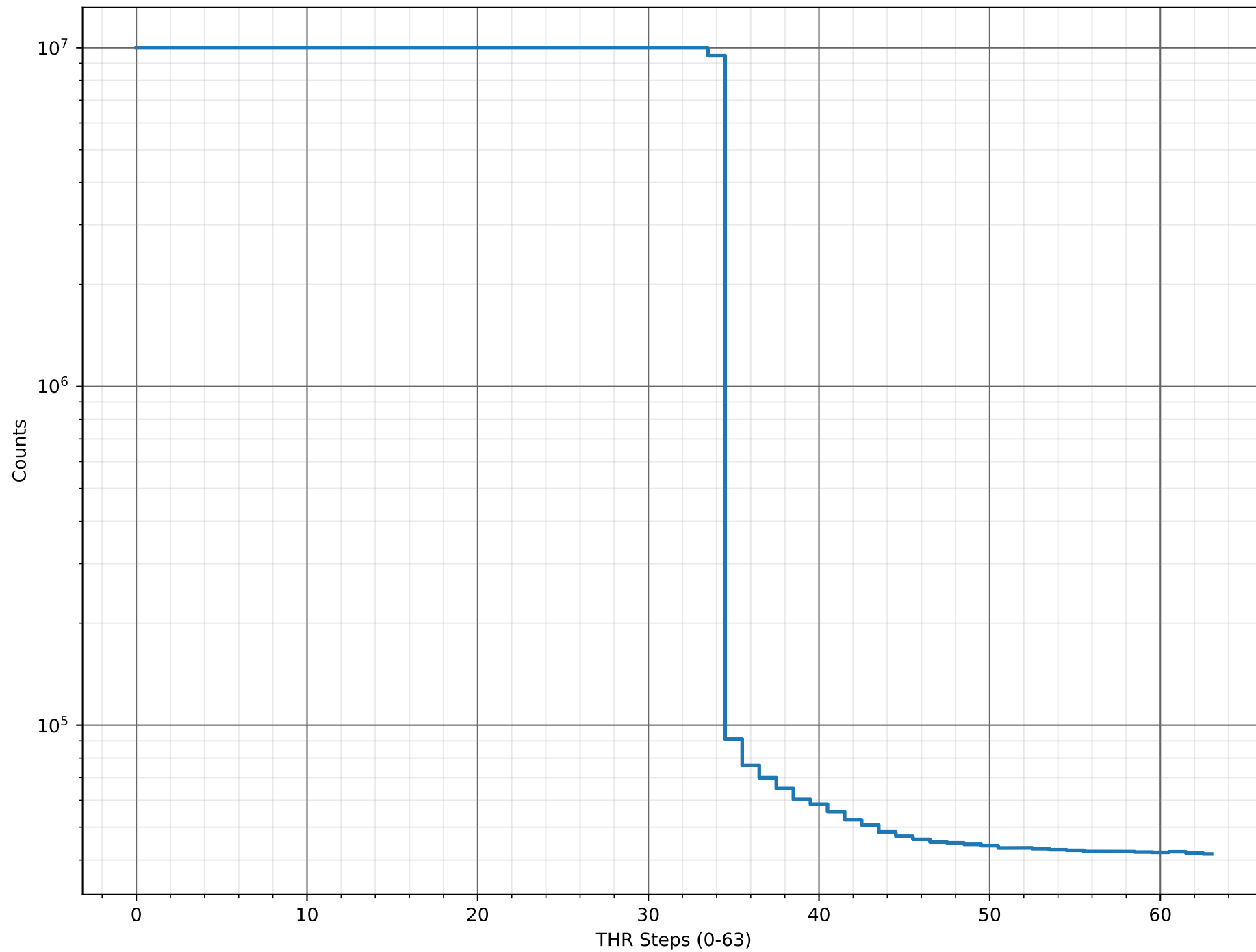
THR Distribution of PMT: B Anode: 30 (UART: 2 FEB: 1 CHIP: 6 CHANNEL: 5)



THR Distribution of PMT: B Anode: 31 (UART: 2 FEB: 1 CHIP: 6 CHANNEL: 3)

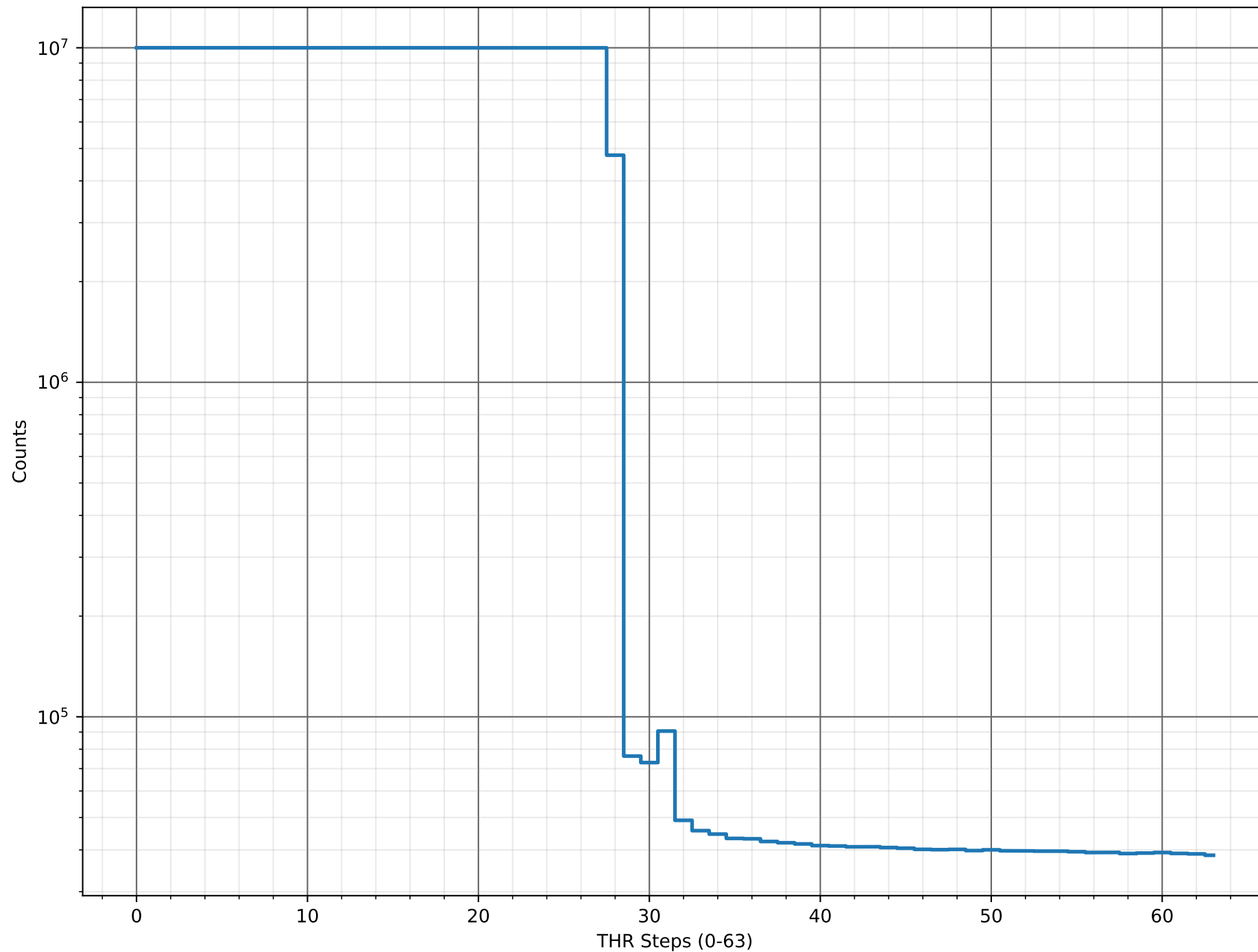


THR Distribution of PMT: B Anode: 32 (UART: 2 FEB: 1 CHIP: 6 CHANNEL: 1)

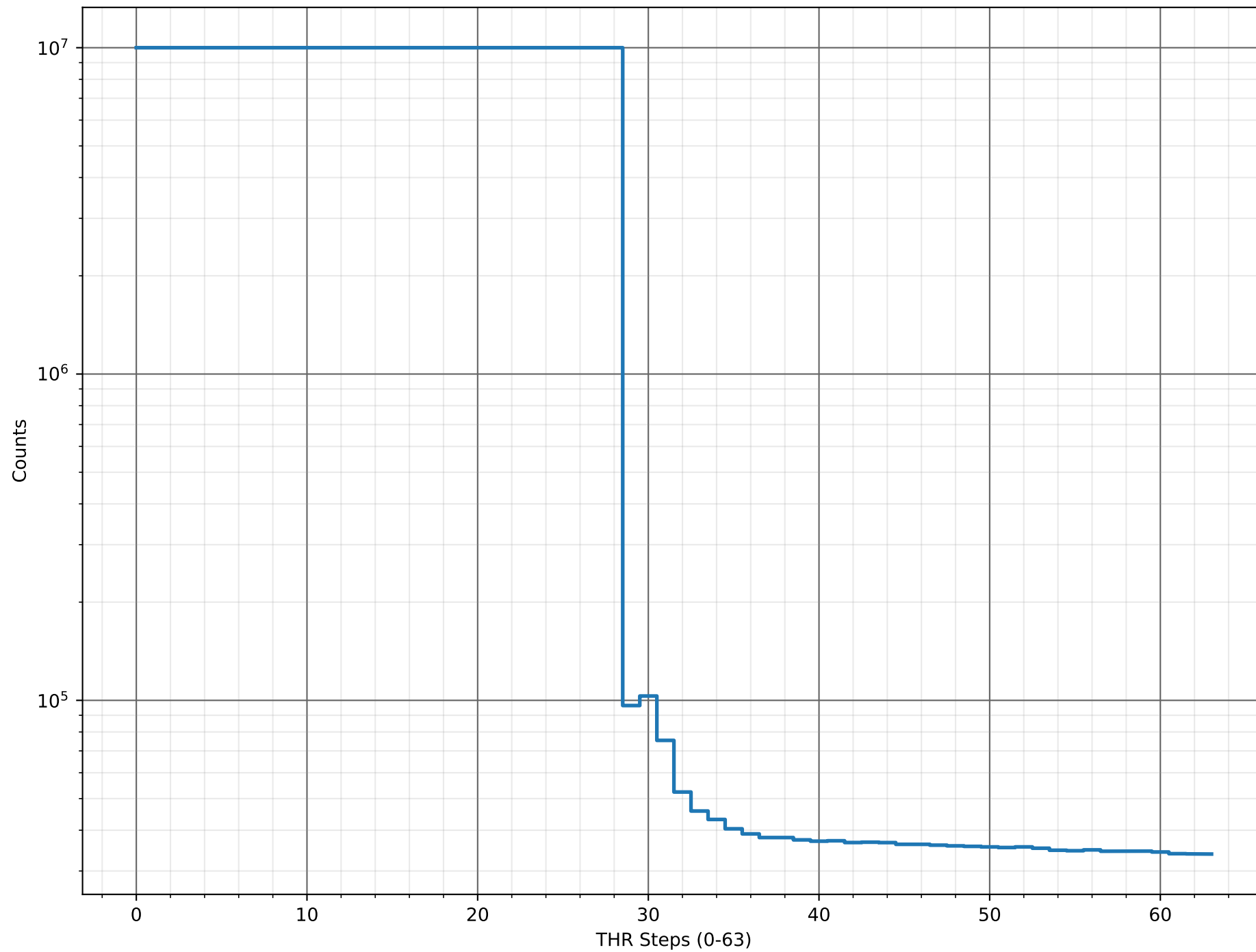




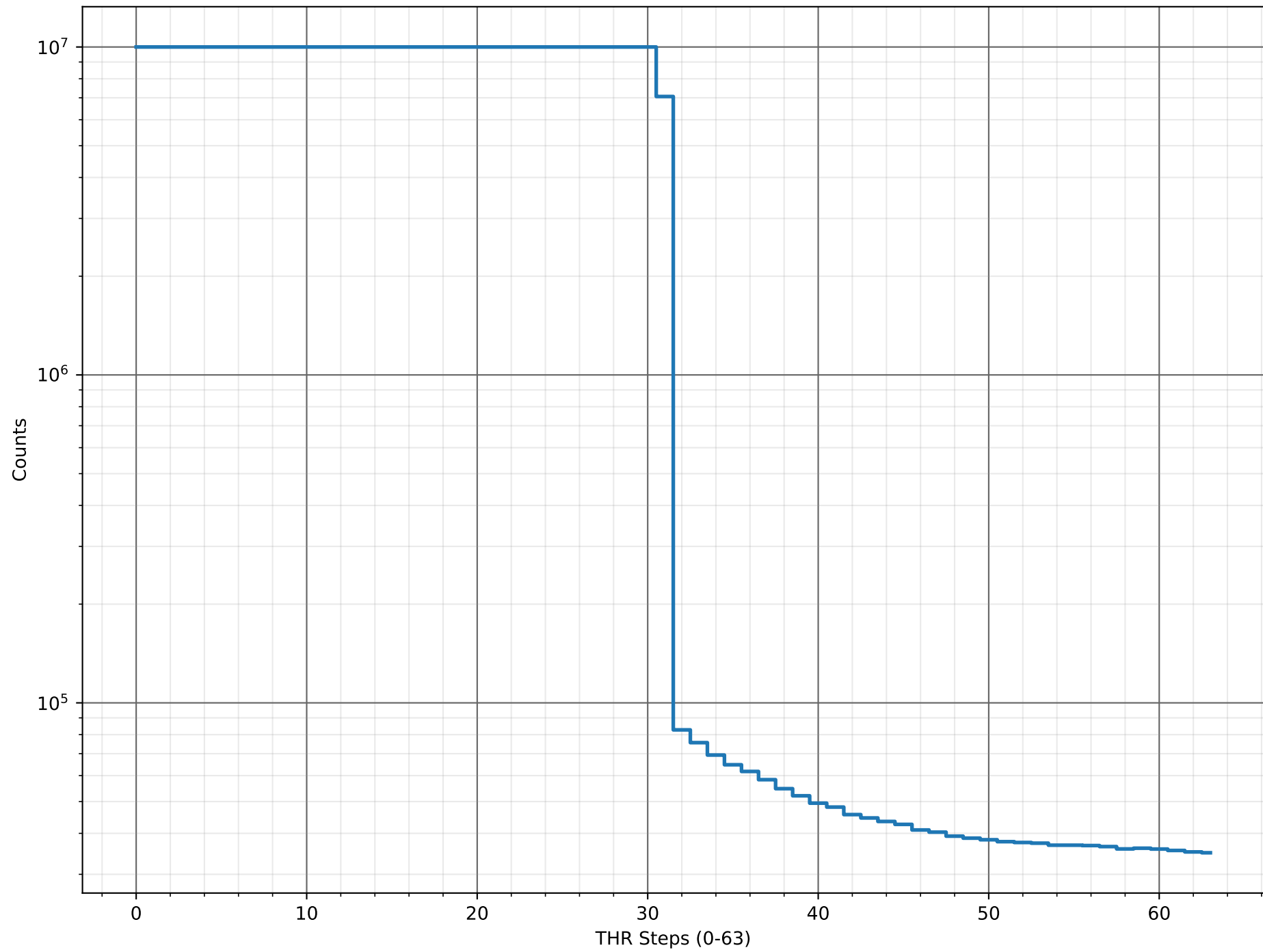
THR Distribution of PMT: B Anode: 33 (UART: 2 FEB: 0 CHIP: 5 CHANNEL: 1)



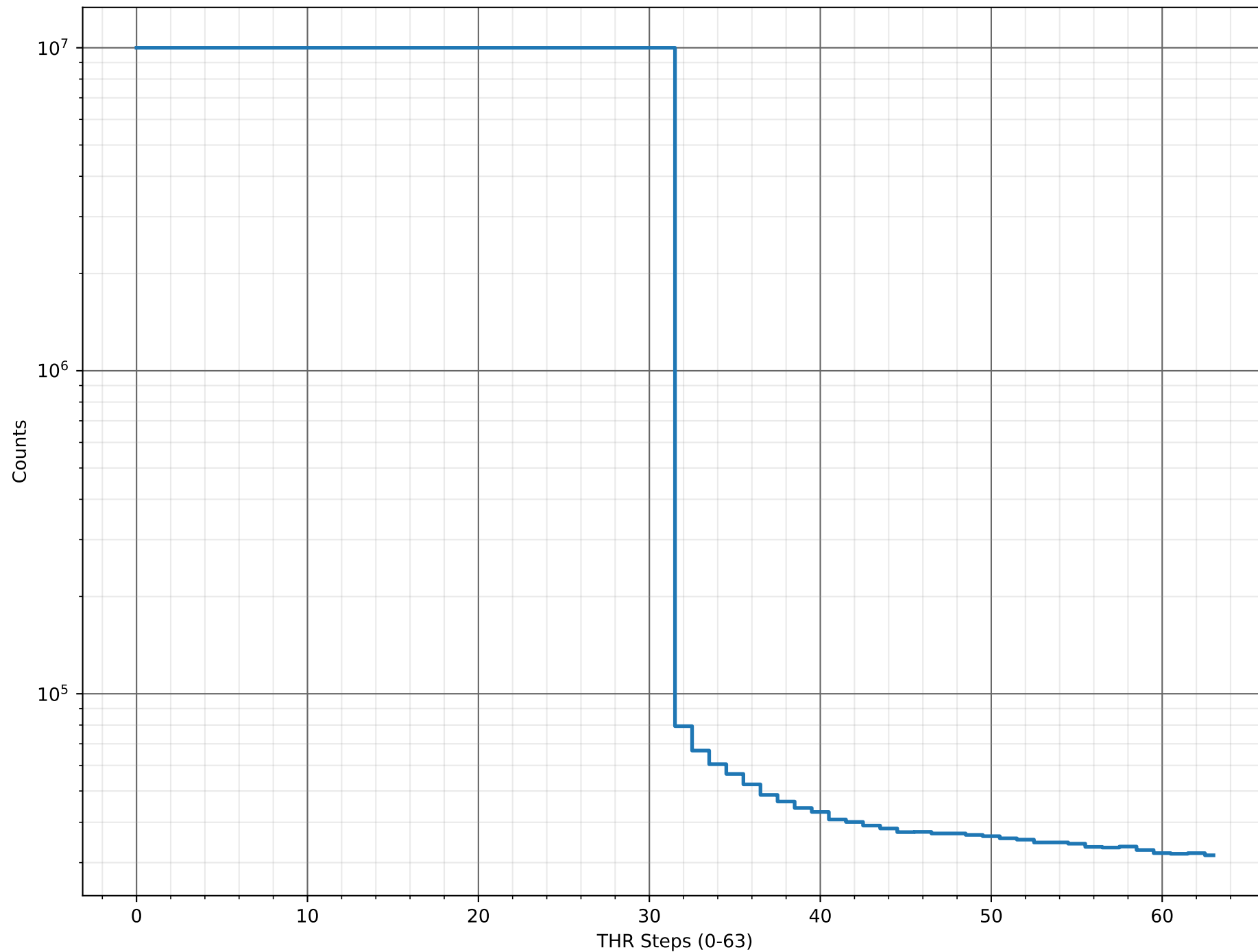
THR Distribution of PMT: B Anode: 34 (UART: 2 FEB: 0 CHIP: 5 CHANNEL: 3)



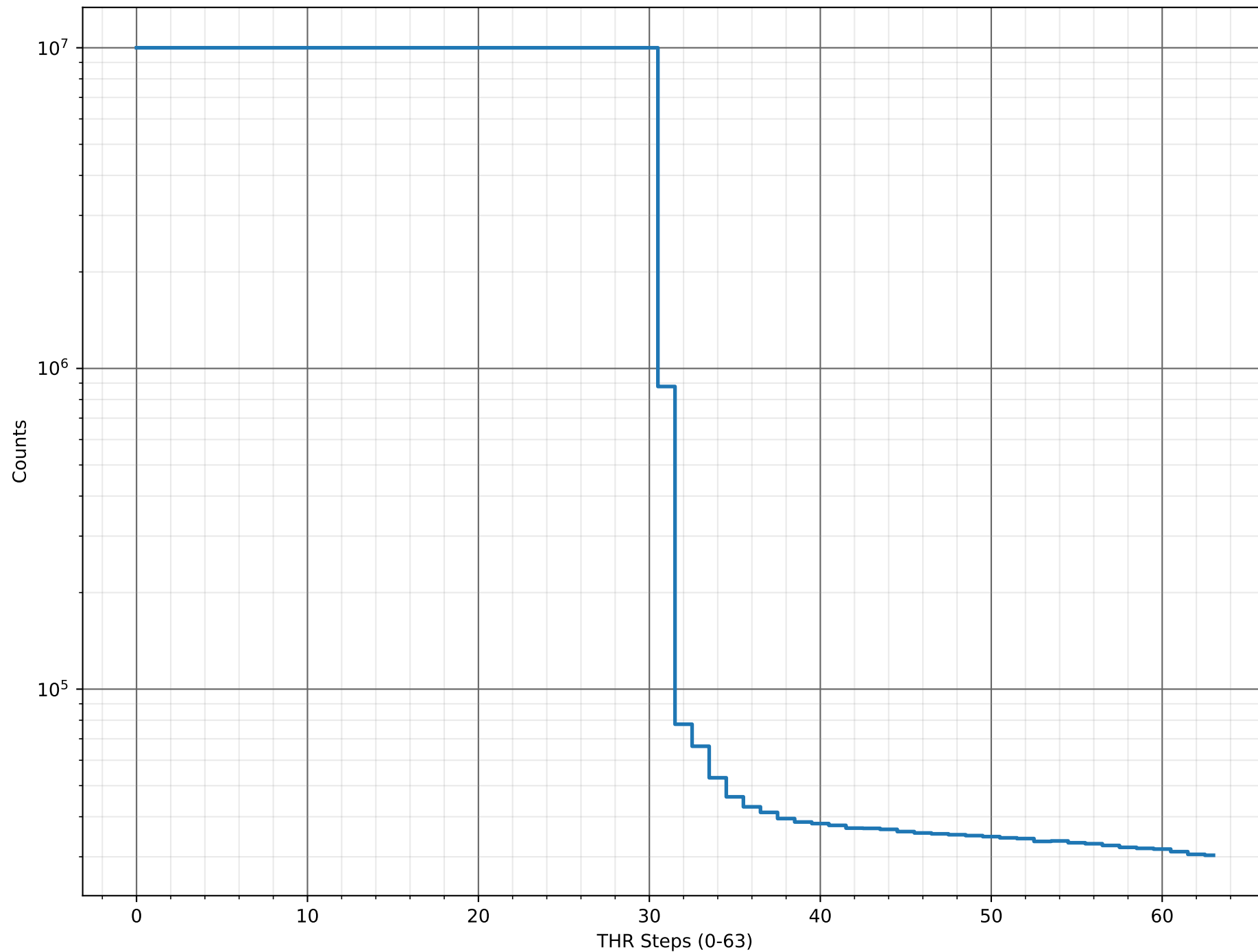
THR Distribution of PMT: B Anode: 35 (UART: 2 FEB: 0 CHIP: 5 CHANNEL: 5)



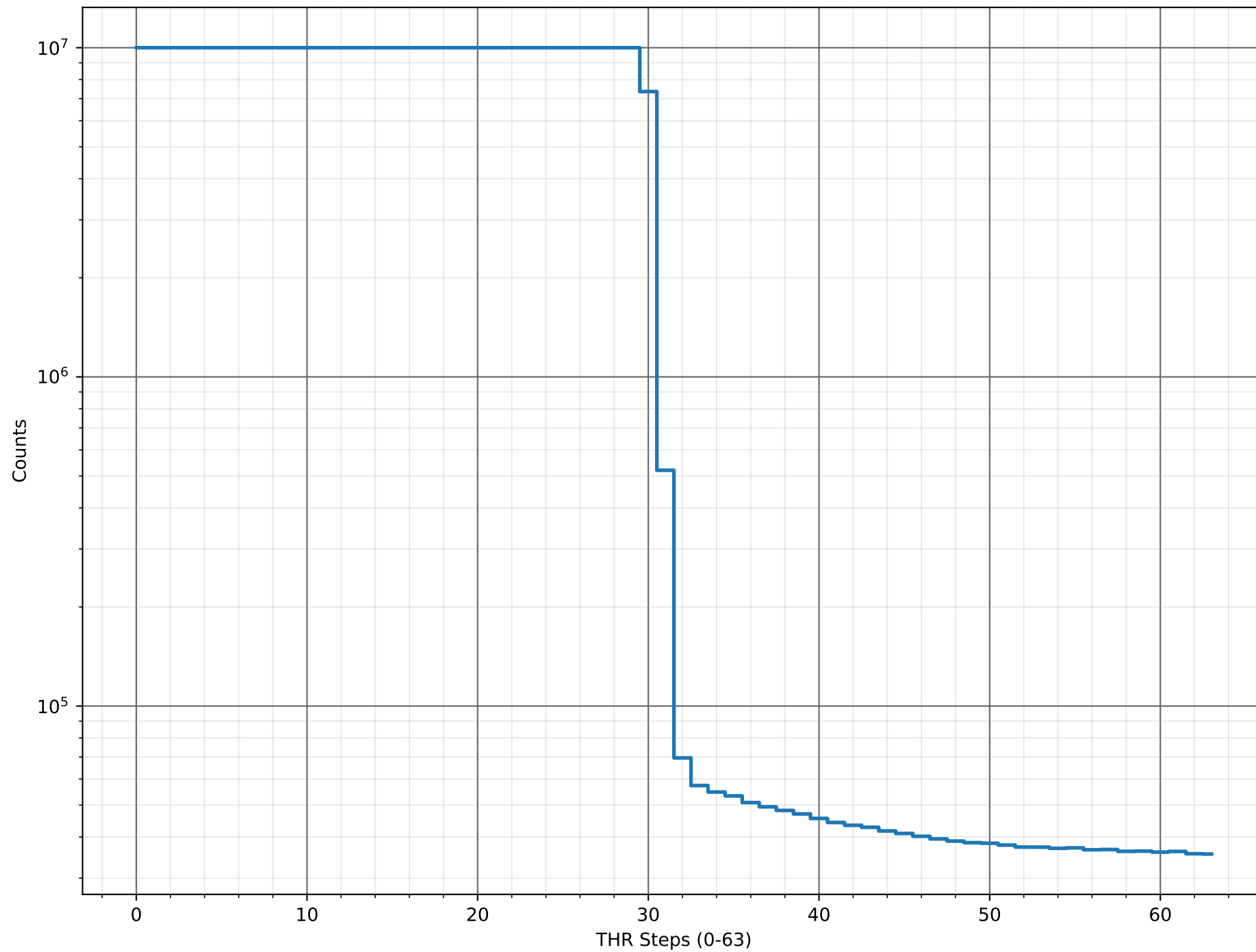
THR Distribution of PMT: B Anode: 36 (UART: 2 FEB: 0 CHIP: 5 CHANNEL: 6)



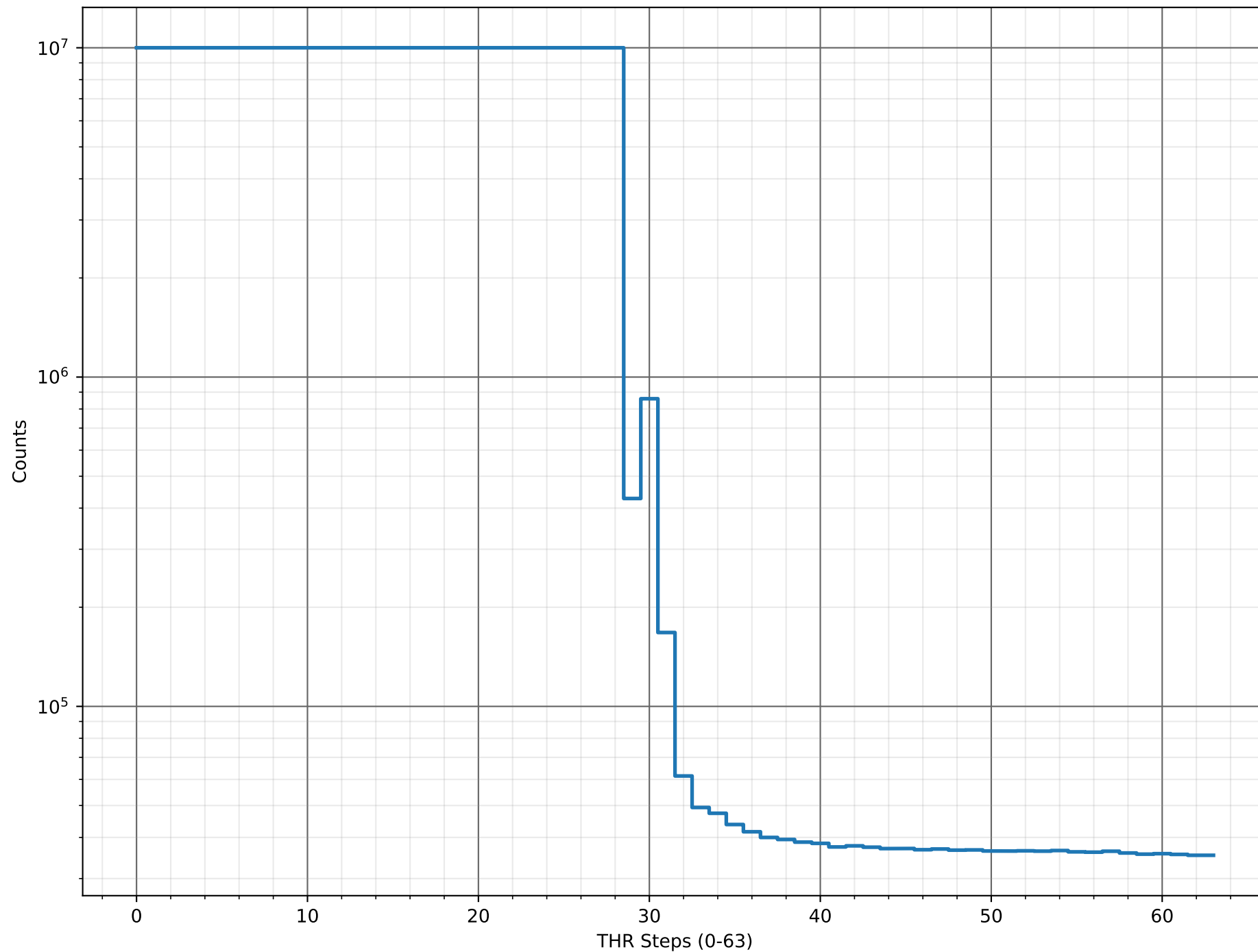
THR Distribution of PMT: B Anode: 37 (UART: 2 FEB: 0 CHIP: 7 CHANNEL: 0)



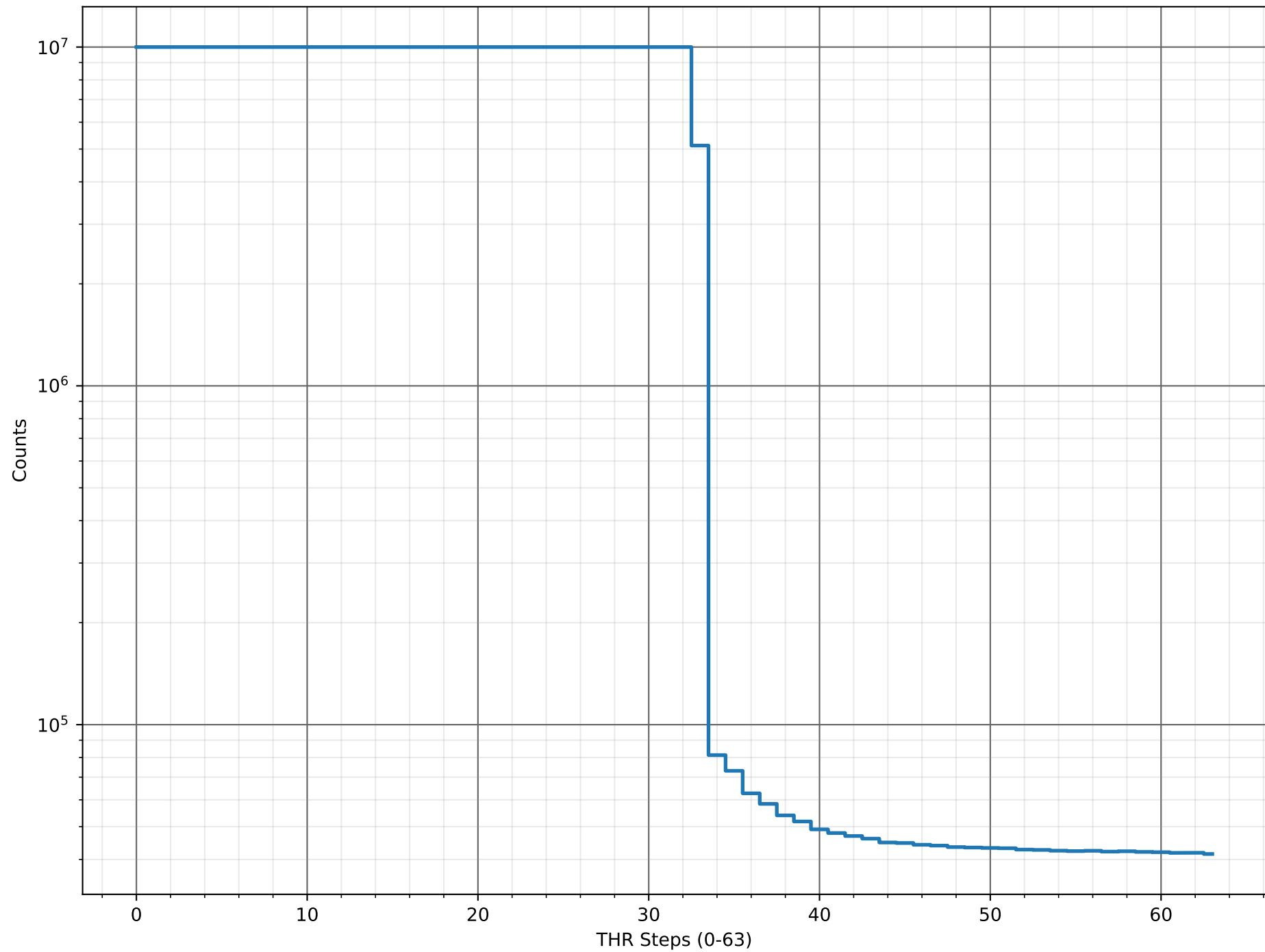
THR Distribution of PMT: B Anode: 38 (UART: 2 FEB: 0 CHIP: 7 CHANNEL: 2)



THR Distribution of PMT: B Anode: 39 (UART: 2 FEB: 0 CHIP: 7 CHANNEL: 4)

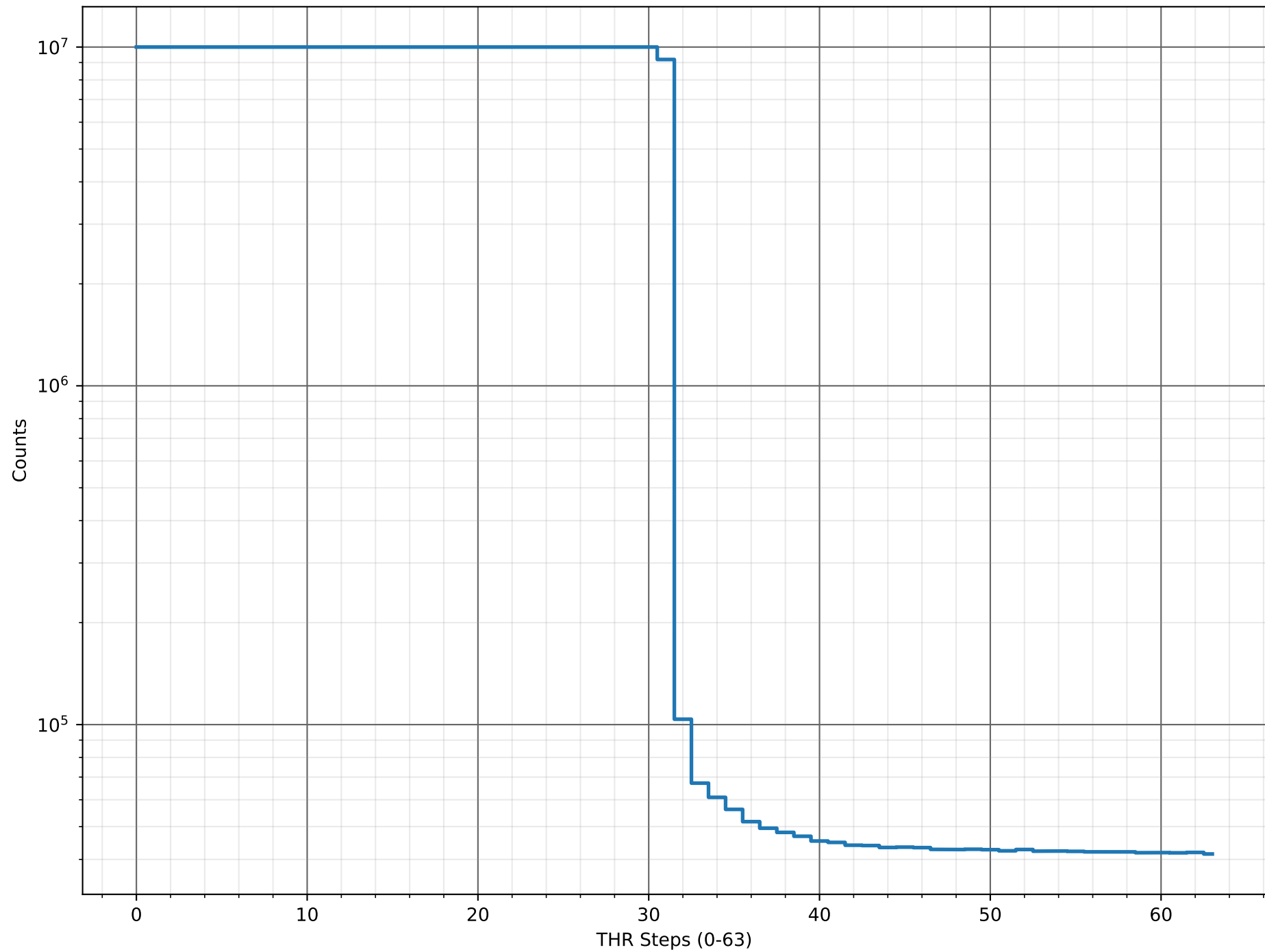


THR Distribution of PMT: B Anode: 40 (UART: 2 FEB: 0 CHIP: 7 CHANNEL: 6)

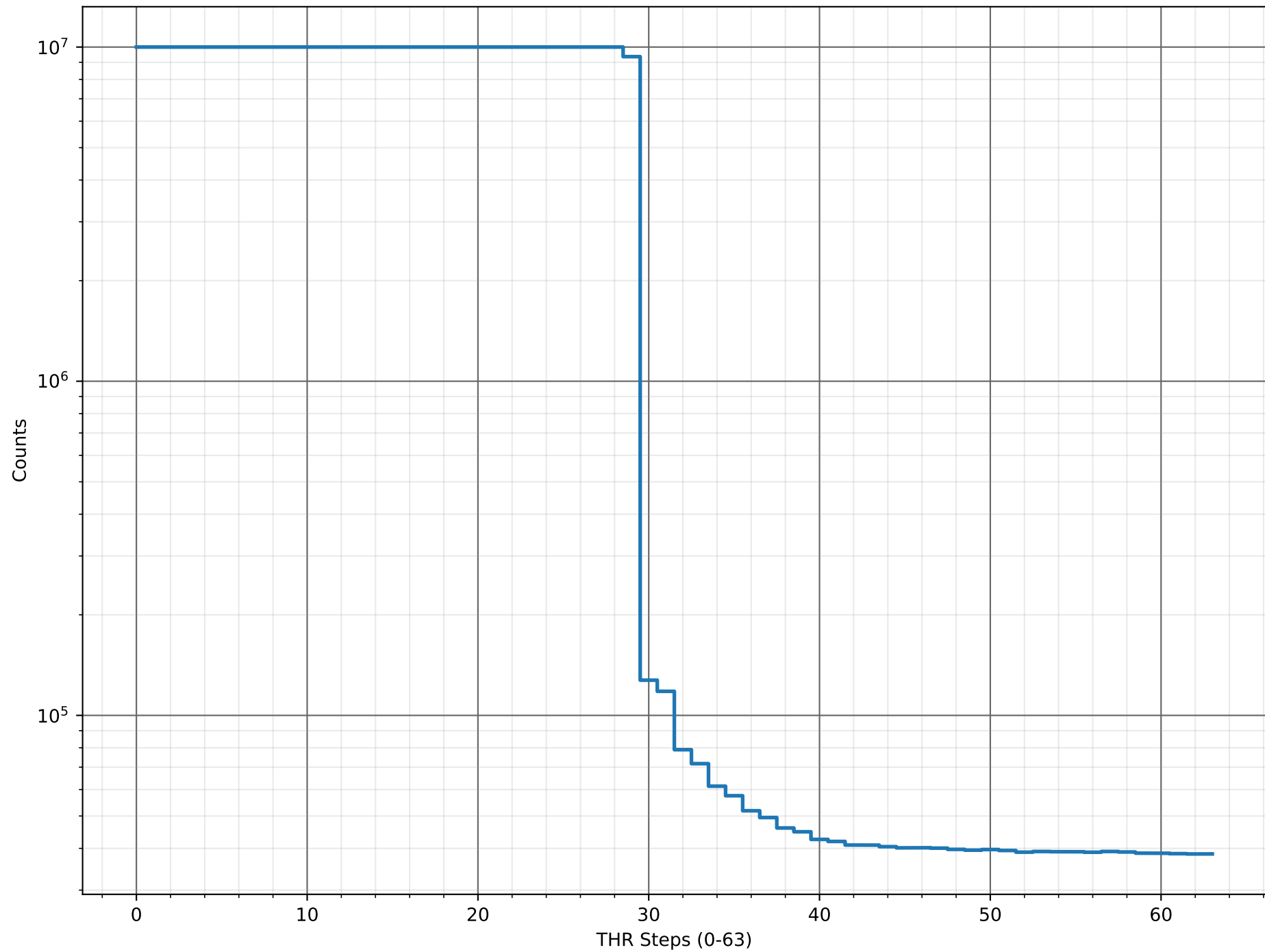




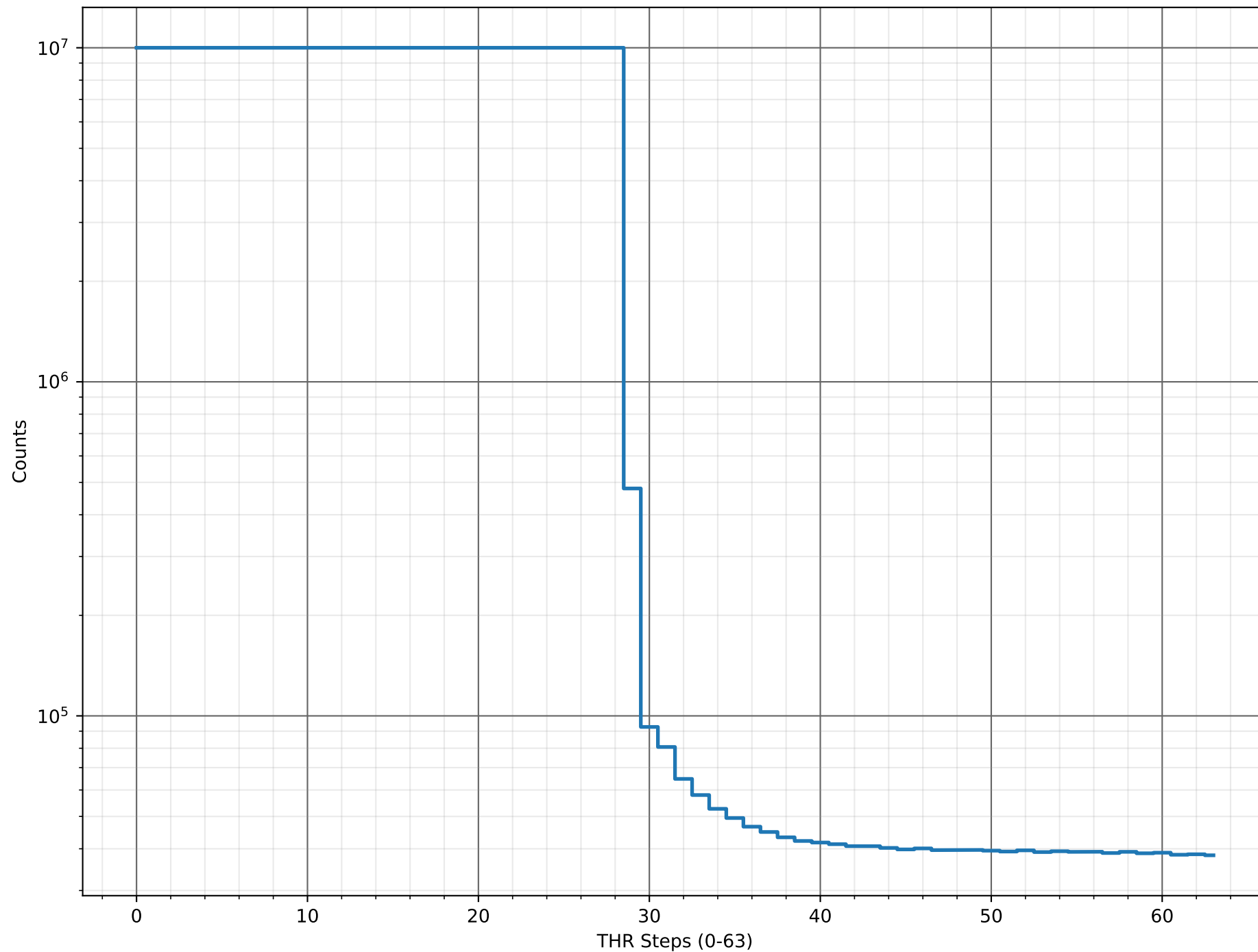
THR Distribution of PMT: B Anode: 41 (UART: 2 FEB: 0 CHIP: 5 CHANNEL: 0)



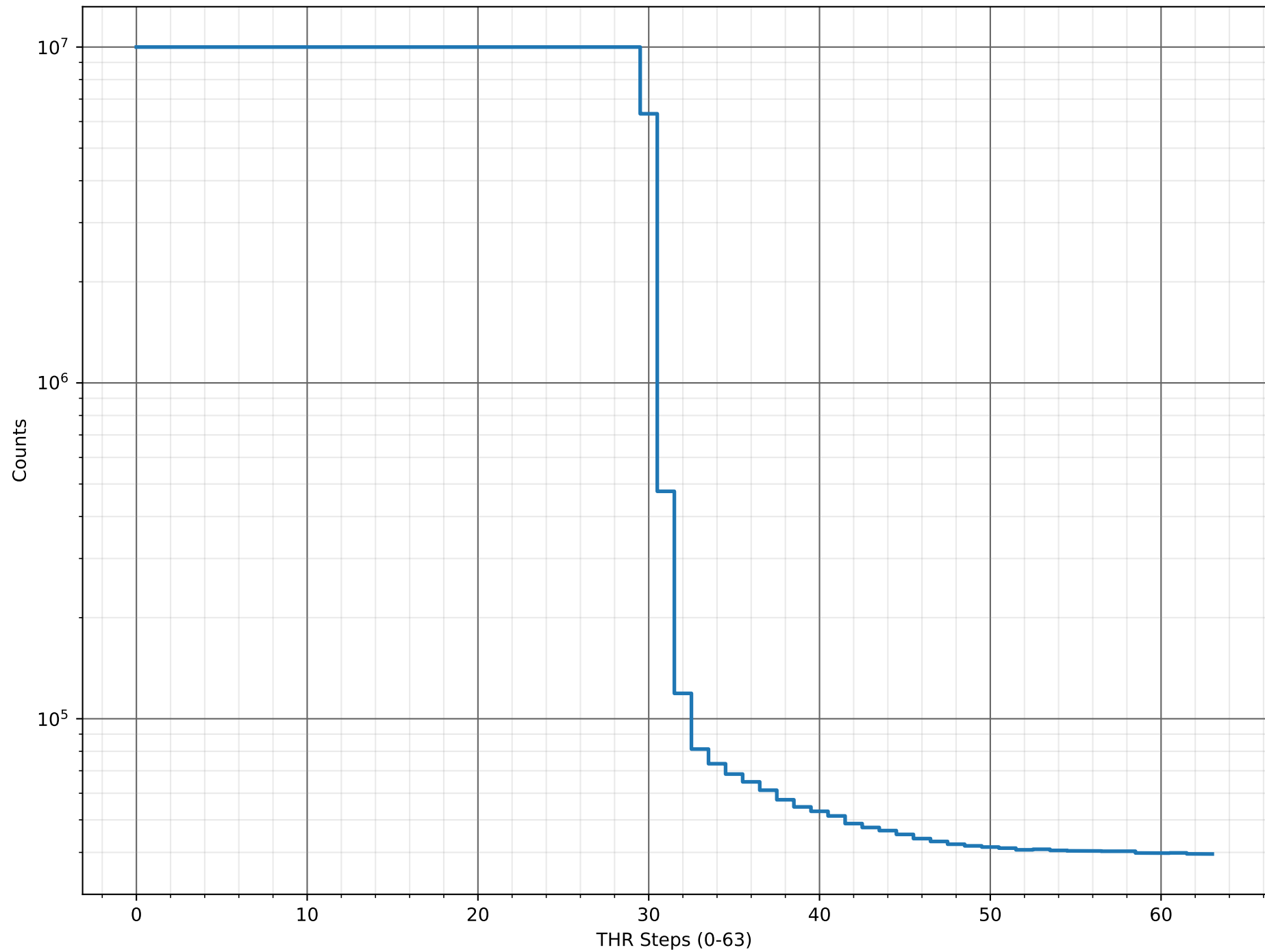
THR Distribution of PMT: B Anode: 42 (UART: 2 FEB: 0 CHIP: 5 CHANNEL: 2)



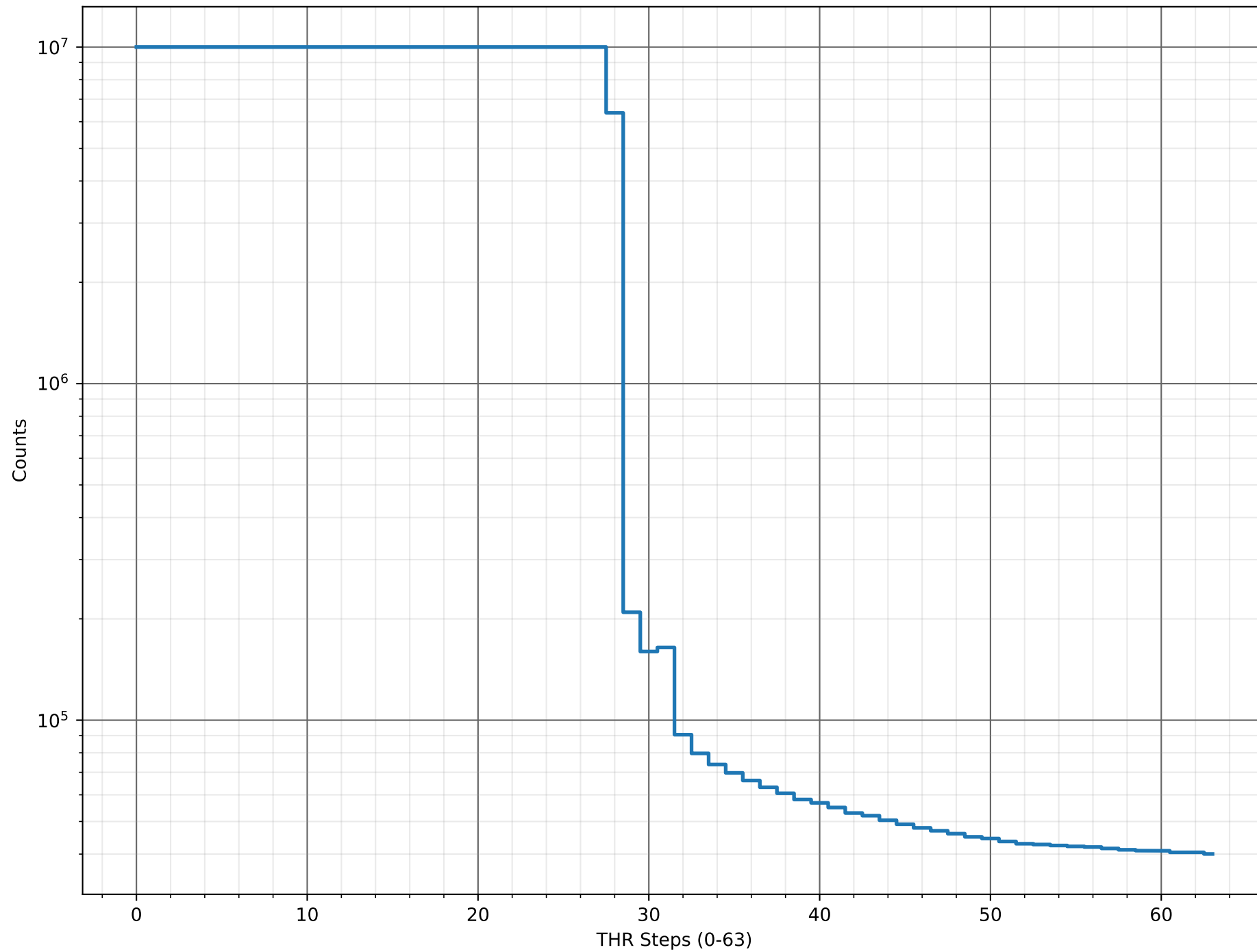
THR Distribution of PMT: B Anode: 43 (UART: 2 FEB: 0 CHIP: 5 CHANNEL: 4)



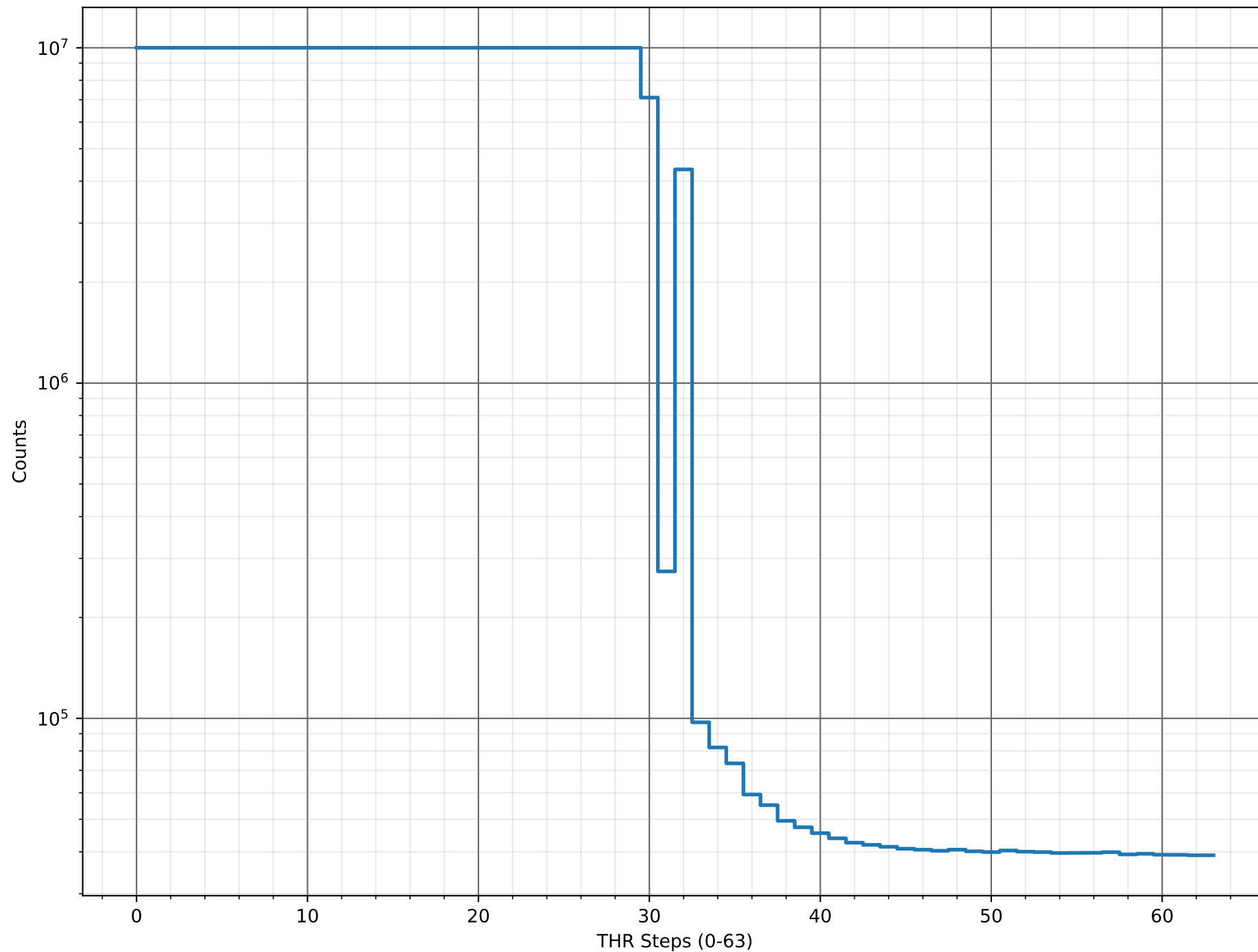
THR Distribution of PMT: B Anode: 44 (UART: 2 FEB: 0 CHIP: 5 CHANNEL: 7)



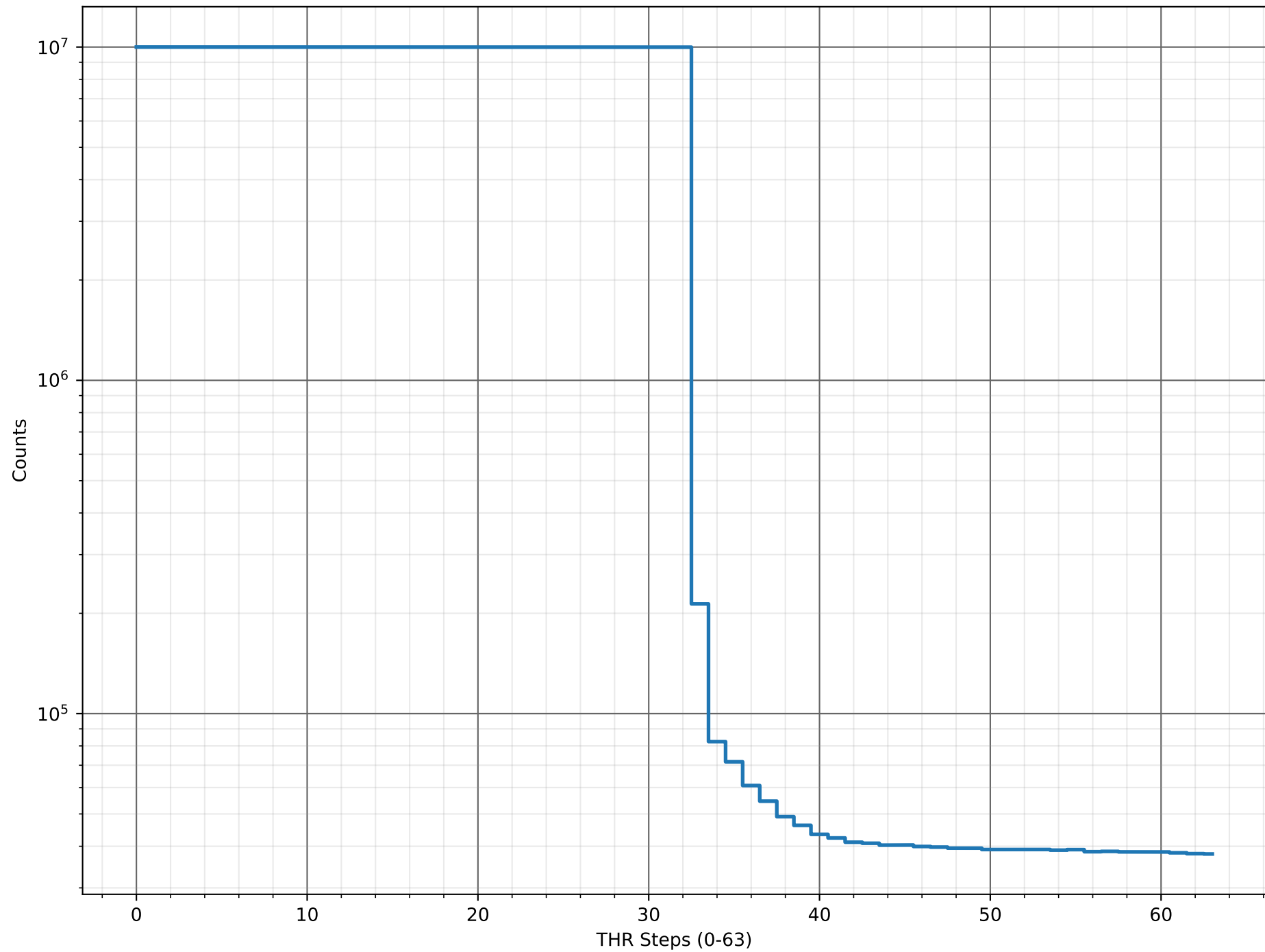
THR Distribution of PMT: B Anode: 45 (UART: 2 FEB: 0 CHIP: 7 CHANNEL: 1)



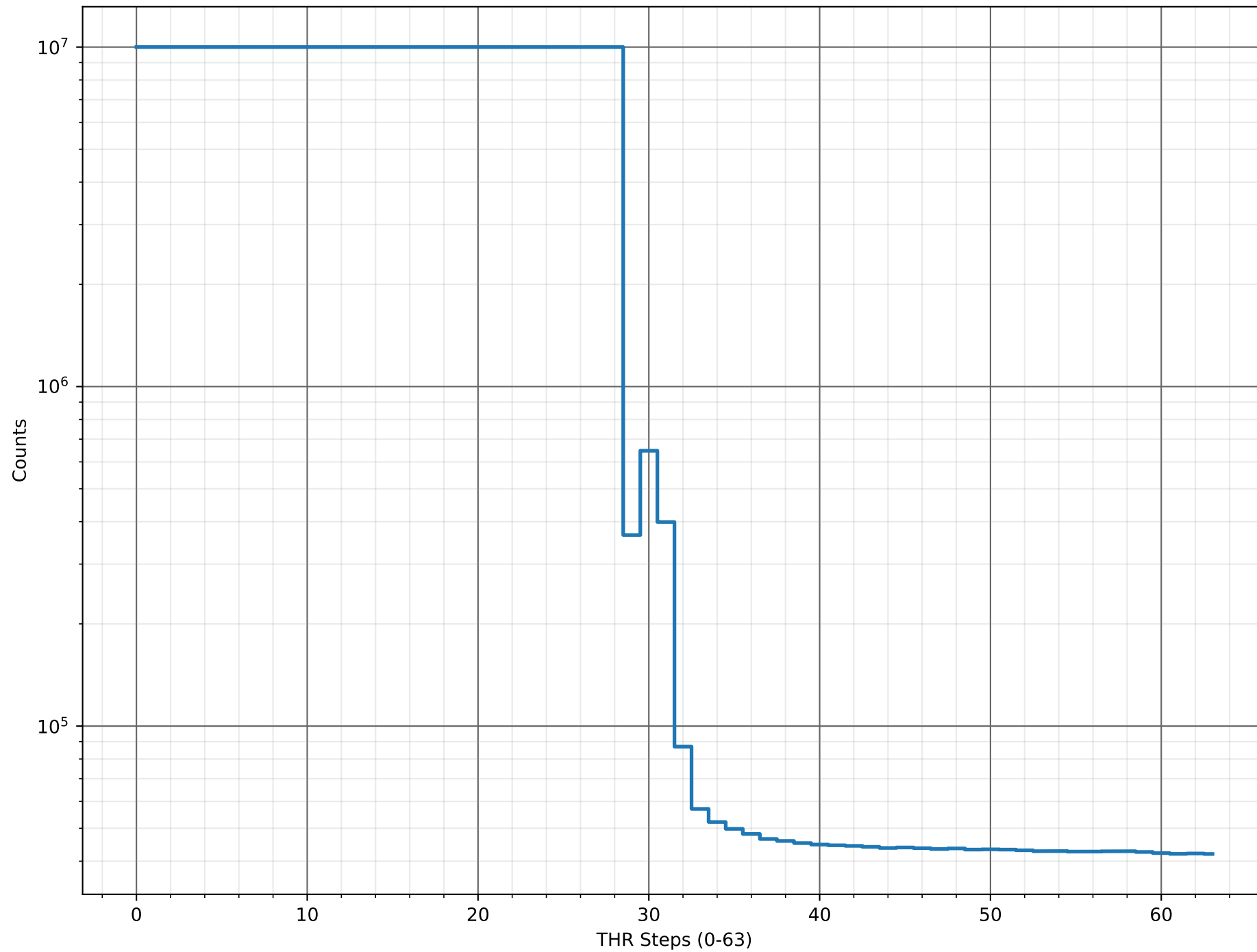
THR Distribution of PMT: B Anode: 46 (UART: 2 FEB: 0 CHIP: 7 CHANNEL: 3)



THR Distribution of PMT: B Anode: 47 (UART: 2 FEB: 0 CHIP: 7 CHANNEL: 5)

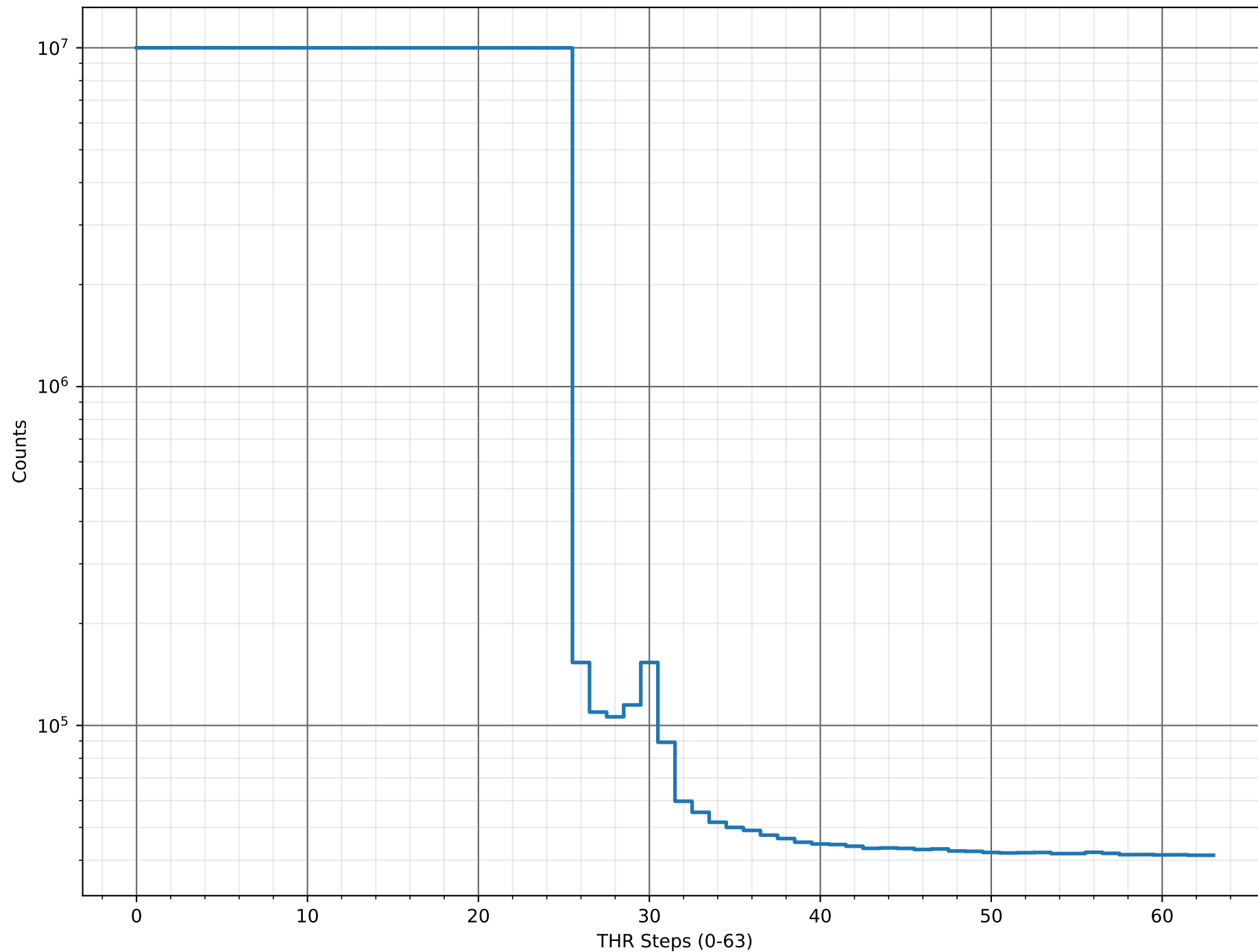


THR Distribution of PMT: B Anode: 48 (UART: 2 FEB: 0 CHIP: 7 CHANNEL: 7)

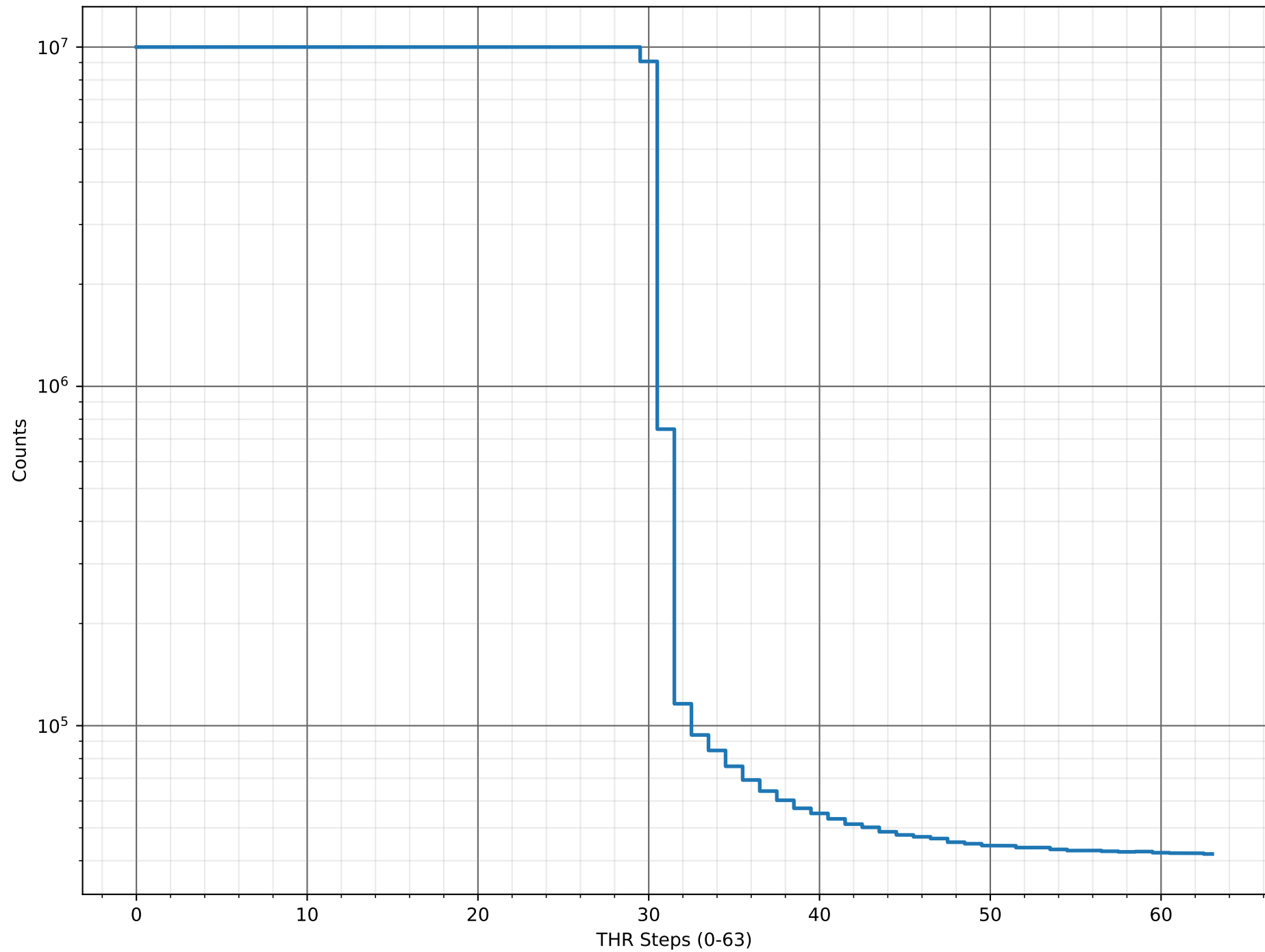




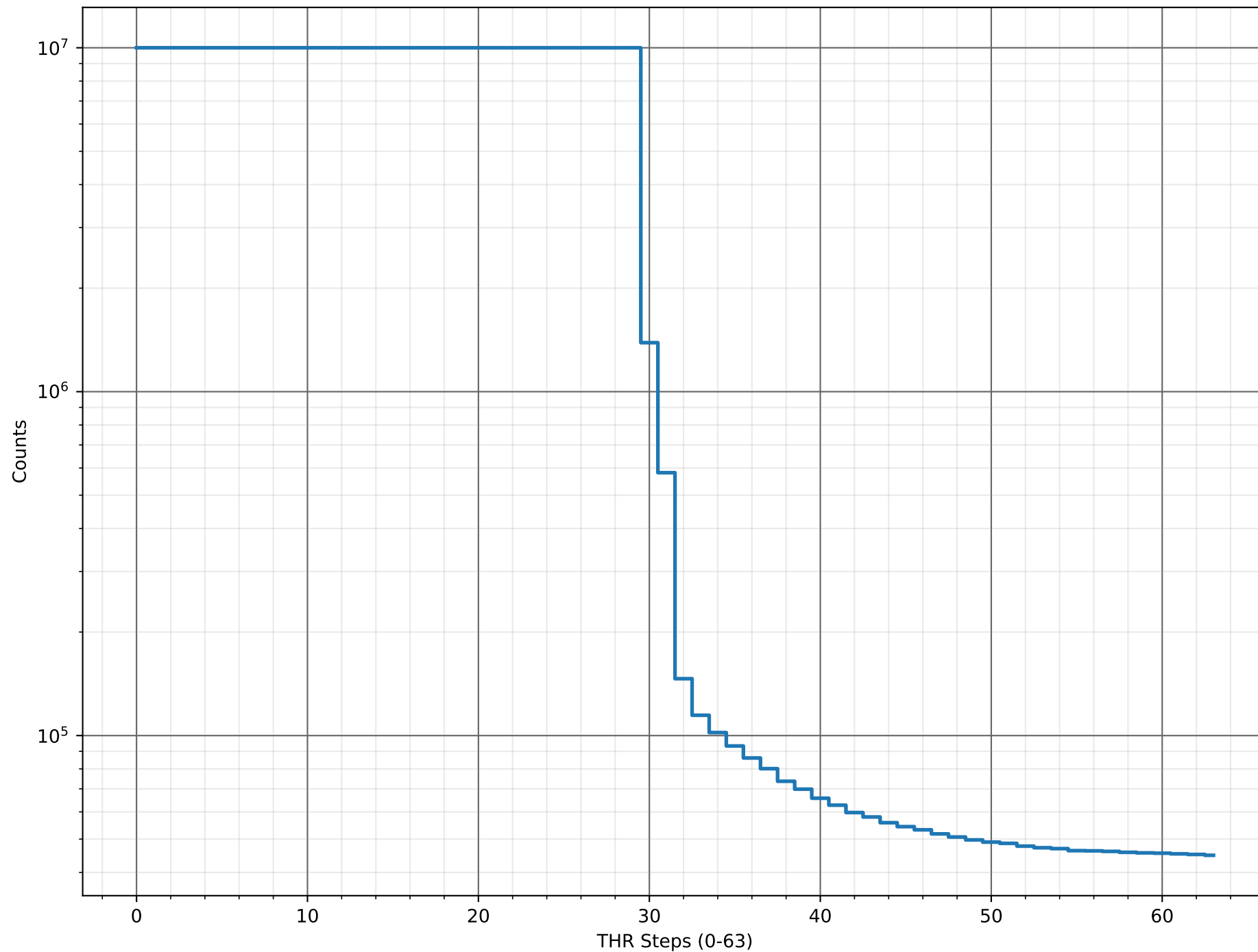
THR Distribution of PMT: B Anode: 49 (UART: 2 FEB: 0 CHIP: 4 CHANNEL: 6)



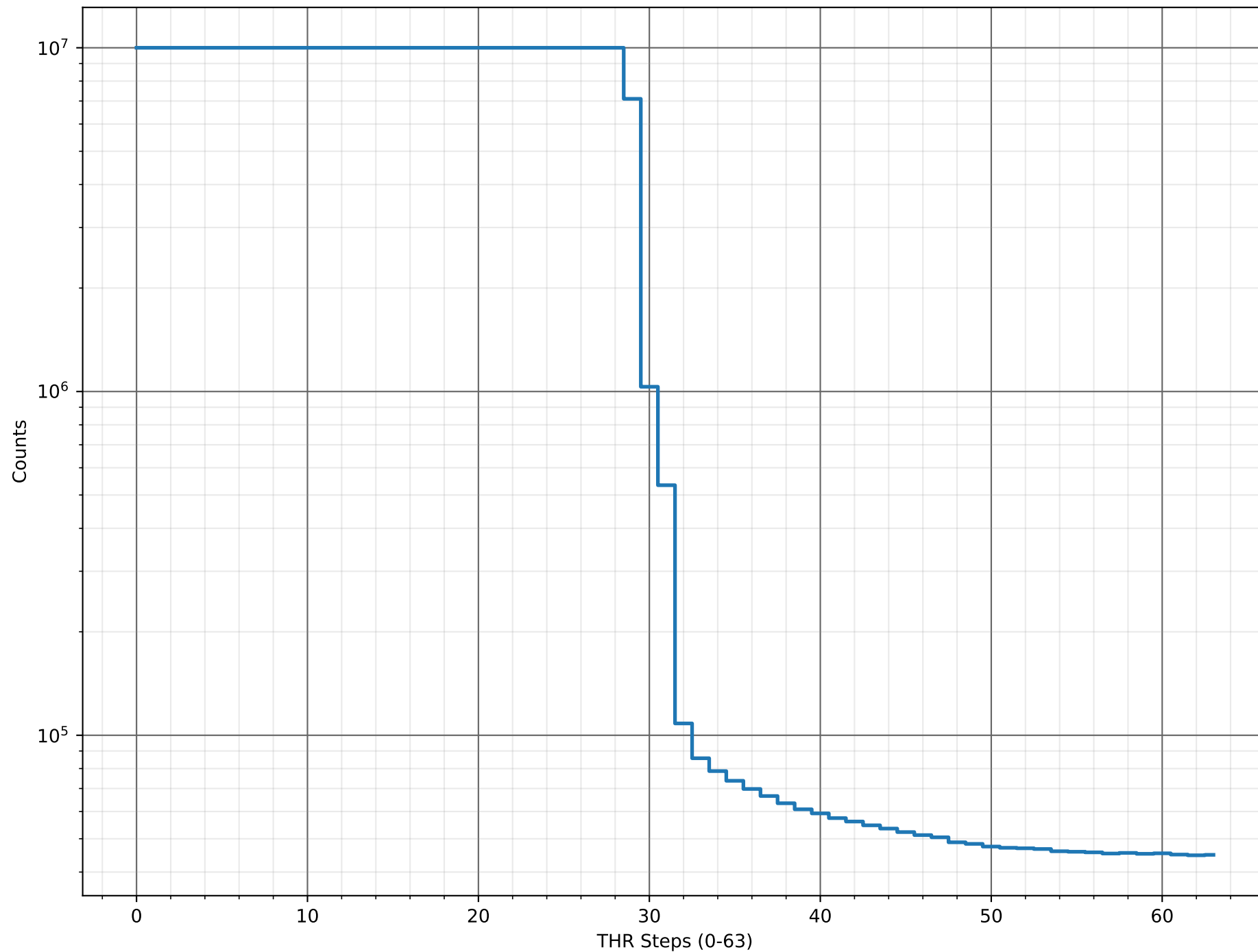
THR Distribution of PMT: B Anode: 50 (UART: 2 FEB: 0 CHIP: 4 CHANNEL: 4)



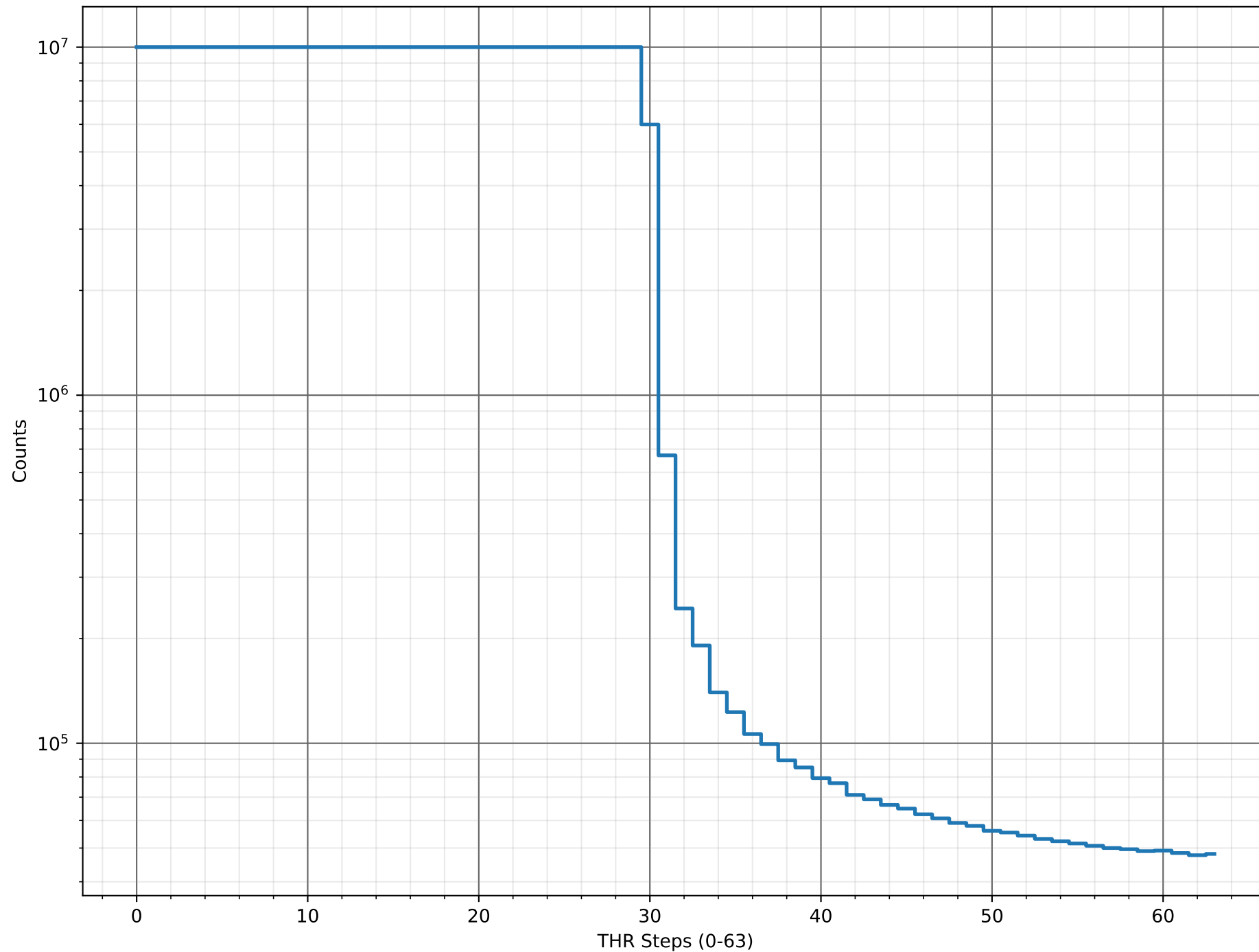
THR Distribution of PMT: B Anode: 51 (UART: 2 FEB: 0 CHIP: 4 CHANNEL: 2)



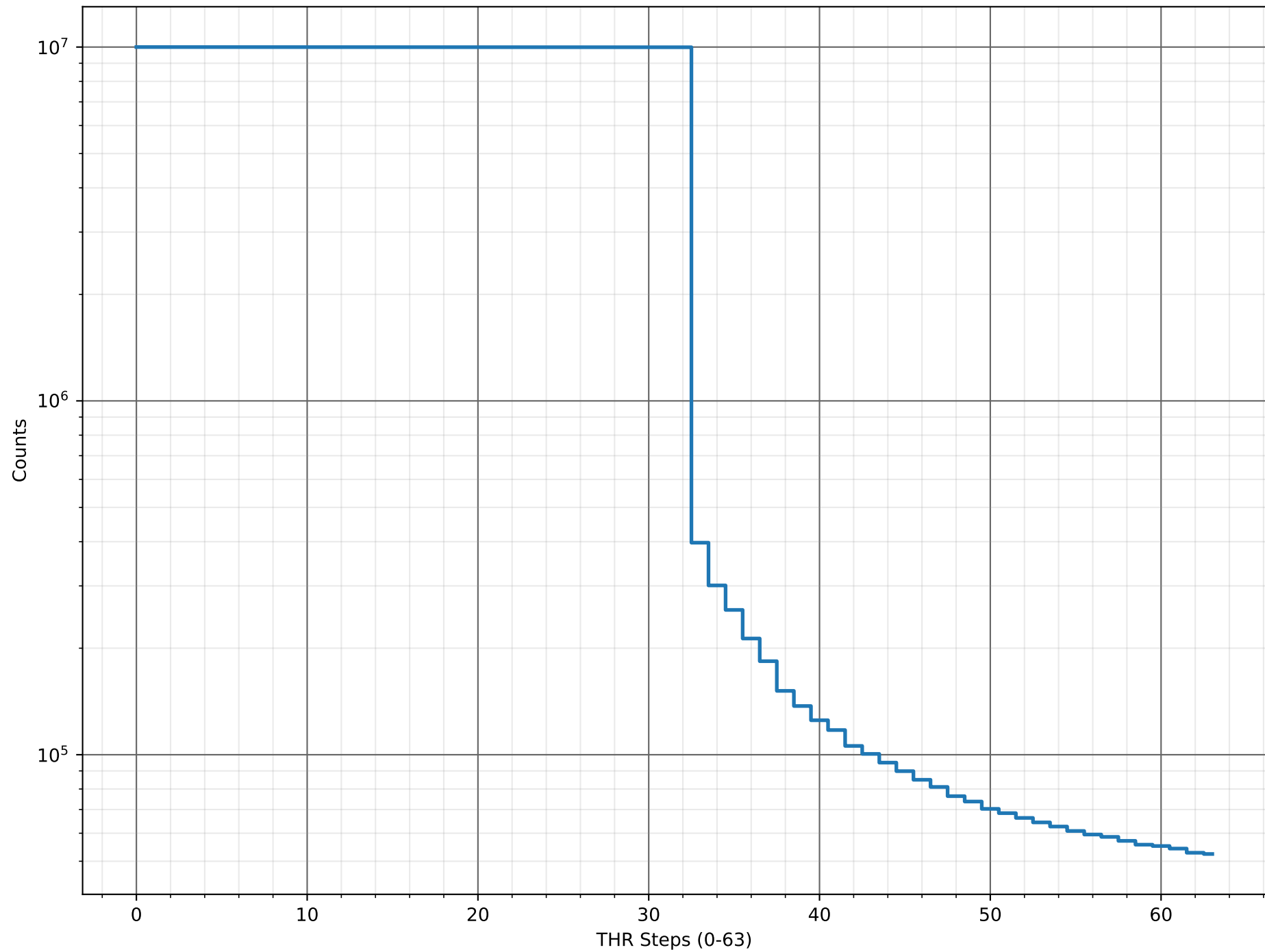
THR Distribution of PMT: B Anode: 52 (UART: 2 FEB: 0 CHIP: 4 CHANNEL: 0)



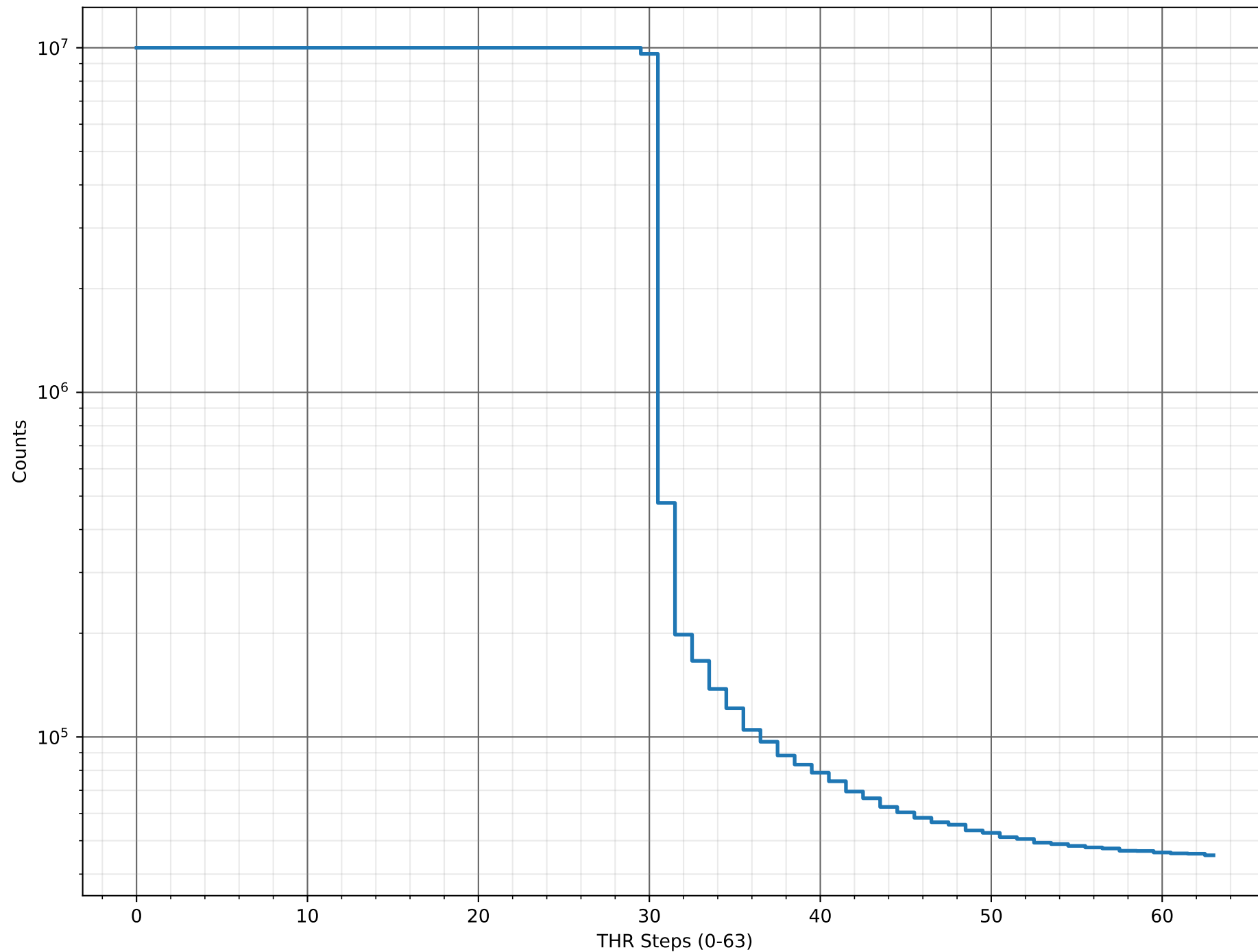
THR Distribution of PMT: B Anode: 53 (UART: 2 FEB: 0 CHIP: 6 CHANNEL: 7)



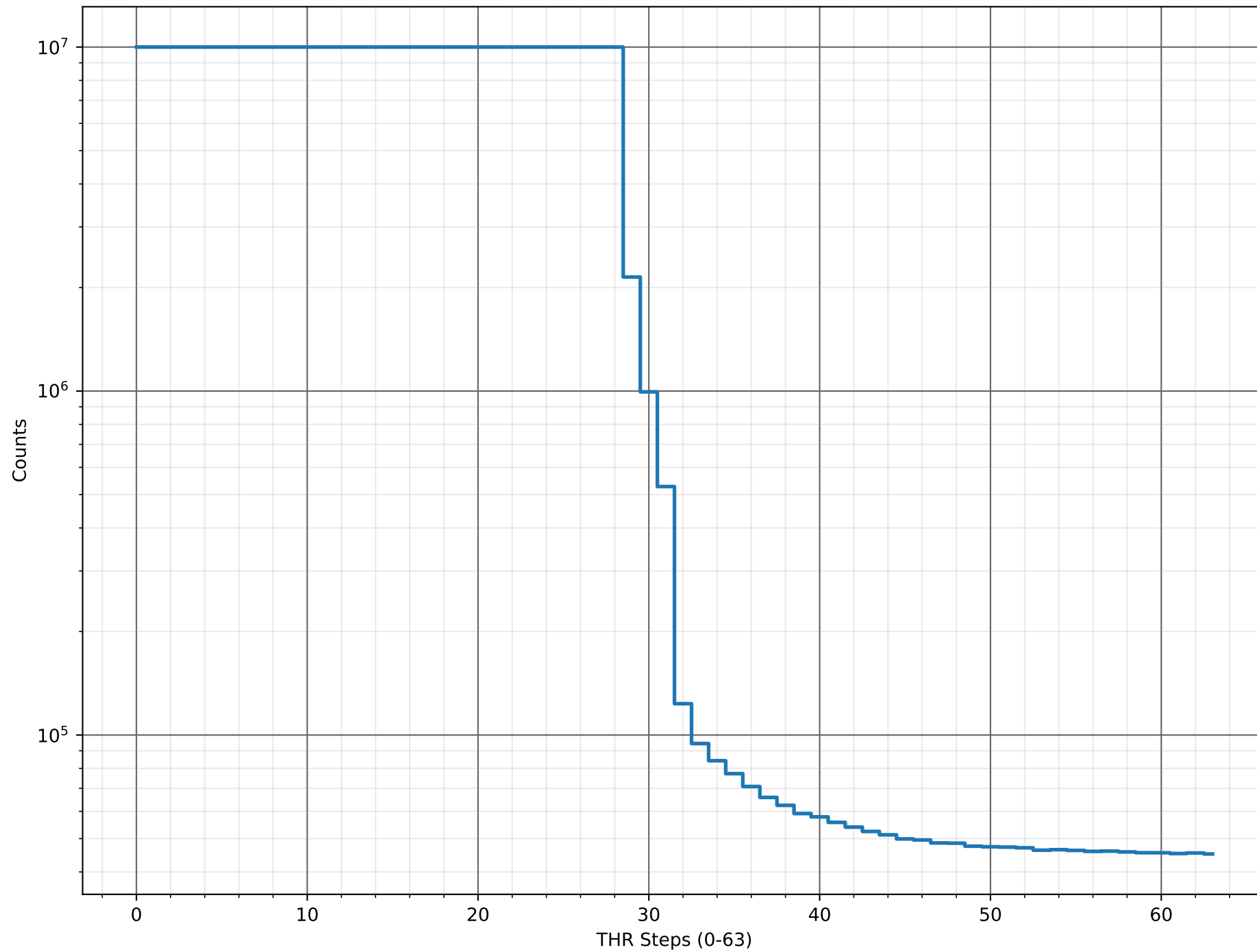
THR Distribution of PMT: B Anode: 54 (UART: 2 FEB: 0 CHIP: 6 CHANNEL: 5)



THR Distribution of PMT: B Anode: 55 (UART: 2 FEB: 0 CHIP: 6 CHANNEL: 3)

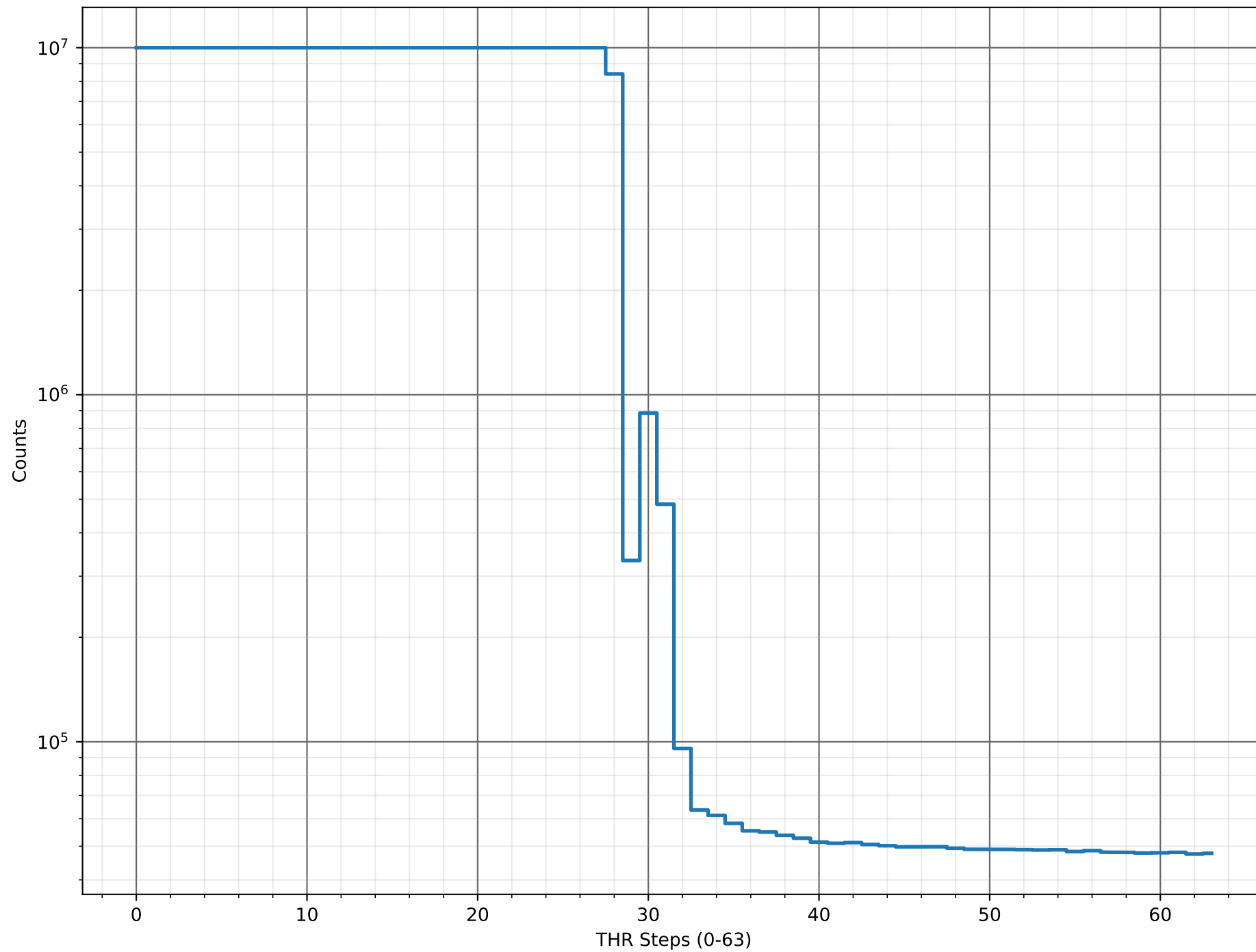


THR Distribution of PMT: B Anode: 56 (UART: 2 FEB: 0 CHIP: 6 CHANNEL: 1)

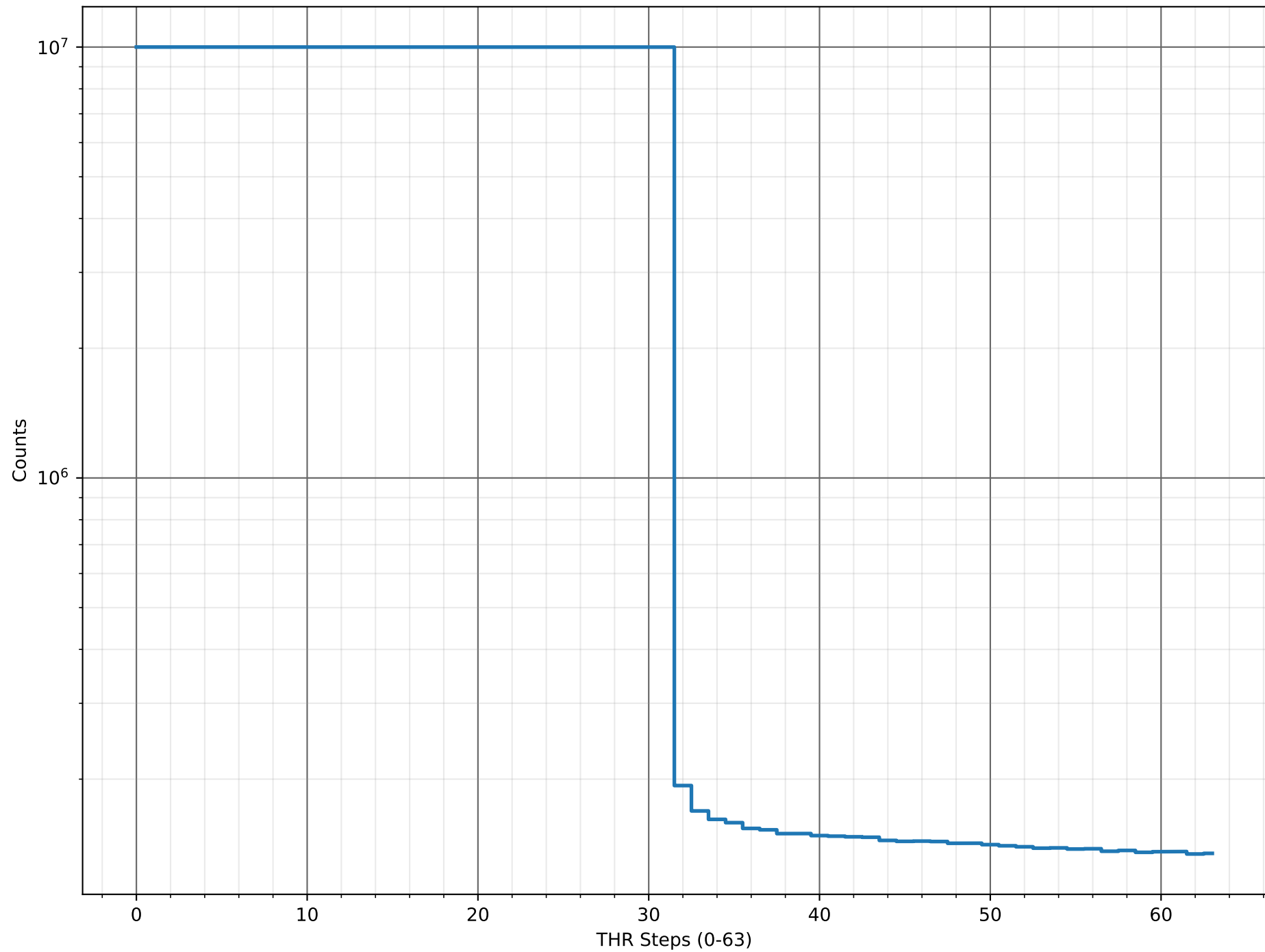




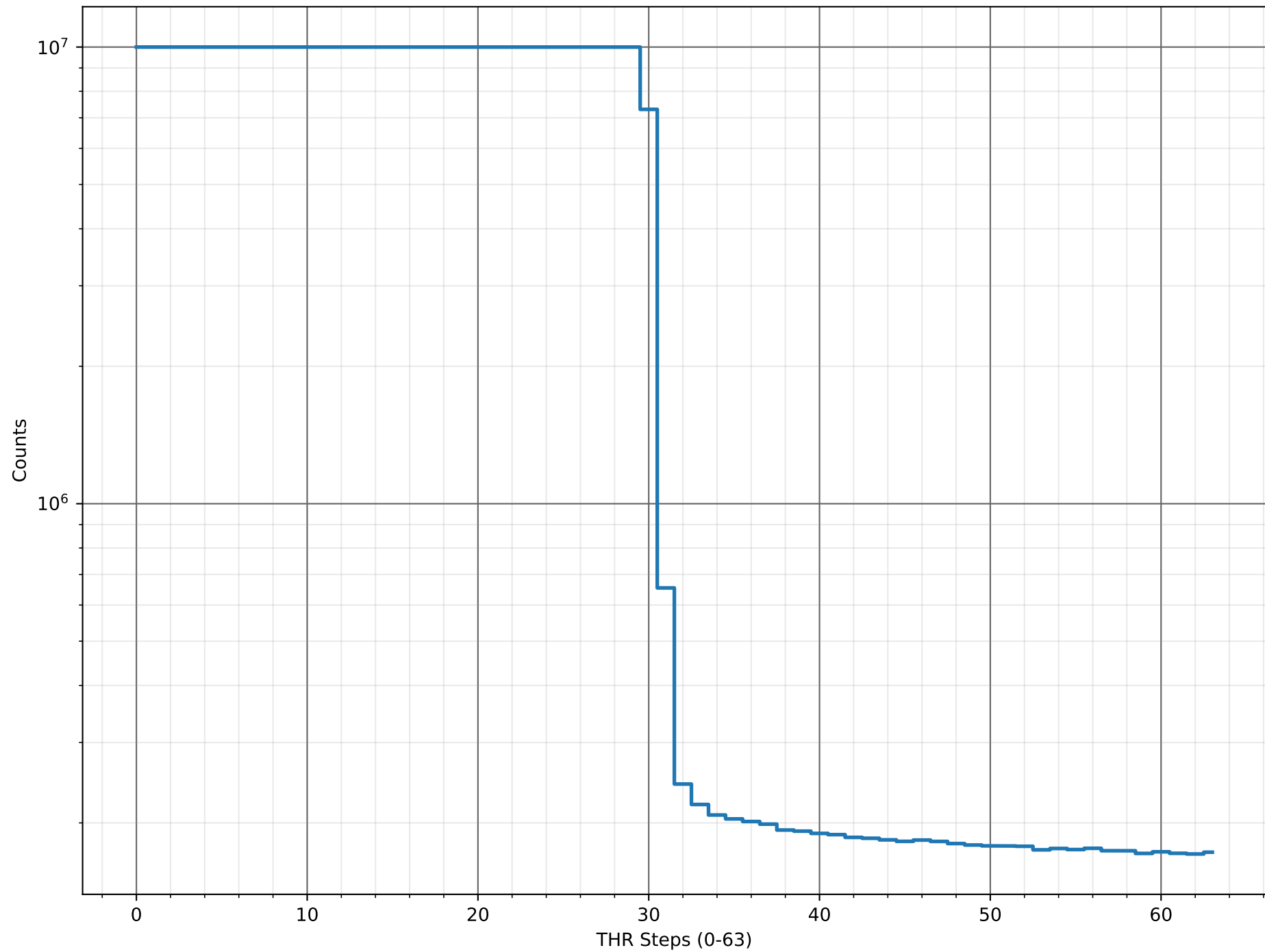
THR Distribution of PMT: B Anode: 57 (UART: 2 FEB: 0 CHIP: 4 CHANNEL: 7)



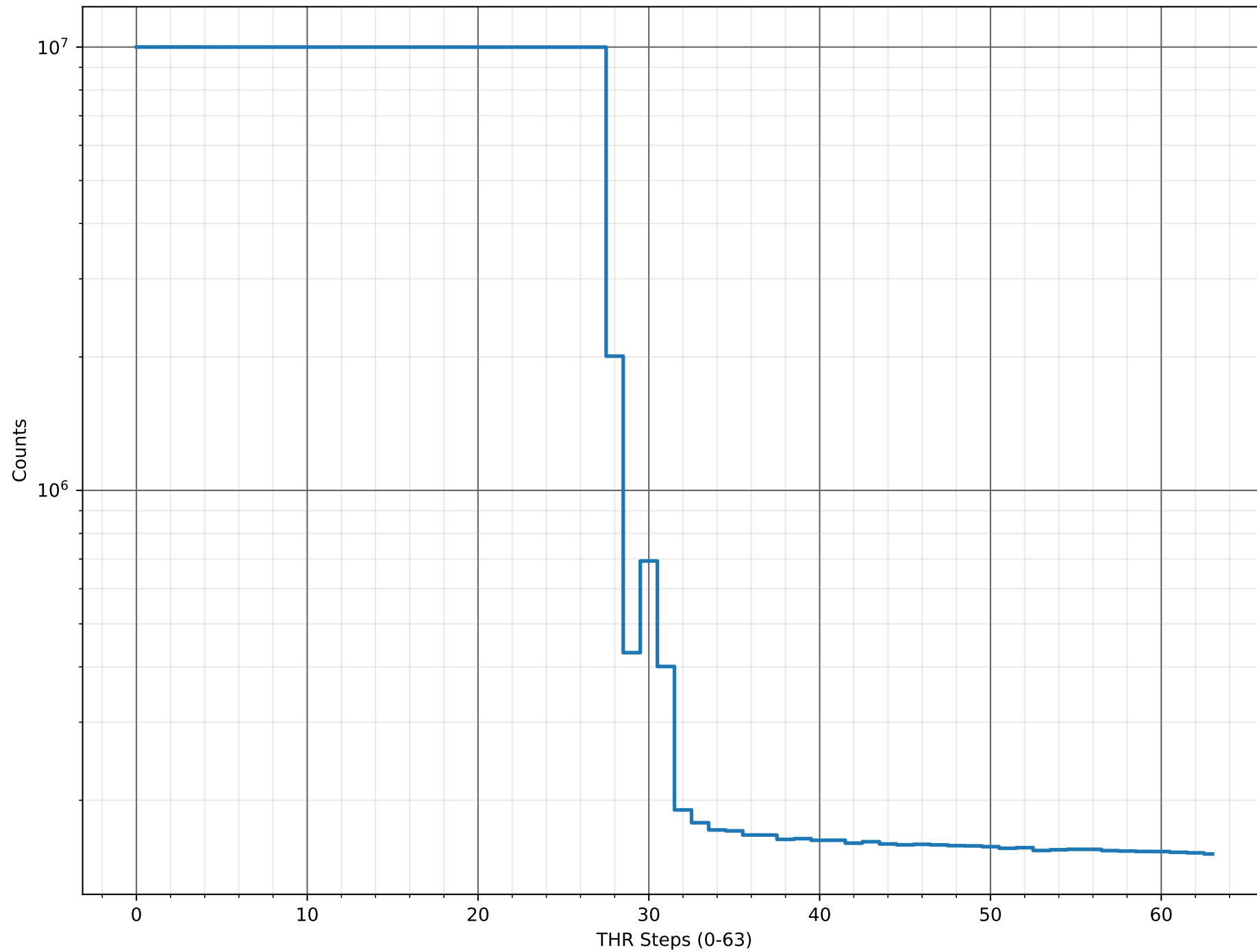
THR Distribution of PMT: B Anode: 58 (UART: 2 FEB: 0 CHIP: 4 CHANNEL: 5)



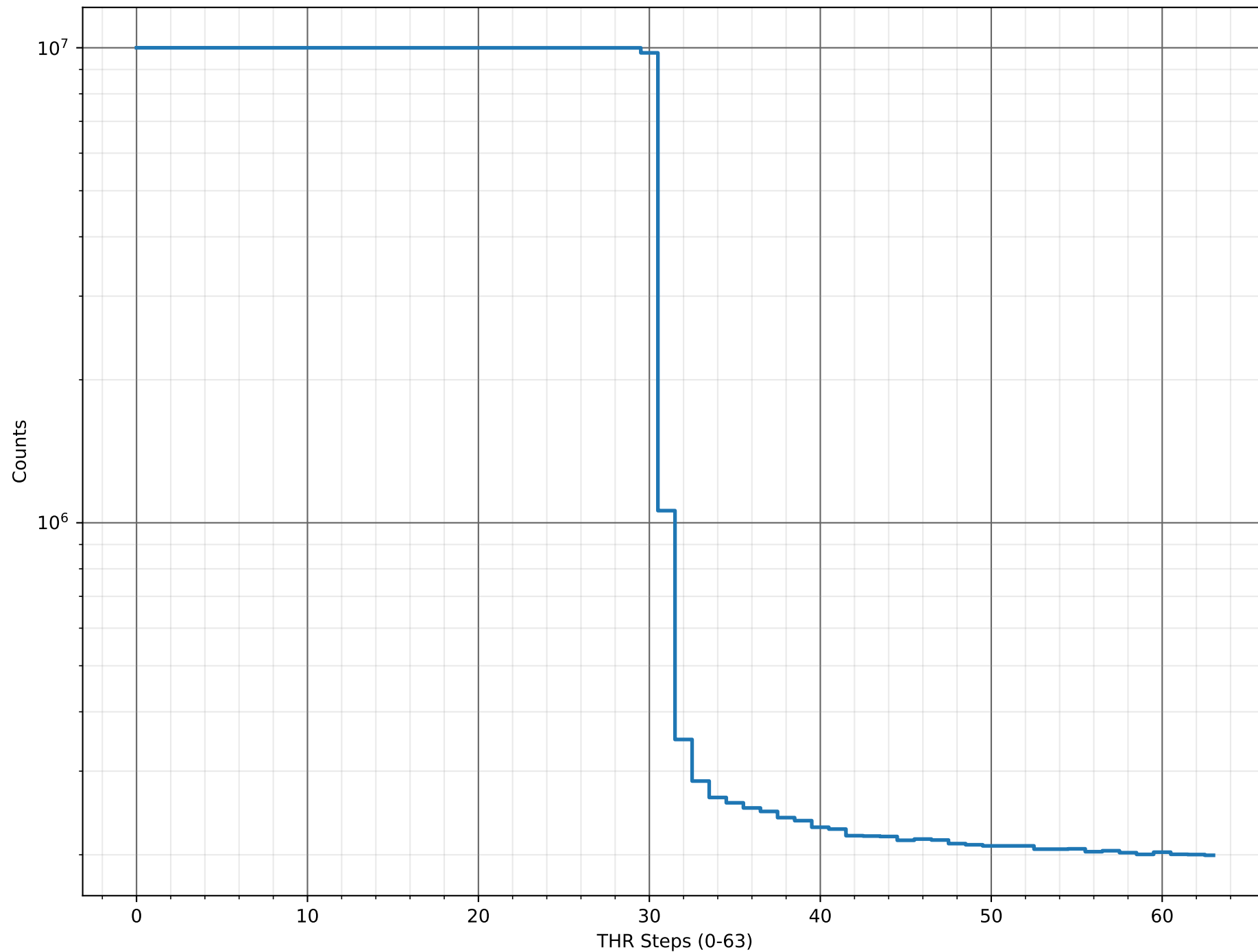
THR Distribution of PMT: B Anode: 59 (UART: 2 FEB: 0 CHIP: 4 CHANNEL: 3)



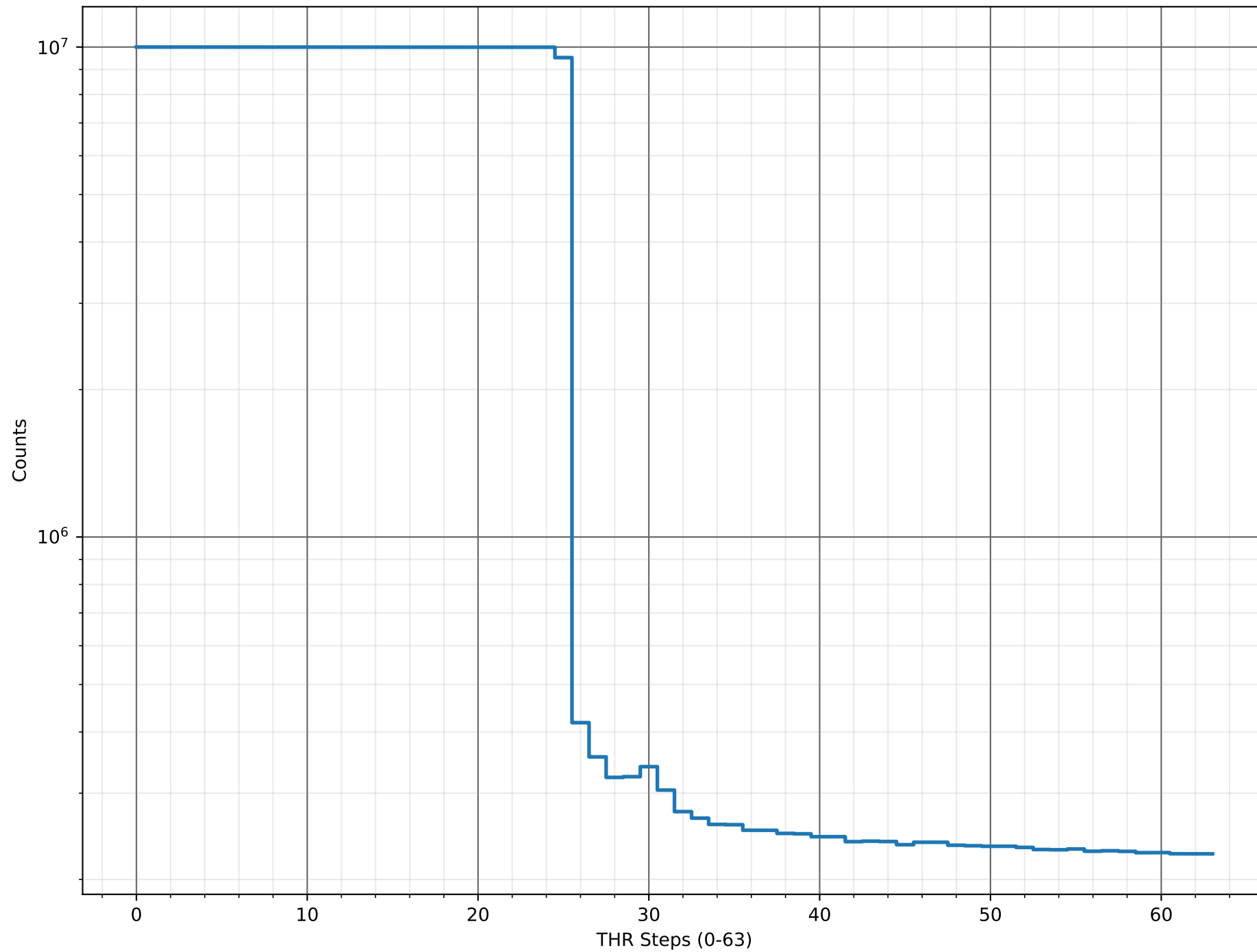
THR Distribution of PMT: B Anode: 60 (UART: 2 FEB: 0 CHIP: 4 CHANNEL: 1)



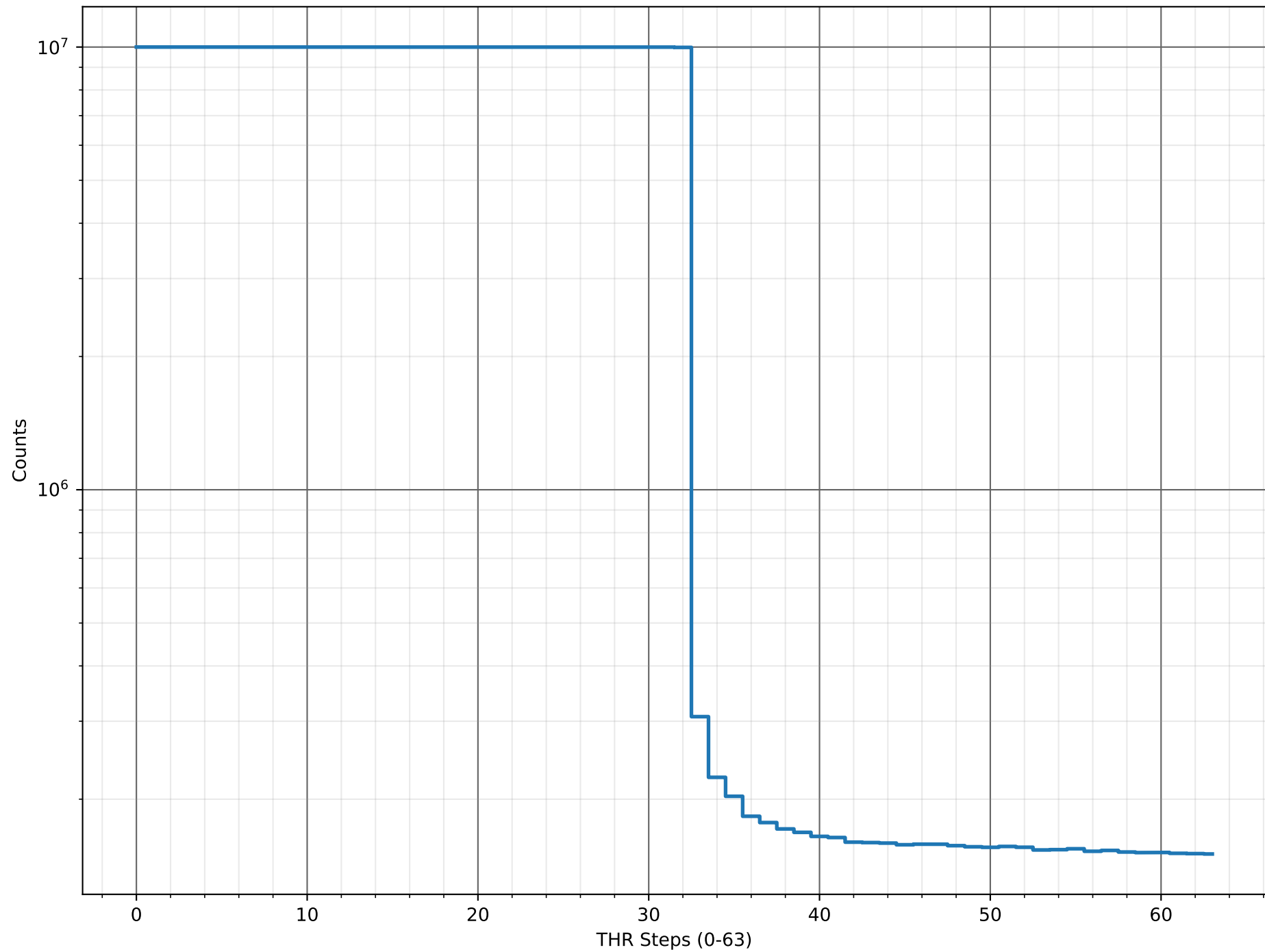
THR Distribution of PMT: B Anode: 61 (UART: 2 FEB: 0 CHIP: 6 CHANNEL: 6)



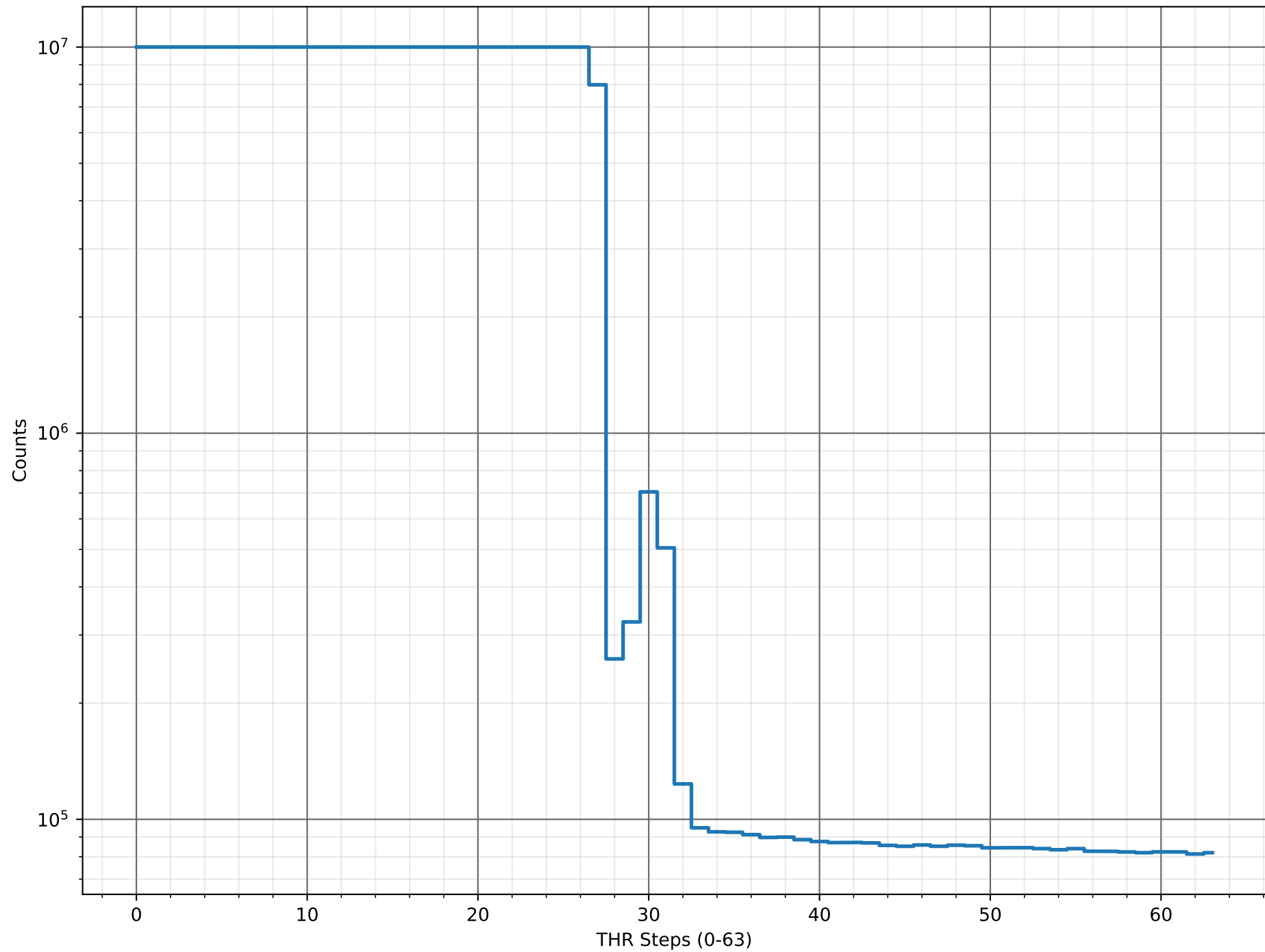
THR Distribution of PMT: B Anode: 62 (UART: 2 FEB: 0 CHIP: 6 CHANNEL: 4)



THR Distribution of PMT: B Anode: 63 (UART: 2 FEB: 0 CHIP: 6 CHANNEL: 2)

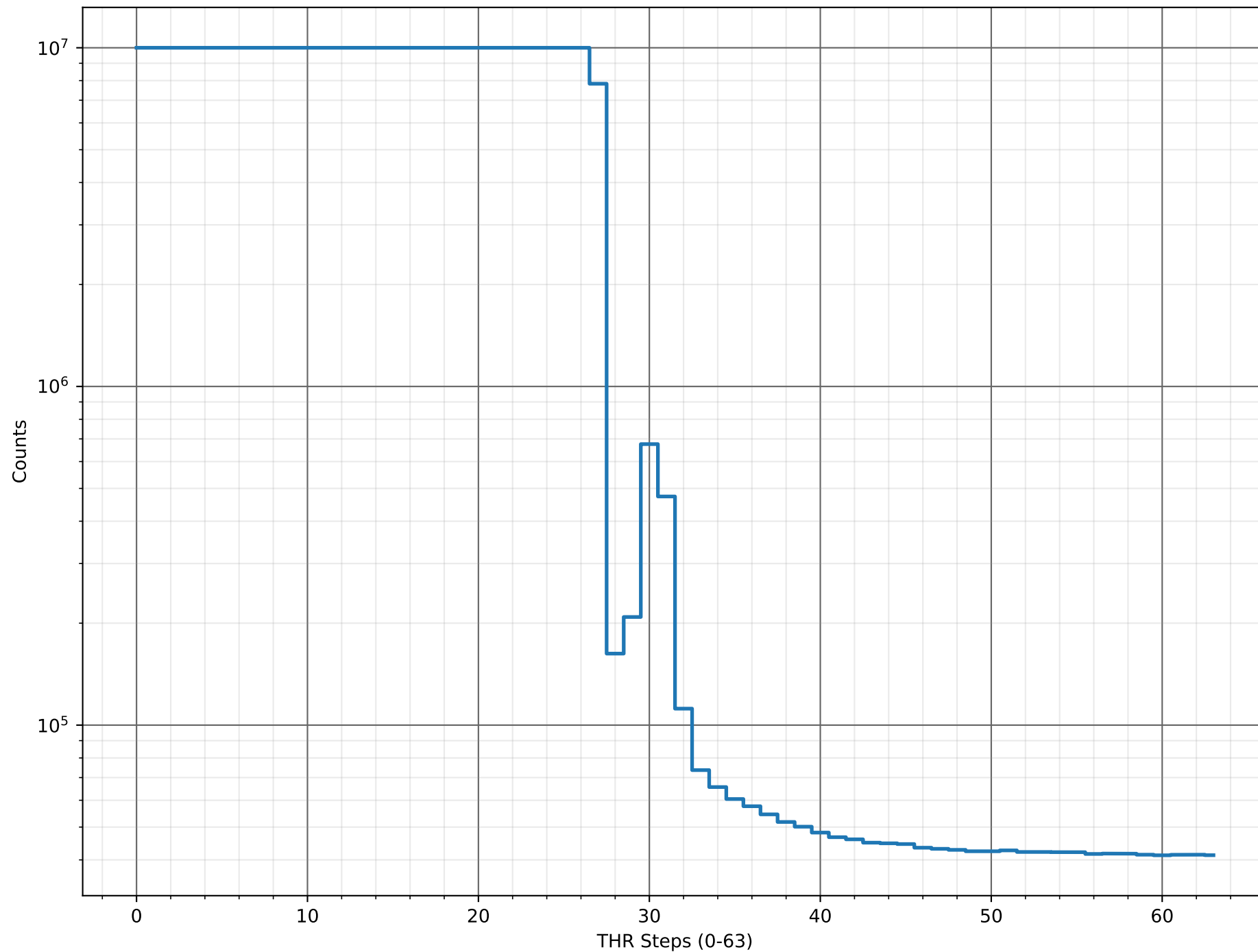


THR Distribution of PMT: B Anode: 64 (UART: 2 FEB: 0 CHIP: 6 CHANNEL: 0)

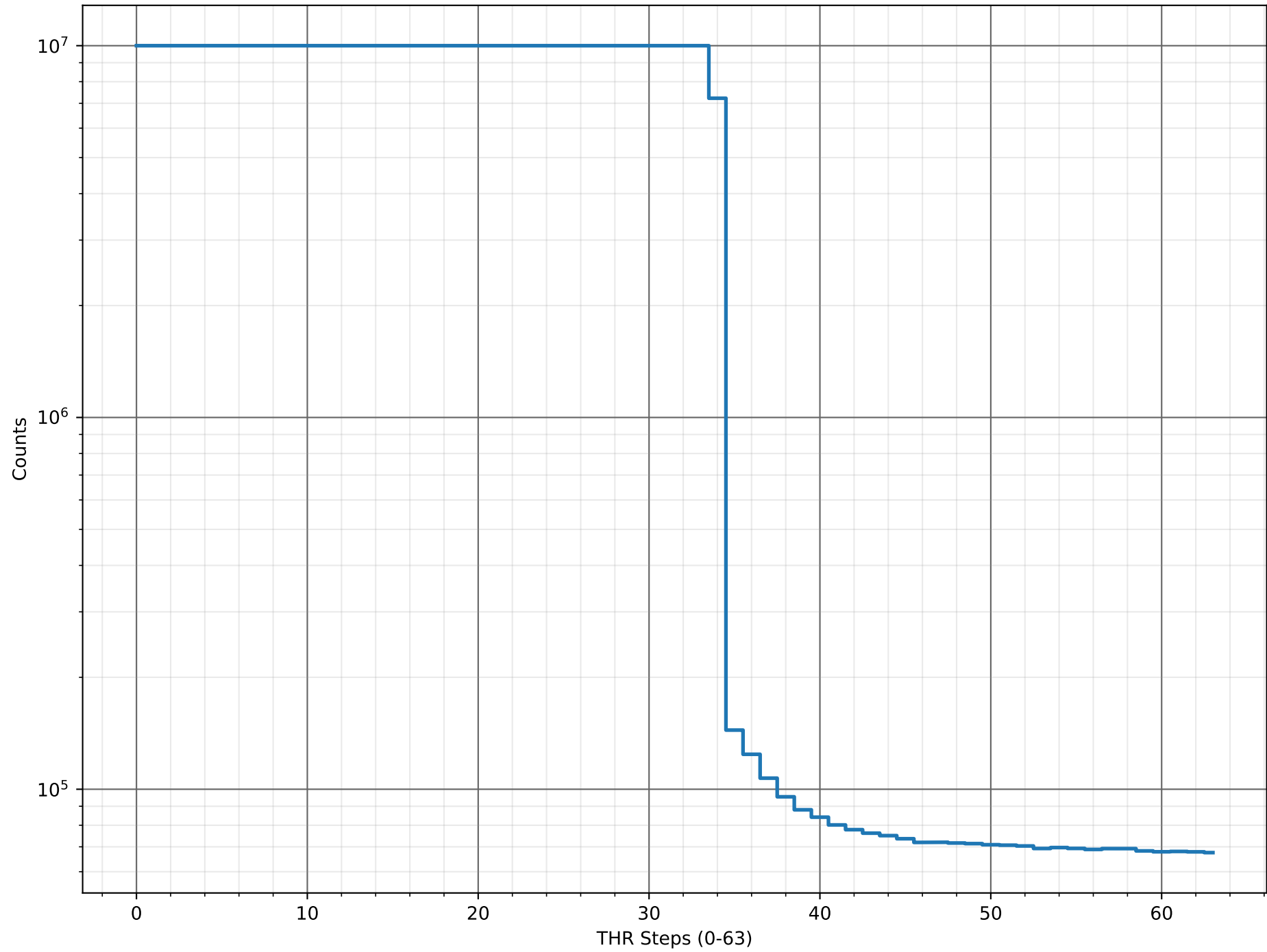




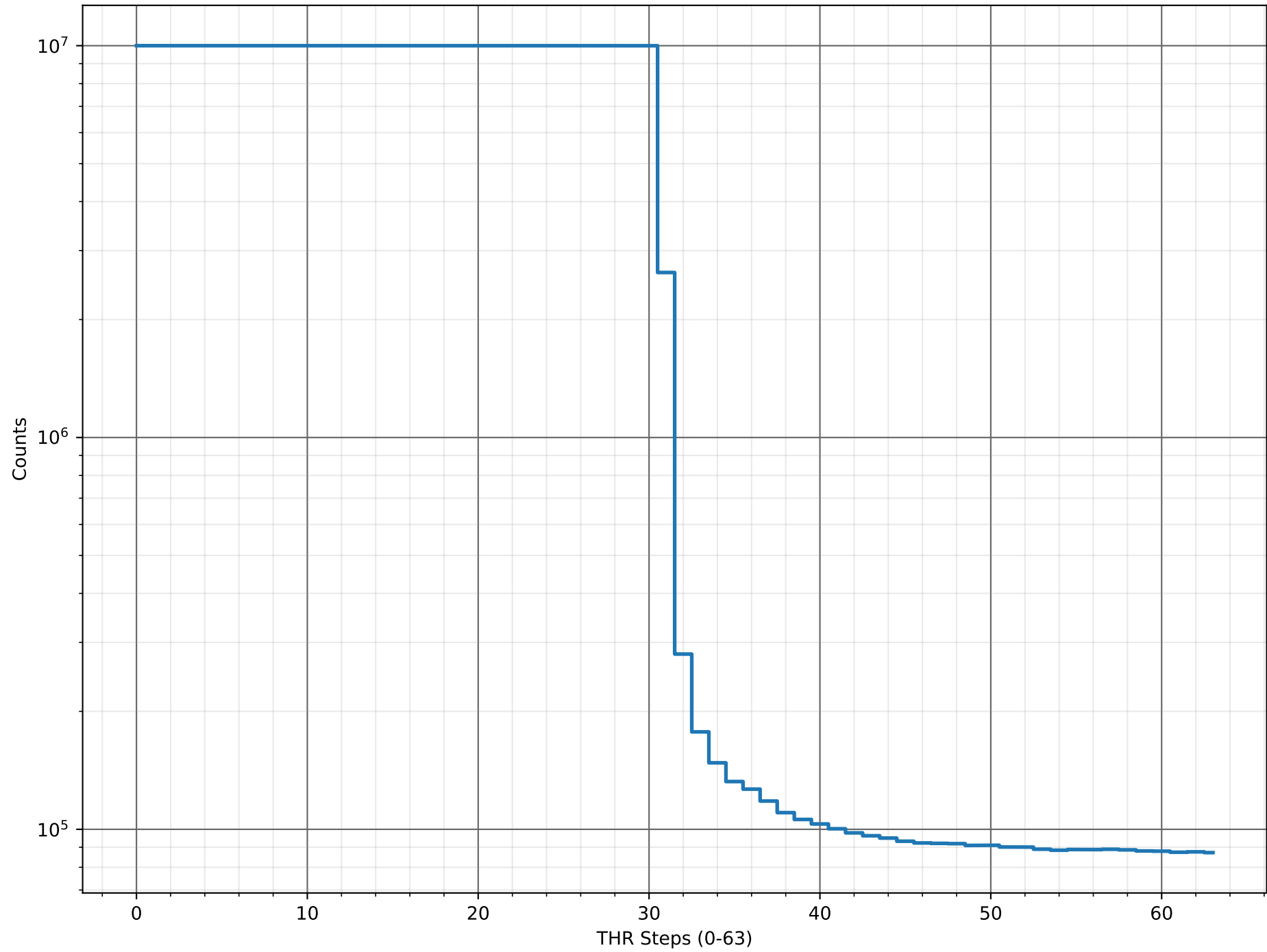
THR Distribution of PMT: C Anode: 1 (UART: 2 FEB: 1 CHIP: 3 CHANNEL: 7)



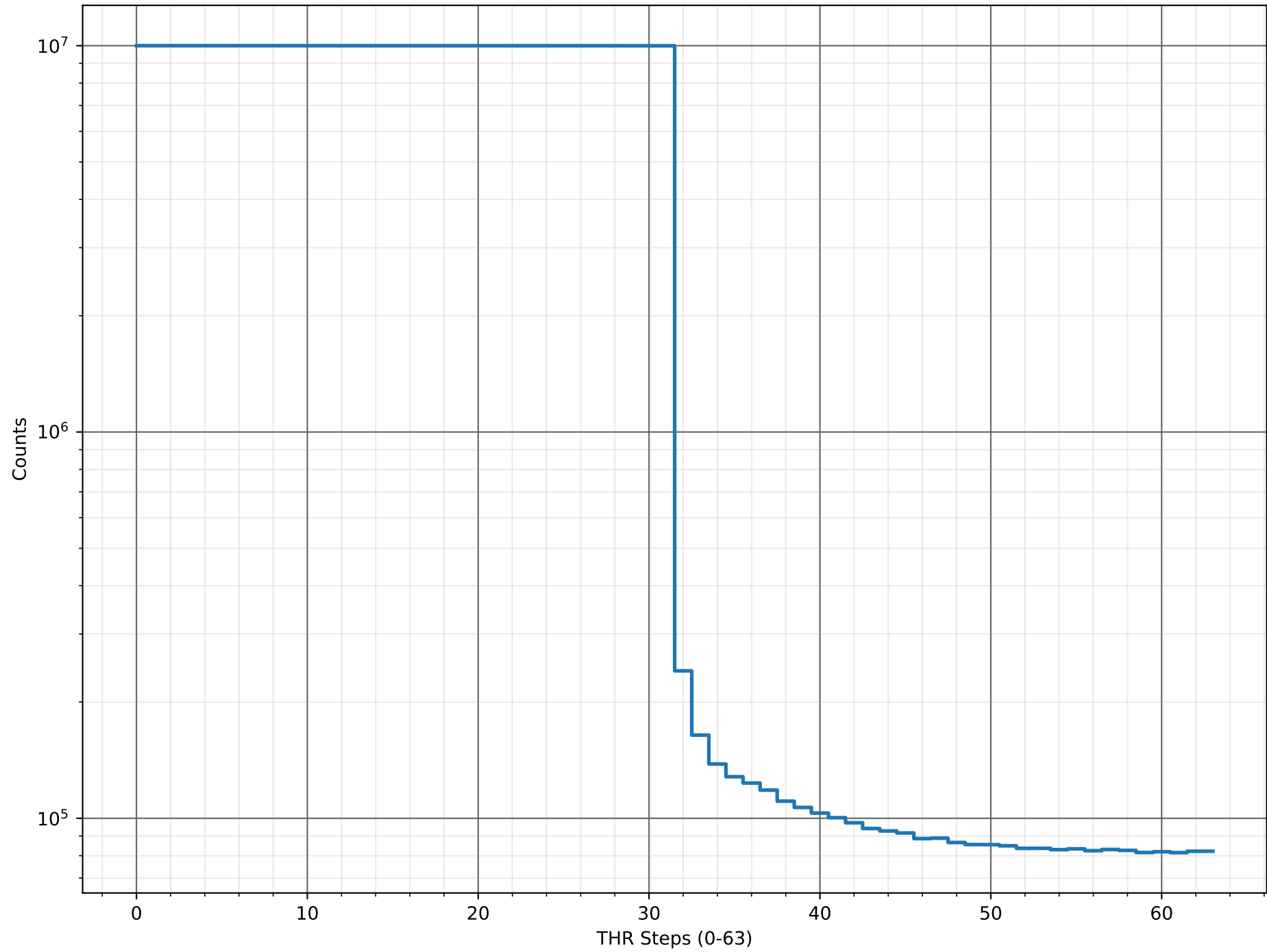
THR Distribution of PMT: C Anode: 2 (UART: 2 FEB: 1 CHIP: 3 CHANNEL: 6)



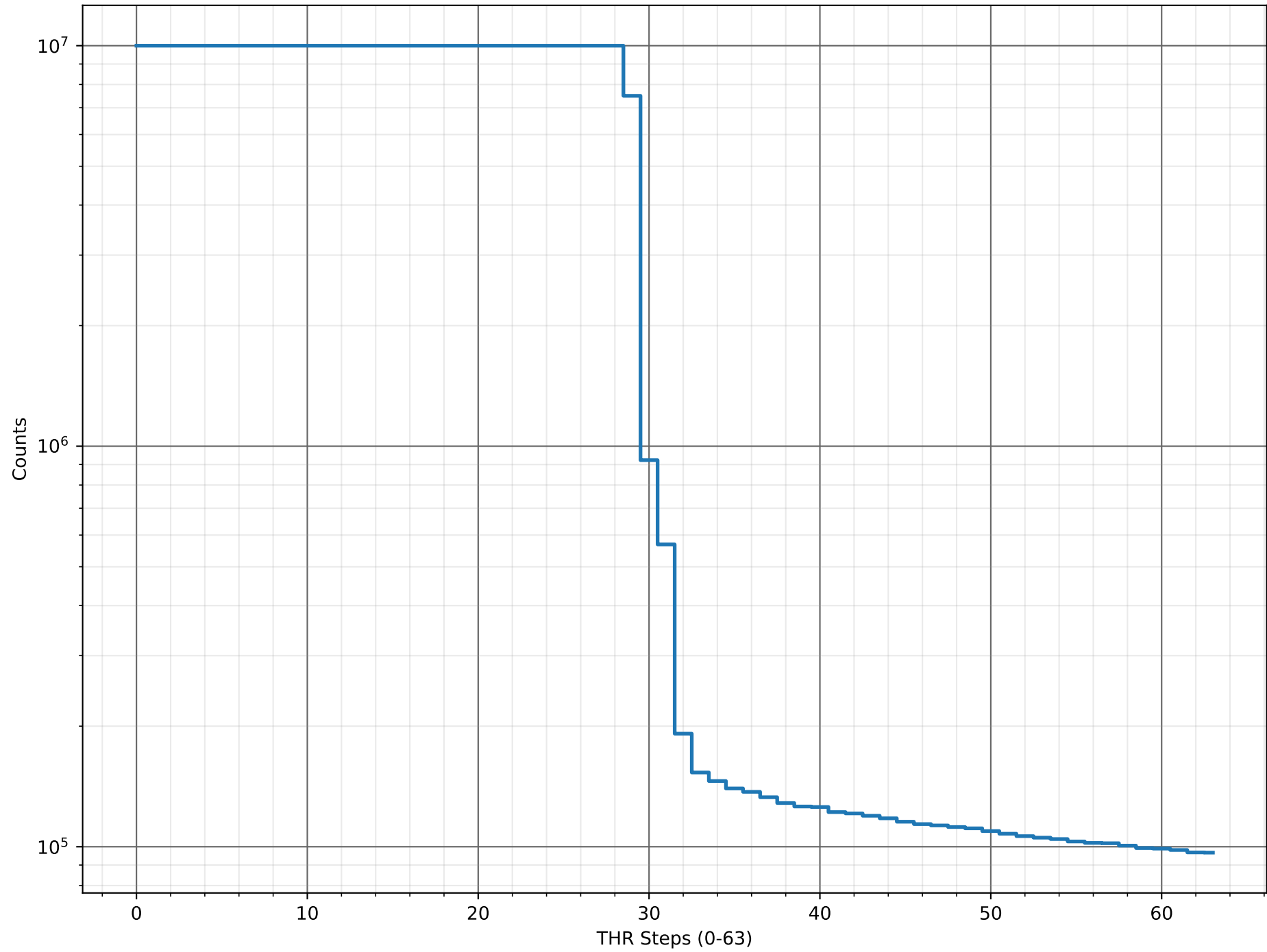
THR Distribution of PMT: C Anode: 3 (UART: 2 FEB: 1 CHIP: 2 CHANNEL: 0)



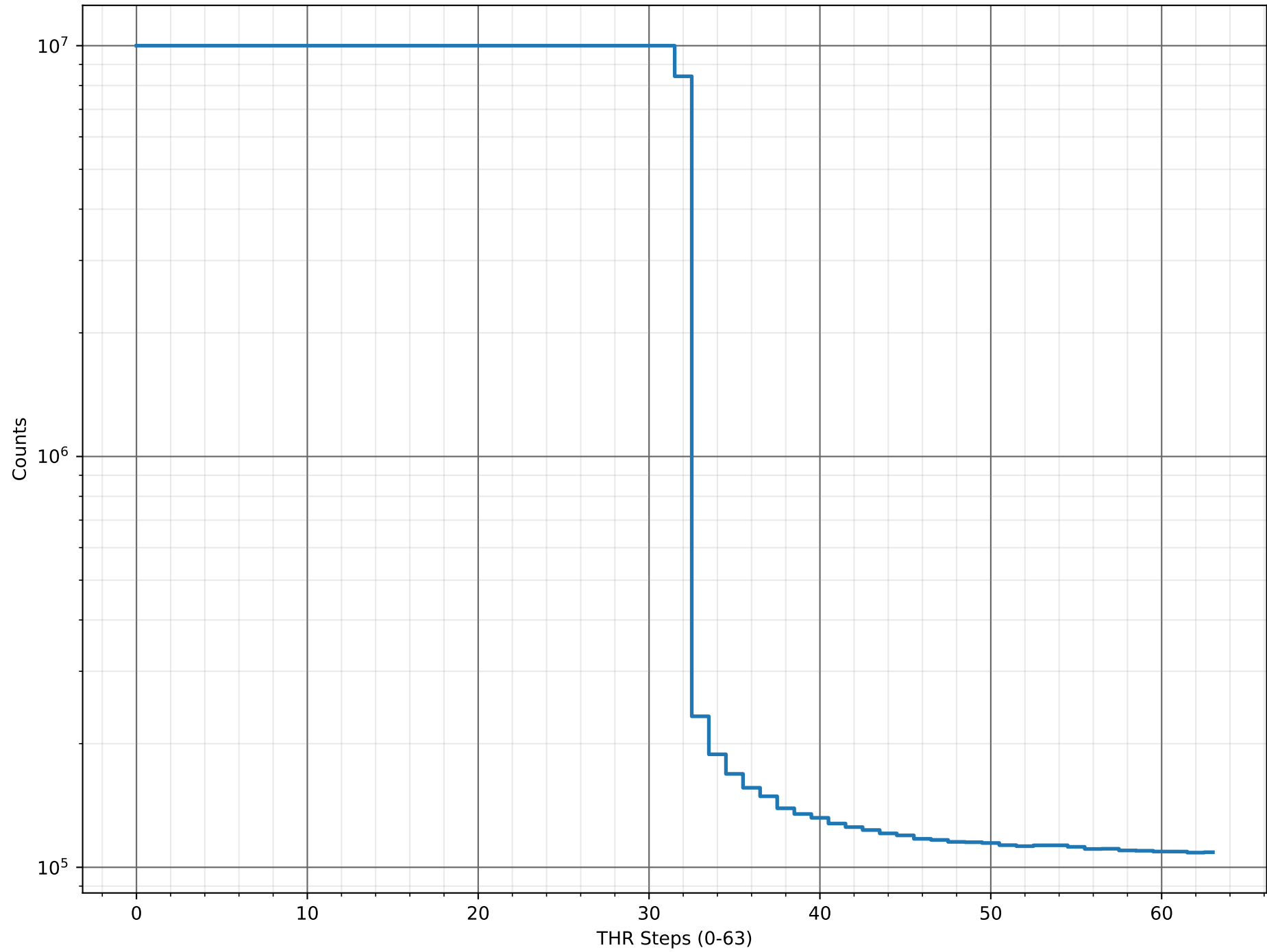
THR Distribution of PMT: C Anode: 4 (UART: 2 FEB: 1 CHIP: 2 CHANNEL: 1)



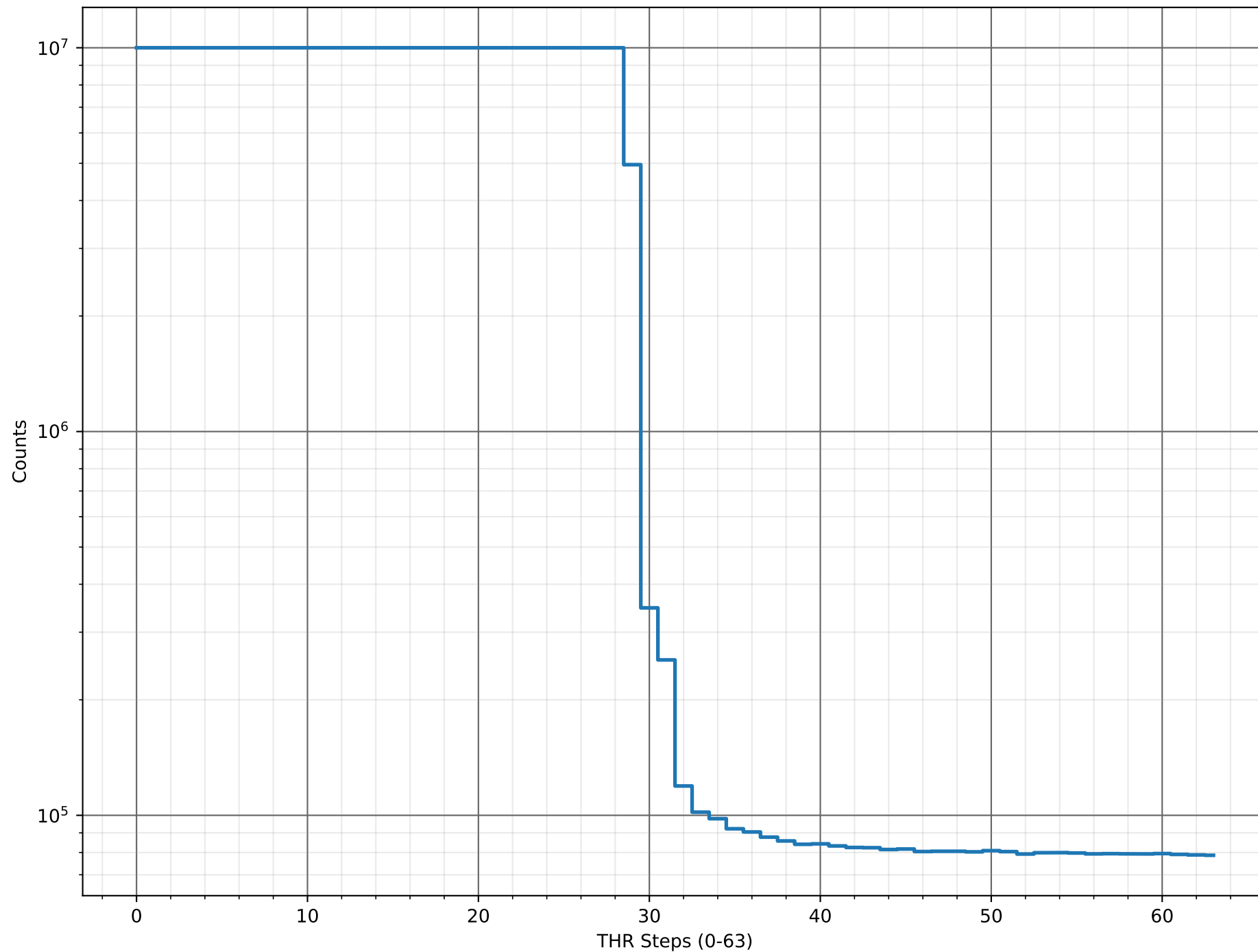
THR Distribution of PMT: C Anode: 5 (UART: 2 FEB: 0 CHIP: 3 CHANNEL: 6)



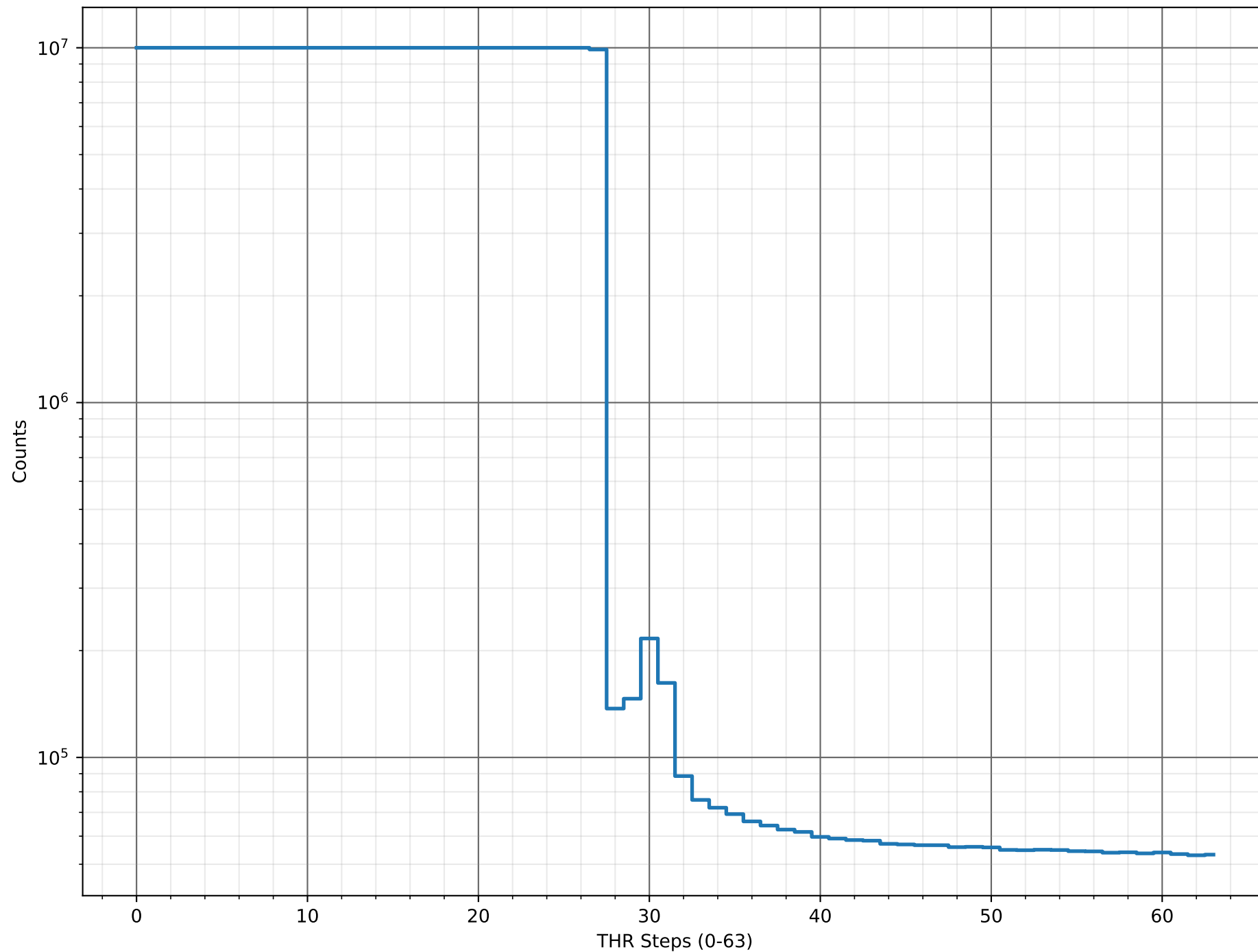
THR Distribution of PMT: C Anode: 6 (UART: 2 FEB: 0 CHIP: 3 CHANNEL: 7)



THR Distribution of PMT: C Anode: 7 (UART: 2 FEB: 0 CHIP: 2 CHANNEL: 1)

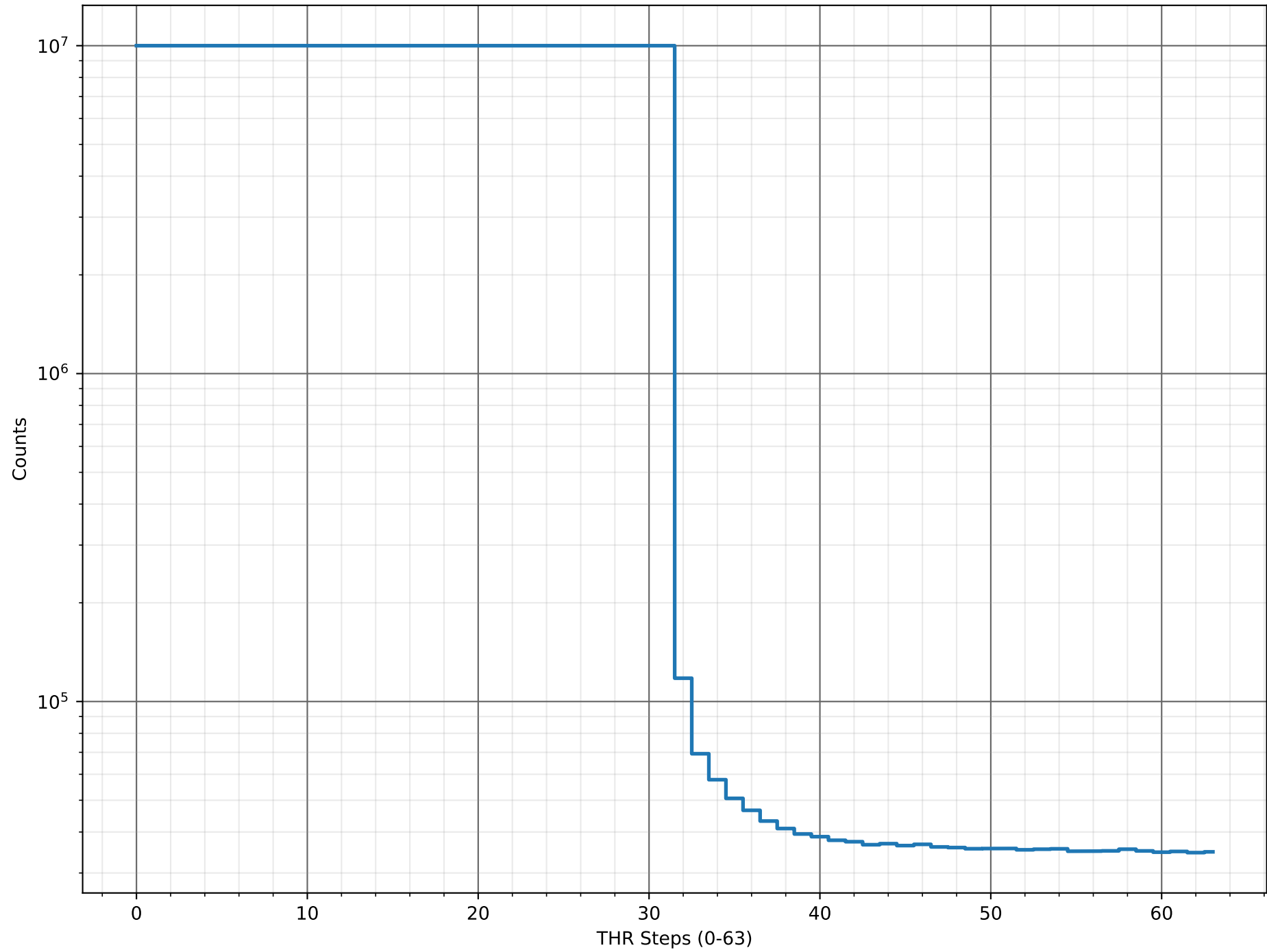


THR Distribution of PMT: C Anode: 8 (UART: 2 FEB: 0 CHIP: 2 CHANNEL: 0)

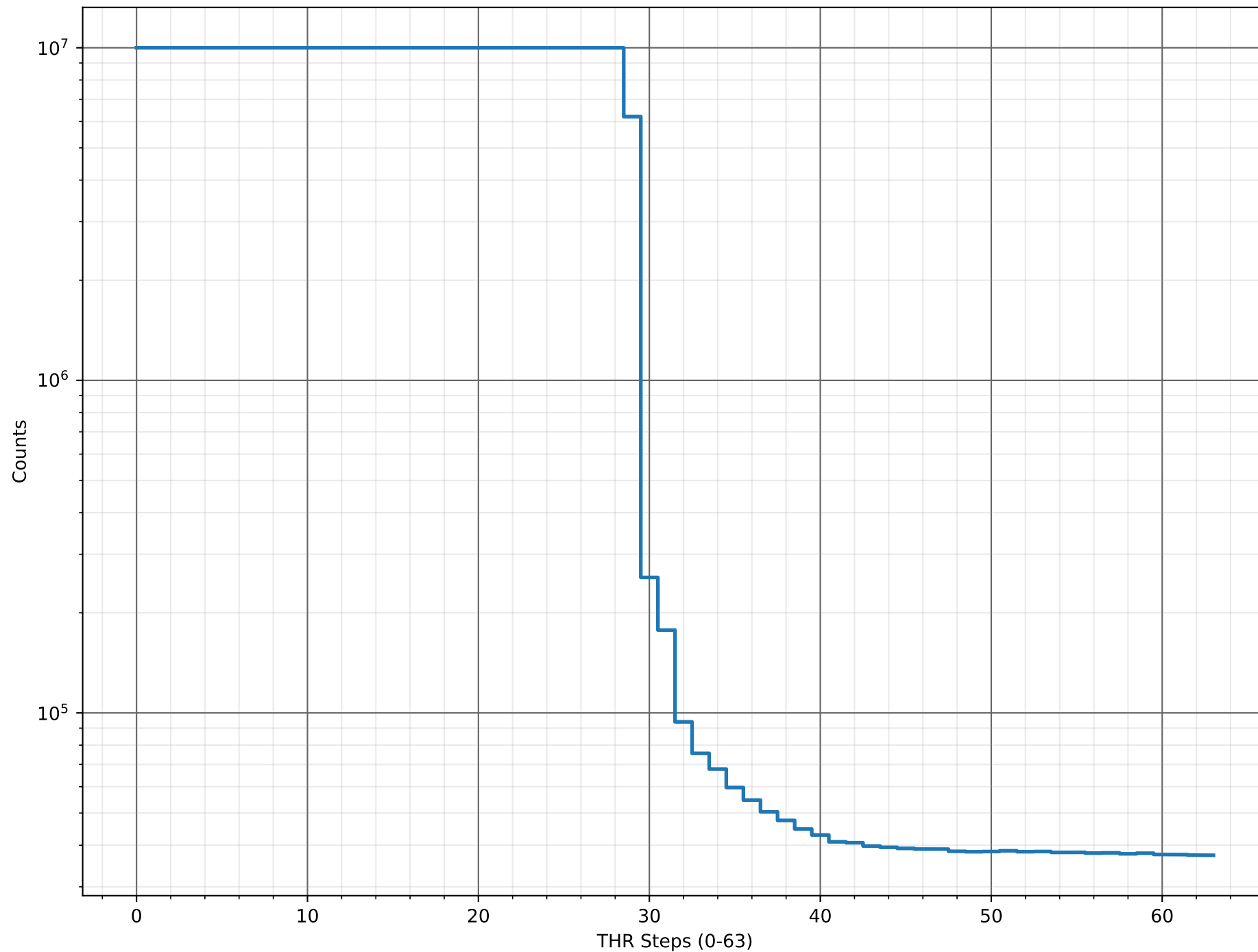




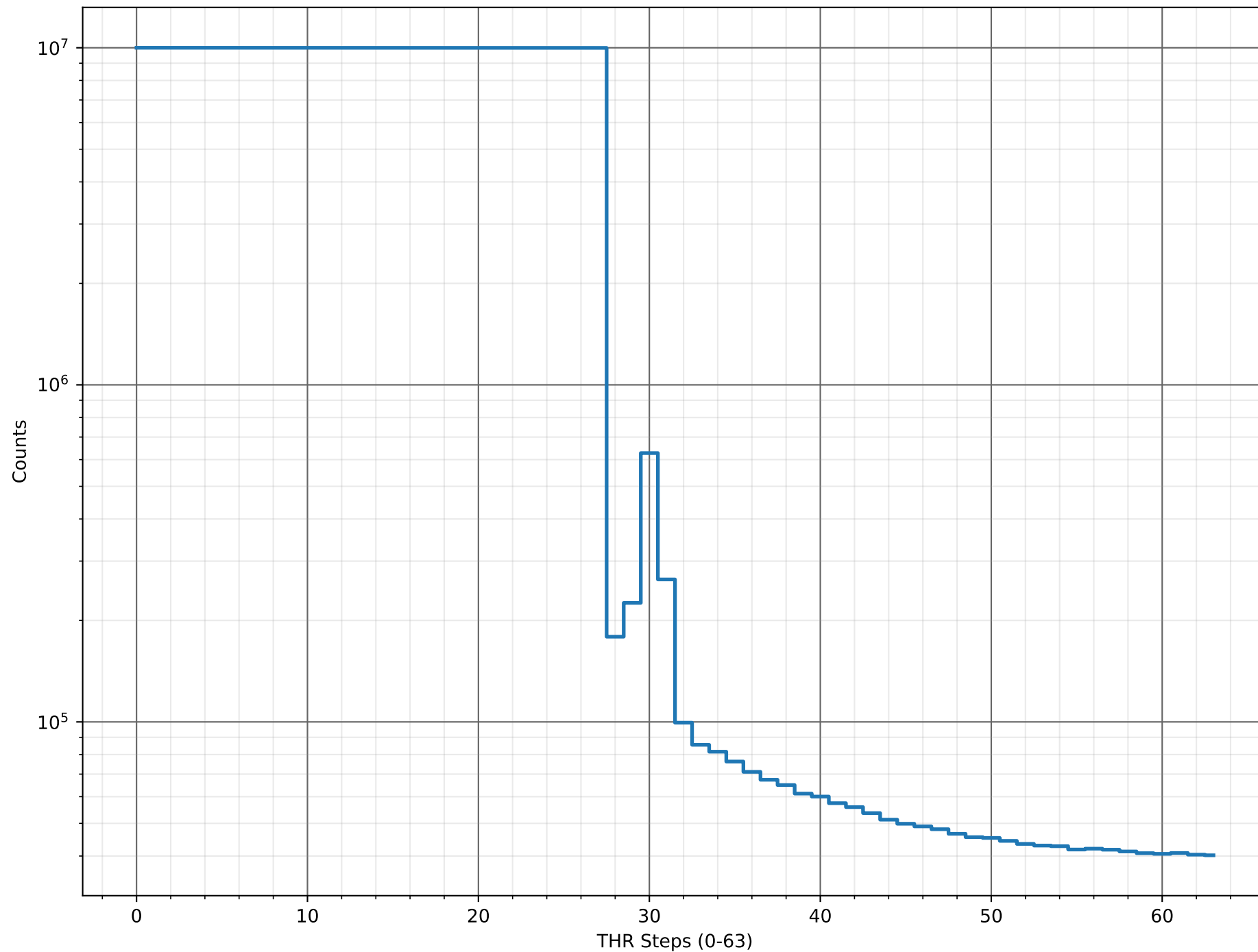
THR Distribution of PMT: C Anode: 9 (UART: 2 FEB: 1 CHIP: 3 CHANNEL: 5)



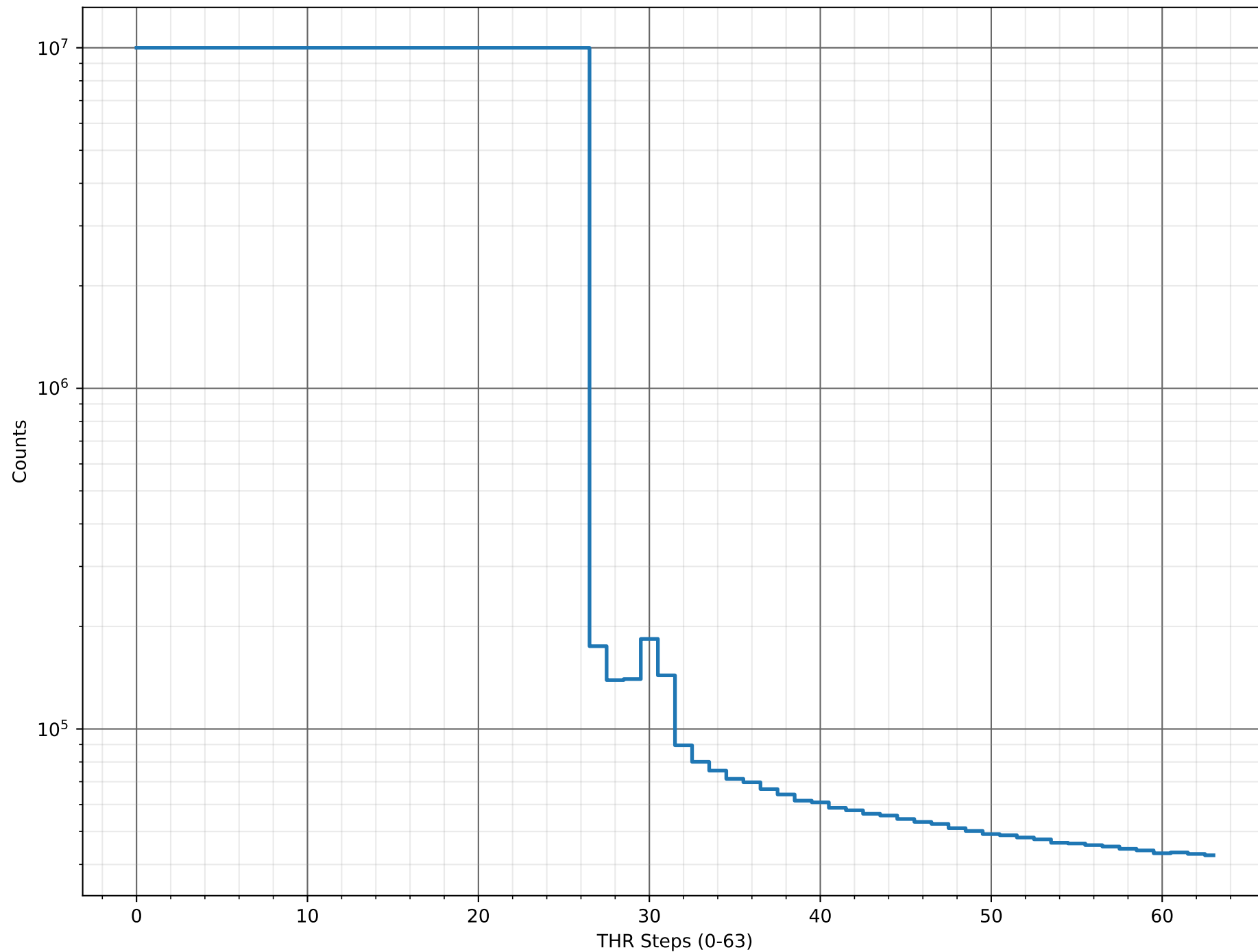
THR Distribution of PMT: C Anode: 10 (UART: 2 FEB: 1 CHIP: 3 CHANNEL: 4)



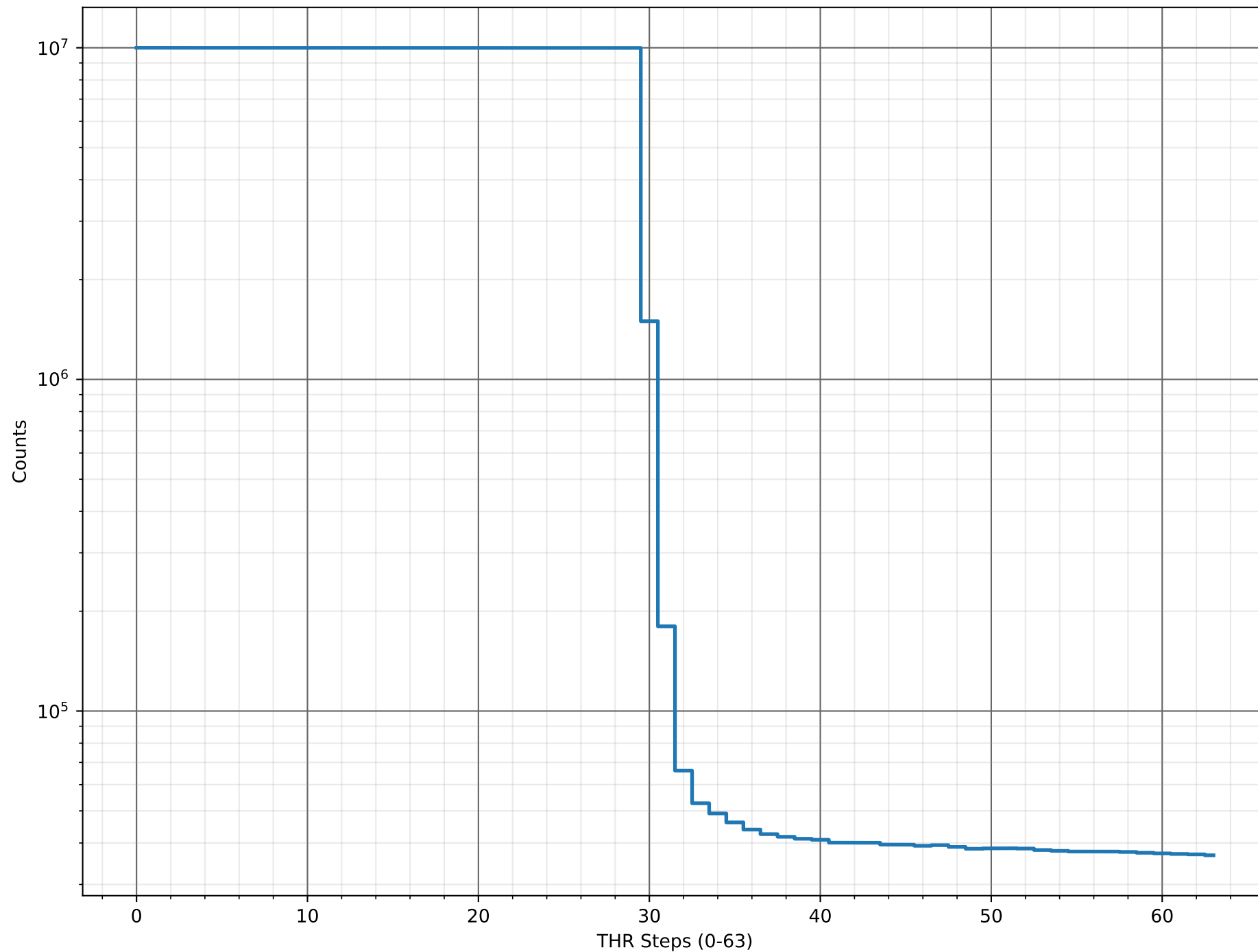
THR Distribution of PMT: C Anode: 11 (UART: 2 FEB: 1 CHIP: 2 CHANNEL: 2)



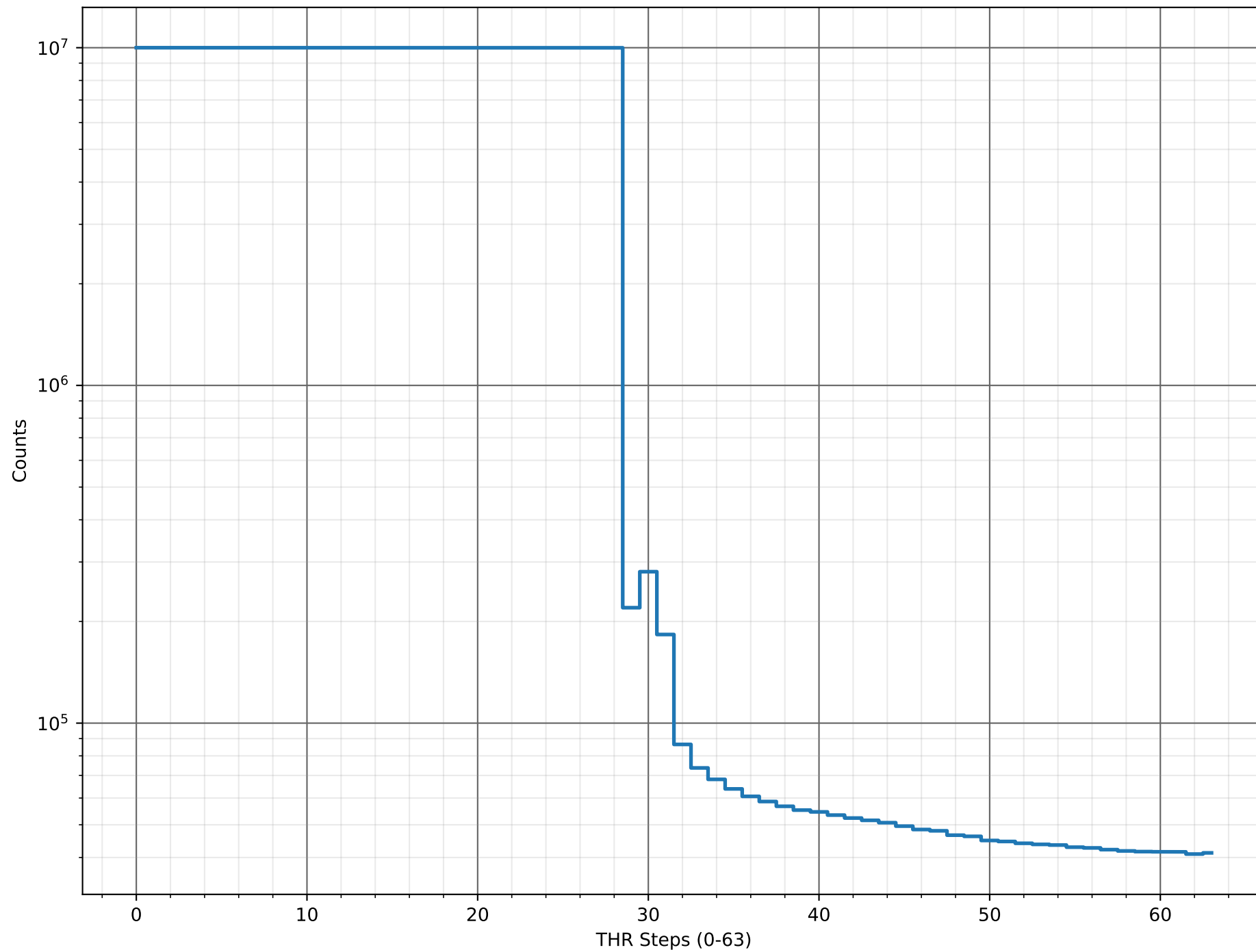
THR Distribution of PMT: C Anode: 12 (UART: 2 FEB: 1 CHIP: 2 CHANNEL: 3)



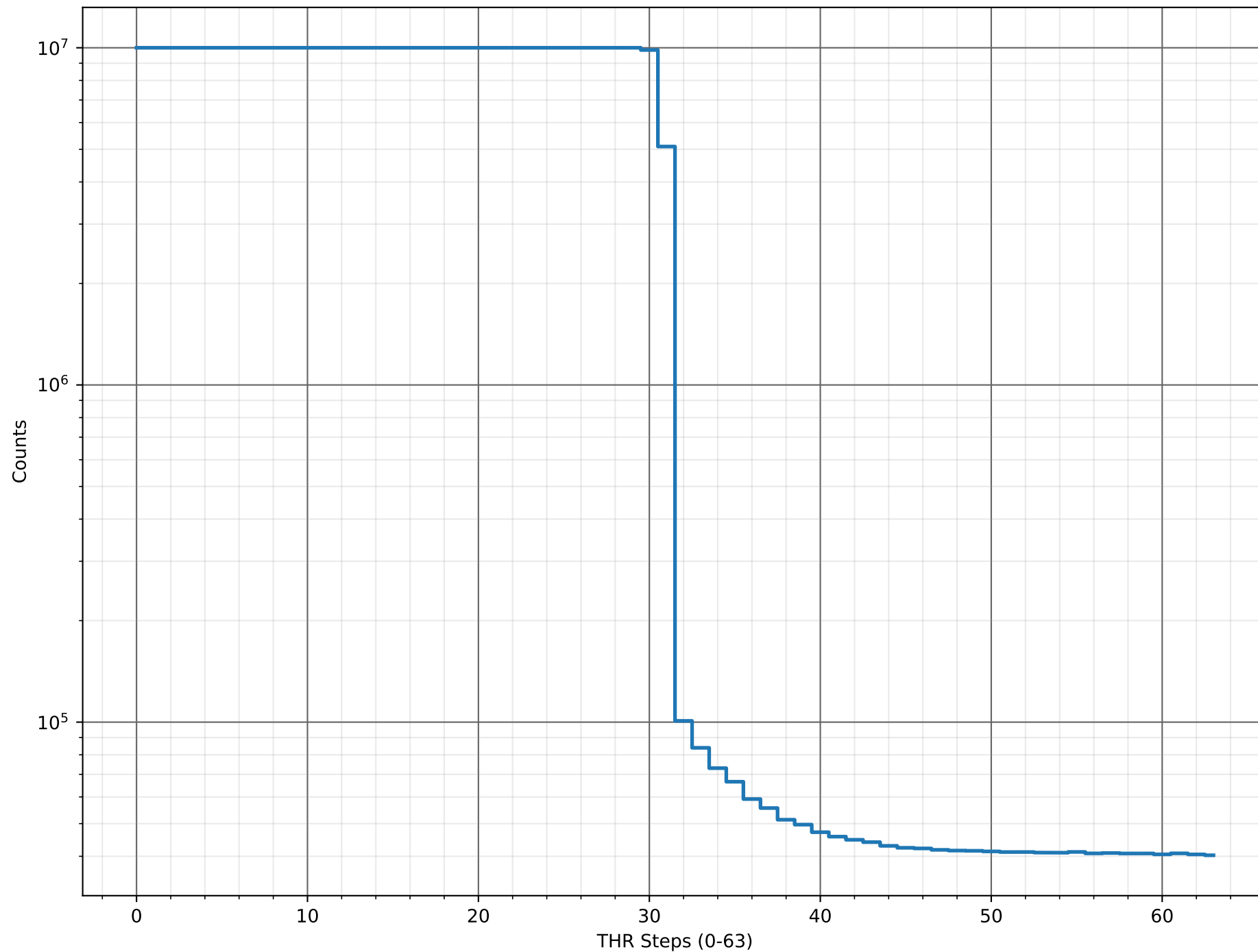
THR Distribution of PMT: C Anode: 13 (UART: 2 FEB: 0 CHIP: 3 CHANNEL: 4)



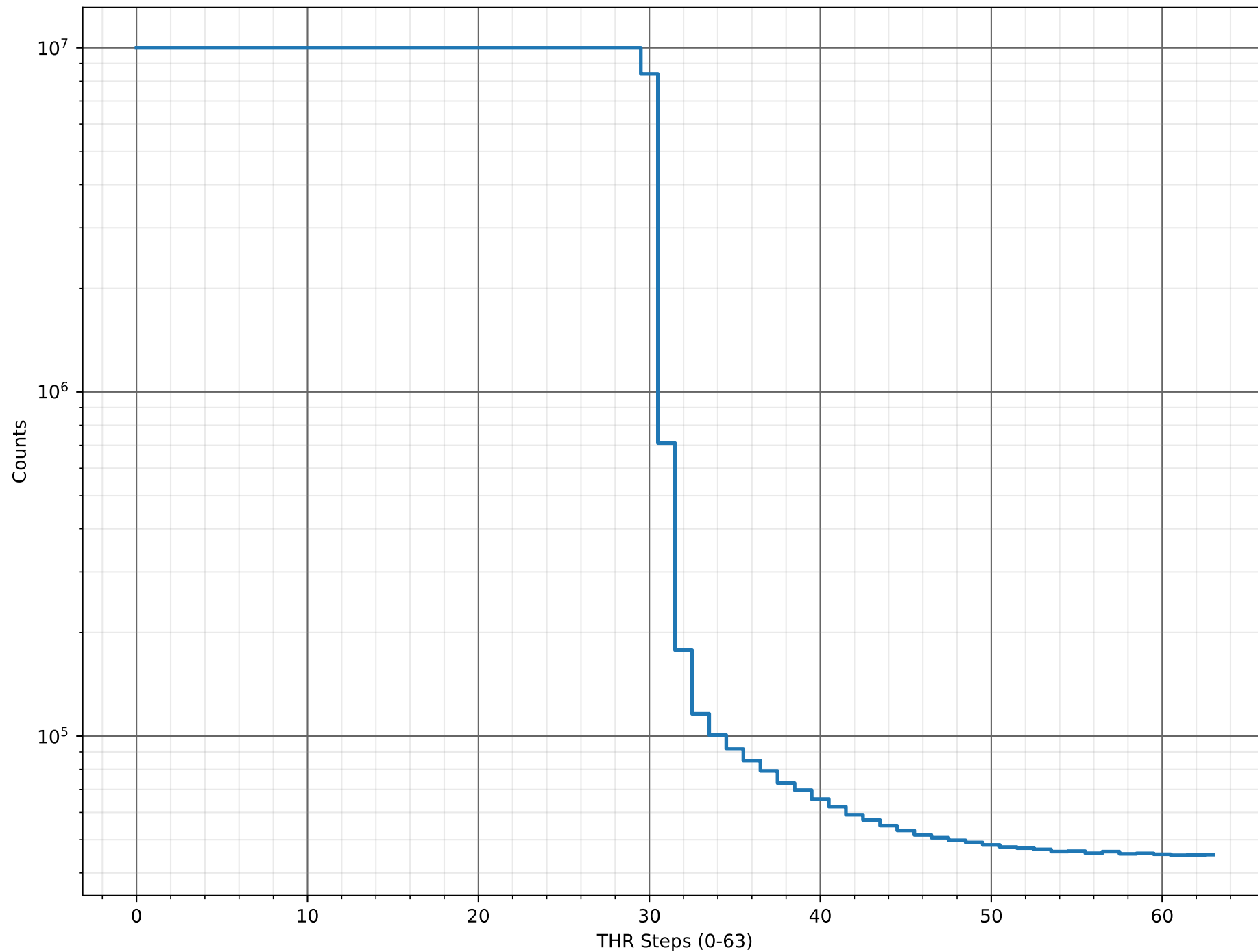
THR Distribution of PMT: C Anode: 14 (UART: 2 FEB: 0 CHIP: 3 CHANNEL: 5)



THR Distribution of PMT: C Anode: 15 (UART: 2 FEB: 0 CHIP: 2 CHANNEL: 3)

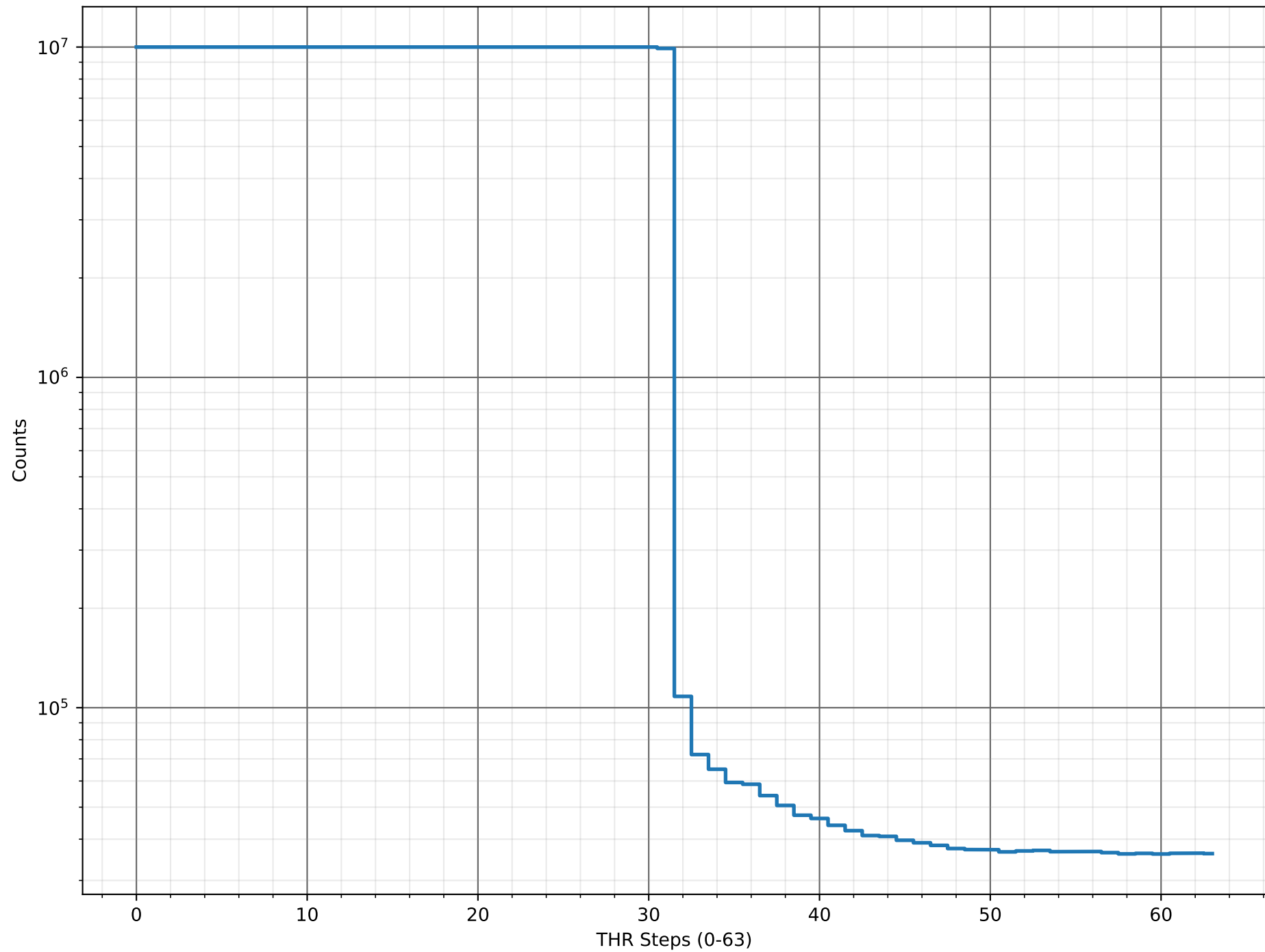


THR Distribution of PMT: C Anode: 16 (UART: 2 FEB: 0 CHIP: 2 CHANNEL: 2)

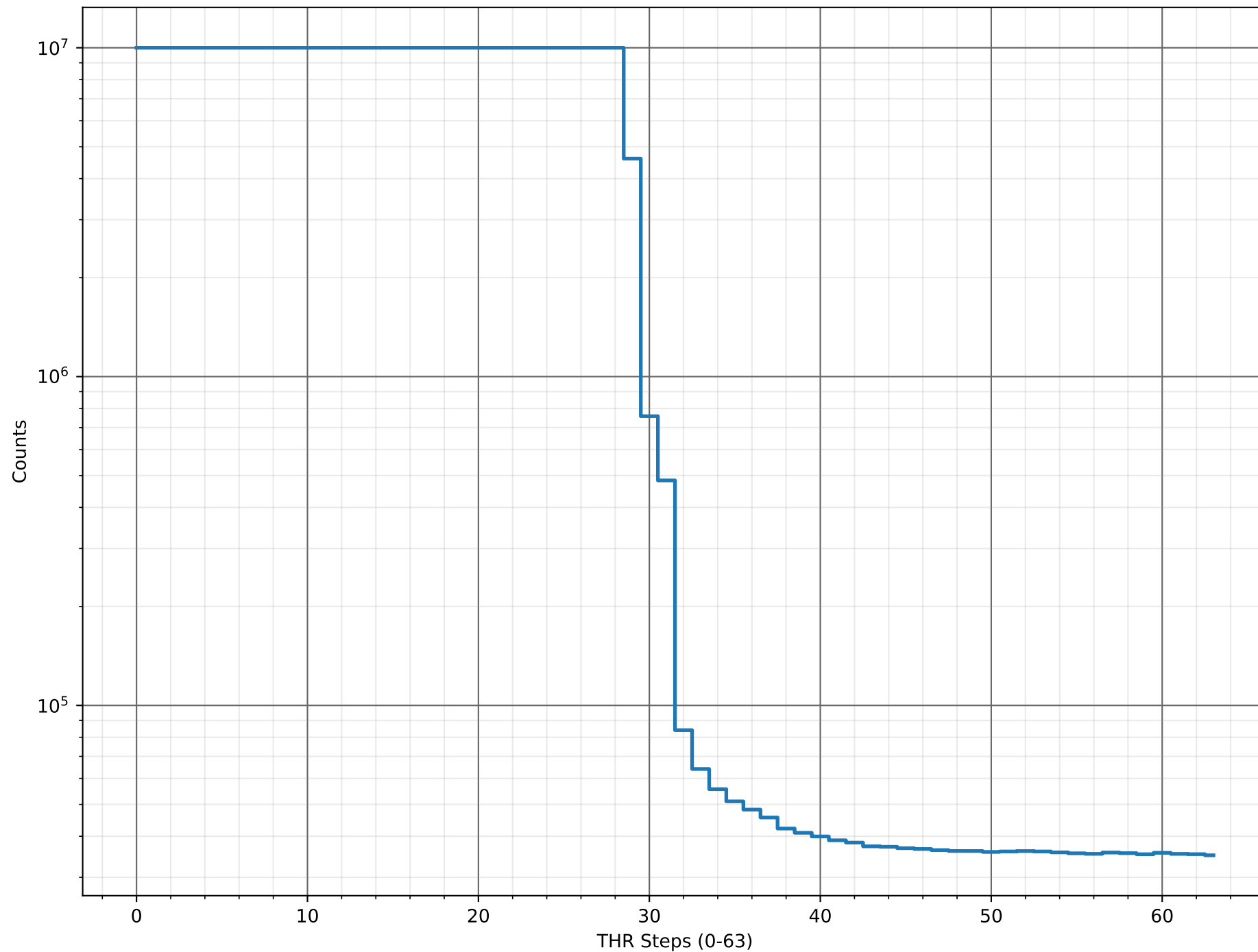




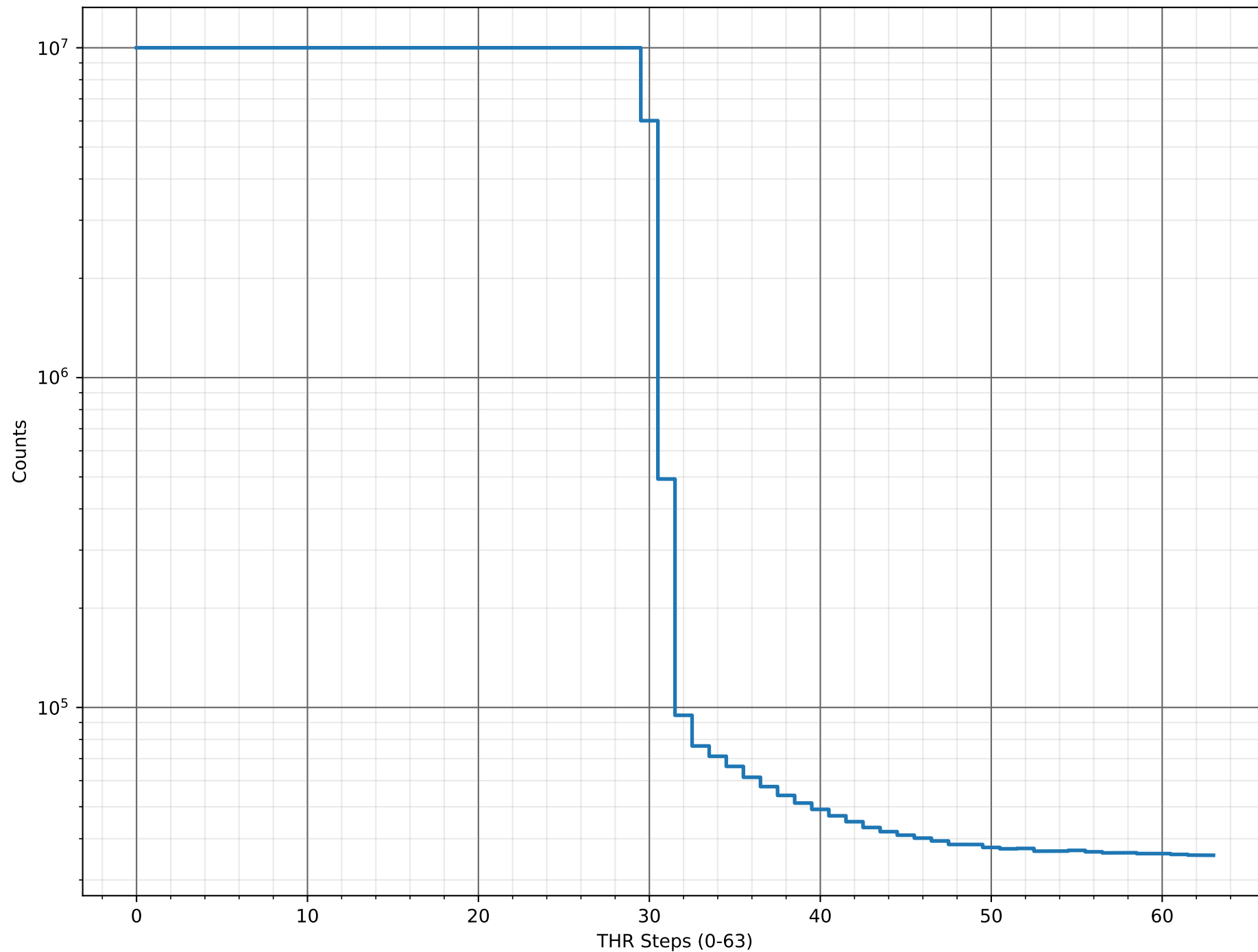
THR Distribution of PMT: C Anode: 17 (UART: 2 FEB: 1 CHIP: 3 CHANNEL: 3)



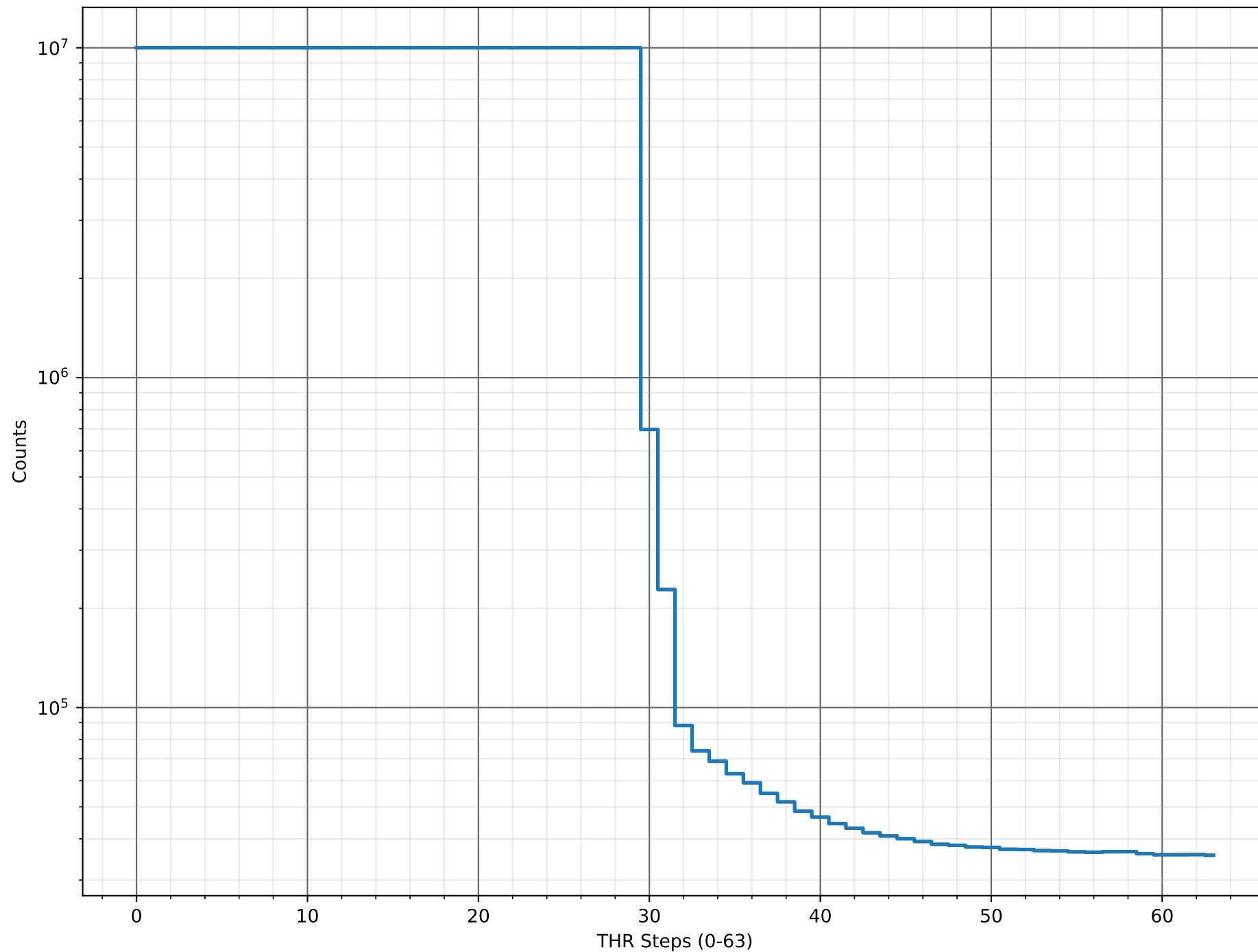
THR Distribution of PMT: C Anode: 18 (UART: 2 FEB: 1 CHIP: 3 CHANNEL: 2)



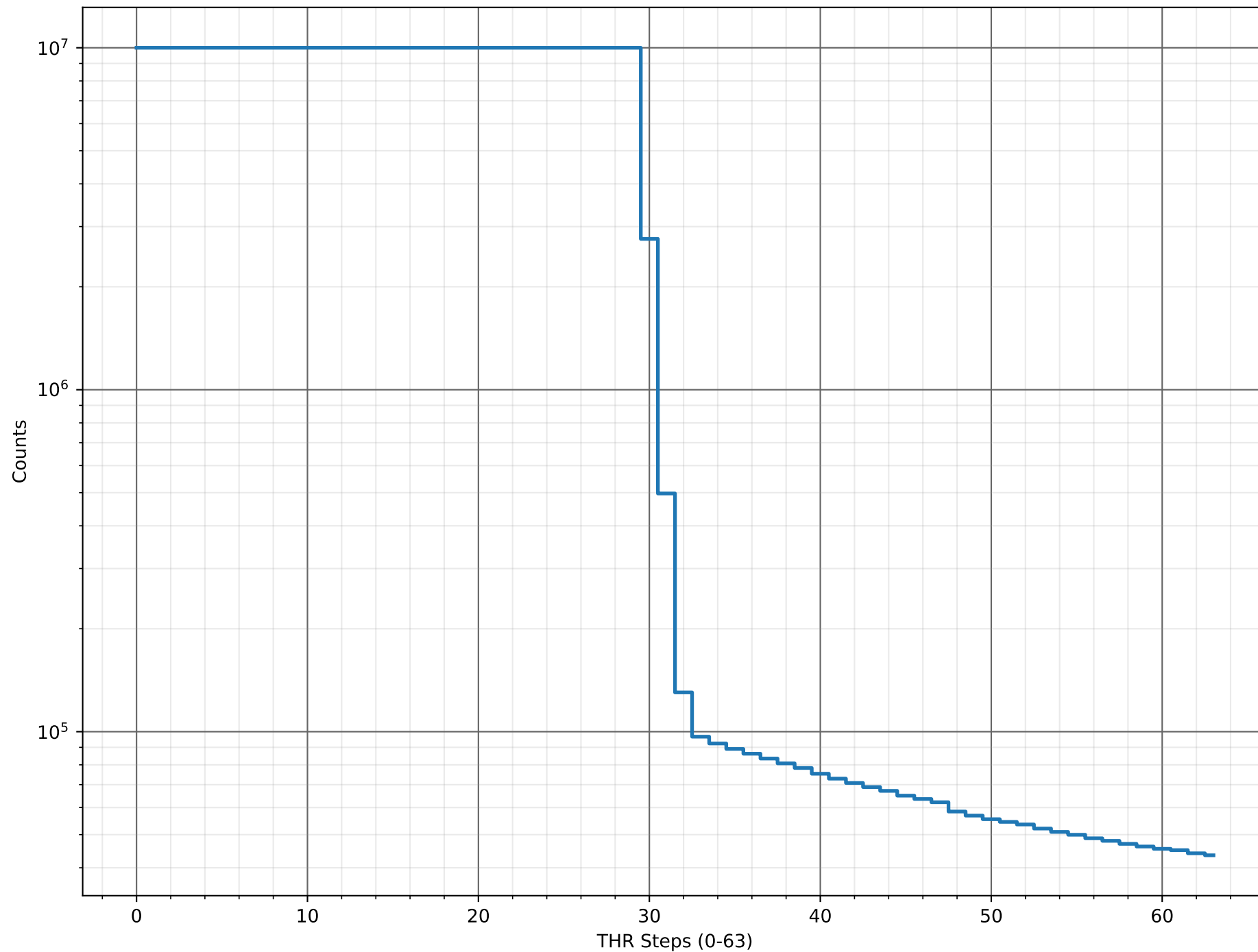
THR Distribution of PMT: C Anode: 19 (UART: 2 FEB: 1 CHIP: 2 CHANNEL: 4)



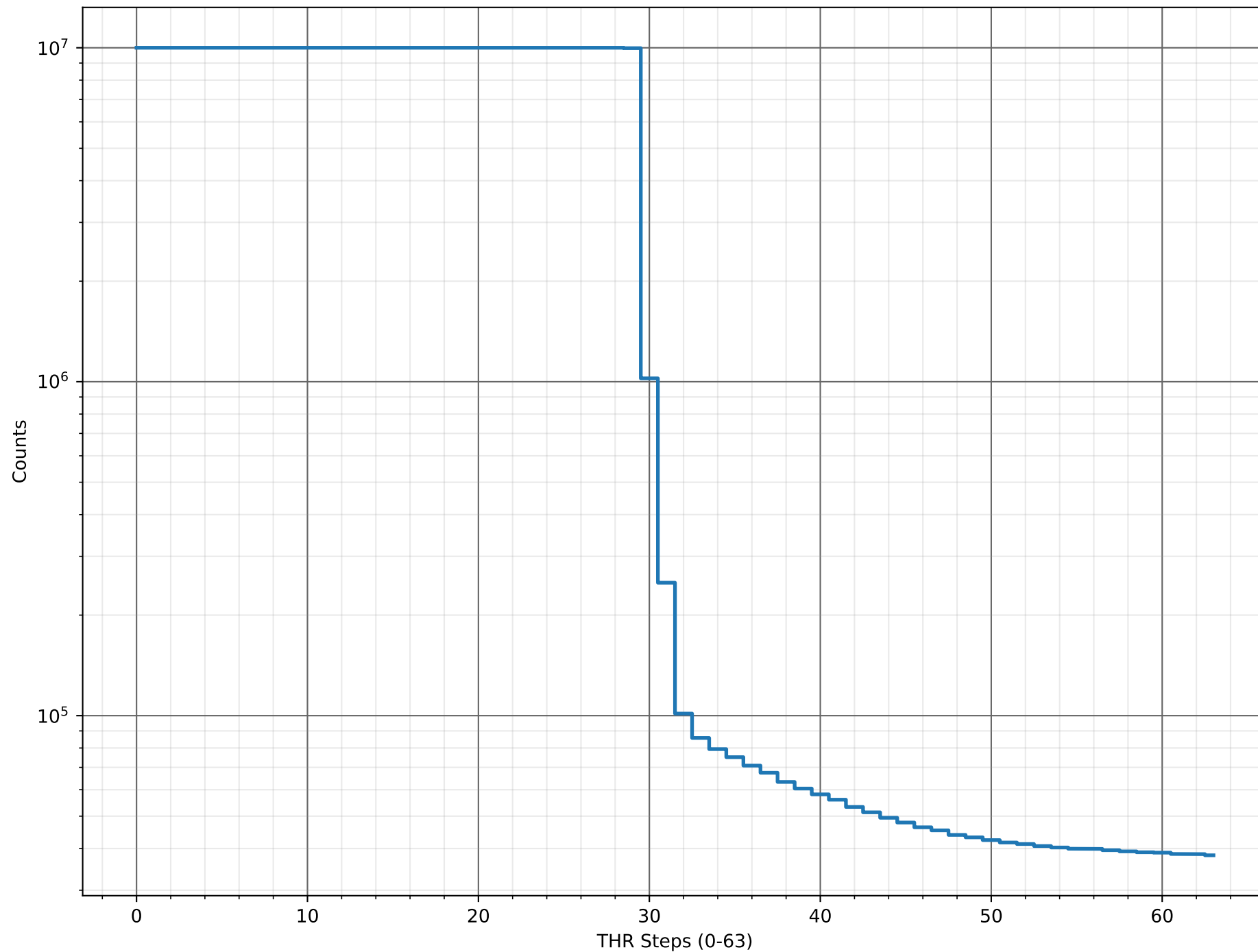
THR Distribution of PMT: C Anode: 20 (UART: 2 FEB: 1 CHIP: 2 CHANNEL: 5)



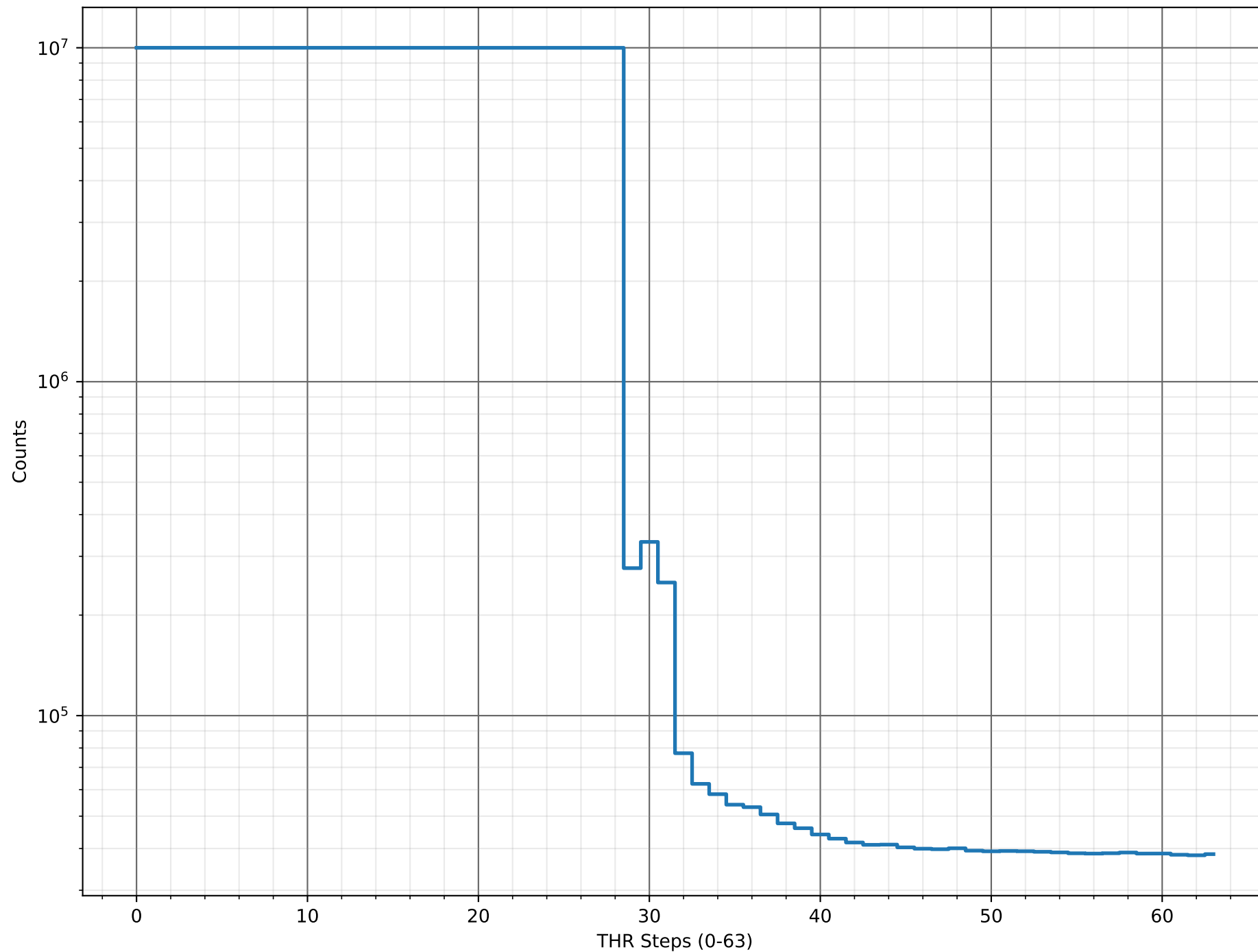
THR Distribution of PMT: C Anode: 21 (UART: 2 FEB: 0 CHIP: 3 CHANNEL: 2)



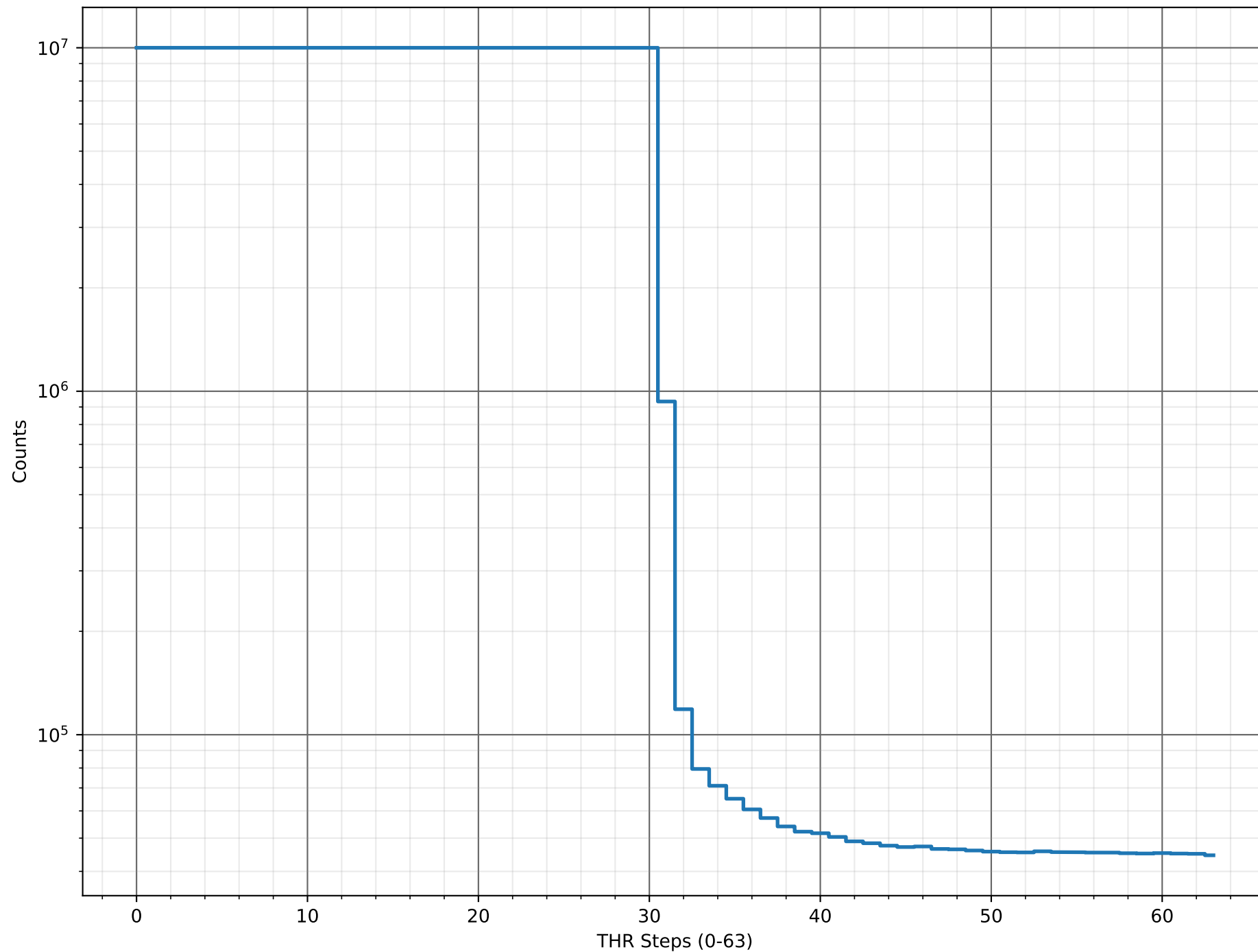
THR Distribution of PMT: C Anode: 22 (UART: 2 FEB: 0 CHIP: 3 CHANNEL: 3)



THR Distribution of PMT: C Anode: 23 (UART: 2 FEB: 0 CHIP: 2 CHANNEL: 5)

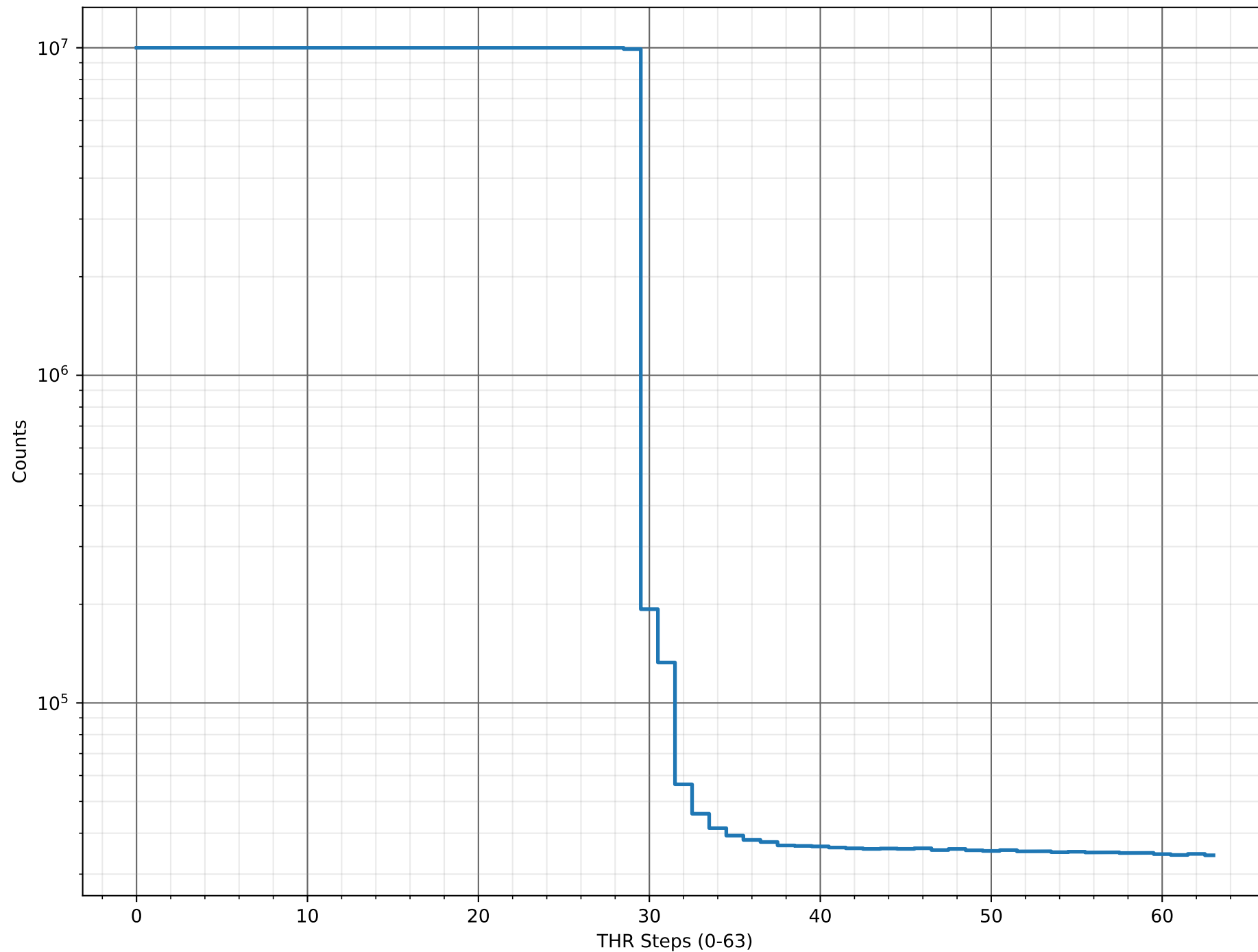


THR Distribution of PMT: C Anode: 24 (UART: 2 FEB: 0 CHIP: 2 CHANNEL: 4)

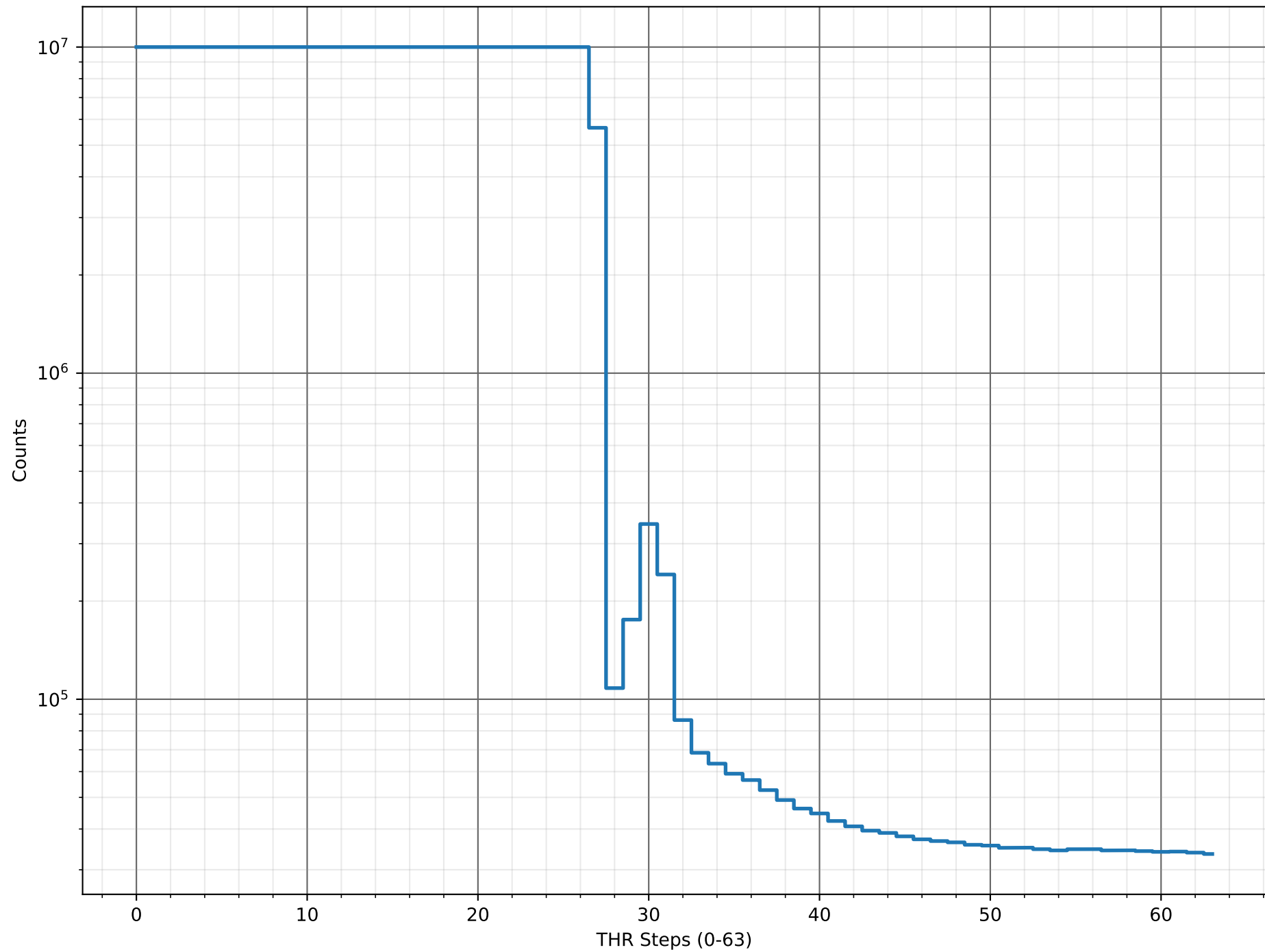




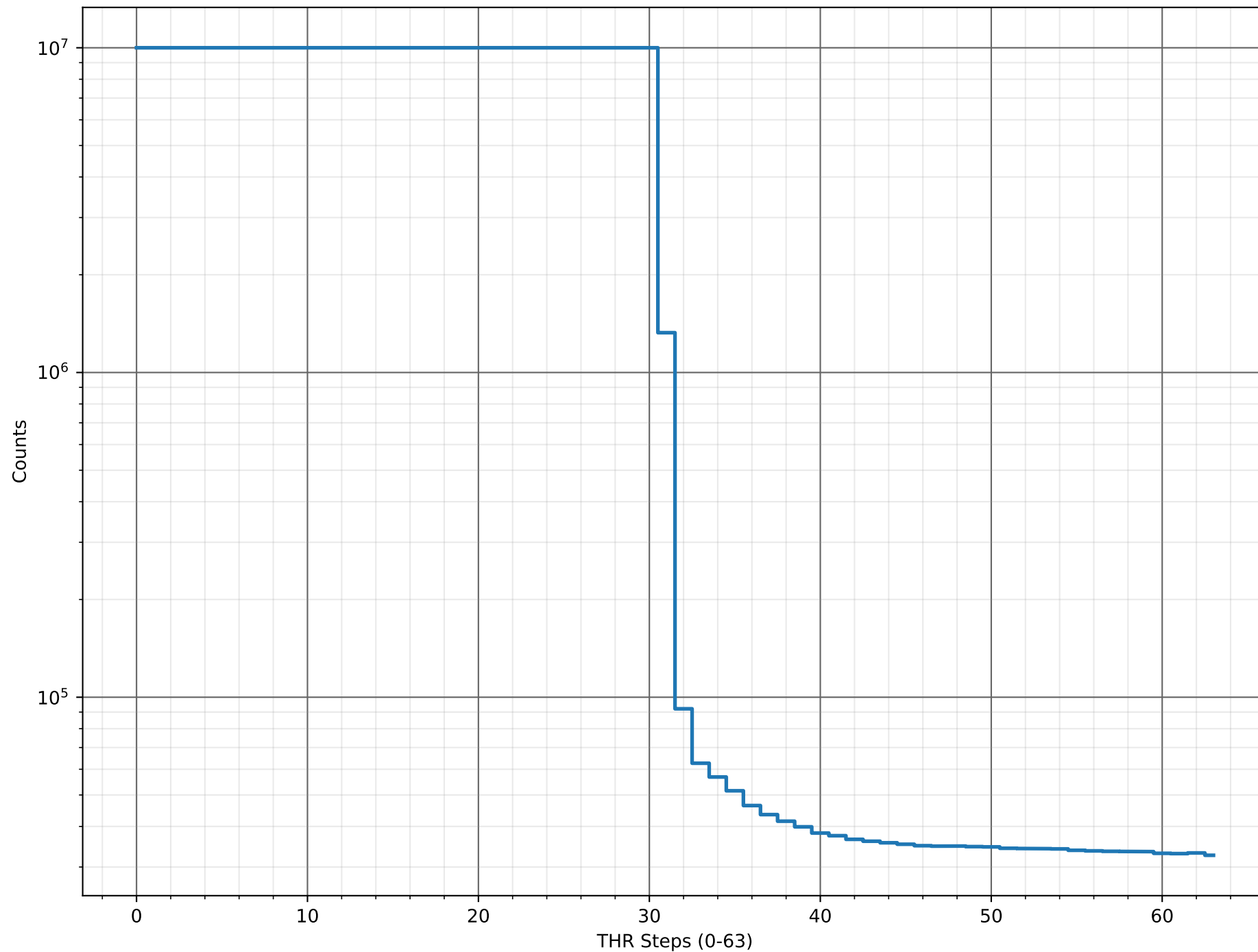
THR Distribution of PMT: C Anode: 25 (UART: 2 FEB: 1 CHIP: 3 CHANNEL: 1)



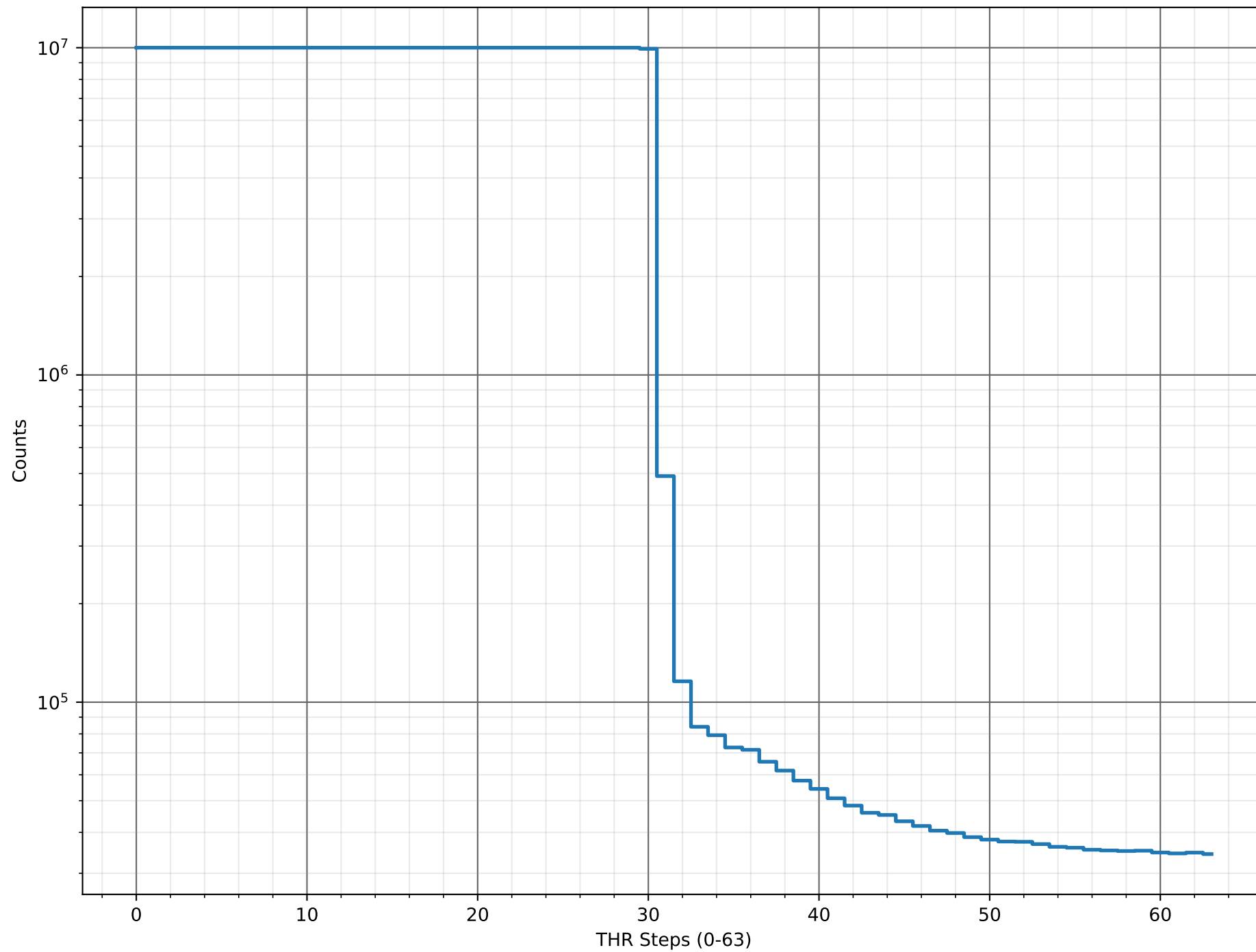
THR Distribution of PMT: C Anode: 26 (UART: 2 FEB: 1 CHIP: 3 CHANNEL: 0)



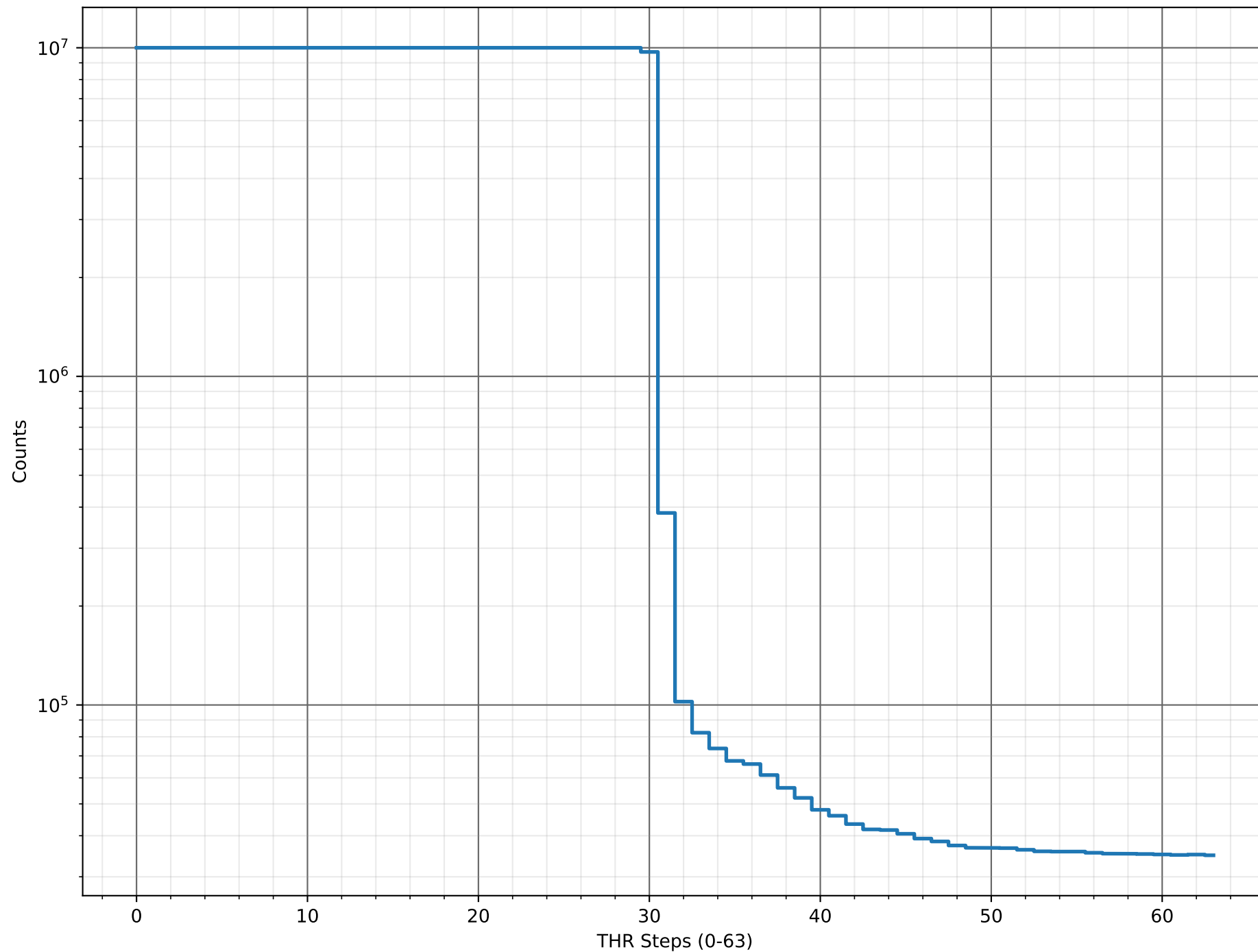
THR Distribution of PMT: C Anode: 27 (UART: 2 FEB: 1 CHIP: 2 CHANNEL: 7)



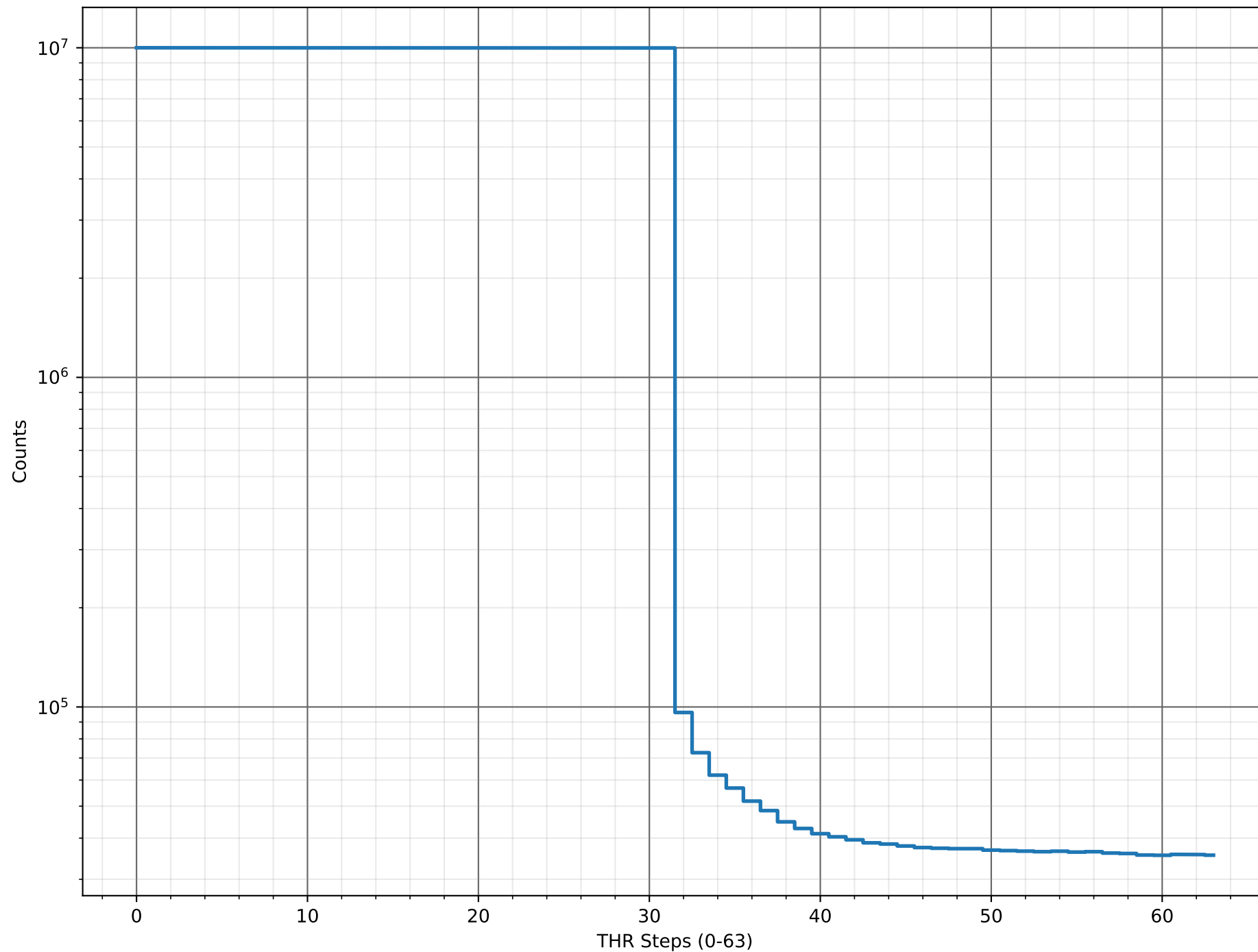
THR Distribution of PMT: C Anode: 28 (UART: 2 FEB: 1 CHIP: 2 CHANNEL: 6)



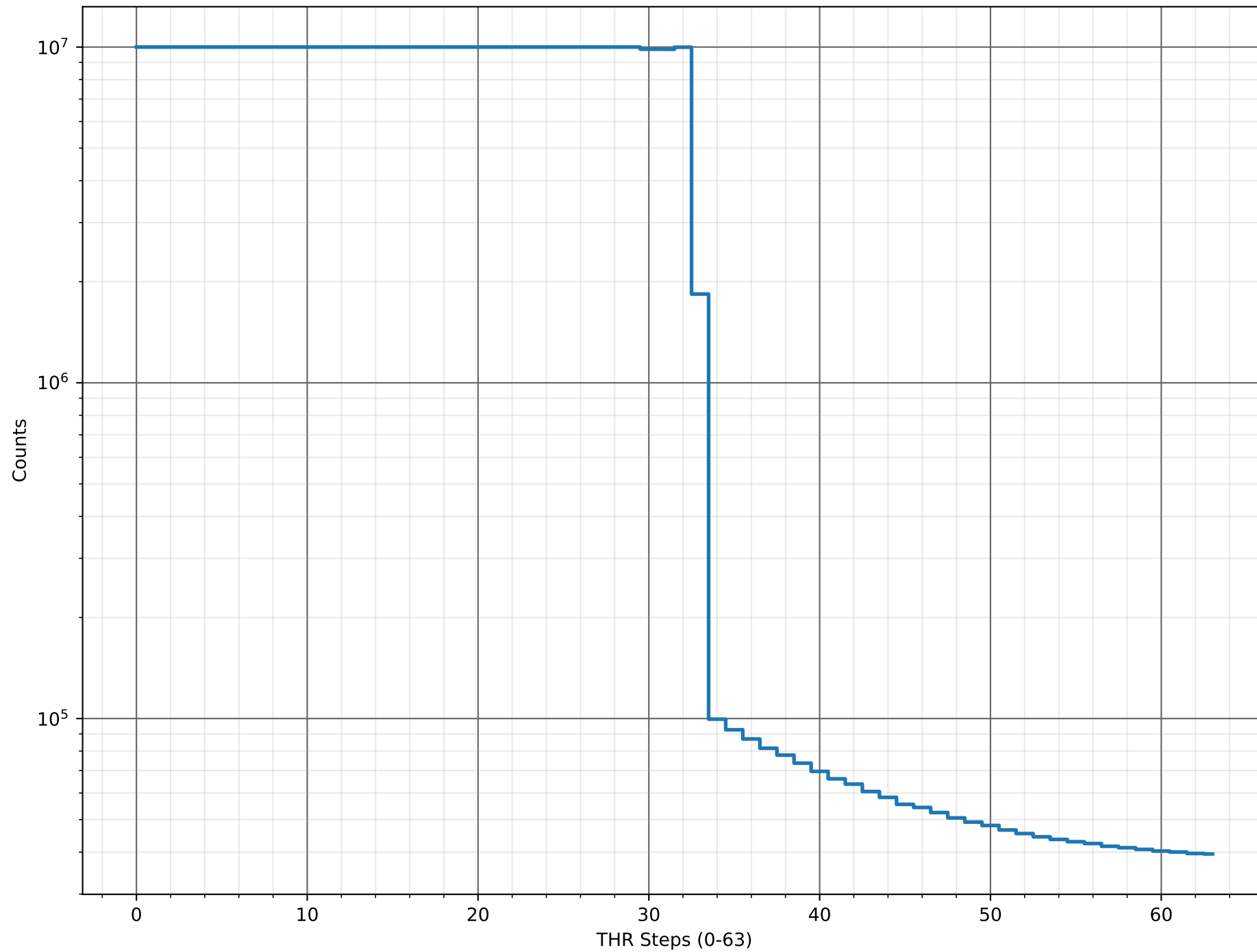
THR Distribution of PMT: C Anode: 29 (UART: 2 FEB: 0 CHIP: 3 CHANNEL: 1)



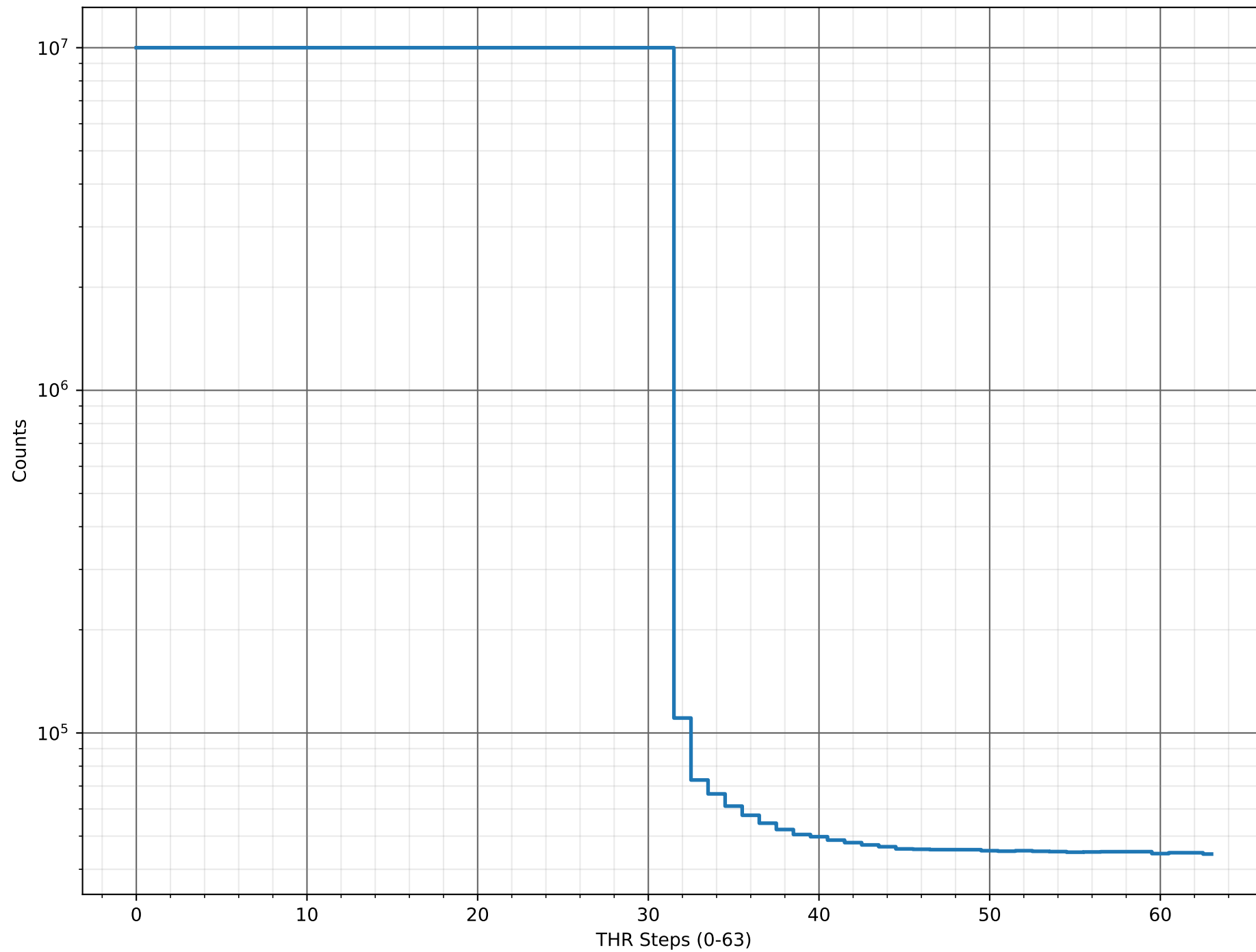
THR Distribution of PMT: C Anode: 30 (UART: 2 FEB: 0 CHIP: 3 CHANNEL: 0)



THR Distribution of PMT: C Anode: 31 (UART: 2 FEB: 0 CHIP: 2 CHANNEL: 7)

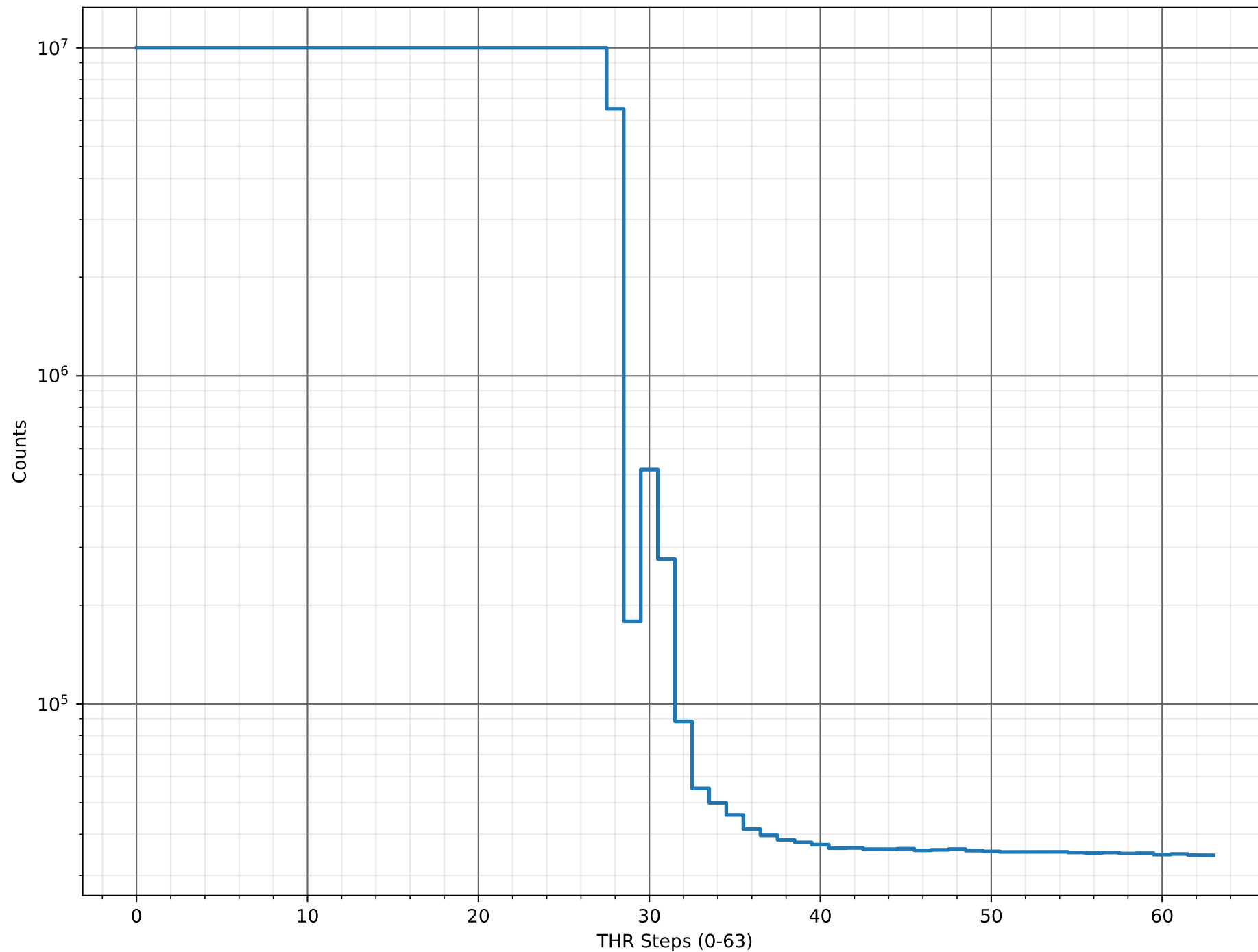


THR Distribution of PMT: C Anode: 32 (UART: 2 FEB: 0 CHIP: 2 CHANNEL: 6)

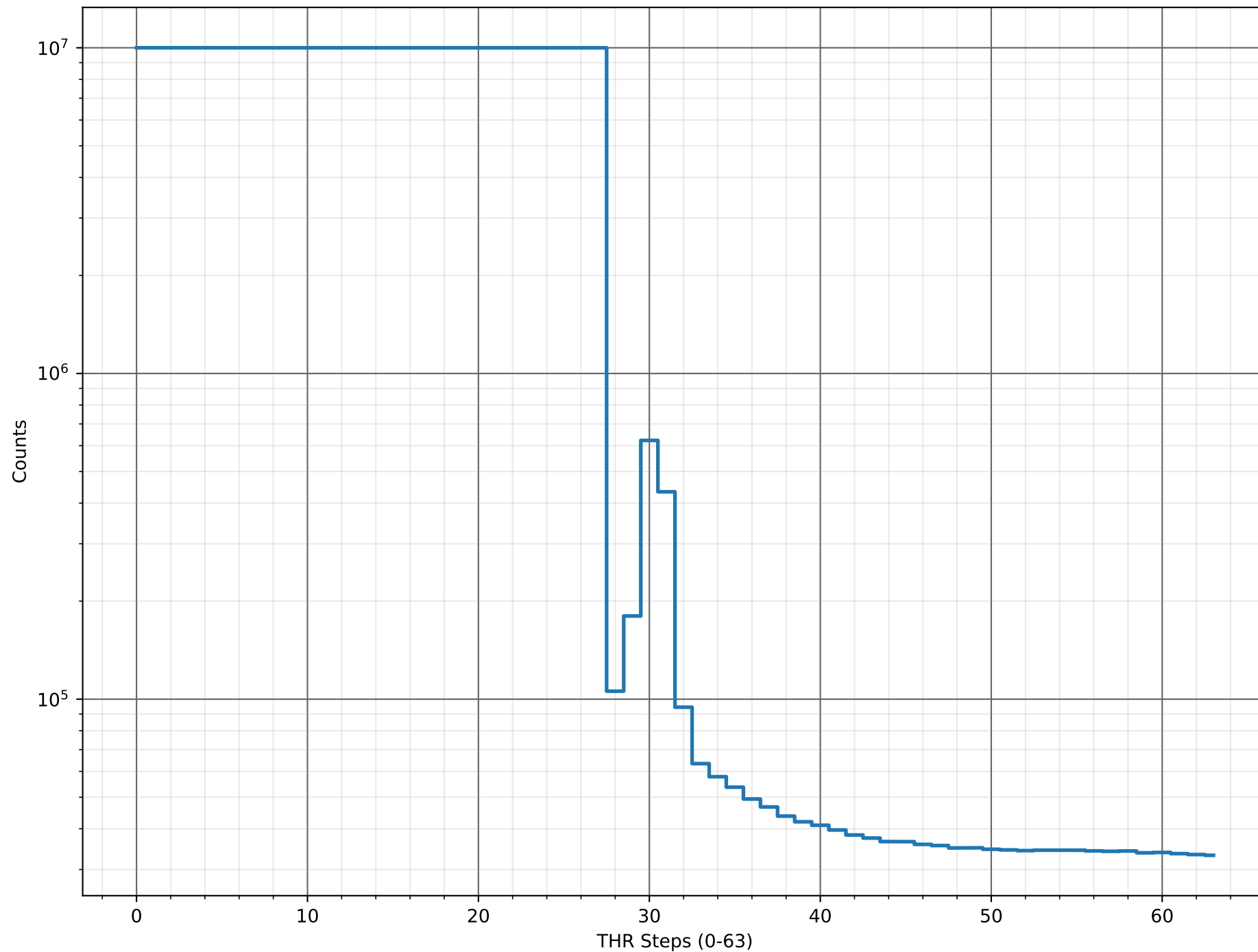




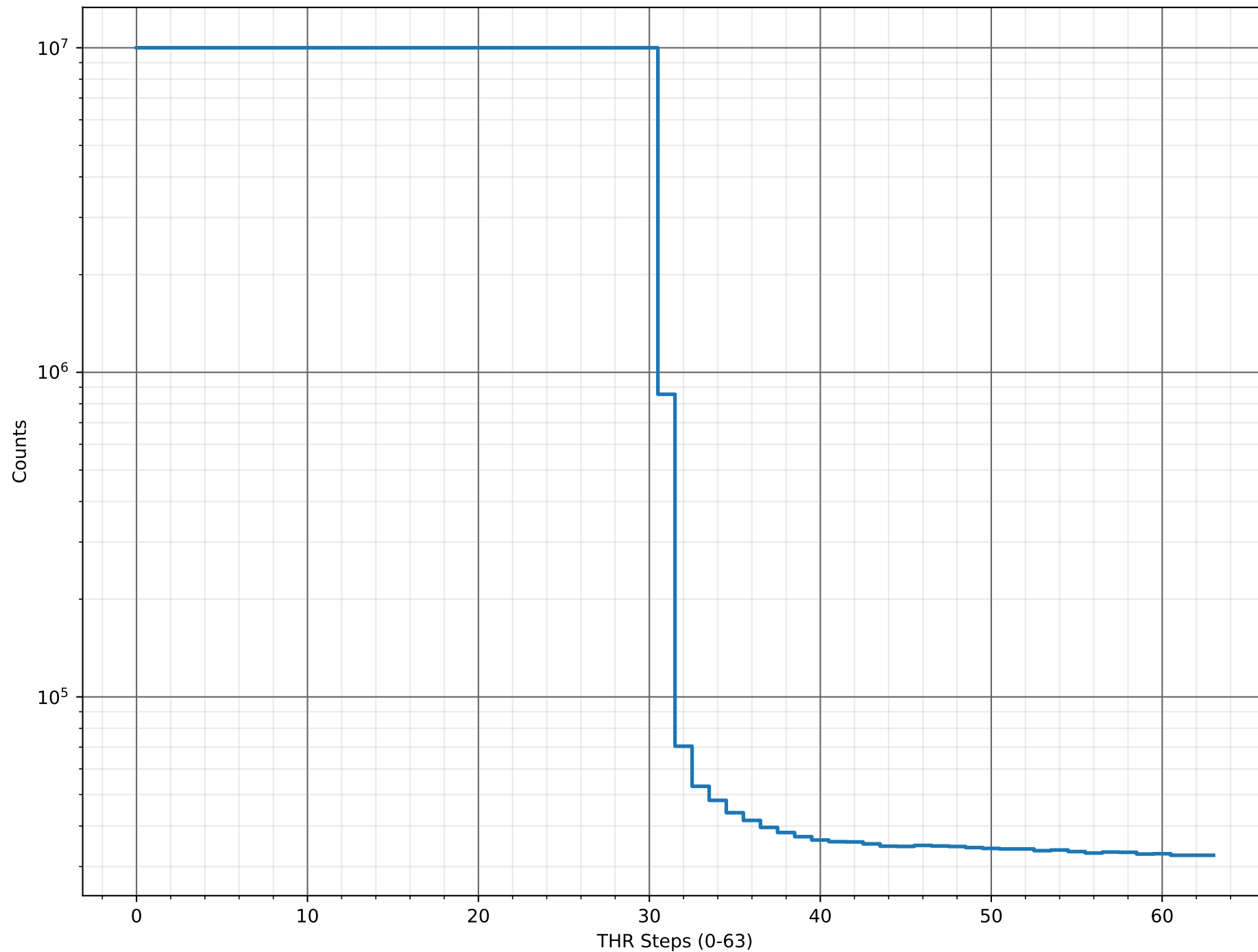
THR Distribution of PMT: C Anode: 33 (UART: 2 FEB: 1 CHIP: 1 CHANNEL: 6)



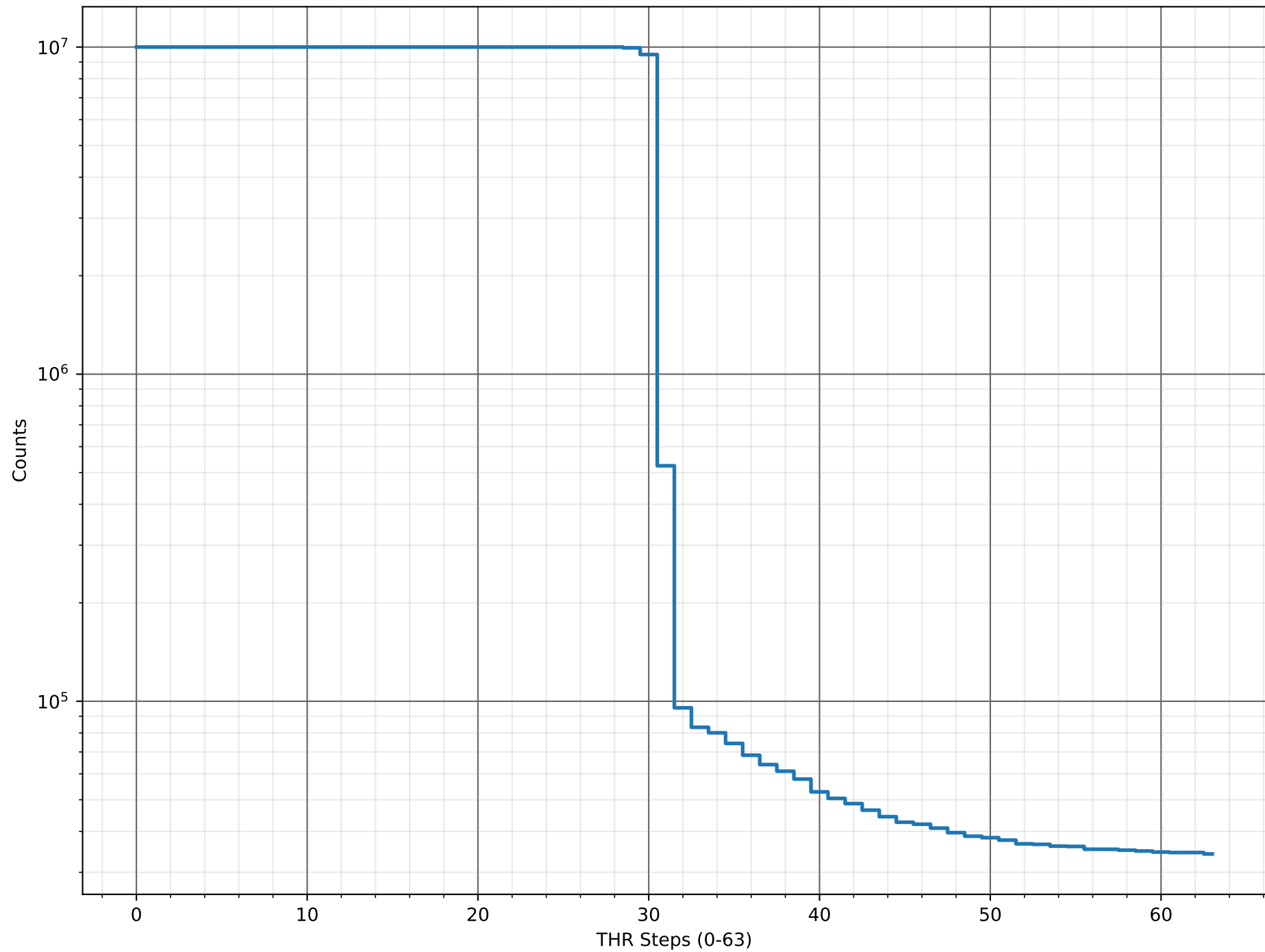
THR Distribution of PMT: C Anode: 34 (UART: 2 FEB: 1 CHIP: 1 CHANNEL: 7)



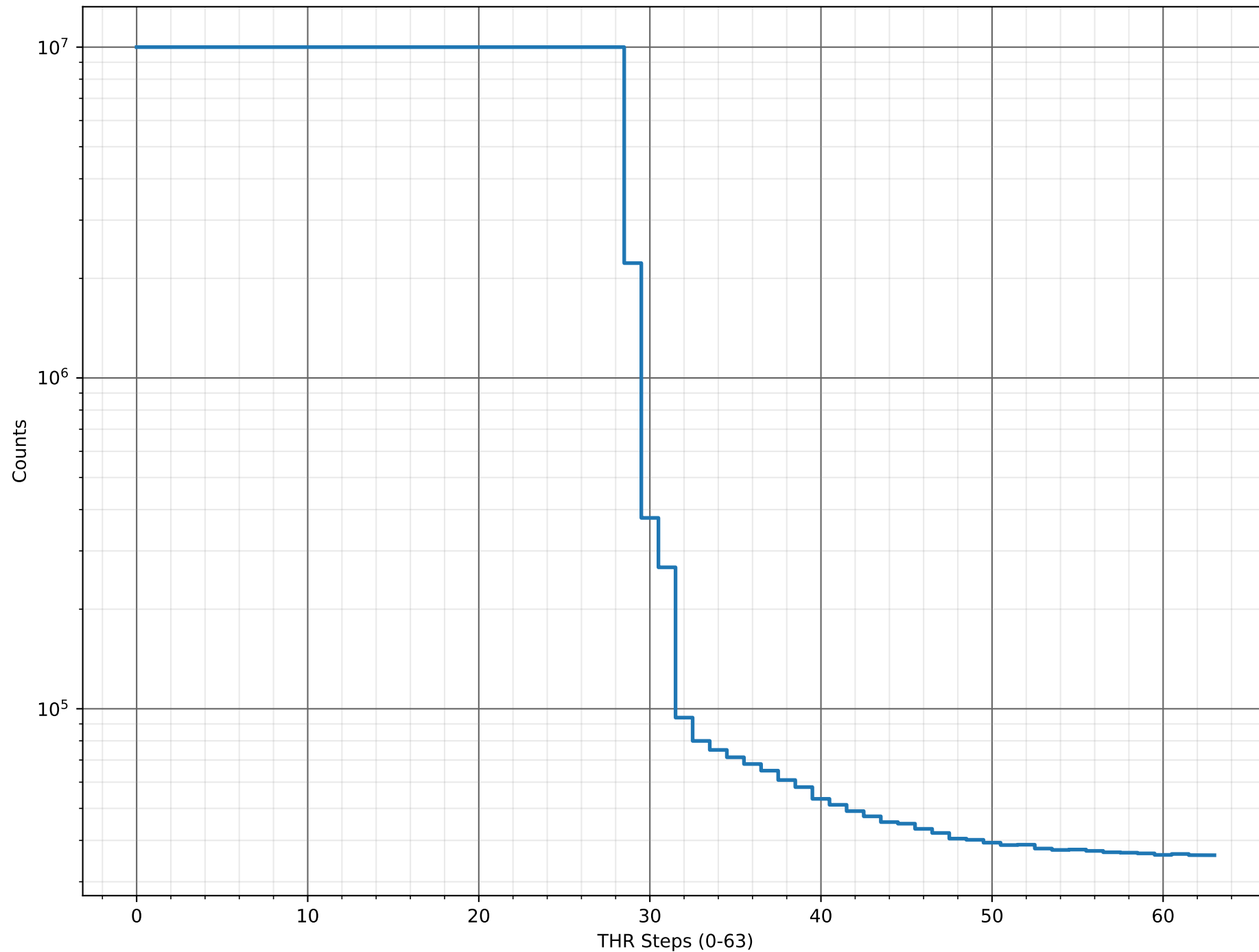
THR Distribution of PMT: C Anode: 35 (UART: 2 FEB: 1 CHIP: 0 CHANNEL: 1)



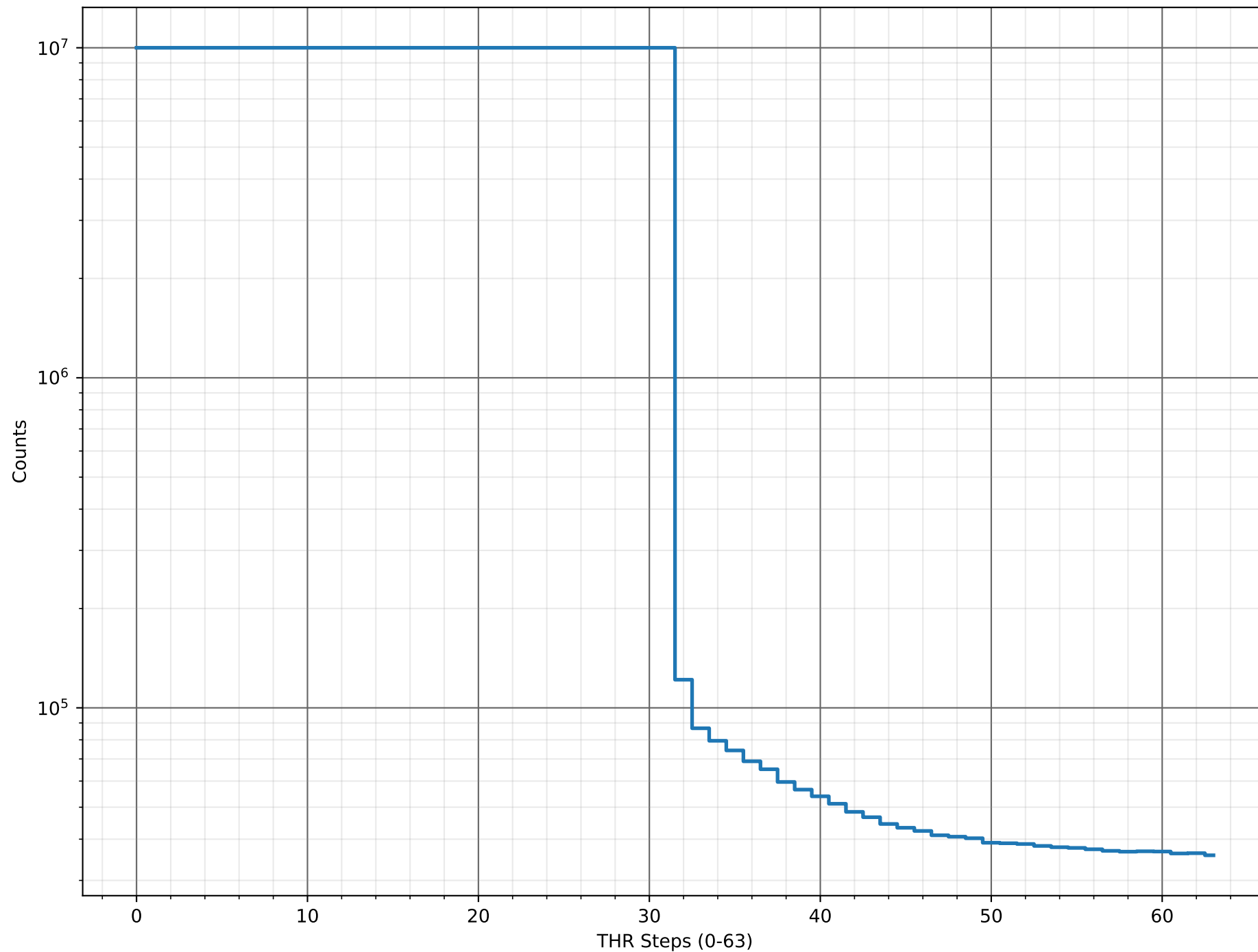
THR Distribution of PMT: C Anode: 36 (UART: 2 FEB: 1 CHIP: 0 CHANNEL: 0)



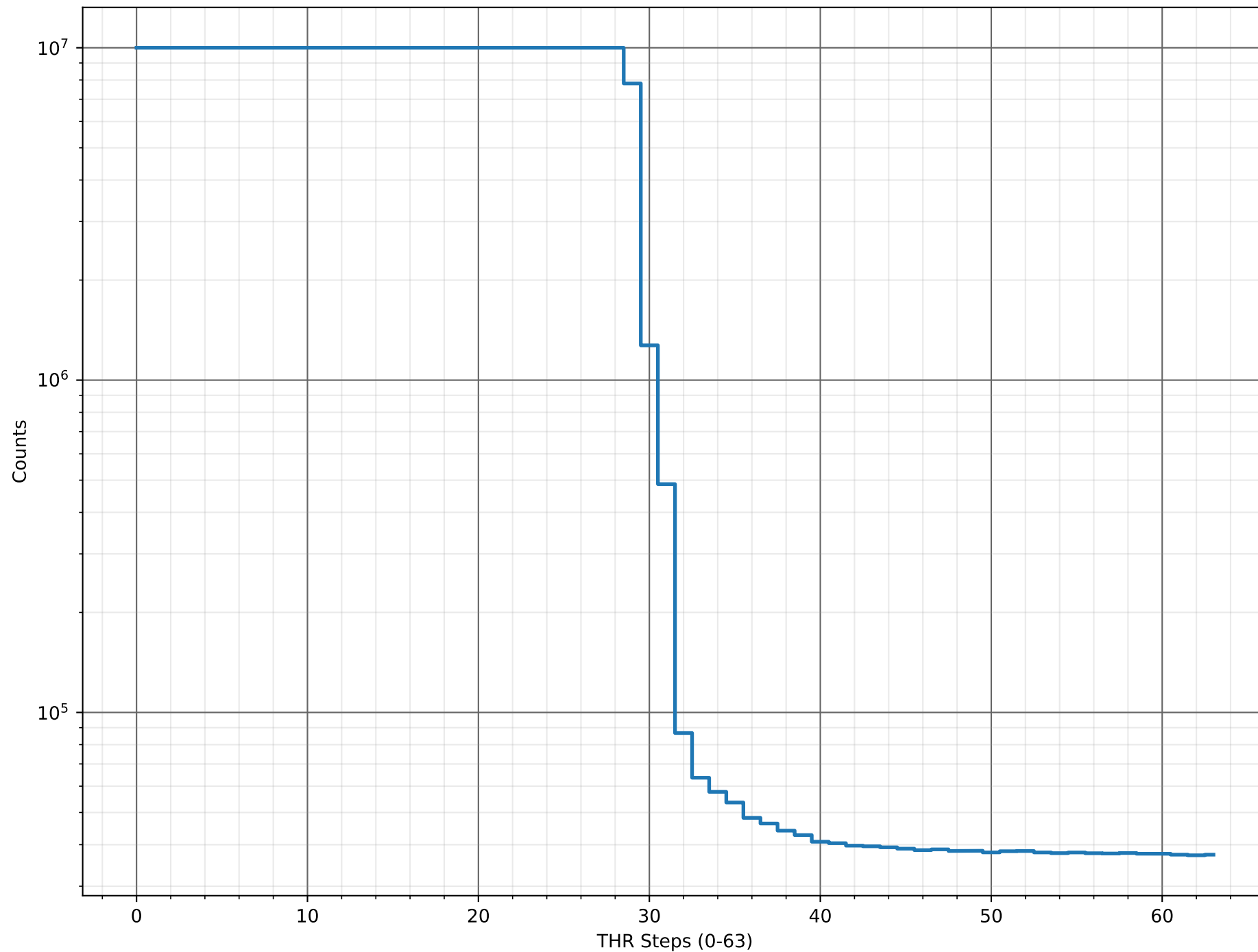
THR Distribution of PMT: C Anode: 37 (UART: 2 FEB: 0 CHIP: 1 CHANNEL: 7)



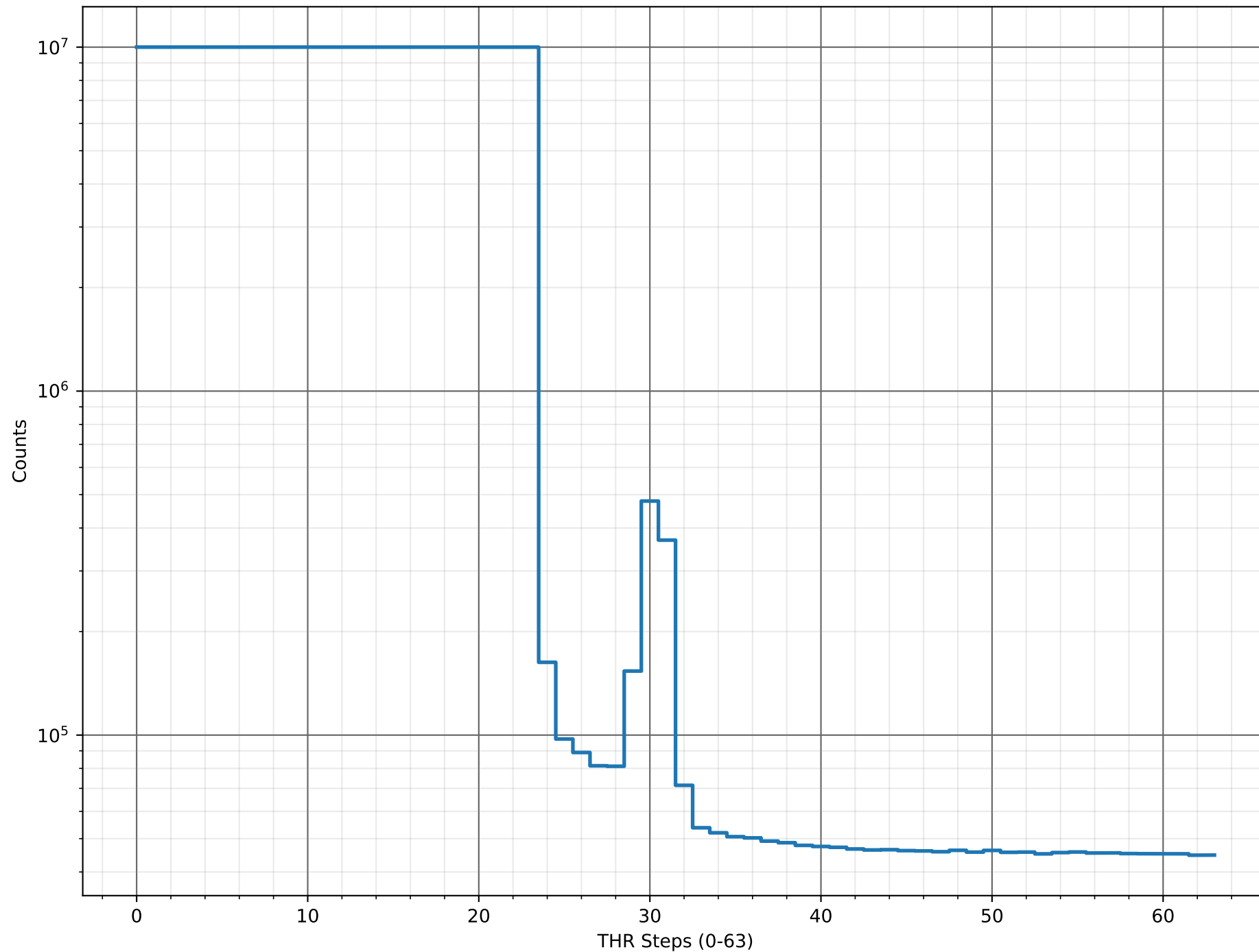
THR Distribution of PMT: C Anode: 38 (UART: 2 FEB: 0 CHIP: 1 CHANNEL: 6)



THR Distribution of PMT: C Anode: 39 (UART: 2 FEB: 0 CHIP: 0 CHANNEL: 0)

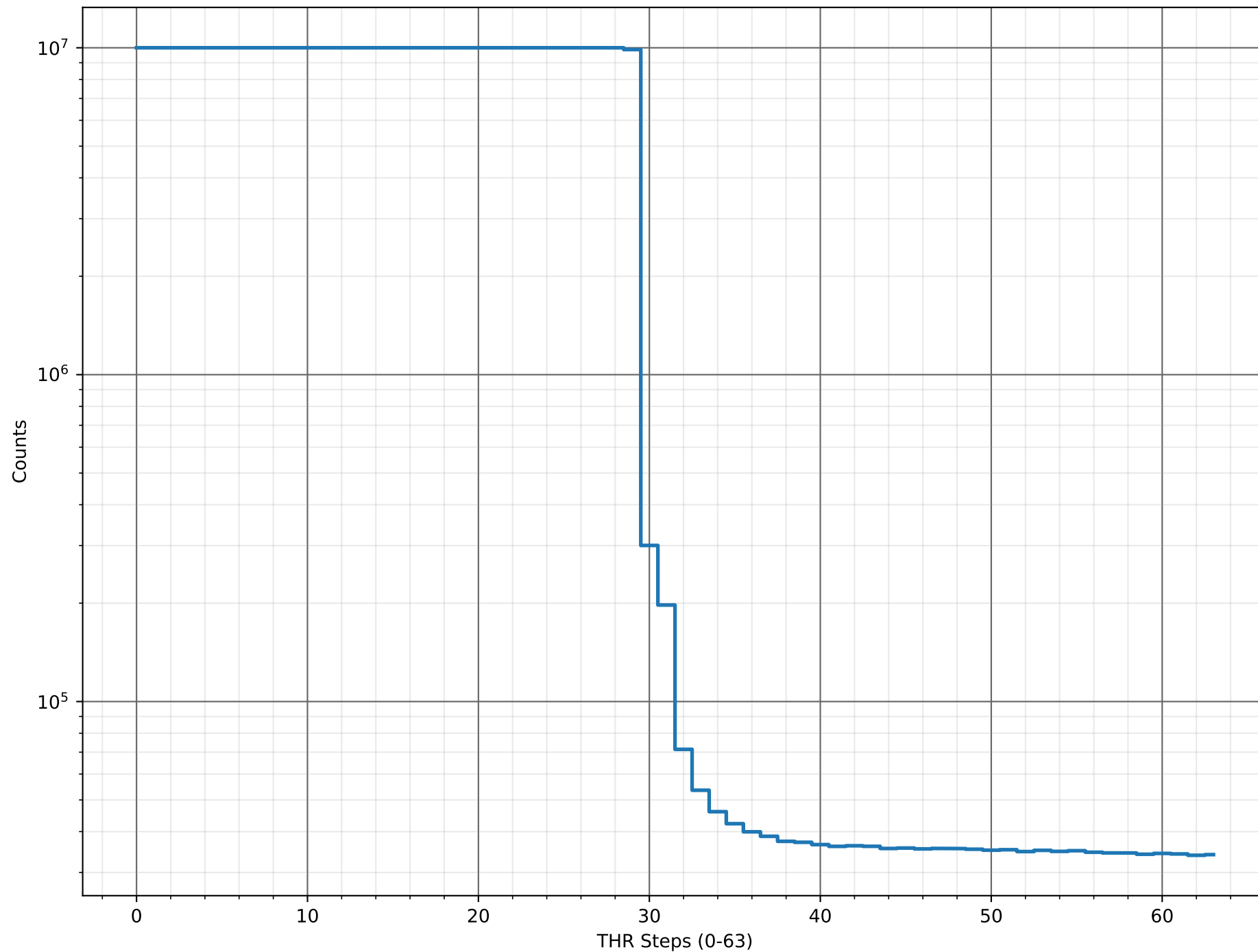


THR Distribution of PMT: C Anode: 40 (UART: 2 FEB: 0 CHIP: 0 CHANNEL: 1)

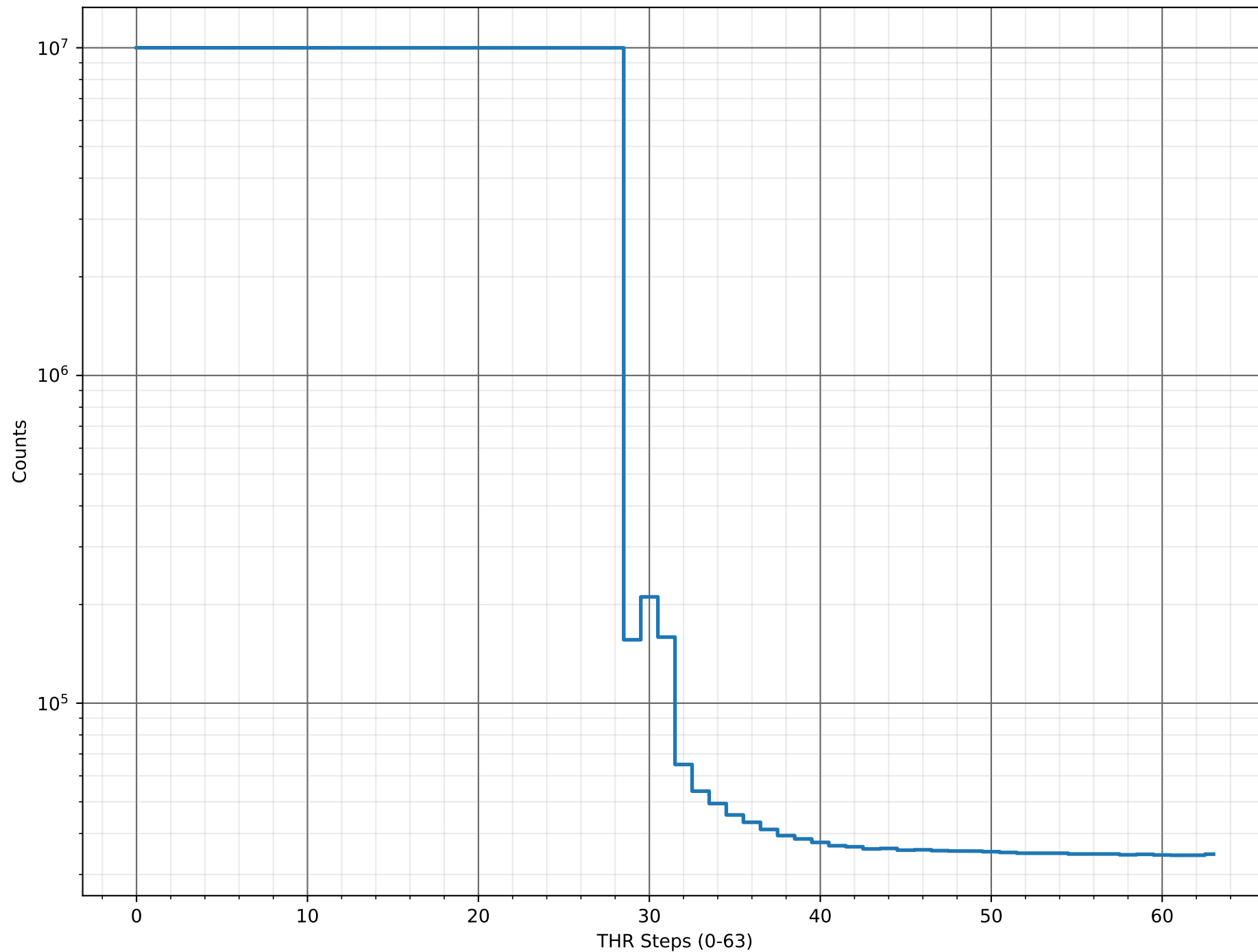




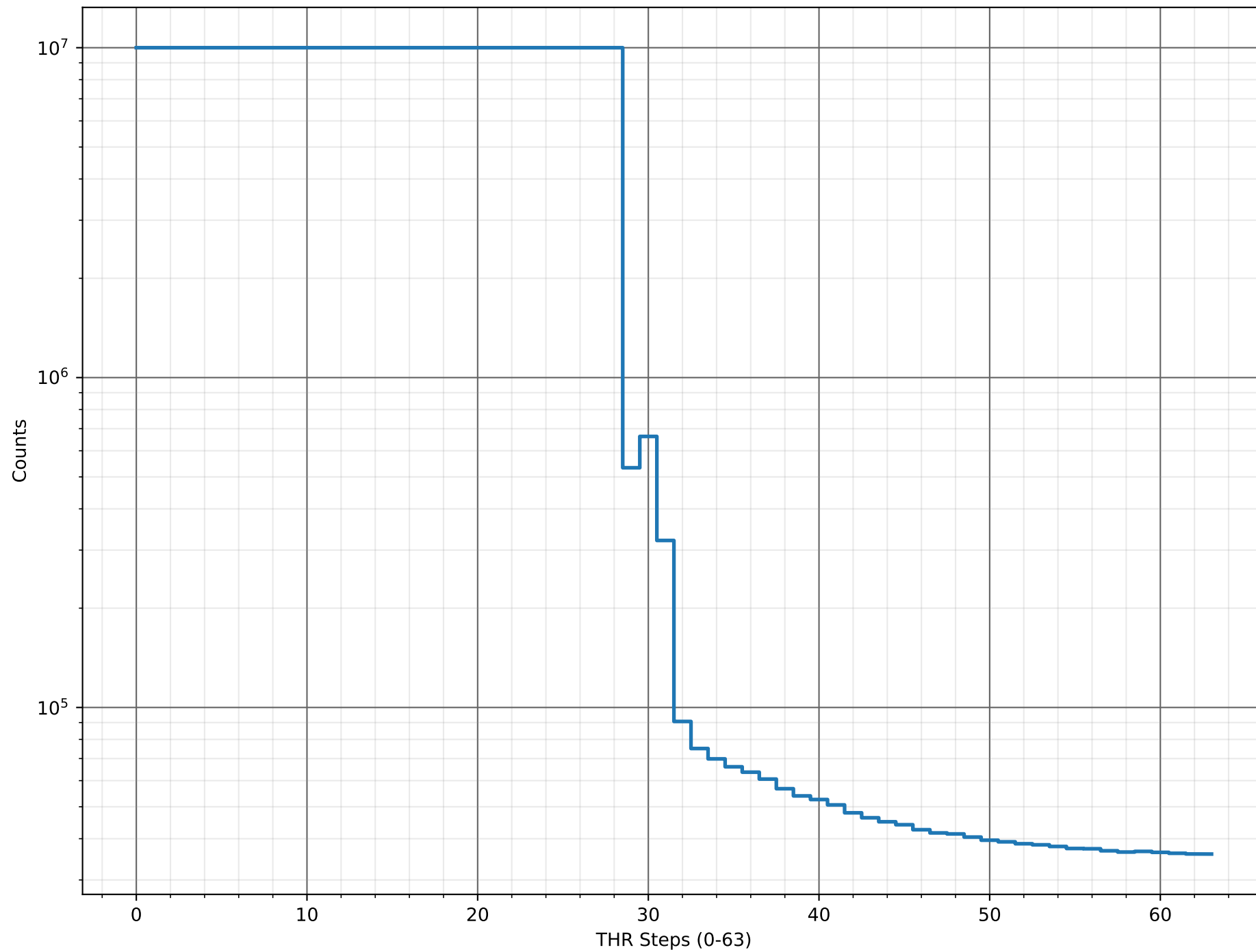
THR Distribution of PMT: C Anode: 41 (UART: 2 FEB: 1 CHIP: 1 CHANNEL: 4)



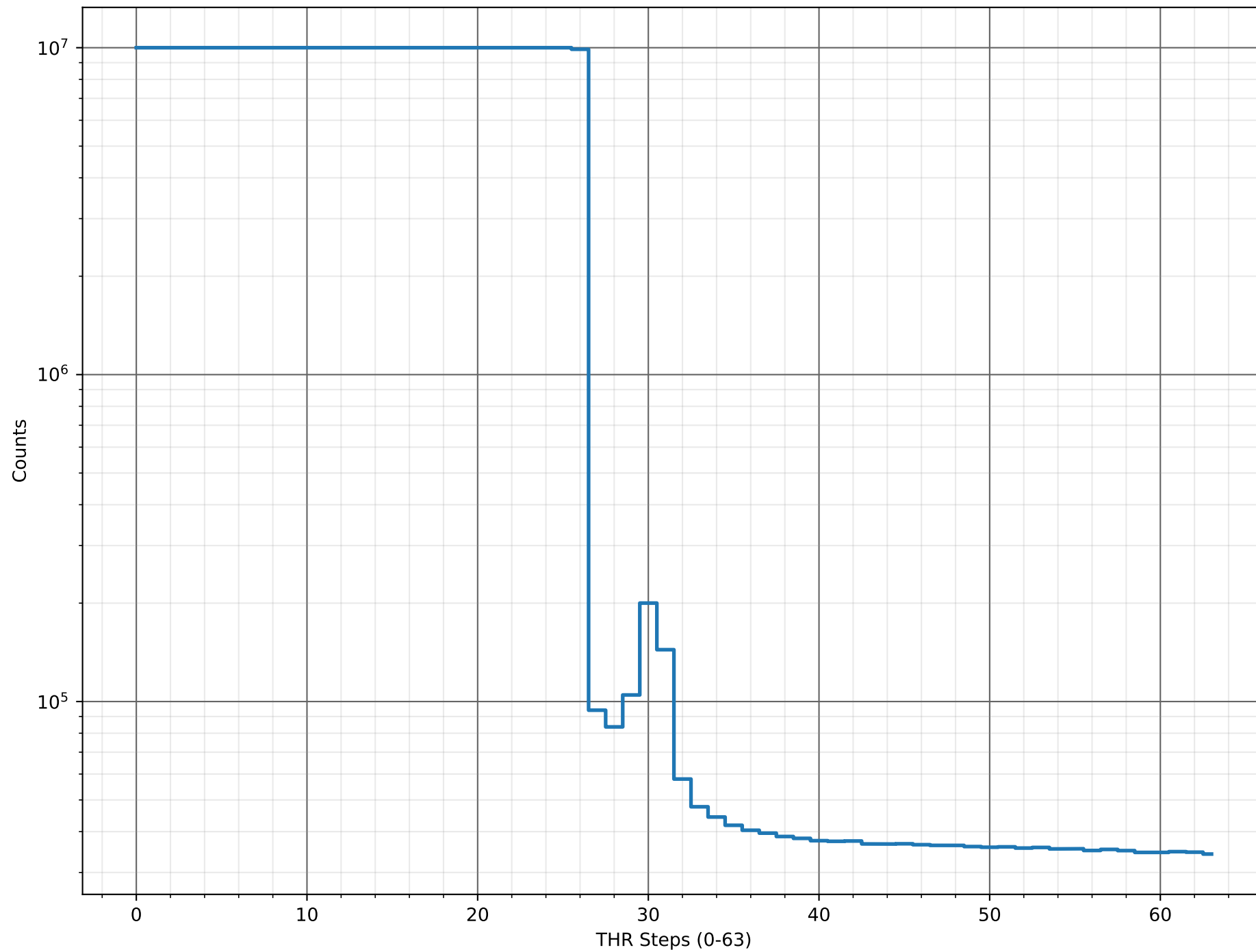
THR Distribution of PMT: C Anode: 42 (UART: 2 FEB: 1 CHIP: 1 CHANNEL: 5)



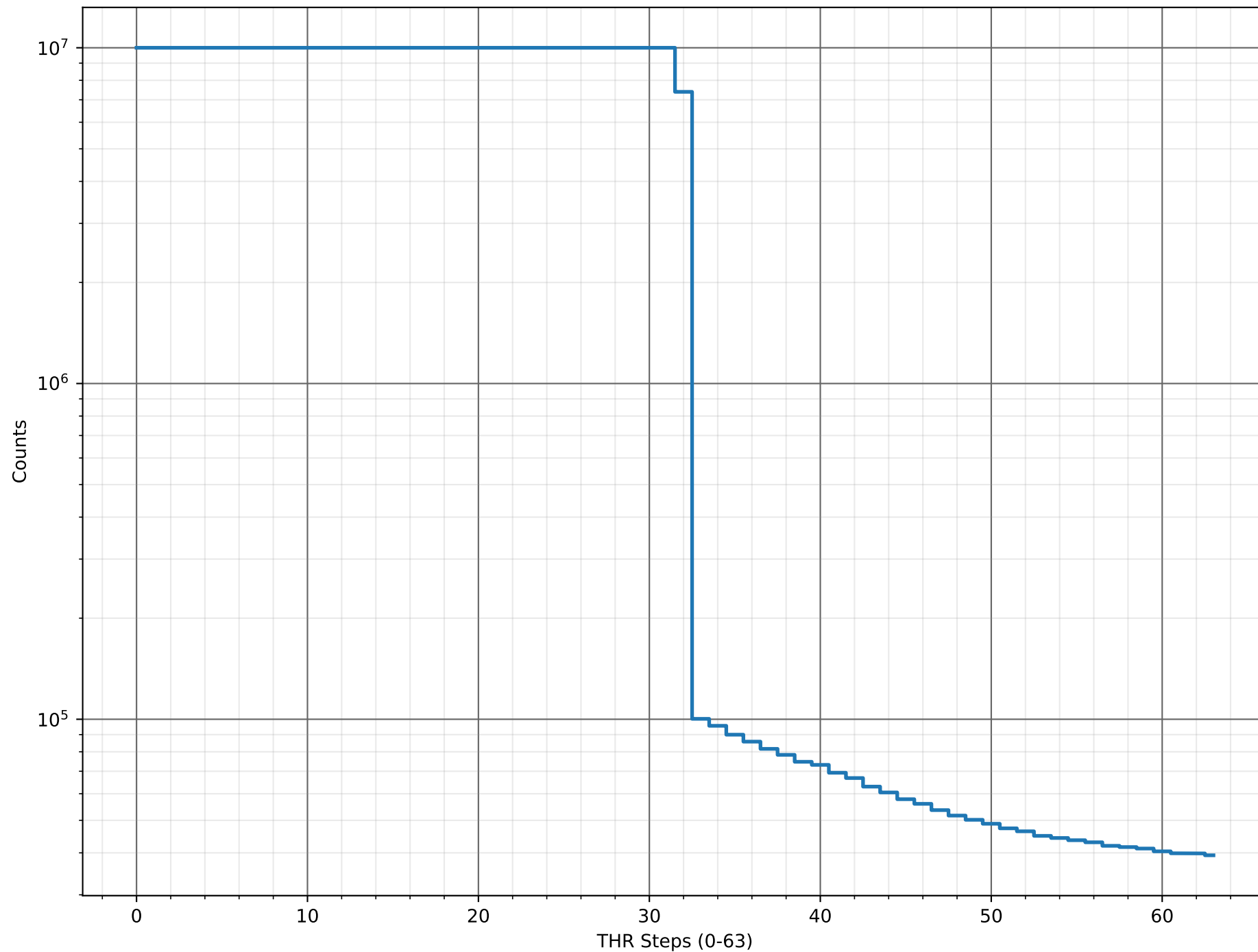
THR Distribution of PMT: C Anode: 43 (UART: 2 FEB: 1 CHIP: 0 CHANNEL: 3)



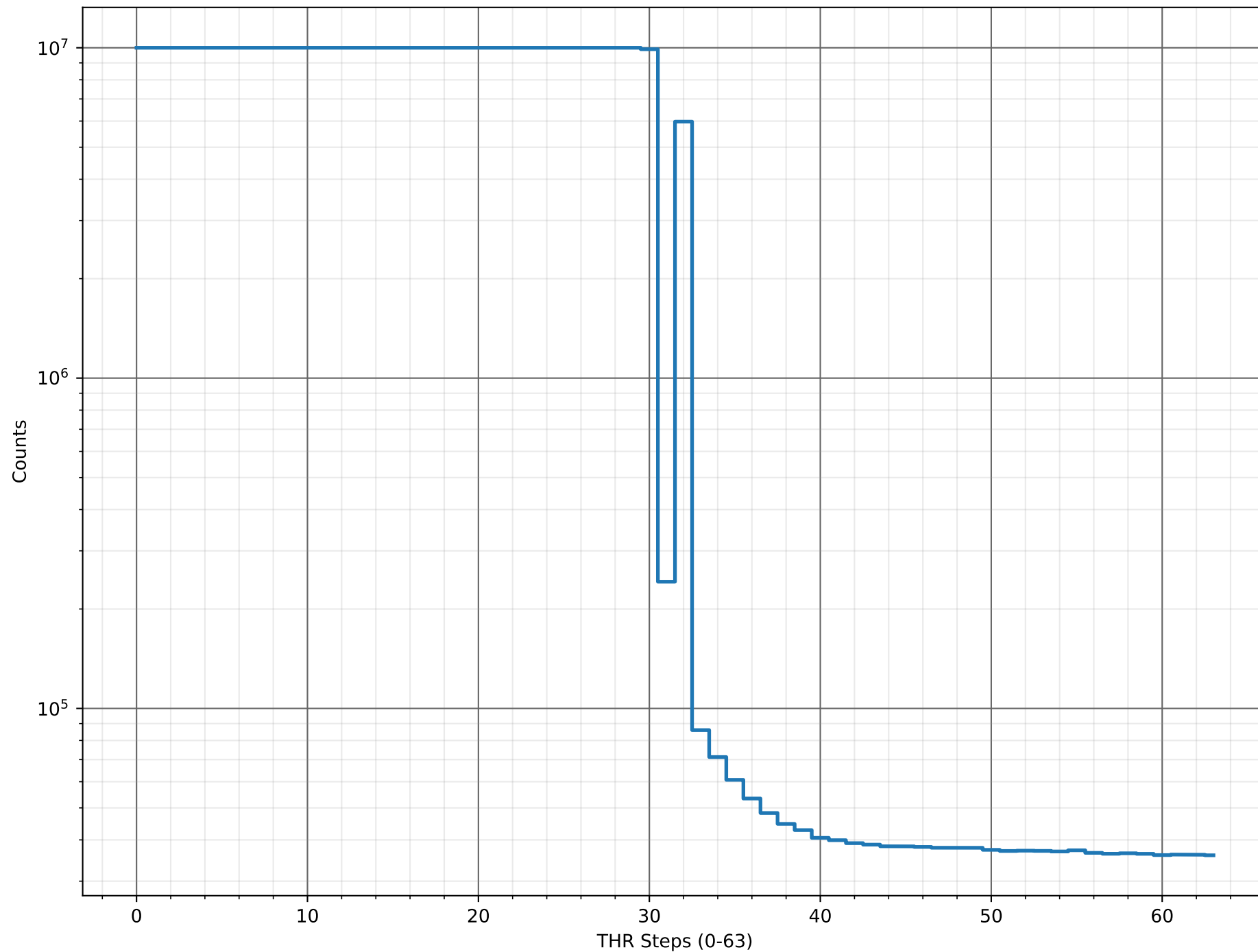
THR Distribution of PMT: C Anode: 44 (UART: 2 FEB: 1 CHIP: 0 CHANNEL: 2)



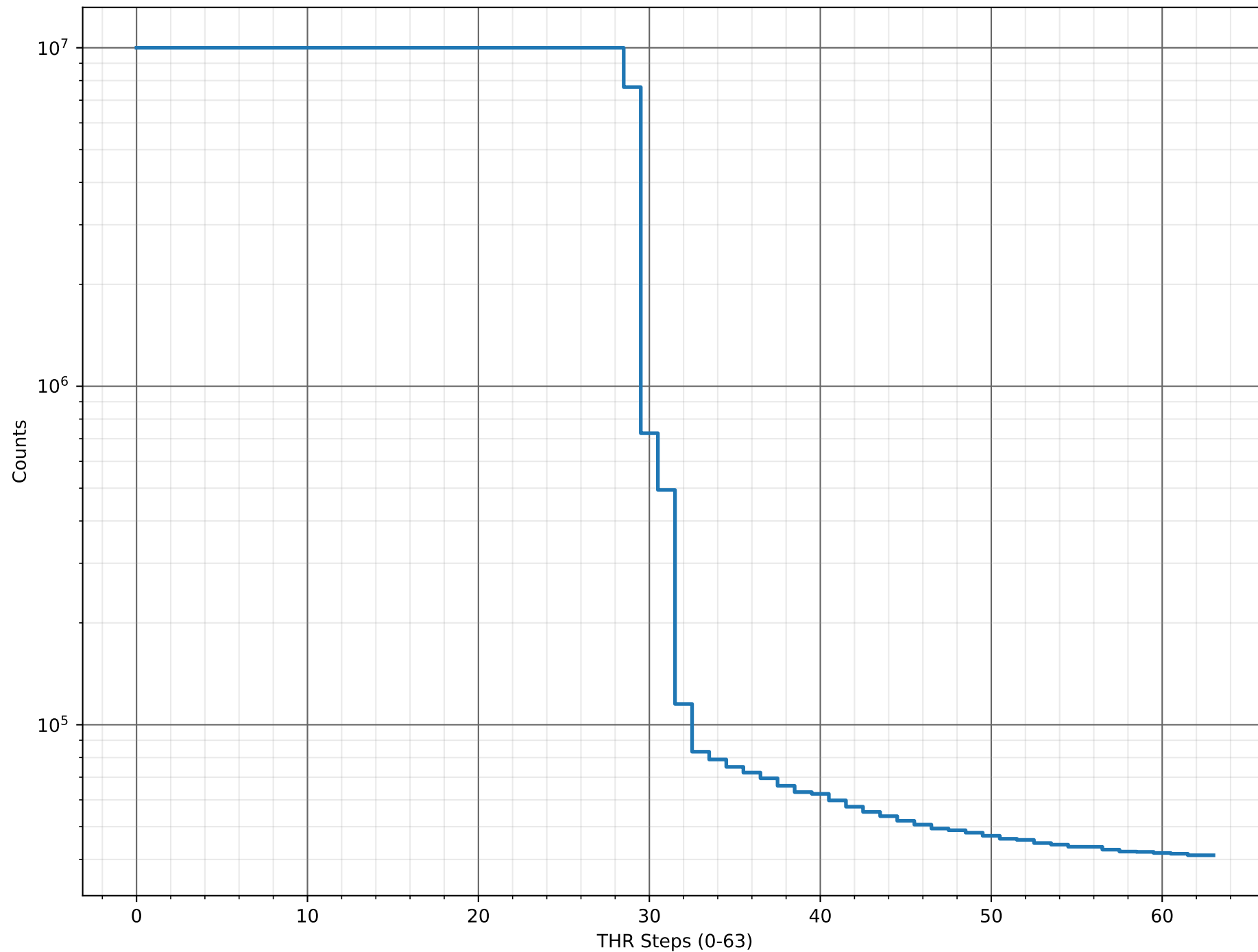
THR Distribution of PMT: C Anode: 45 (UART: 2 FEB: 0 CHIP: 1 CHANNEL: 5)



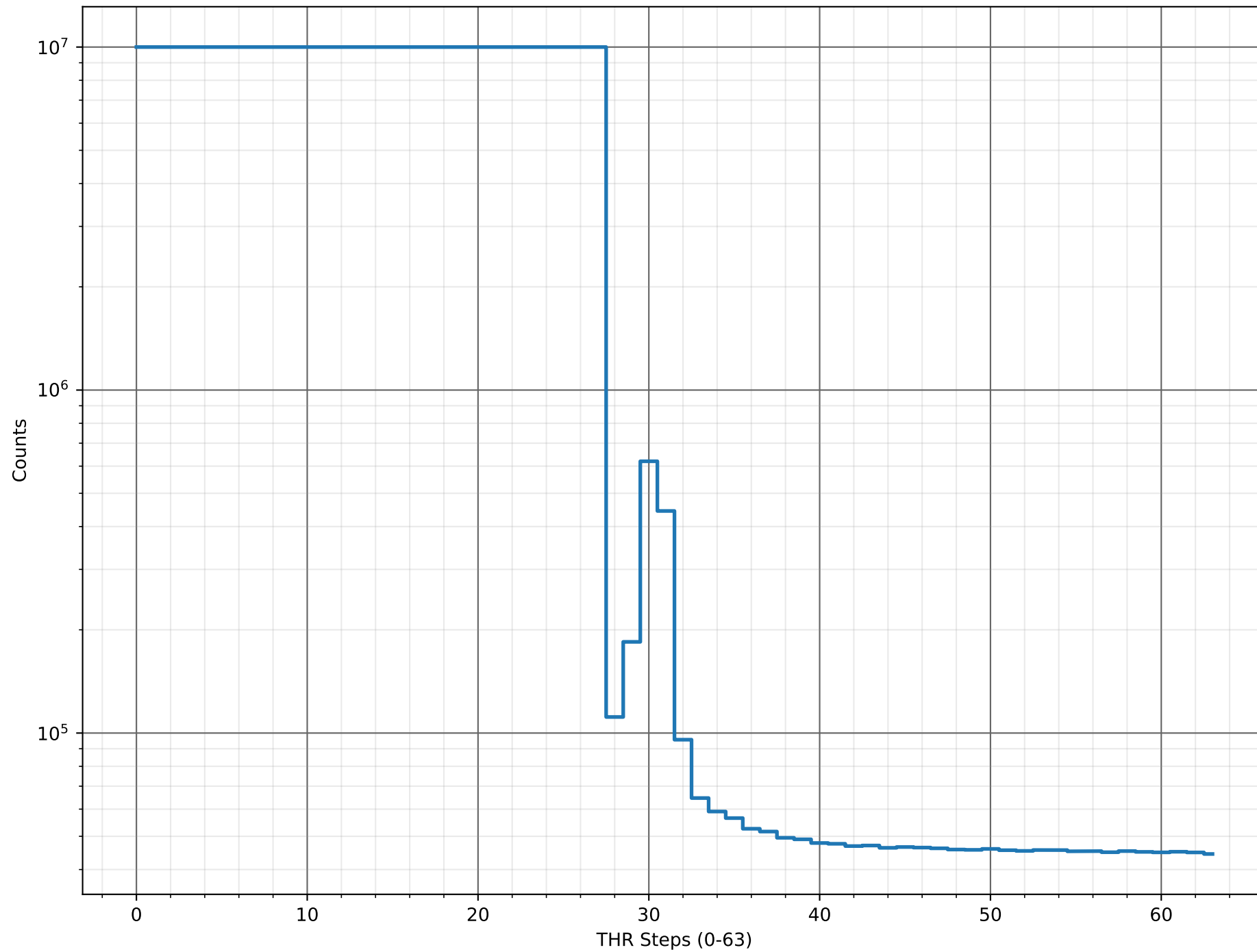
THR Distribution of PMT: C Anode: 46 (UART: 2 FEB: 0 CHIP: 1 CHANNEL: 4)



THR Distribution of PMT: C Anode: 47 (UART: 2 FEB: 0 CHIP: 0 CHANNEL: 2)

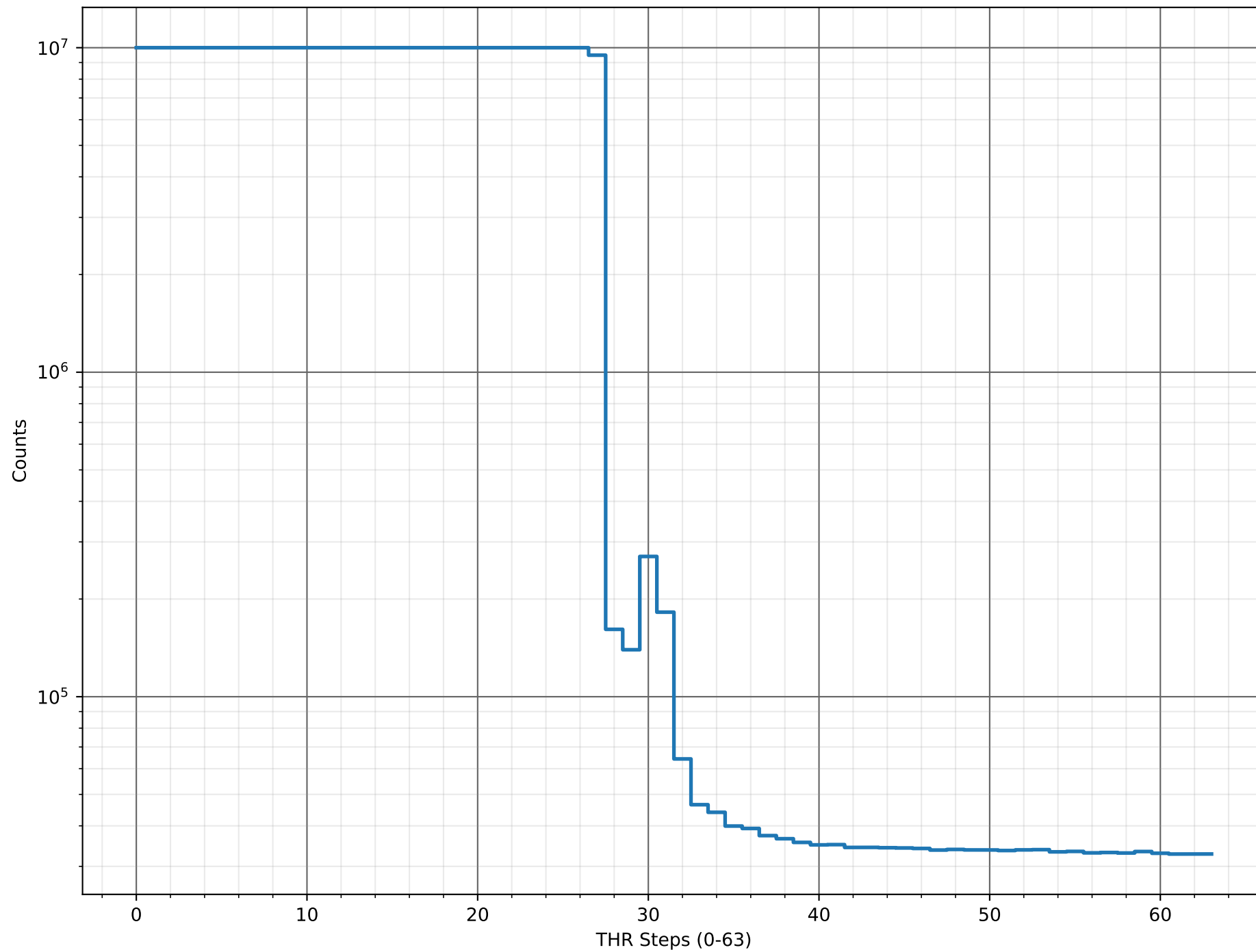


THR Distribution of PMT: C Anode: 48 (UART: 2 FEB: 0 CHIP: 0 CHANNEL: 3)

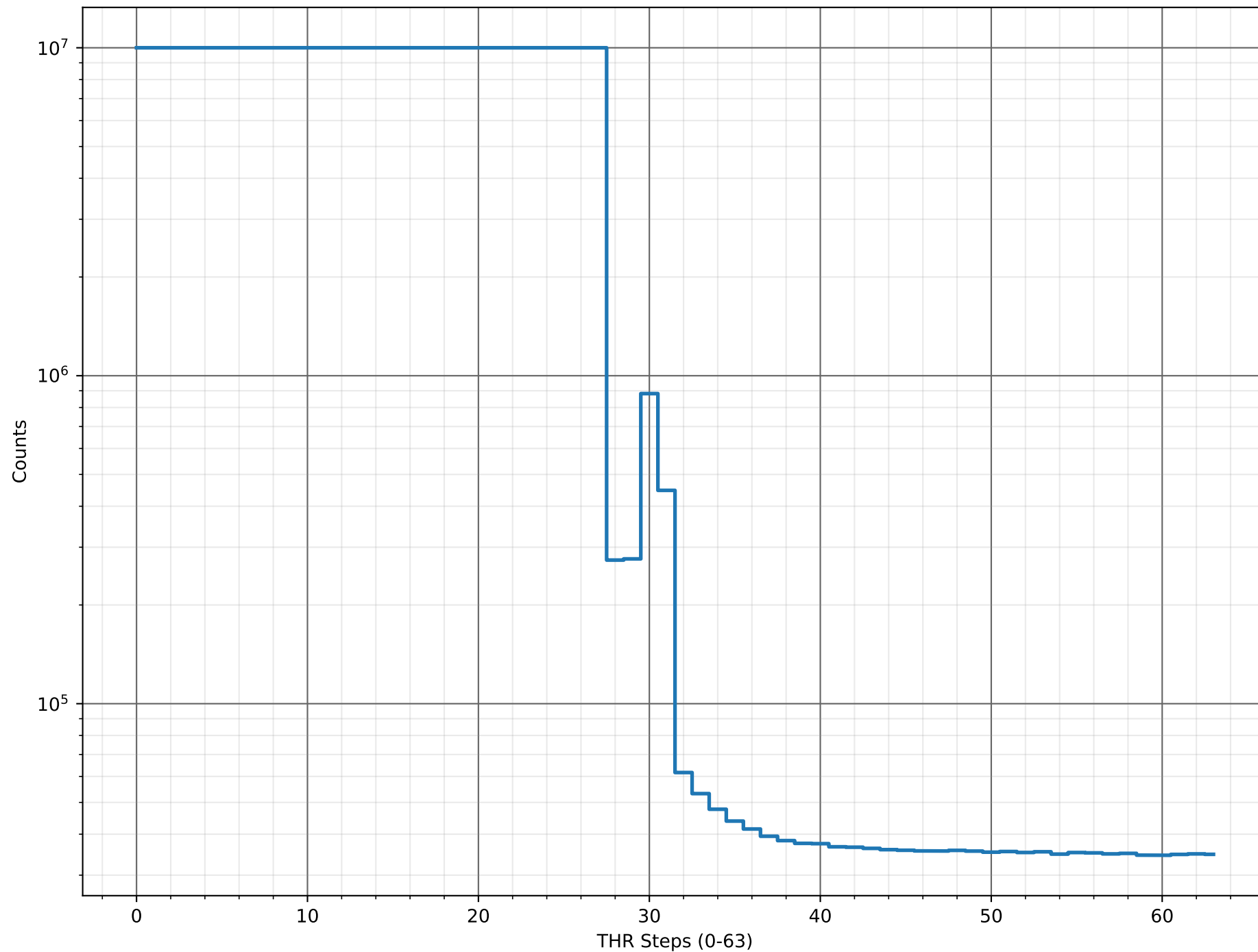




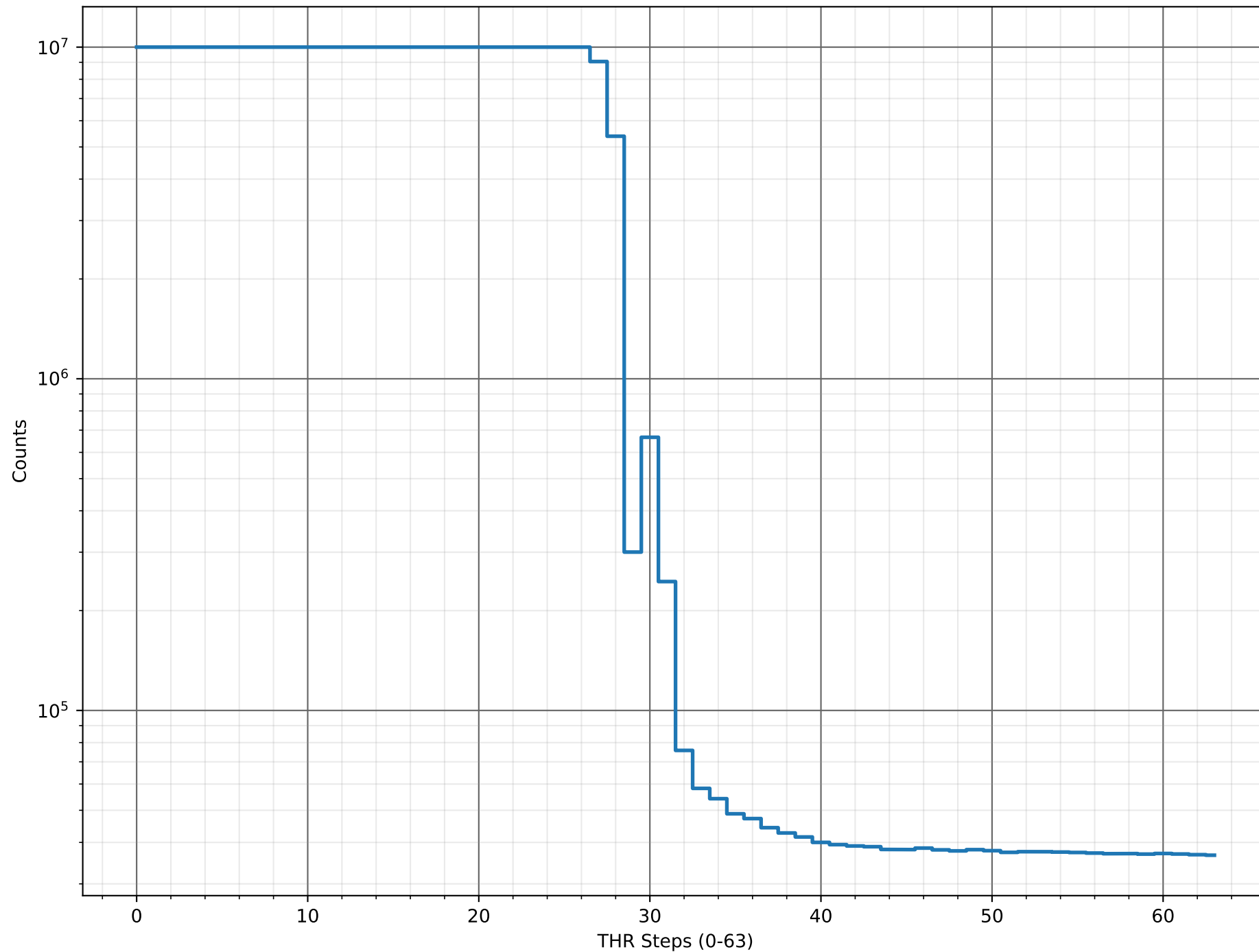
THR Distribution of PMT: C Anode: 49 (UART: 2 FEB: 1 CHIP: 1 CHANNEL: 2)



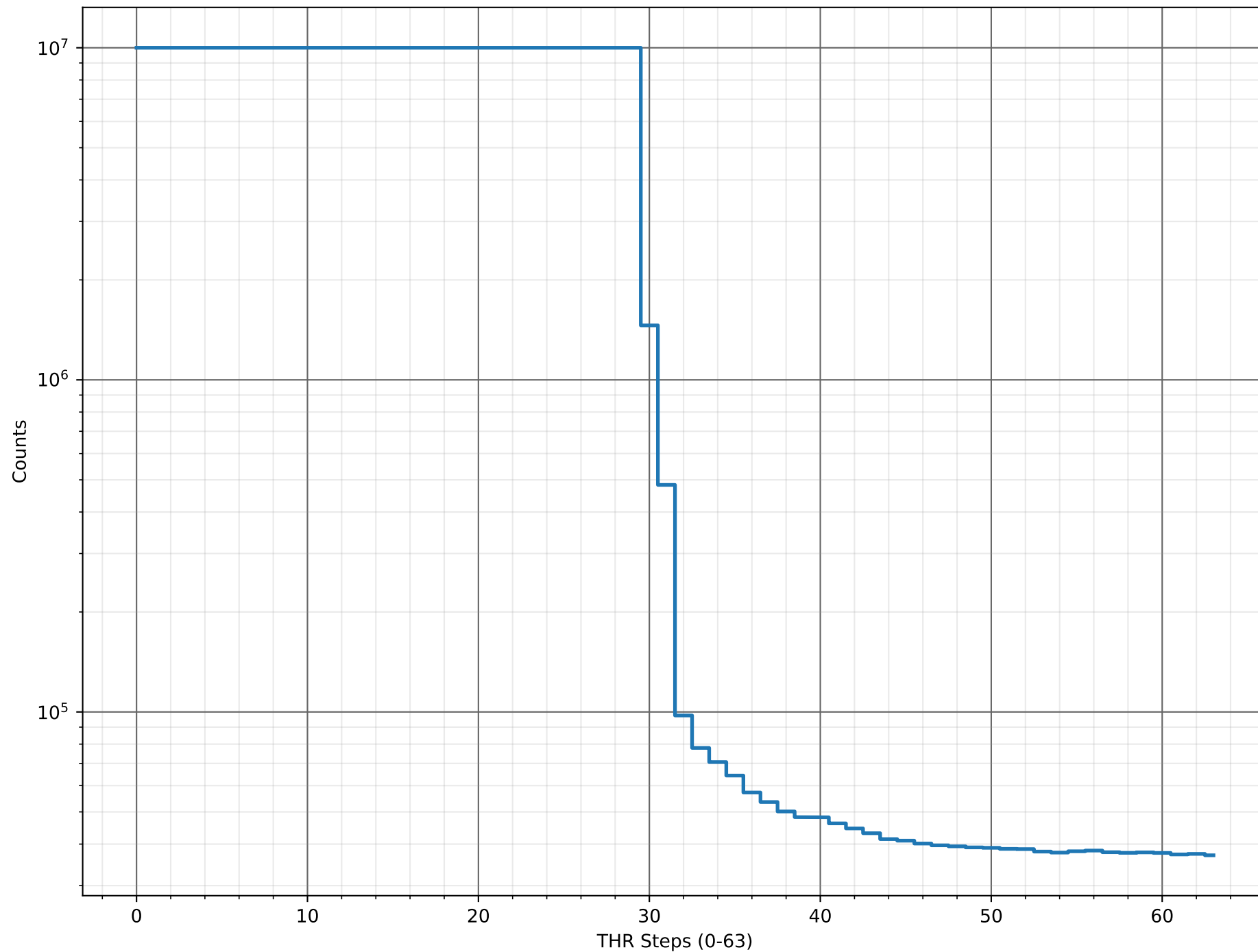
THR Distribution of PMT: C Anode: 50 (UART: 2 FEB: 1 CHIP: 1 CHANNEL: 3)



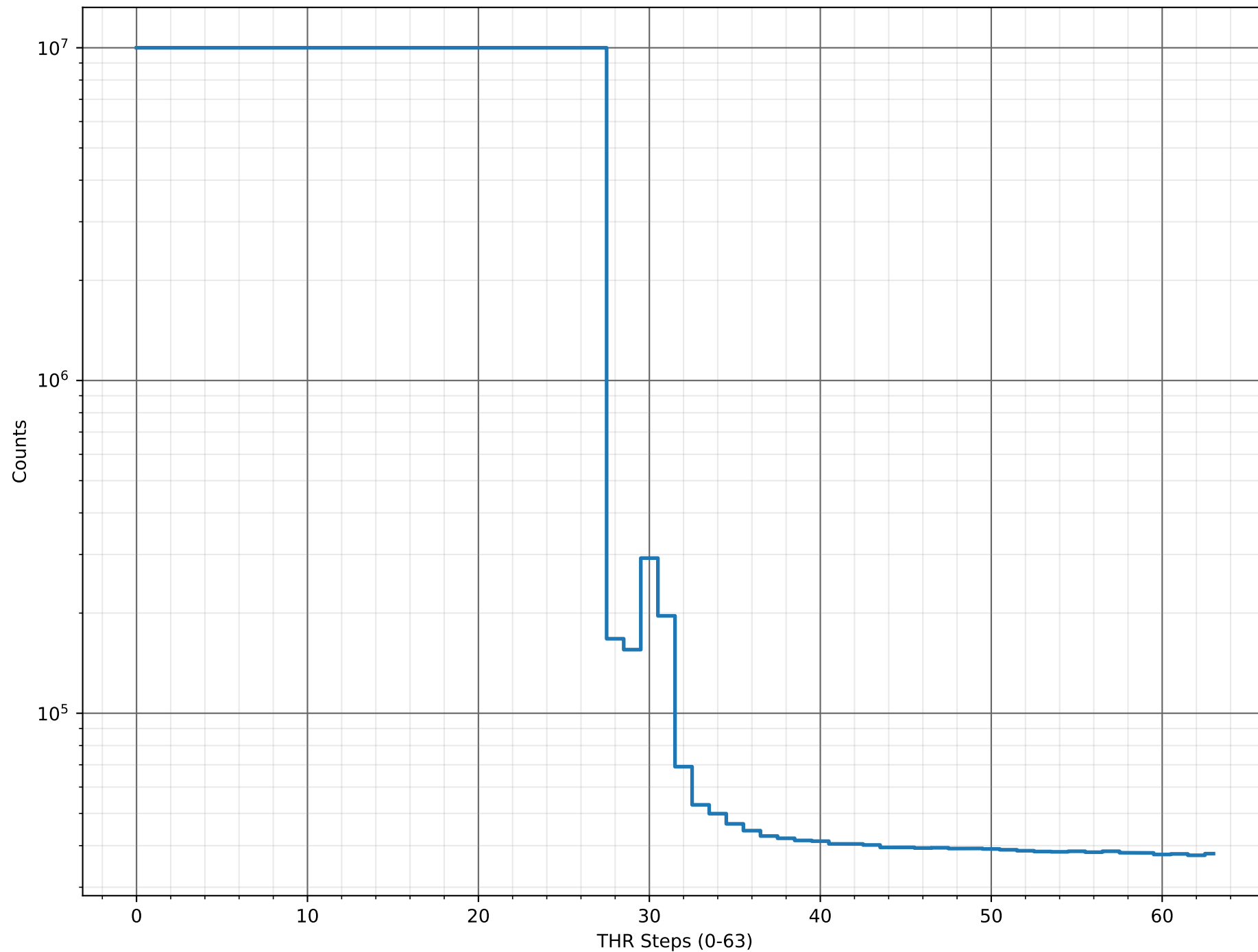
THR Distribution of PMT: C Anode: 51 (UART: 2 FEB: 1 CHIP: 0 CHANNEL: 5)



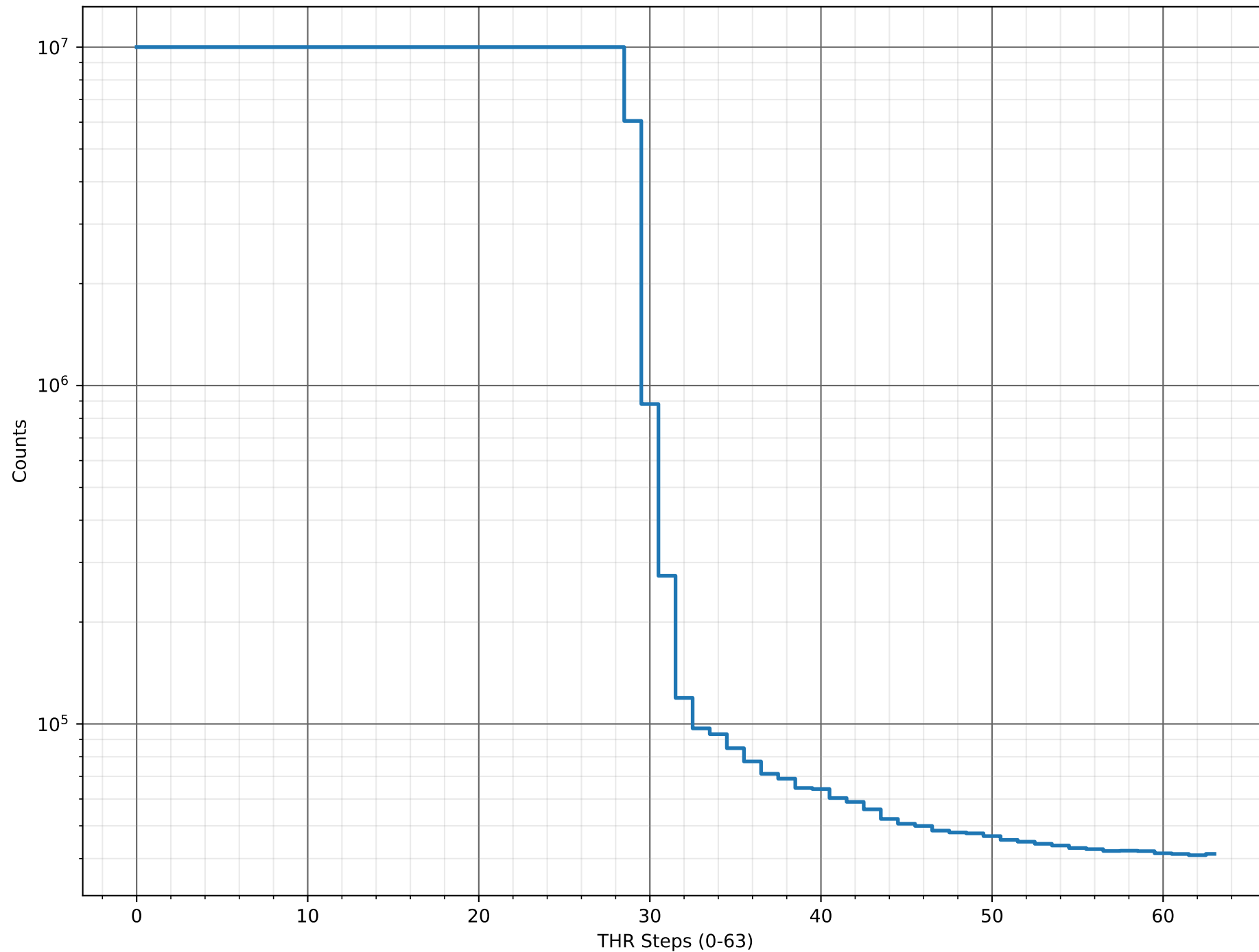
THR Distribution of PMT: C Anode: 52 (UART: 2 FEB: 1 CHIP: 0 CHANNEL: 4)



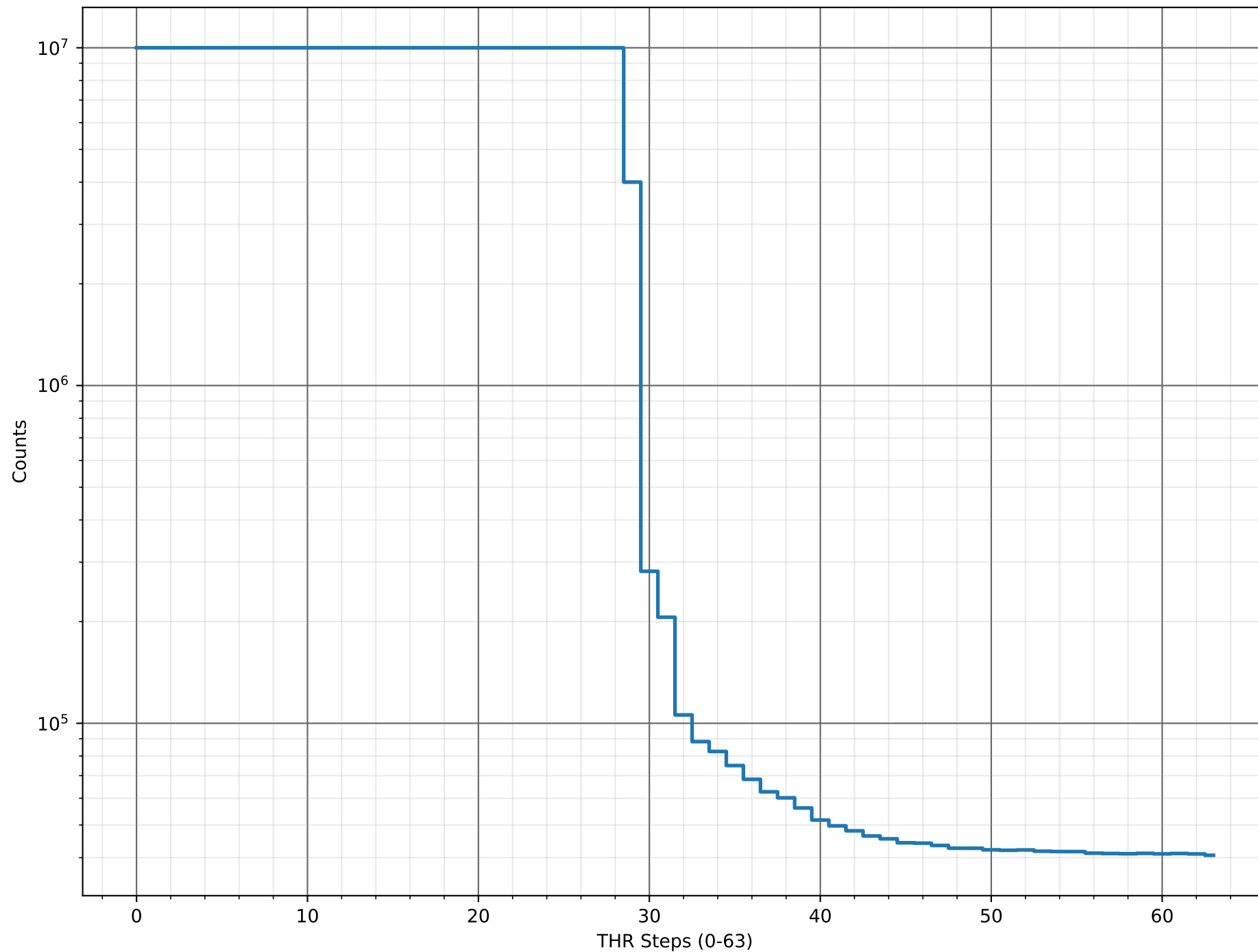
THR Distribution of PMT: C Anode: 53 (UART: 2 FEB: 0 CHIP: 1 CHANNEL: 3)



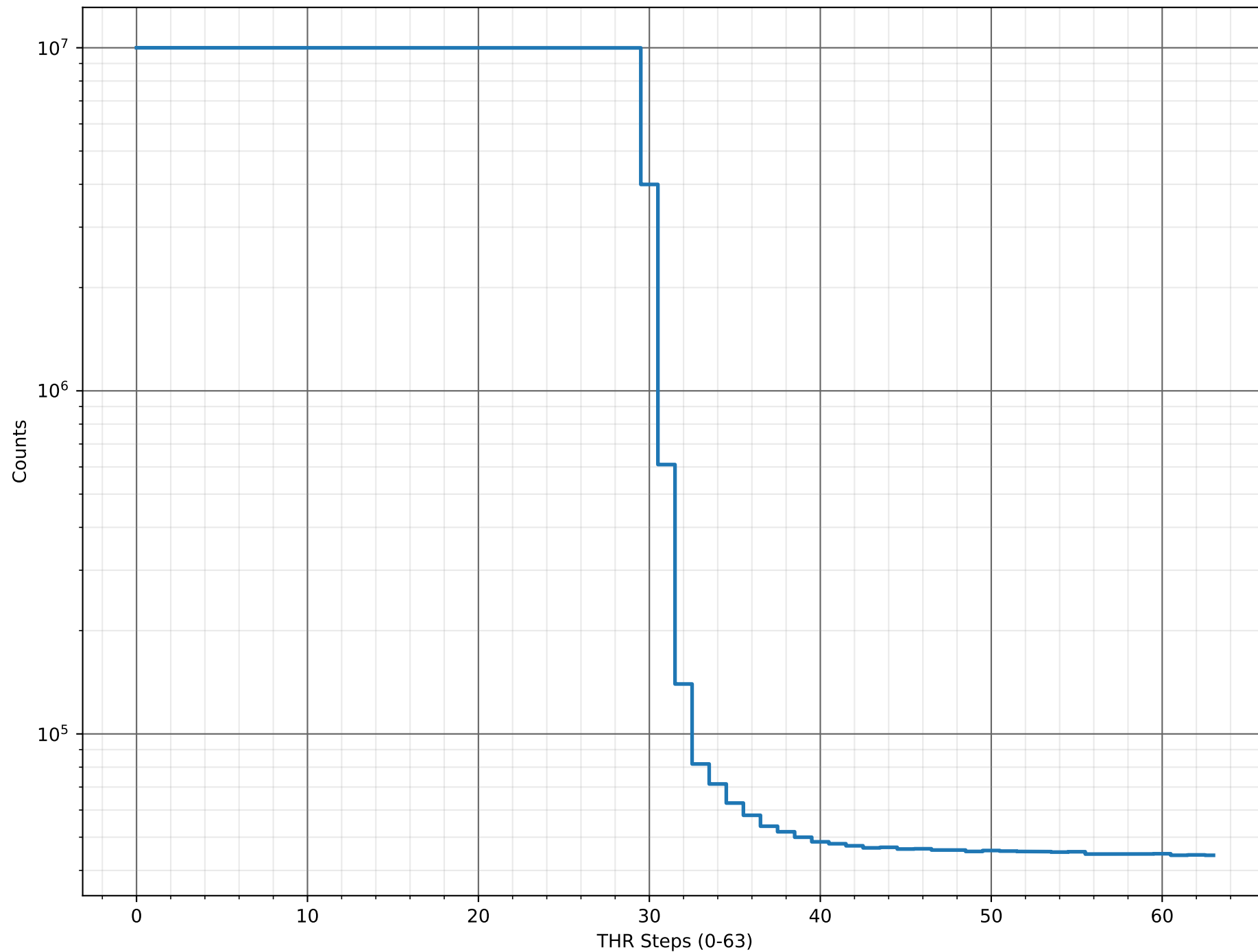
THR Distribution of PMT: C Anode: 54 (UART: 2 FEB: 0 CHIP: 1 CHANNEL: 2)



THR Distribution of PMT: C Anode: 55 (UART: 2 FEB: 0 CHIP: 0 CHANNEL: 4)

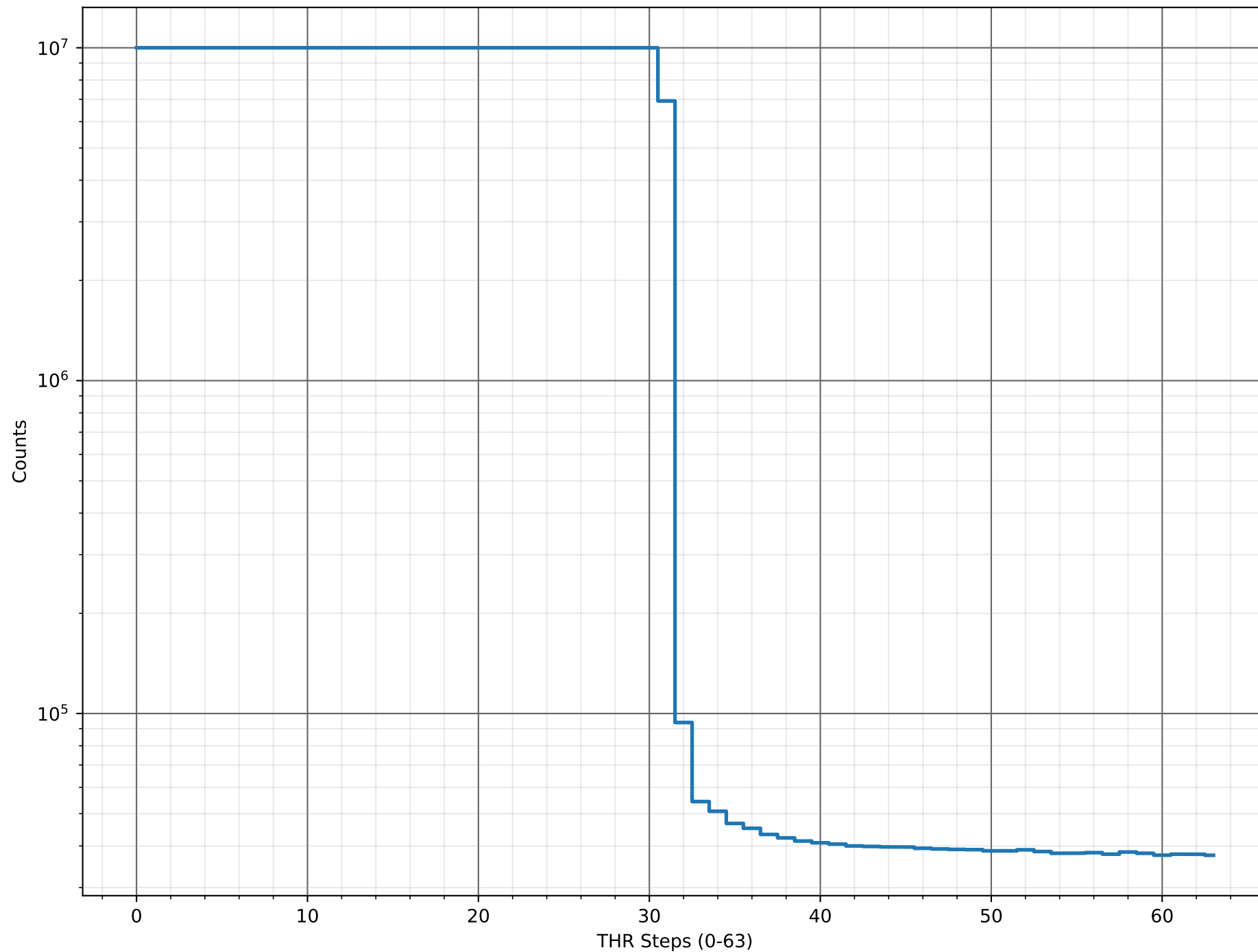


THR Distribution of PMT: C Anode: 56 (UART: 2 FEB: 0 CHIP: 0 CHANNEL: 5)

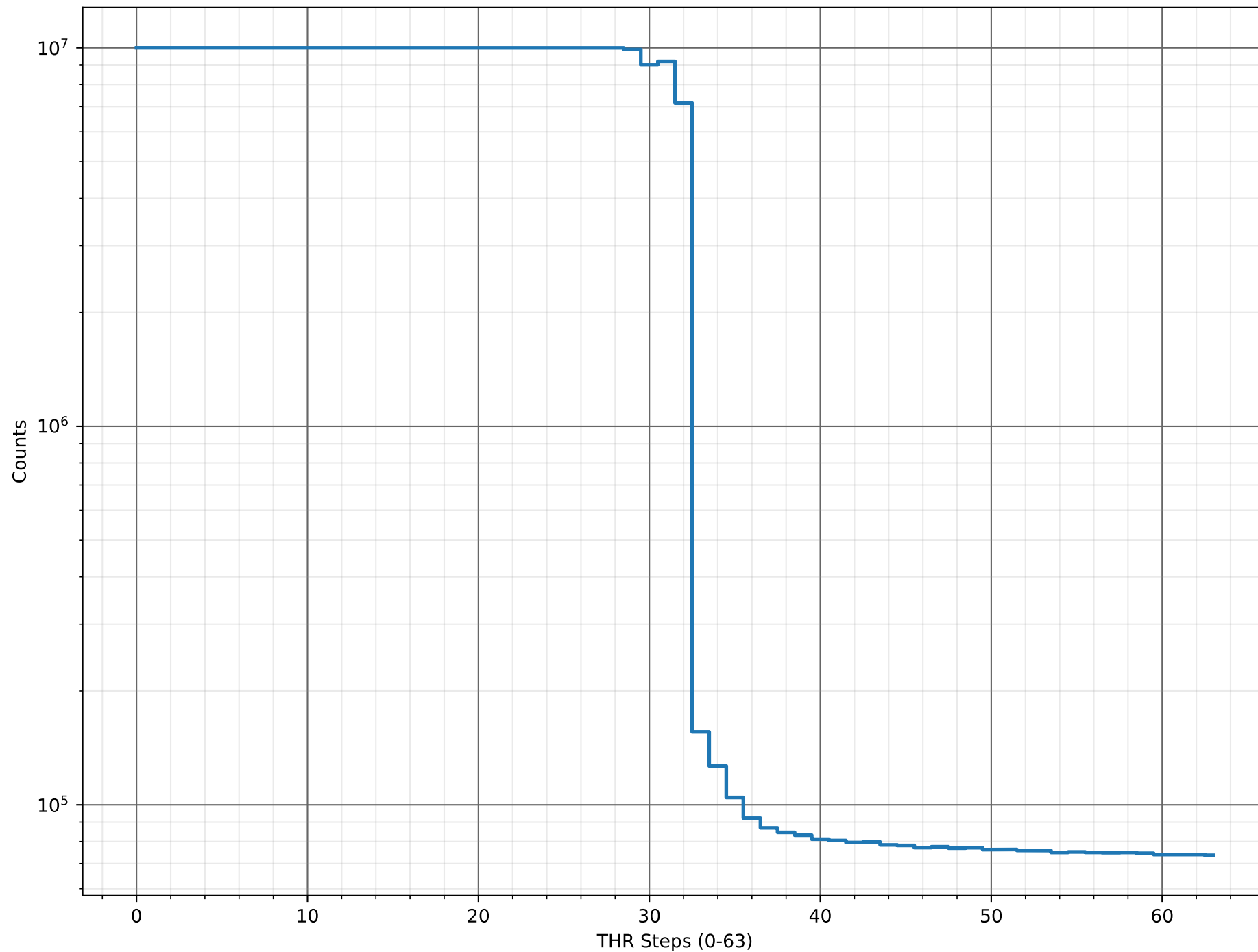




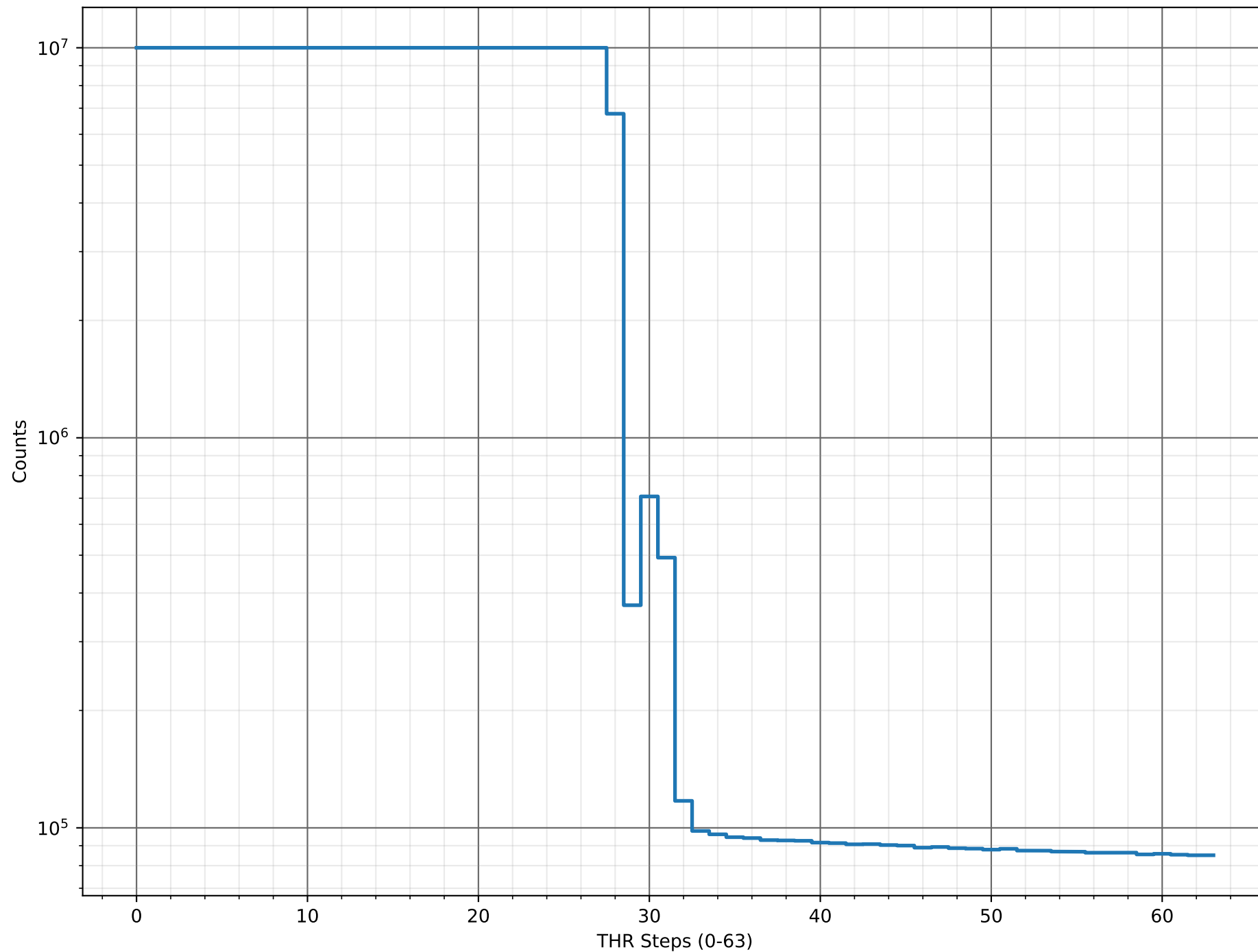
THR Distribution of PMT: C Anode: 57 (UART: 2 FEB: 1 CHIP: 1 CHANNEL: 0)



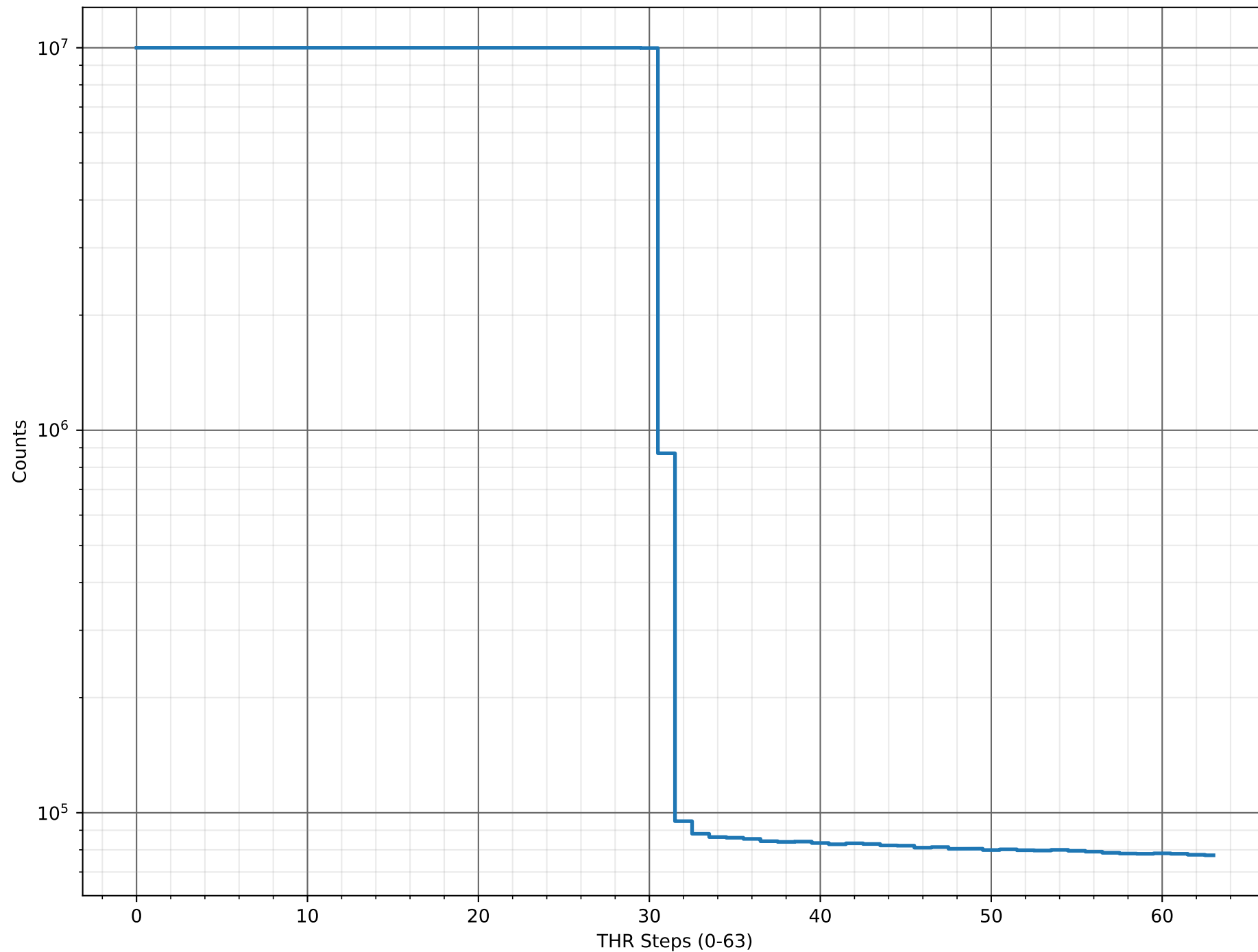
THR Distribution of PMT: C Anode: 58 (UART: 2 FEB: 1 CHIP: 1 CHANNEL: 1)



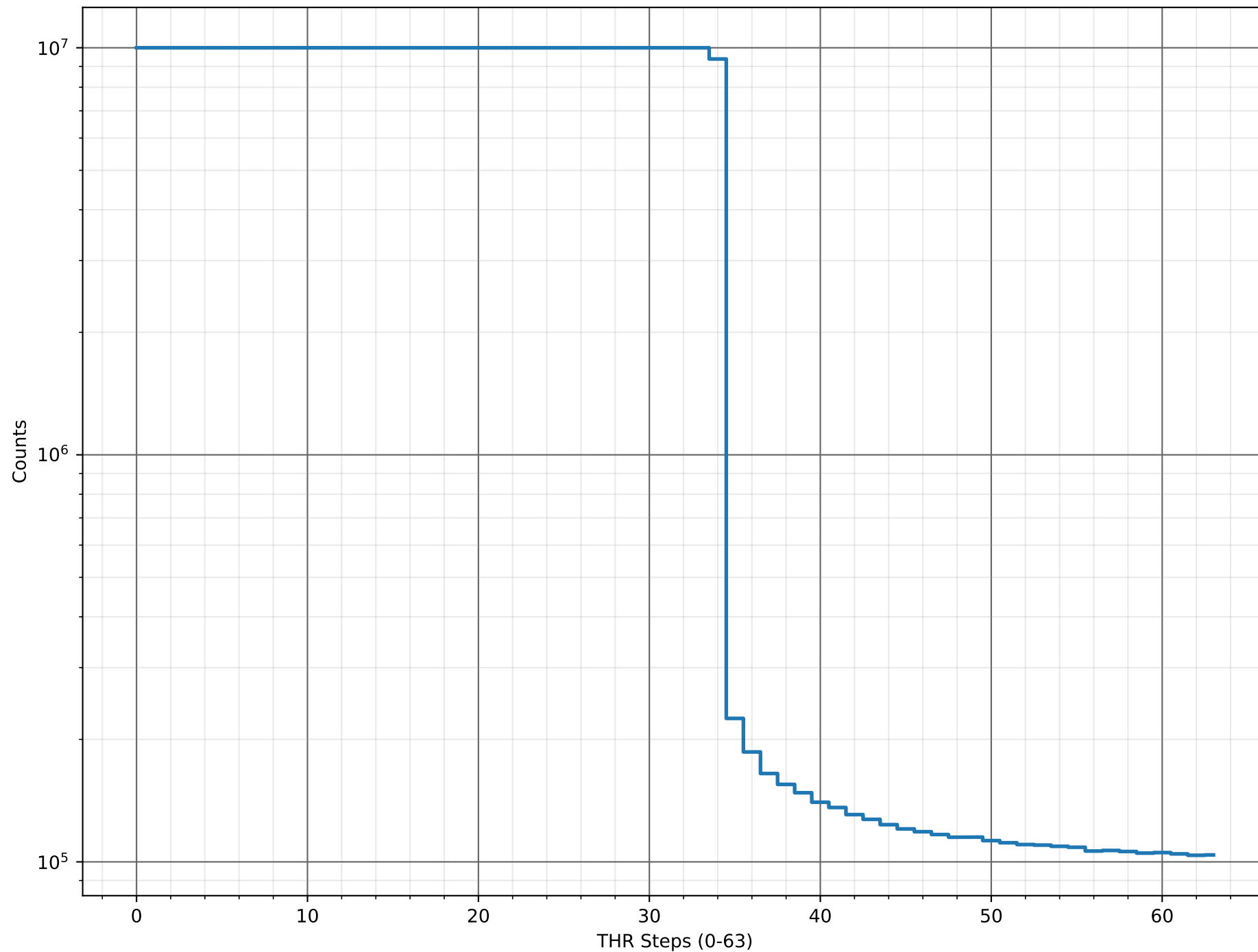
THR Distribution of PMT: C Anode: 59 (UART: 2 FEB: 1 CHIP: 0 CHANNEL: 7)



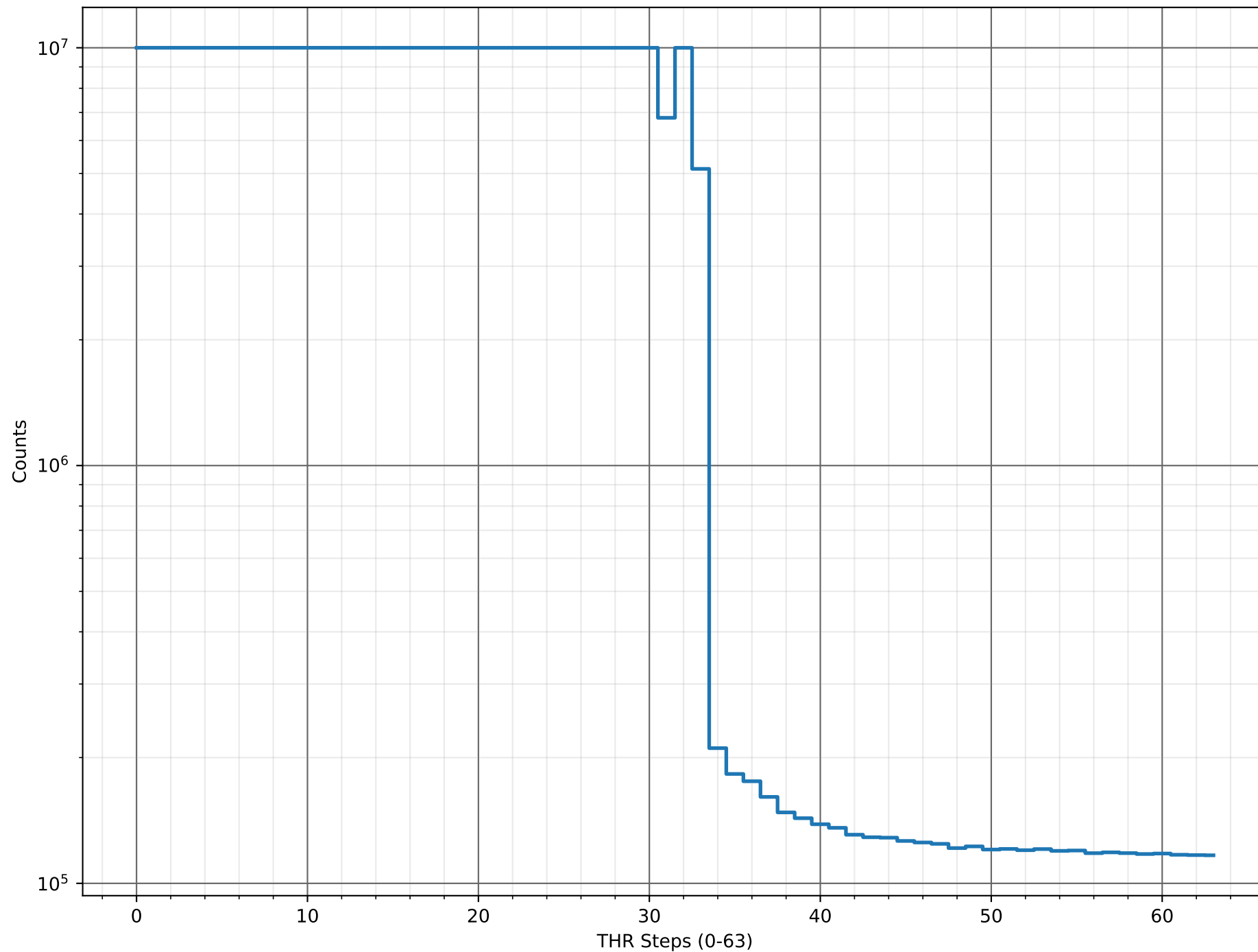
THR Distribution of PMT: C Anode: 60 (UART: 2 FEB: 1 CHIP: 0 CHANNEL: 6)



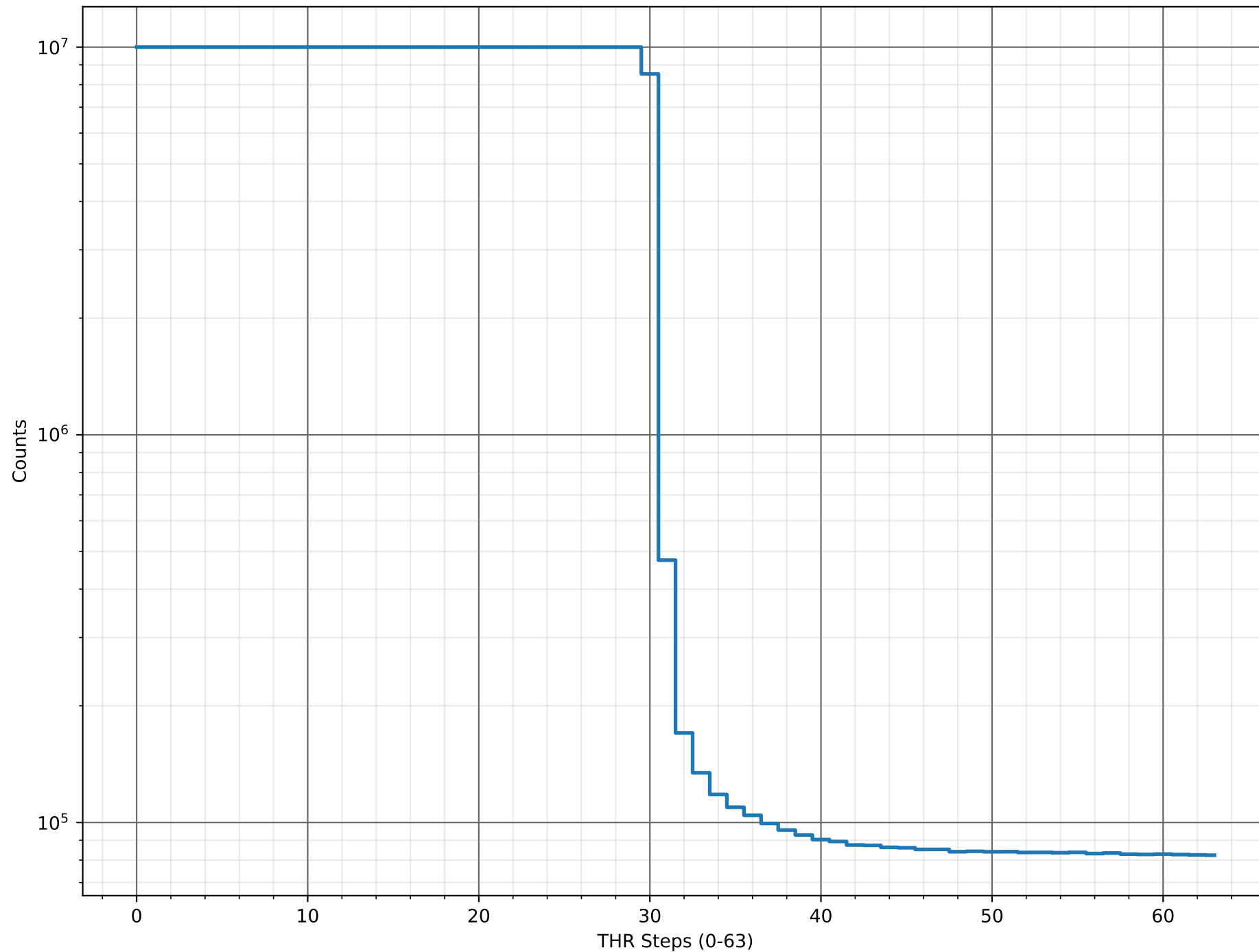
THR Distribution of PMT: C Anode: 61 (UART: 2 FEB: 0 CHIP: 1 CHANNEL: 1)



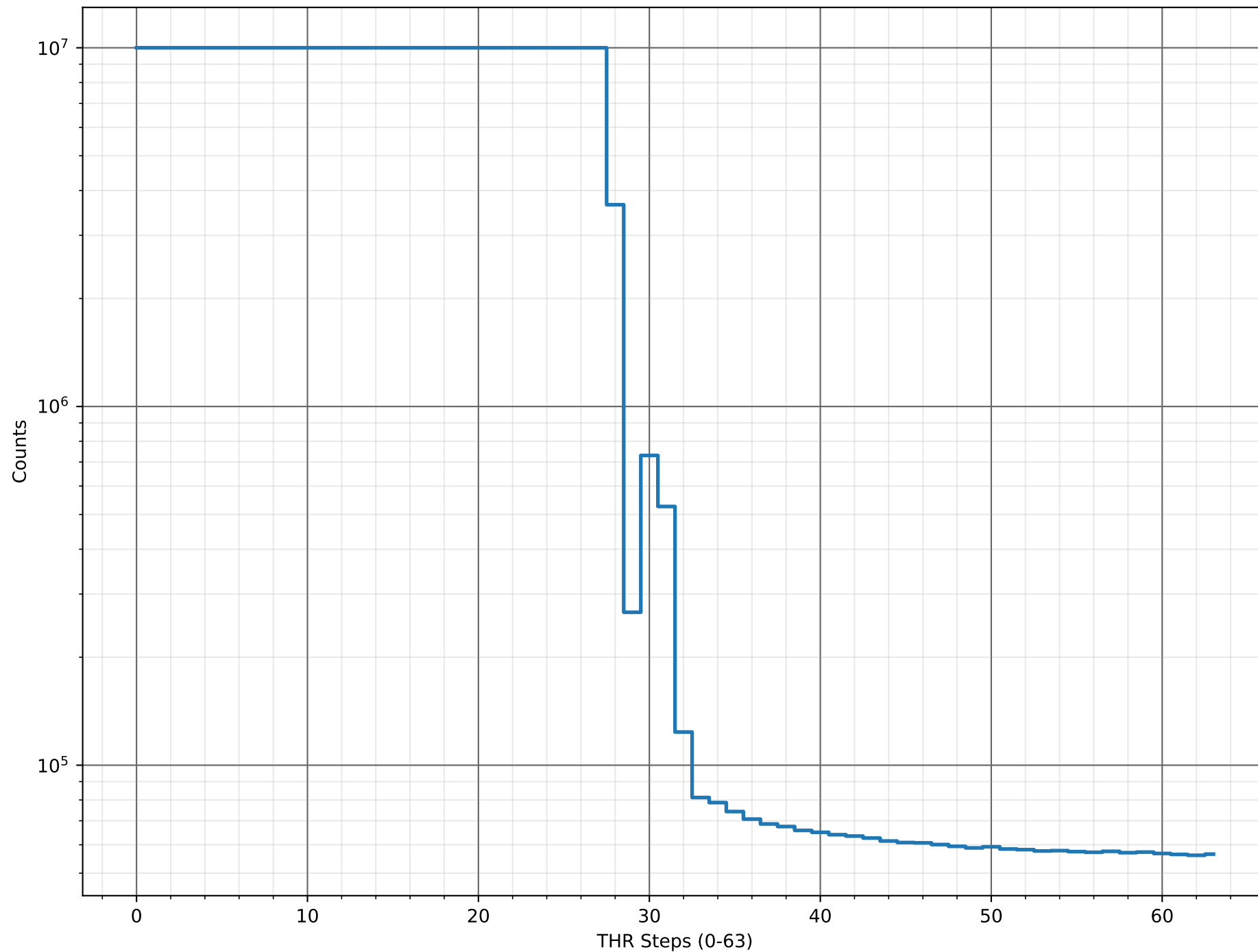
THR Distribution of PMT: C Anode: 62 (UART: 2 FEB: 0 CHIP: 1 CHANNEL: 0)



THR Distribution of PMT: C Anode: 63 (UART: 2 FEB: 0 CHIP: 0 CHANNEL: 6)

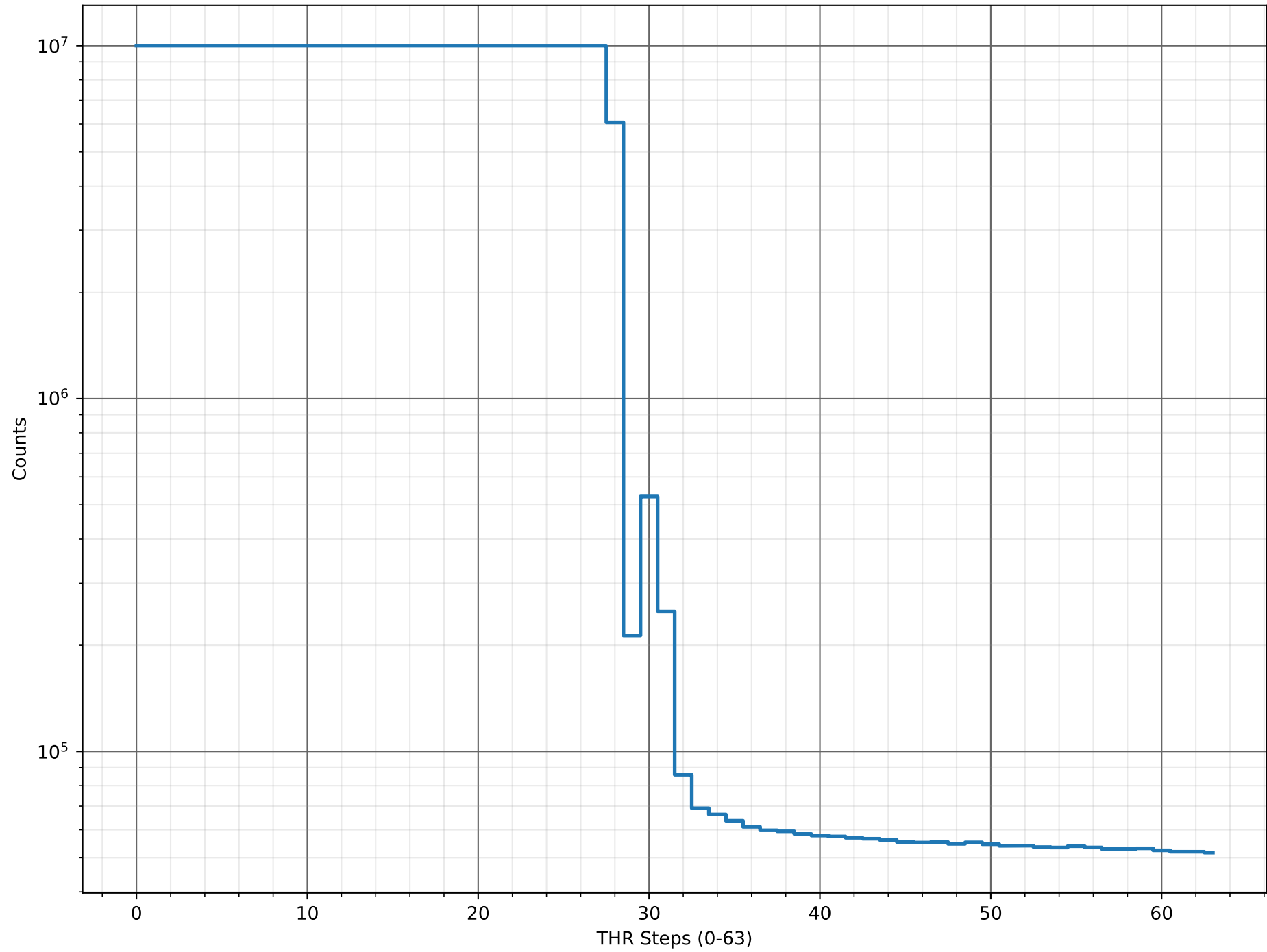


THR Distribution of PMT: C Anode: 64 (UART: 2 FEB: 0 CHIP: 0 CHANNEL: 7)

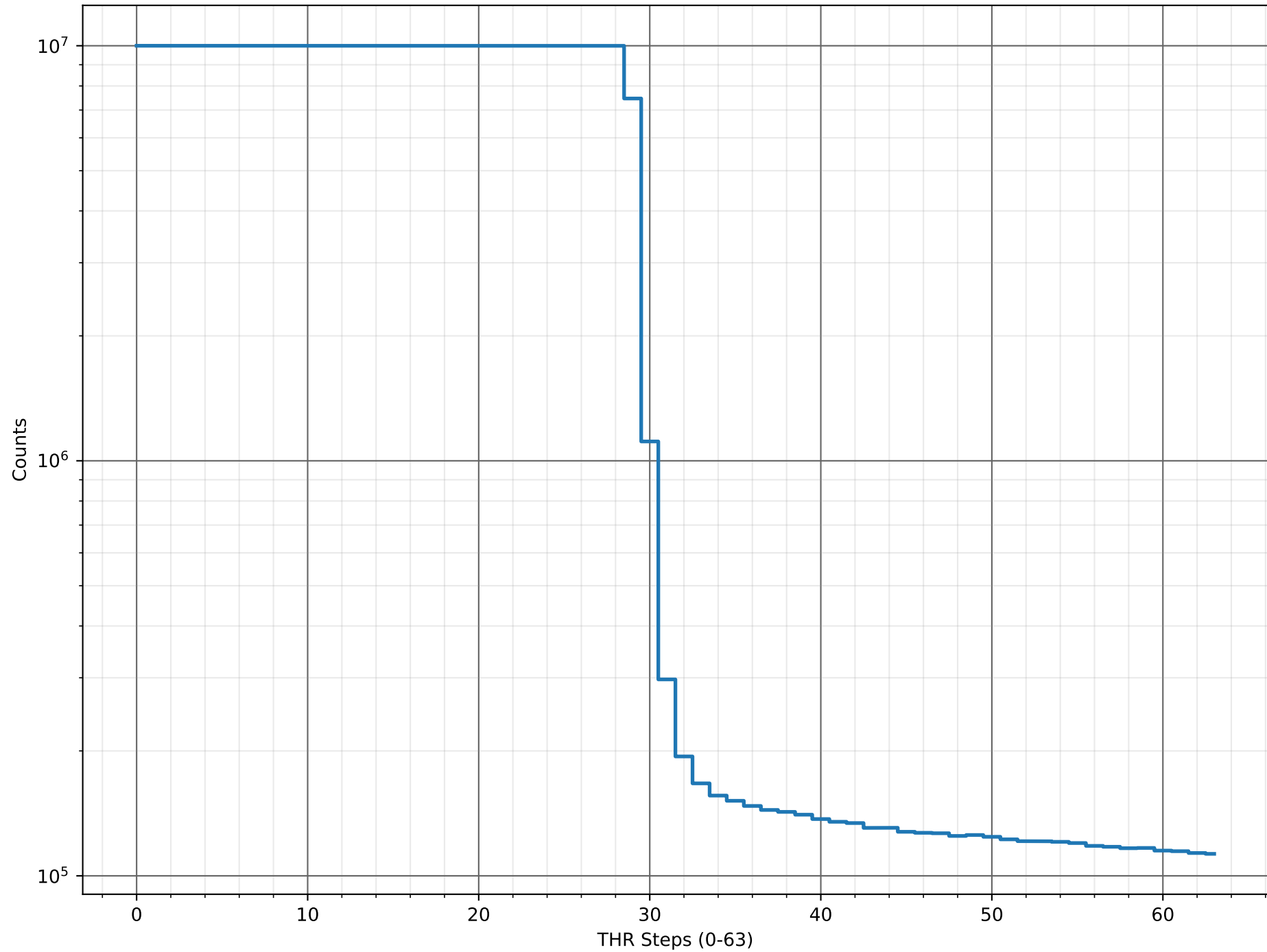




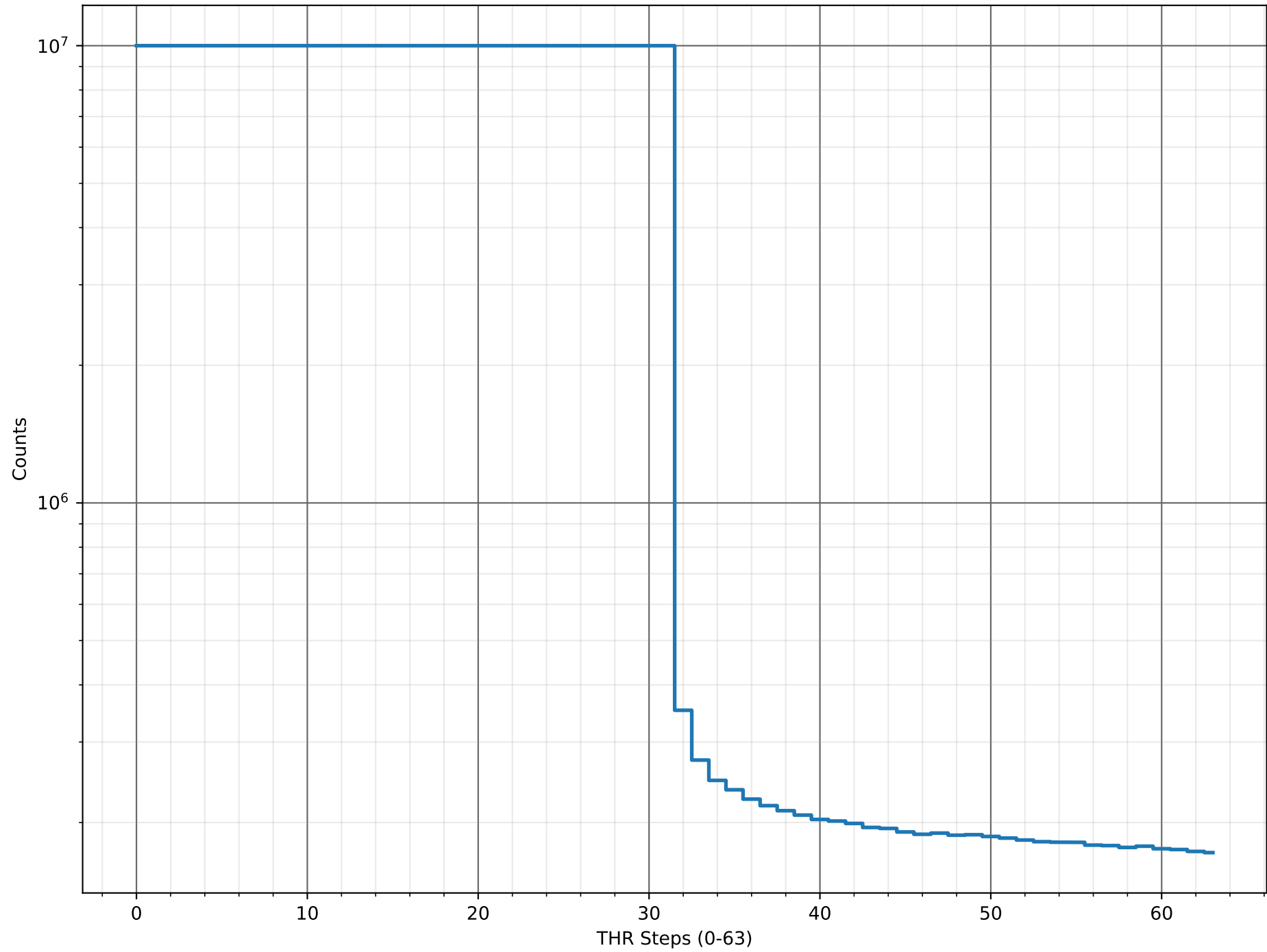
THR Distribution of PMT: D Anode: 1 (UART: 1 FEB: 1 CHIP: 2 CHANNEL: 0)



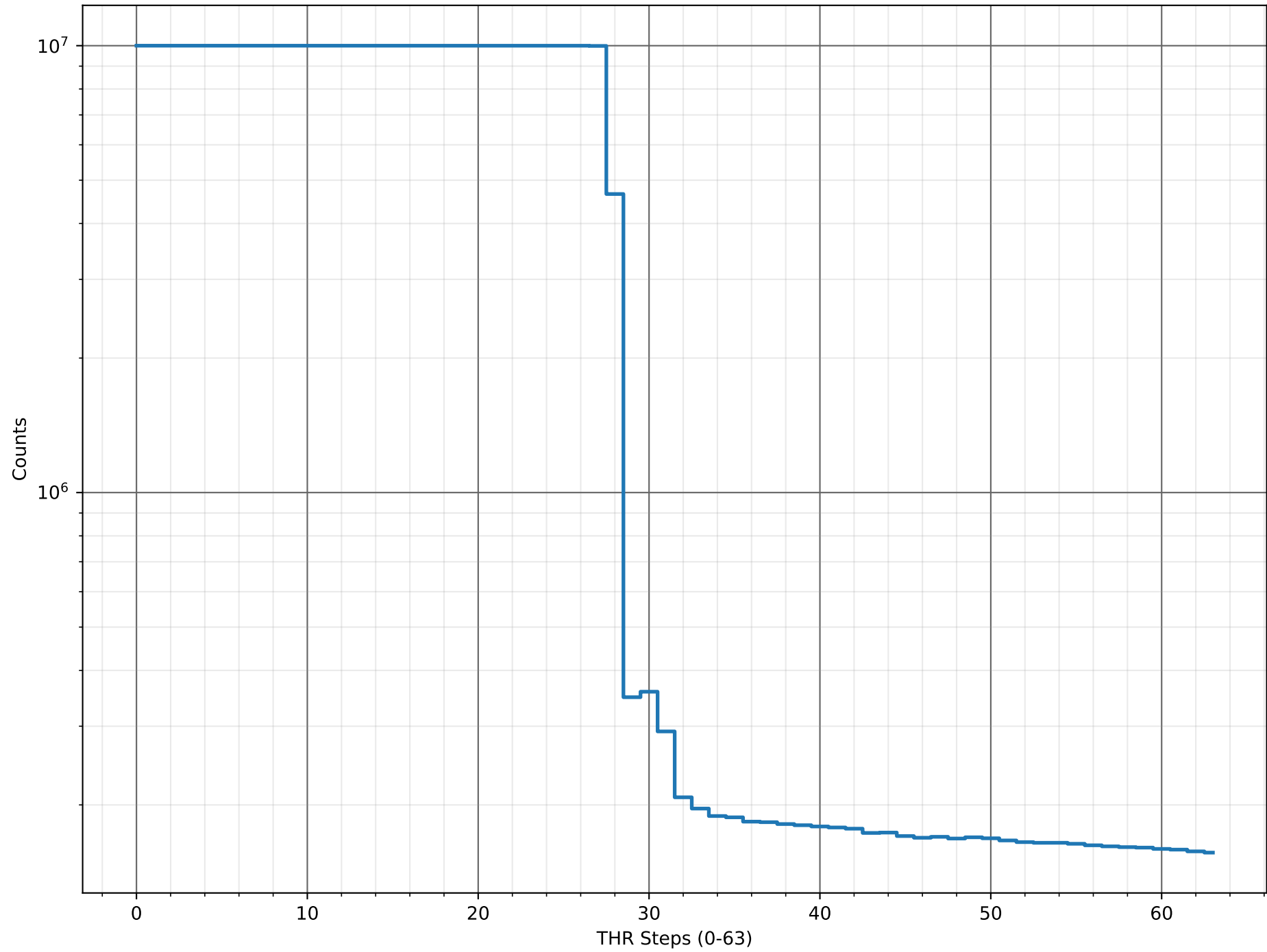
THR Distribution of PMT: D Anode: 2 (UART: 1 FEB: 1 CHIP: 2 CHANNEL: 2)



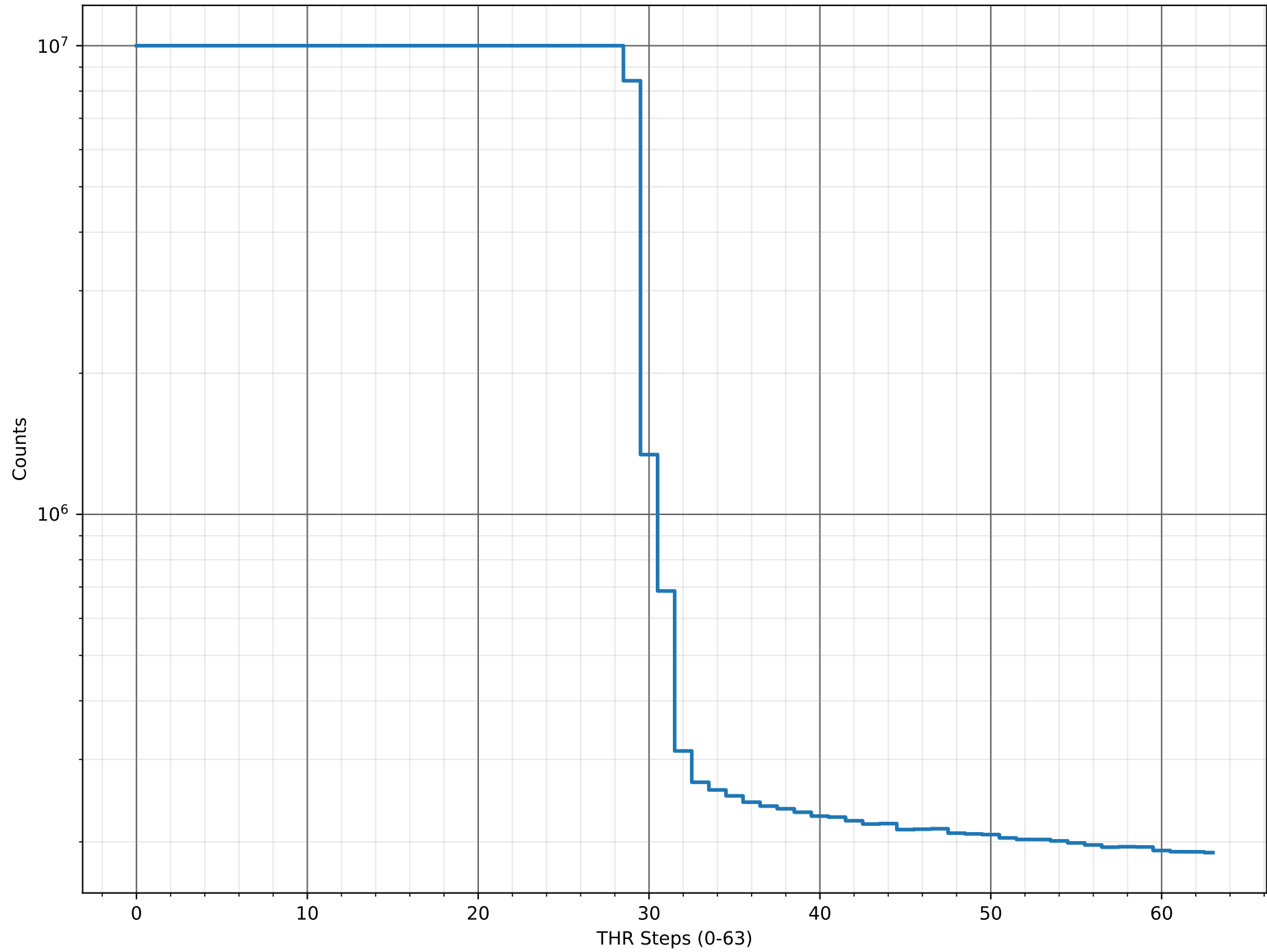
THR Distribution of PMT: D Anode: 3 (UART: 1 FEB: 1 CHIP: 2 CHANNEL: 4)



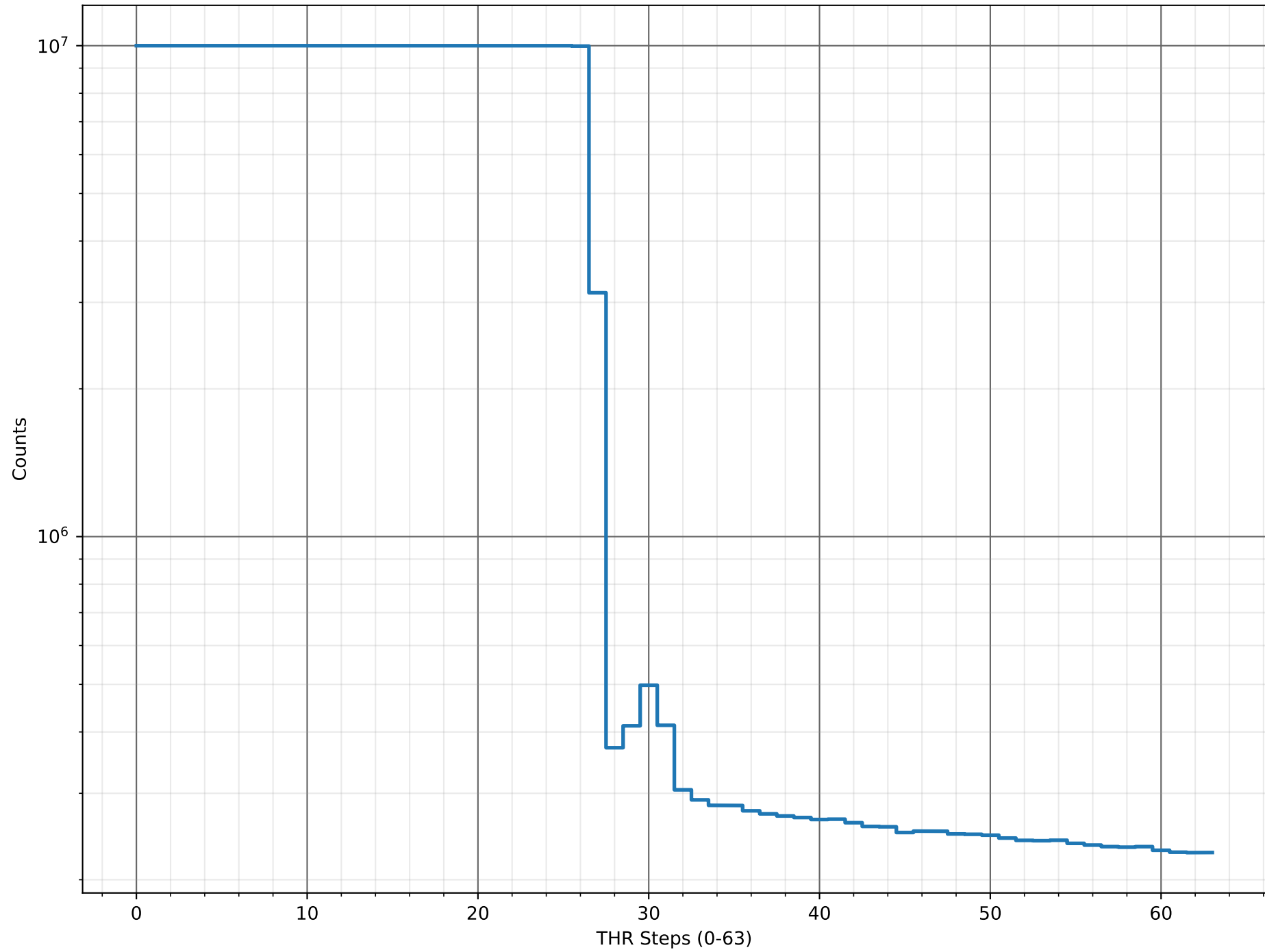
THR Distribution of PMT: D Anode: 4 (UART: 1 FEB: 1 CHIP: 2 CHANNEL: 6)



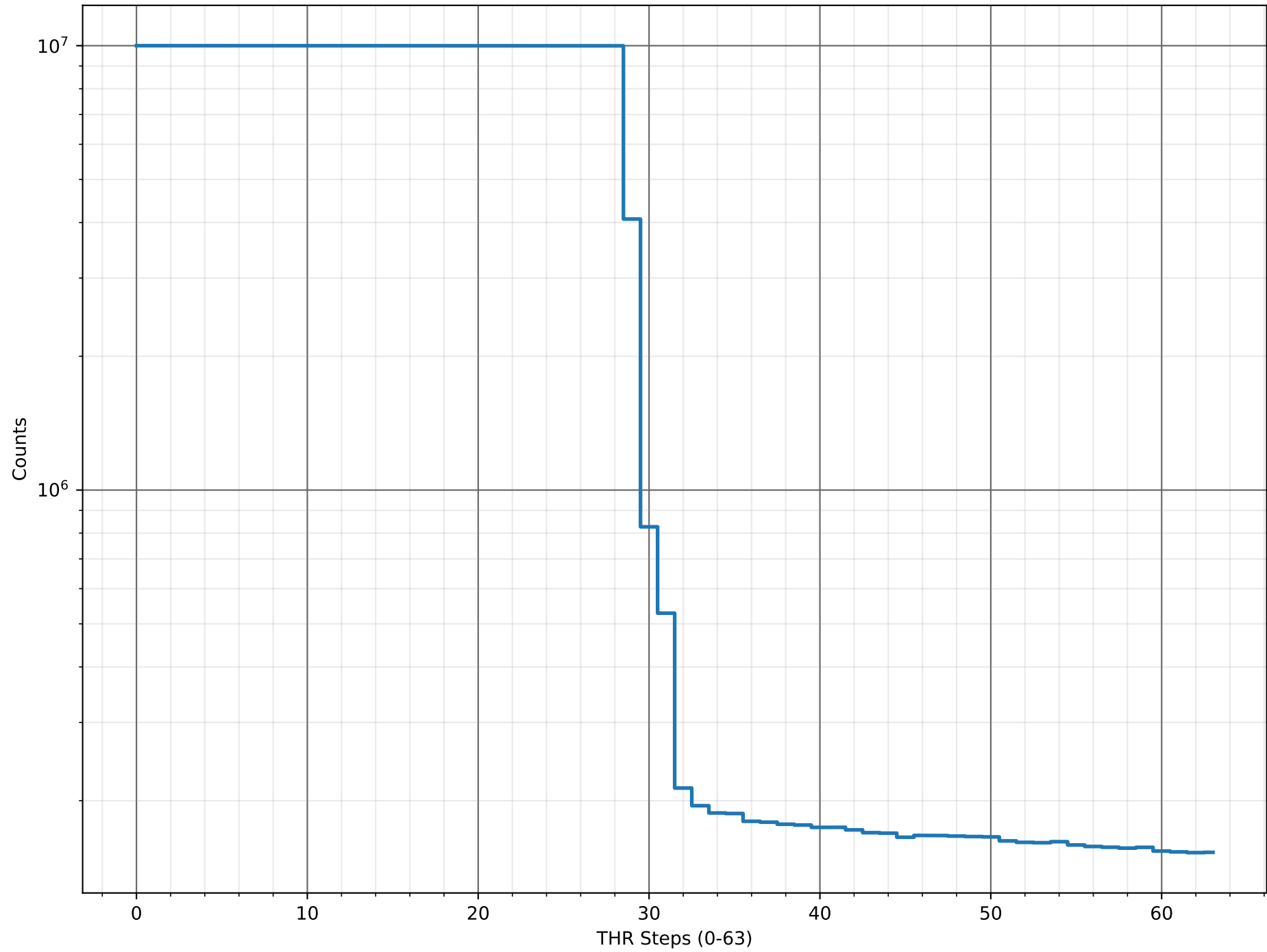
THR Distribution of PMT: D Anode: 5 (UART: 1 FEB: 1 CHIP: 0 CHANNEL: 1)



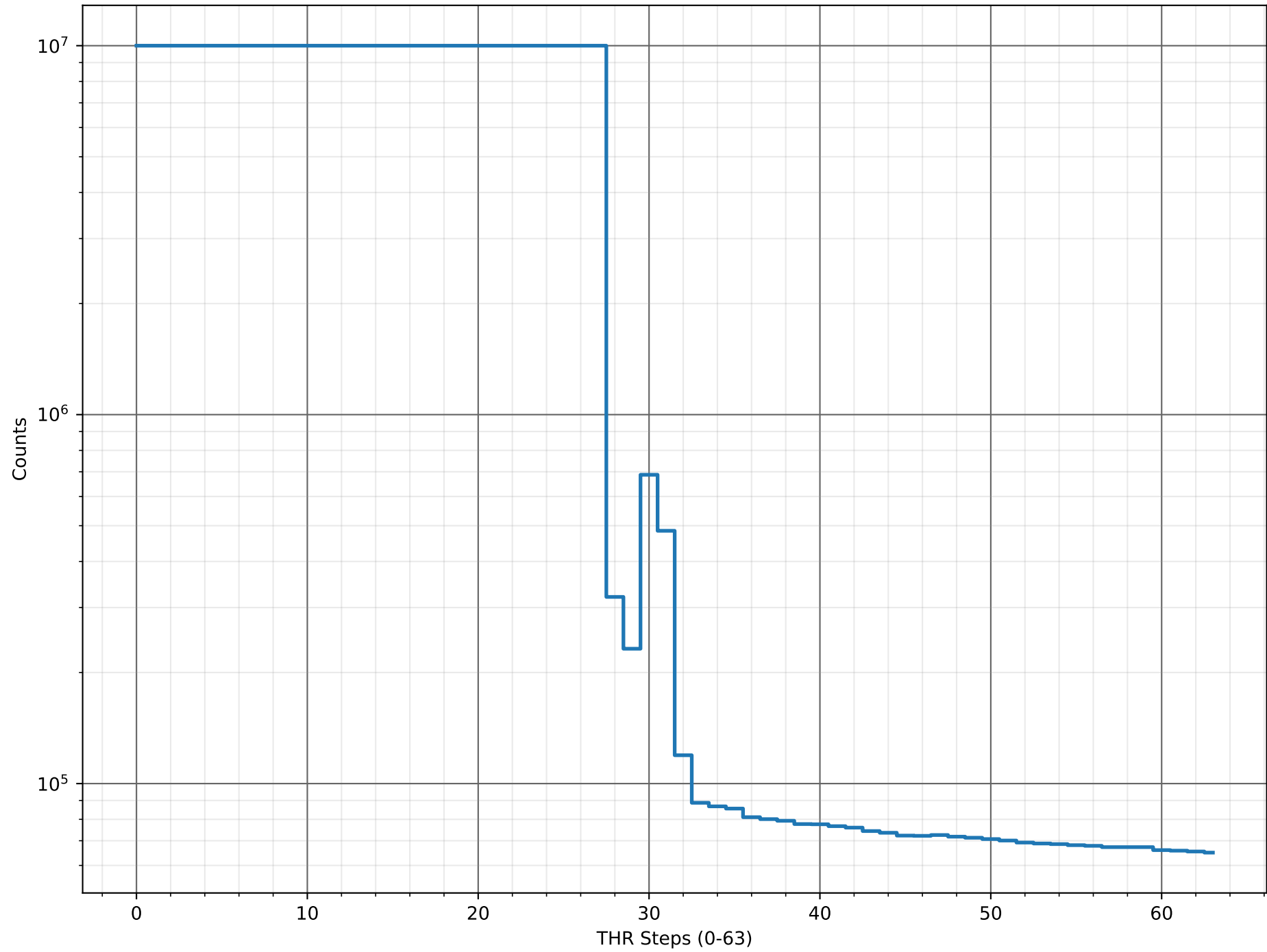
THR Distribution of PMT: D Anode: 6 (UART: 1 FEB: 1 CHIP: 0 CHANNEL: 3)



THR Distribution of PMT: D Anode: 7 (UART: 1 FEB: 1 CHIP: 0 CHANNEL: 5)

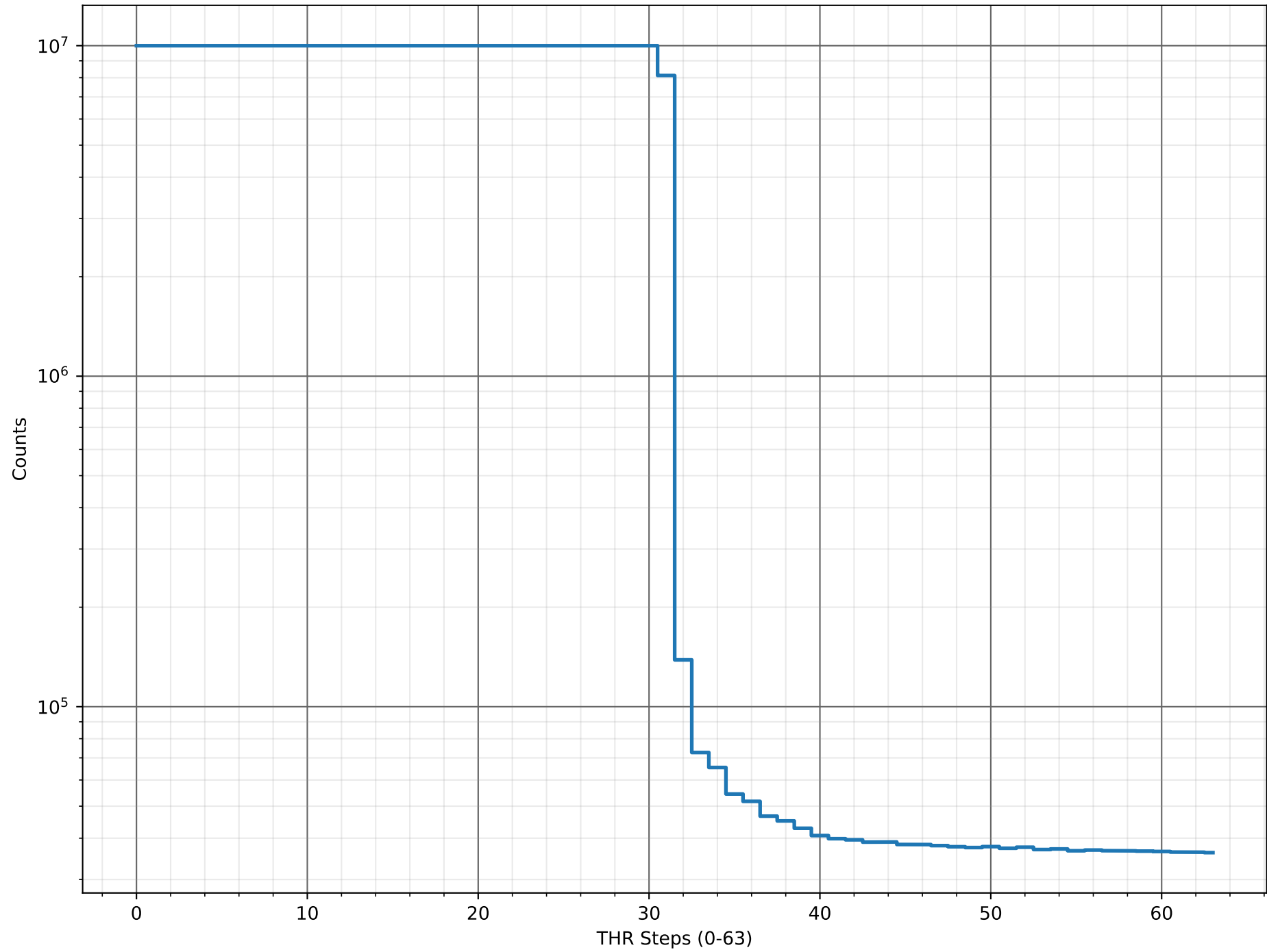


THR Distribution of PMT: D Anode: 8 (UART: 1 FEB: 1 CHIP: 0 CHANNEL: 7)

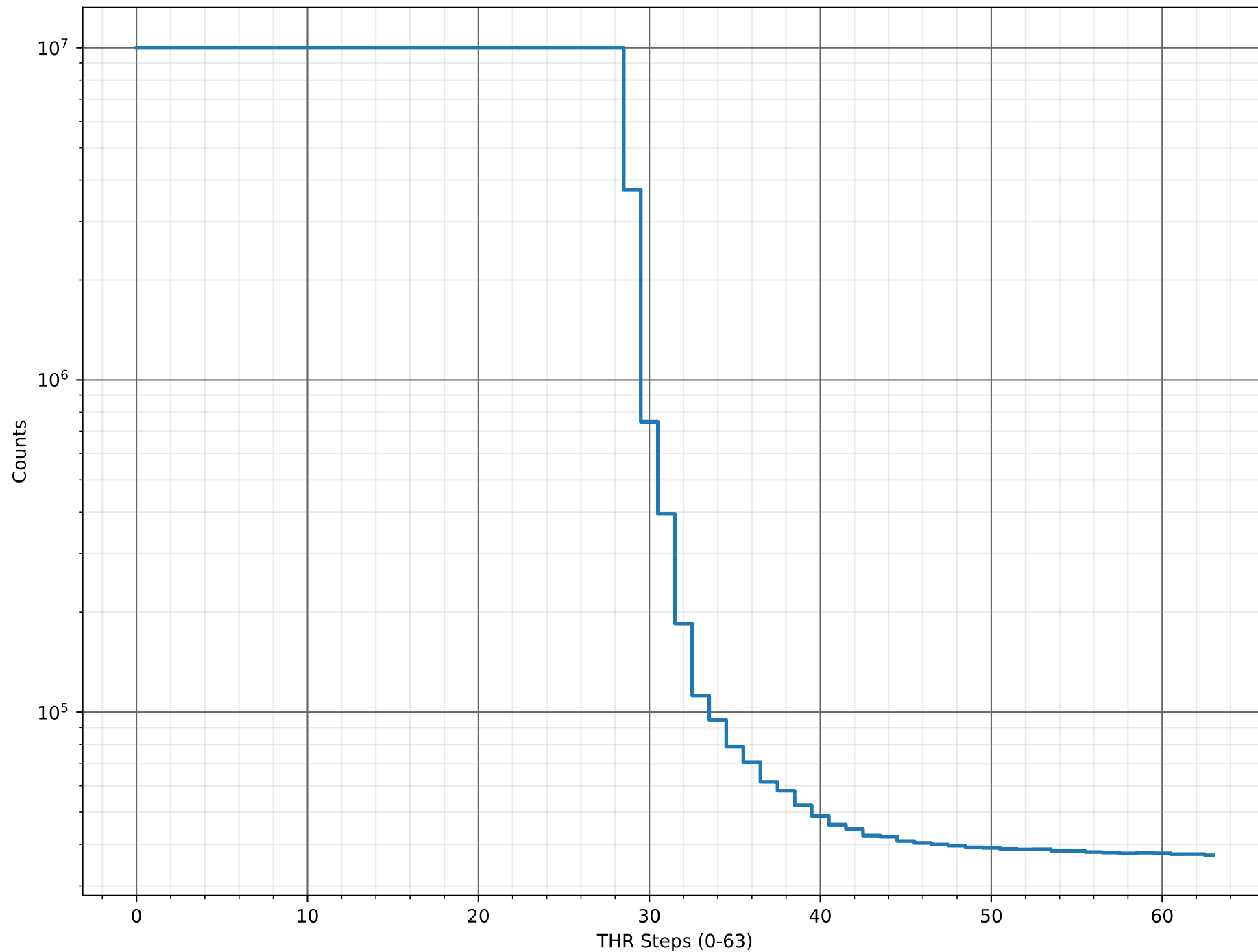




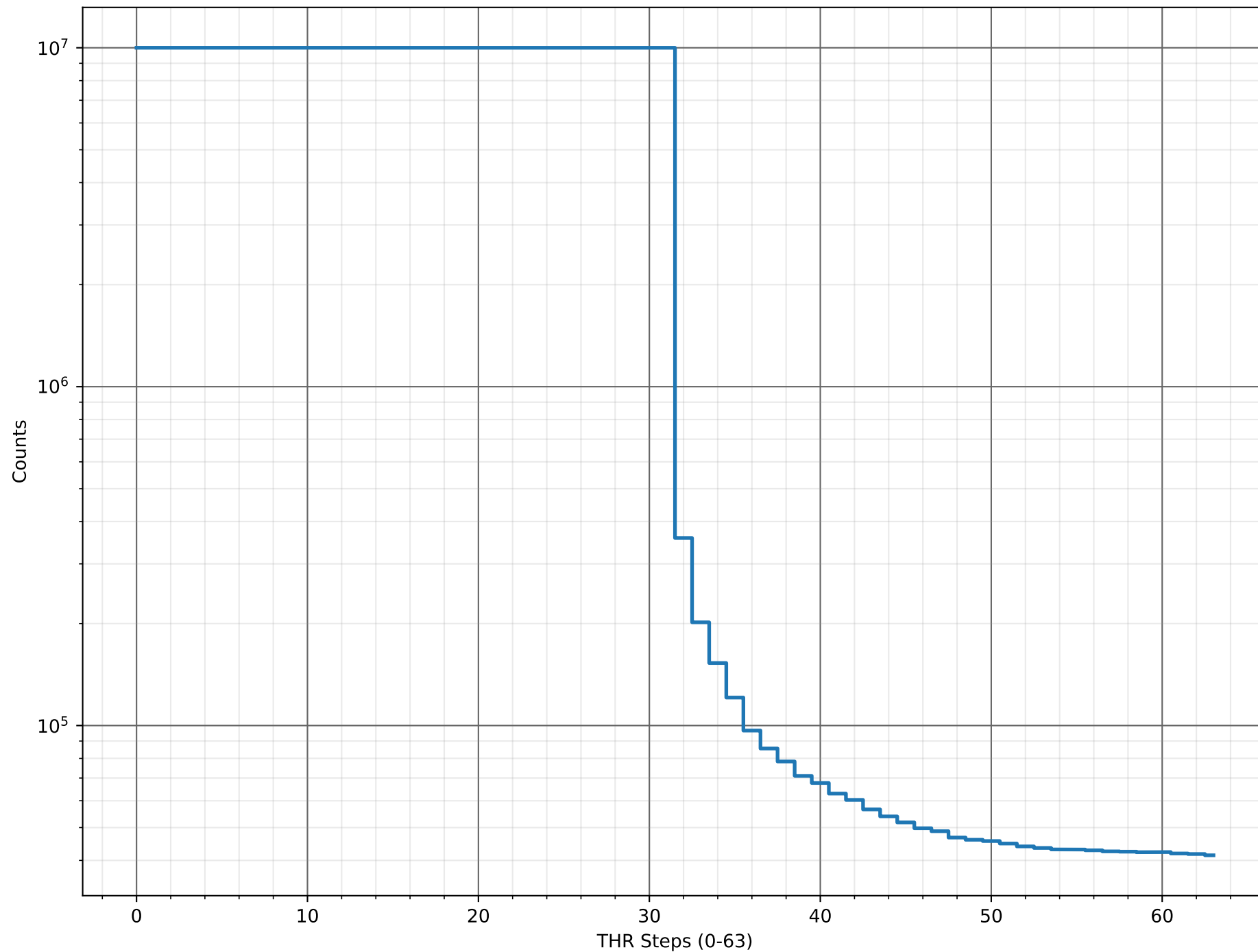
THR Distribution of PMT: D Anode: 9 (UART: 1 FEB: 1 CHIP: 2 CHANNEL: 1)



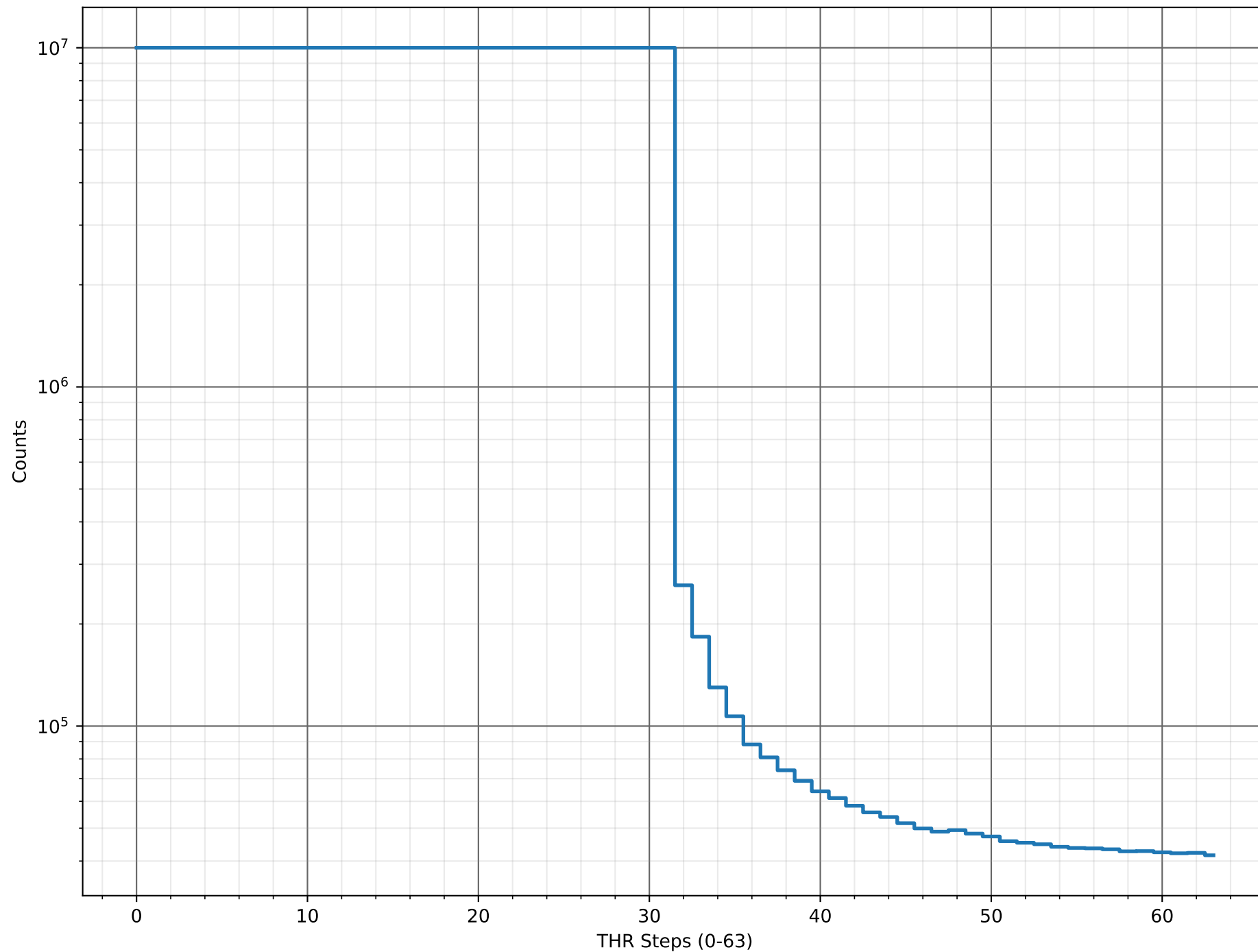
THR Distribution of PMT: D Anode: 10 (UART: 1 FEB: 1 CHIP: 2 CHANNEL: 3)



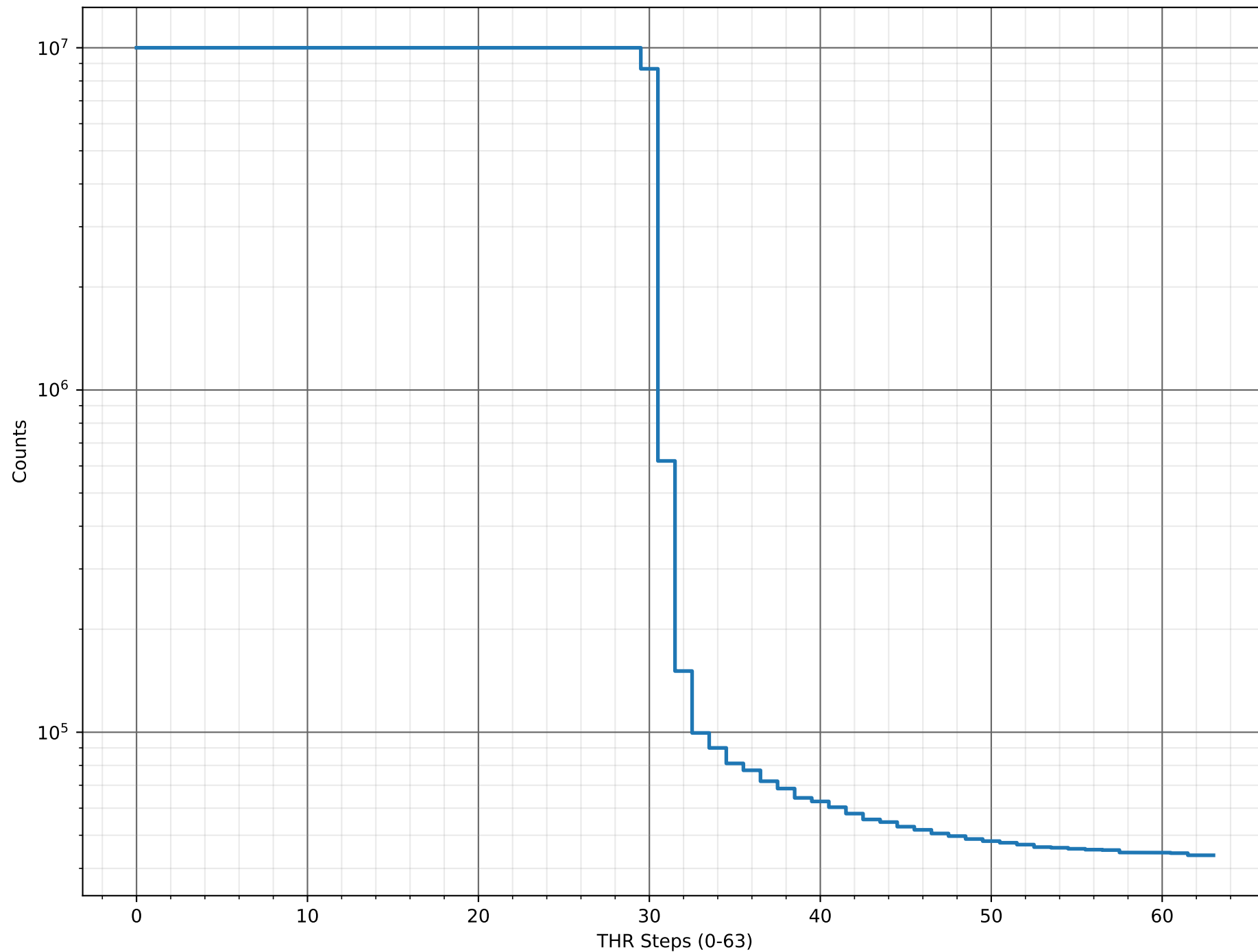
THR Distribution of PMT: D Anode: 11 (UART: 1 FEB: 1 CHIP: 2 CHANNEL: 5)



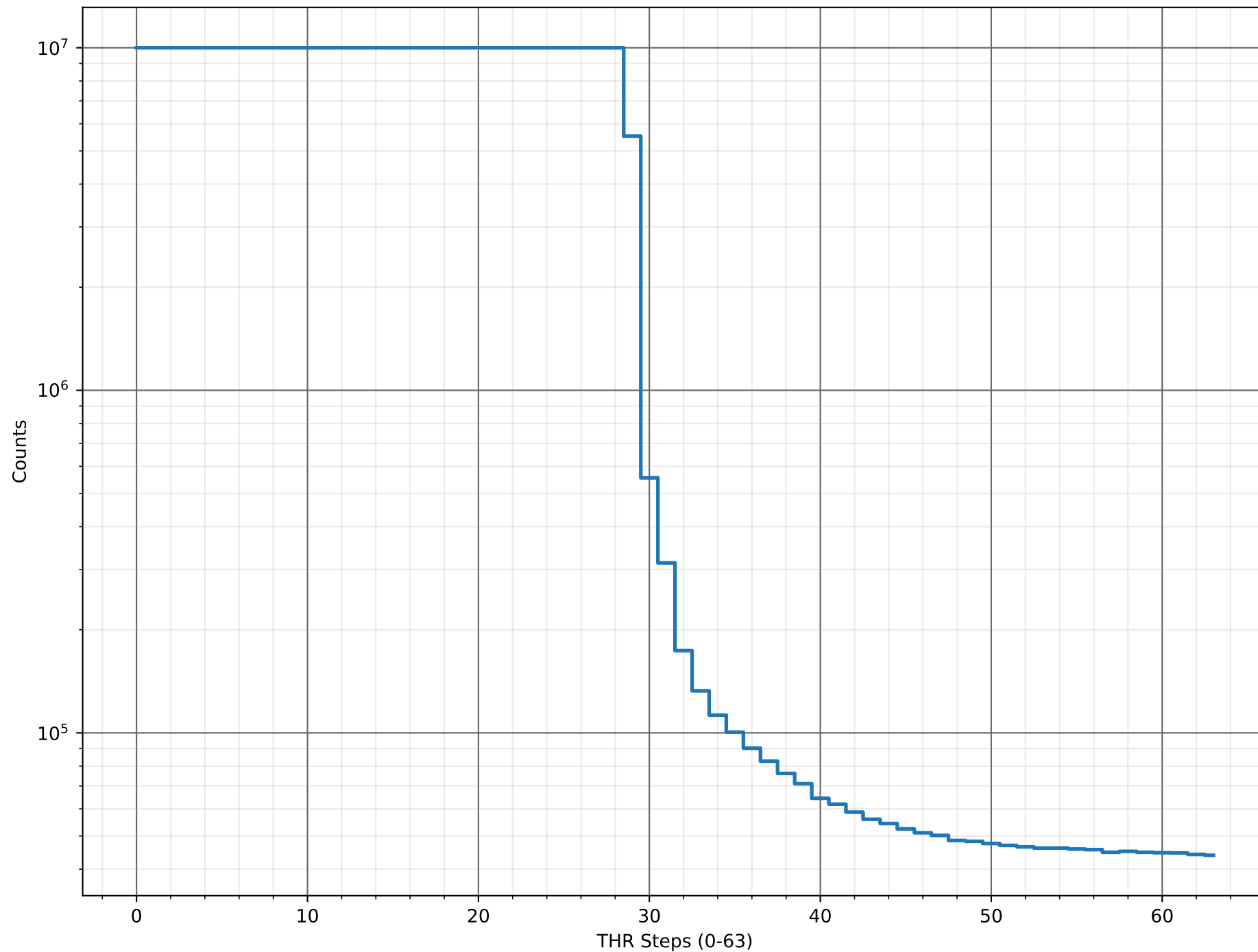
THR Distribution of PMT: D Anode: 12 (UART: 1 FEB: 1 CHIP: 2 CHANNEL: 7)



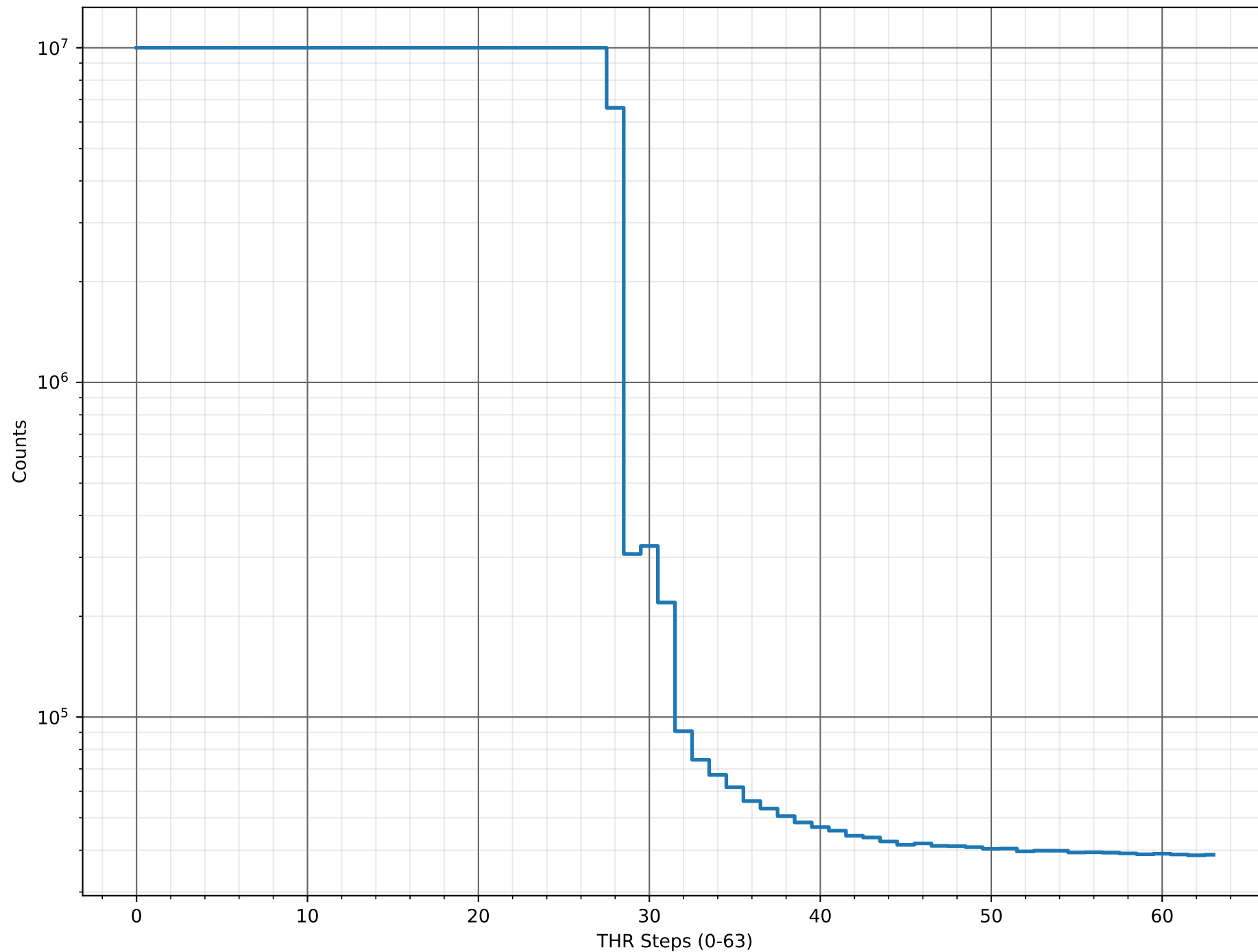
THR Distribution of PMT: D Anode: 13 (UART: 1 FEB: 1 CHIP: 0 CHANNEL: 0)



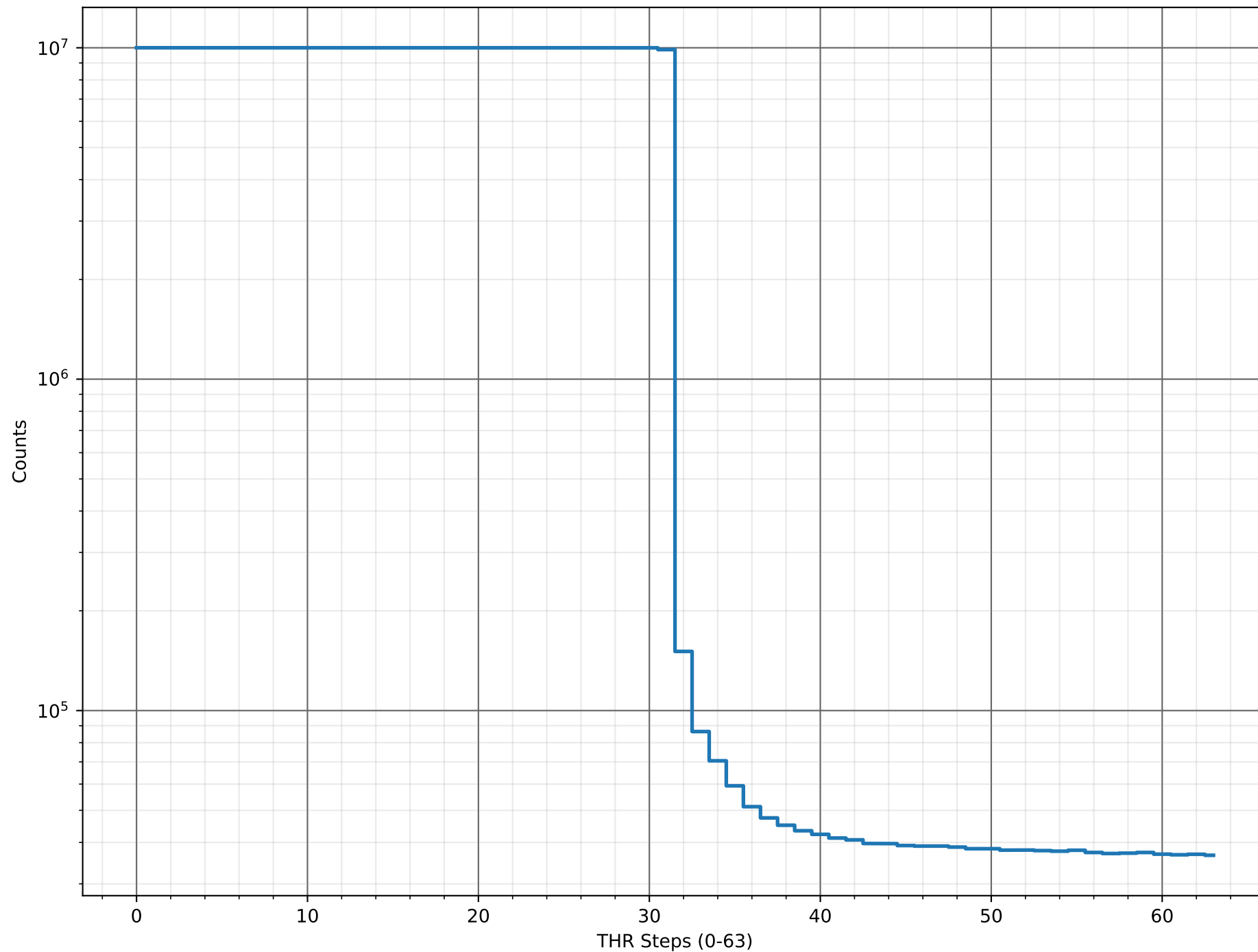
THR Distribution of PMT: D Anode: 14 (UART: 1 FEB: 1 CHIP: 0 CHANNEL: 2)



THR Distribution of PMT: D Anode: 15 (UART: 1 FEB: 1 CHIP: 0 CHANNEL: 4)

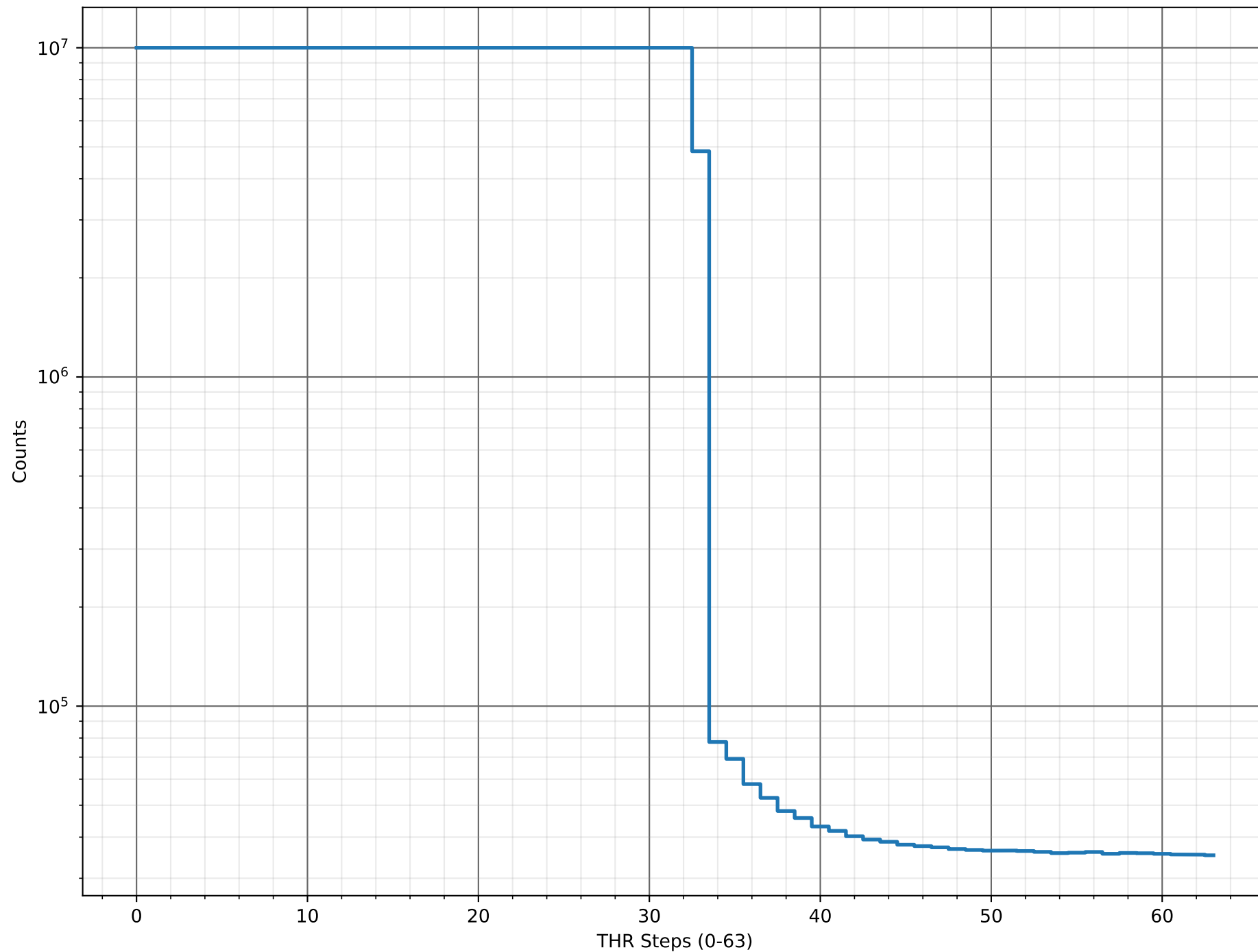


THR Distribution of PMT: D Anode: 16 (UART: 1 FEB: 1 CHIP: 0 CHANNEL: 6)

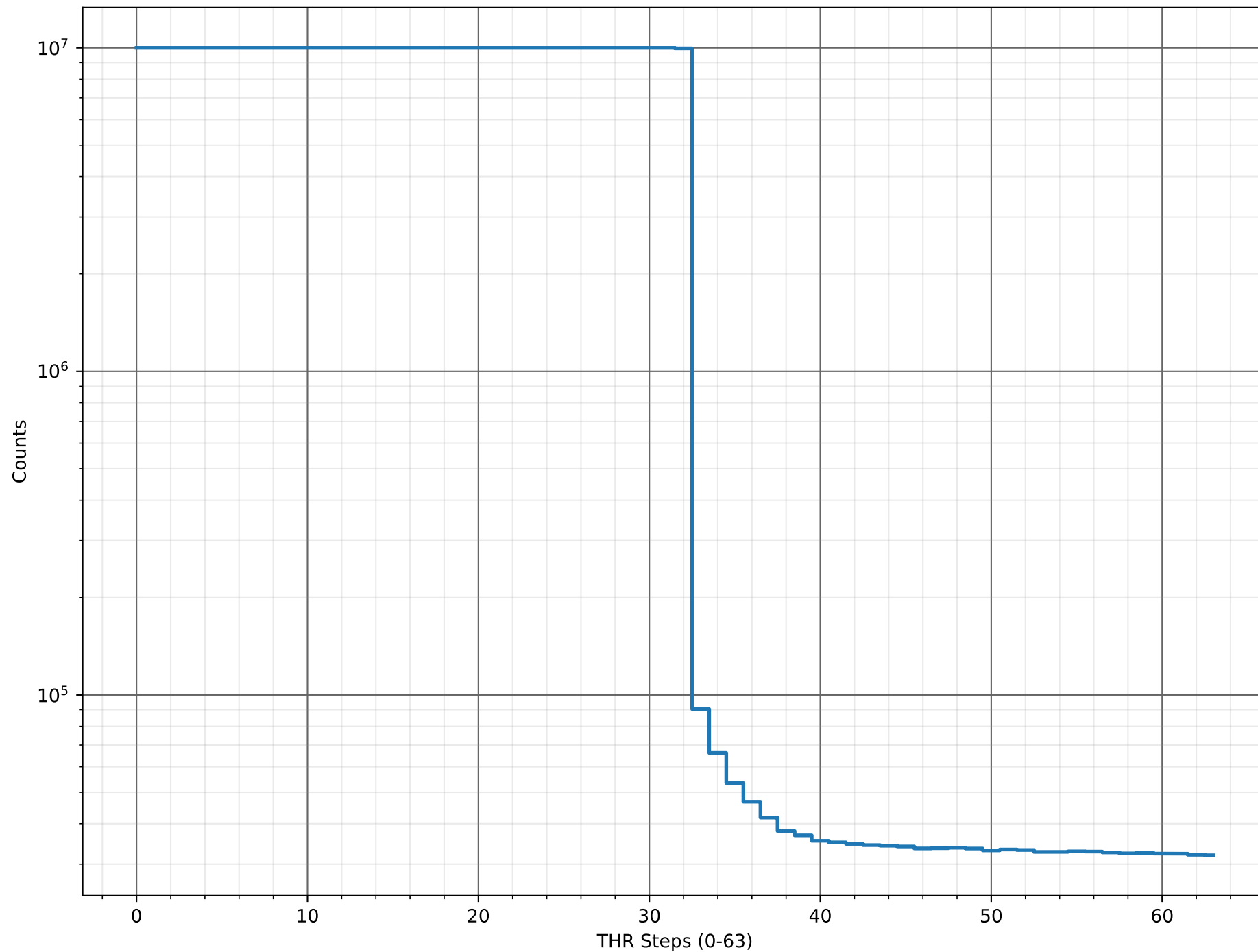




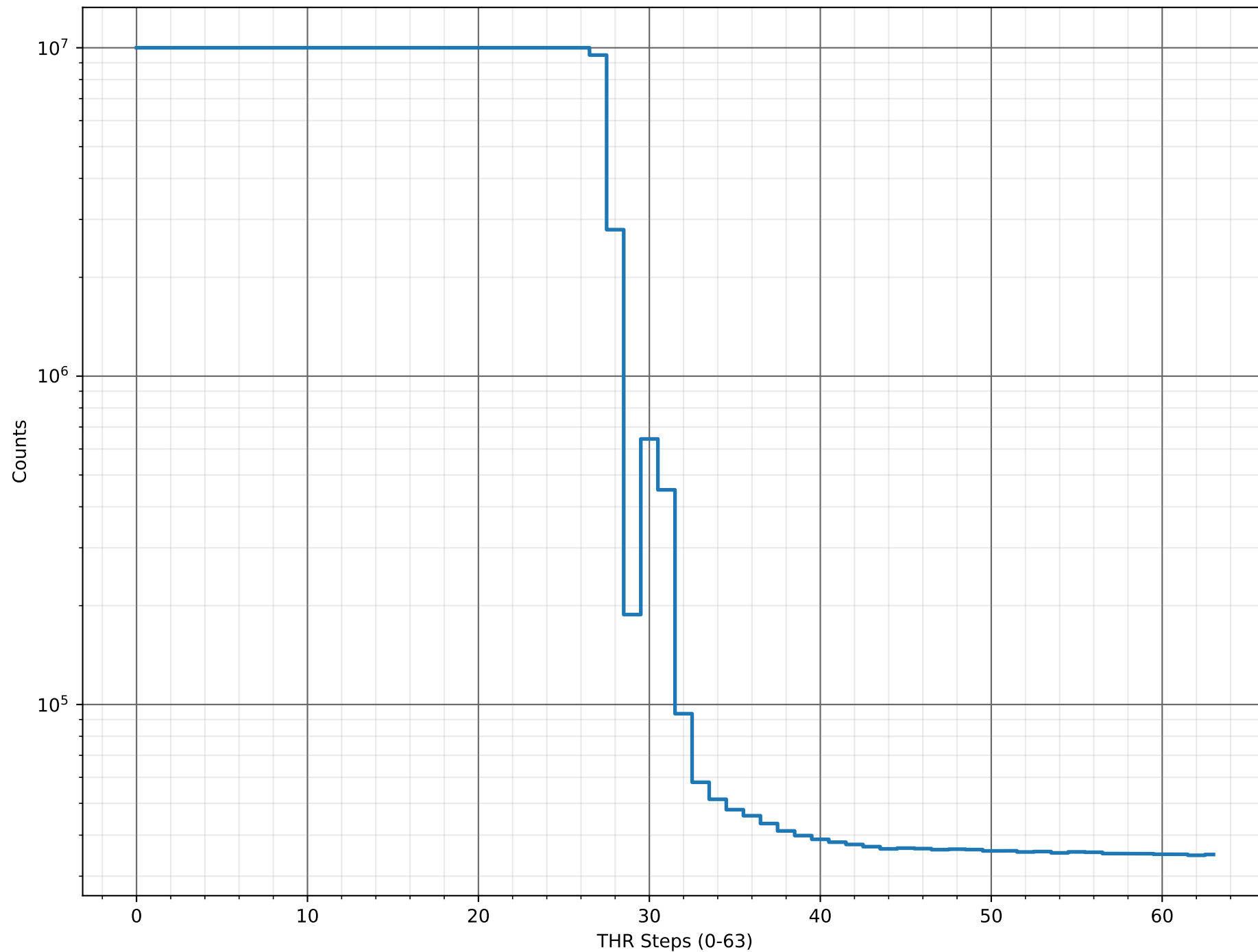
THR Distribution of PMT: D Anode: 17 (UART: 1 FEB: 1 CHIP: 3 CHANNEL: 7)



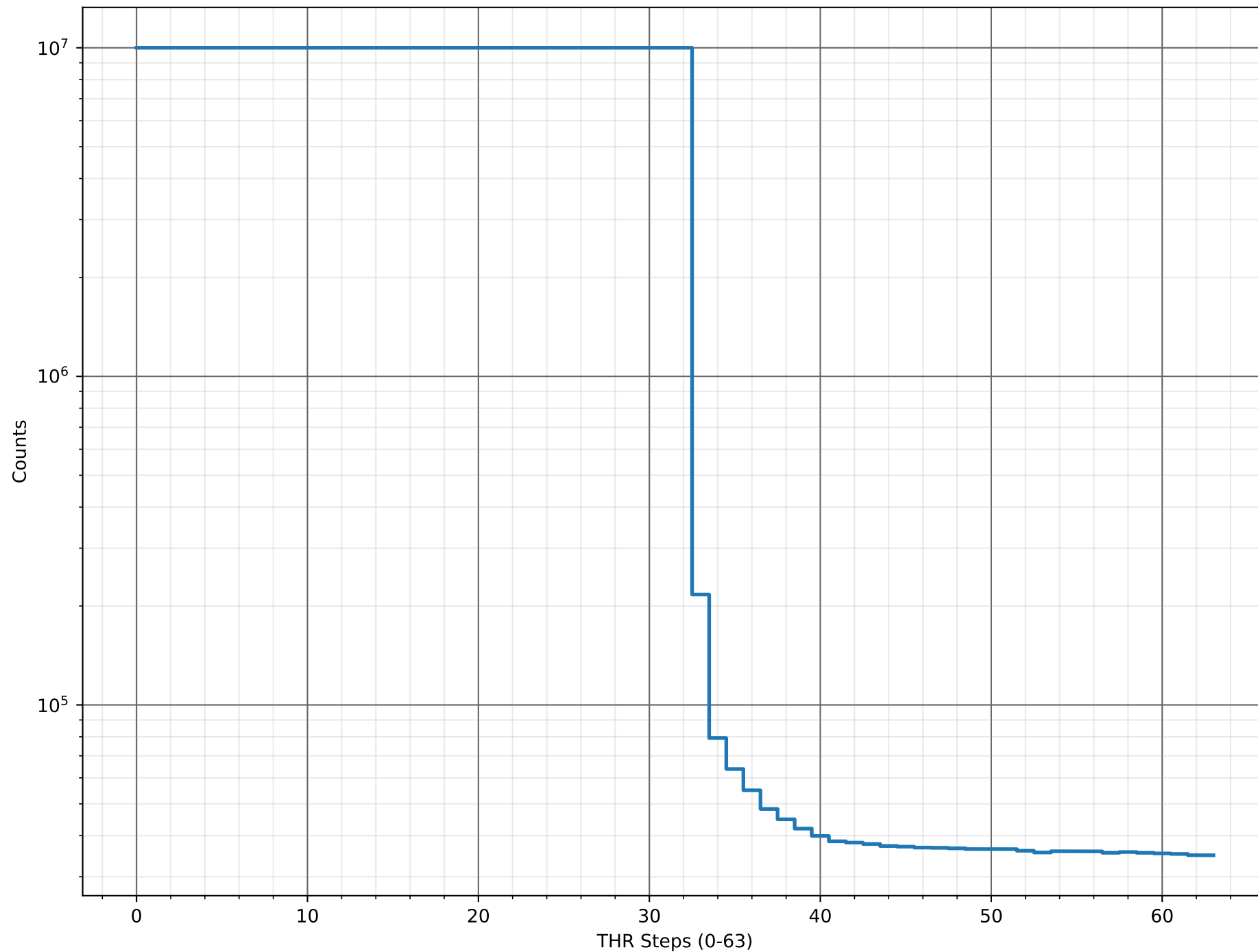
THR Distribution of PMT: D Anode: 18 (UART: 1 FEB: 1 CHIP: 3 CHANNEL: 5)



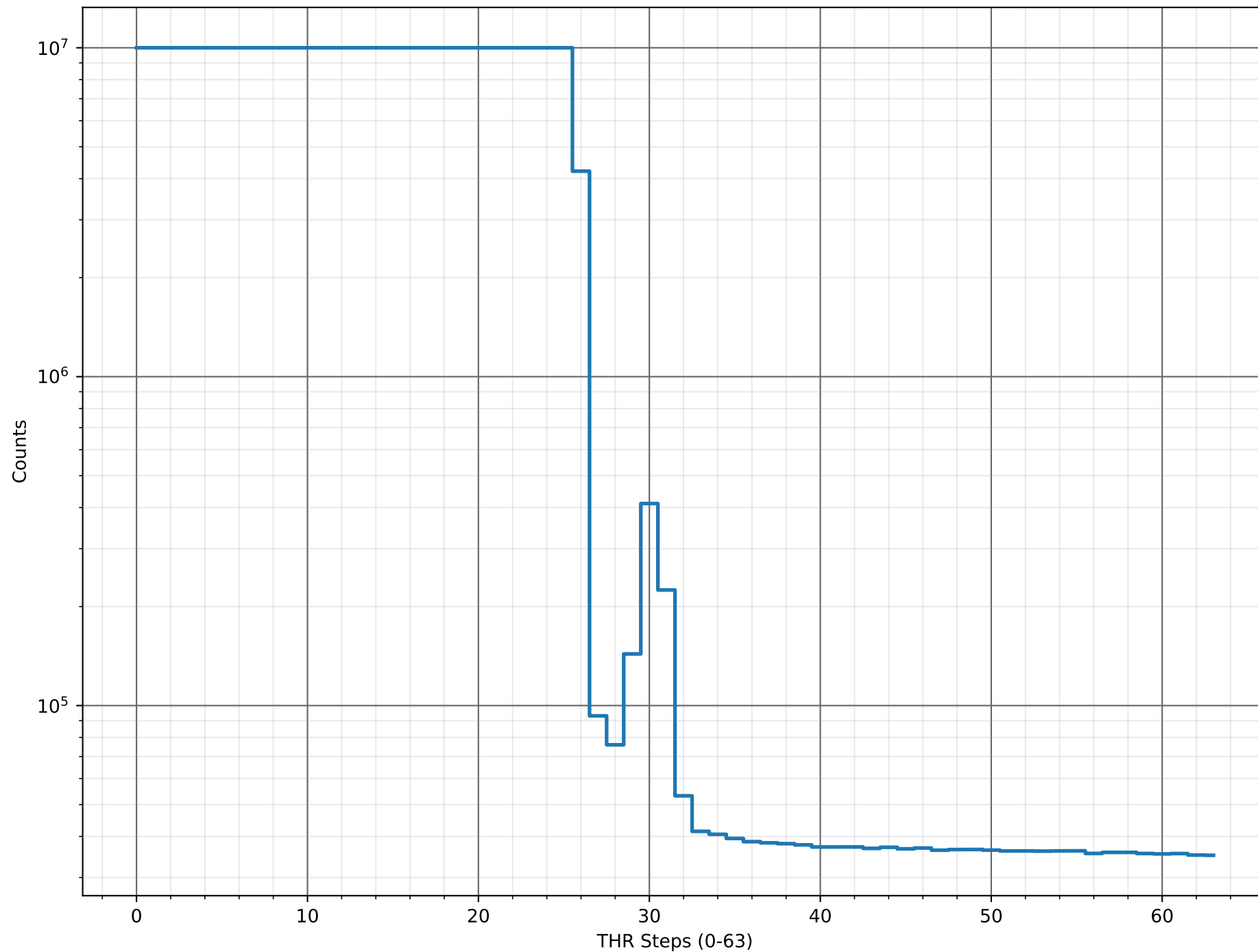
THR Distribution of PMT: D Anode: 19 (UART: 1 FEB: 1 CHIP: 3 CHANNEL: 3)



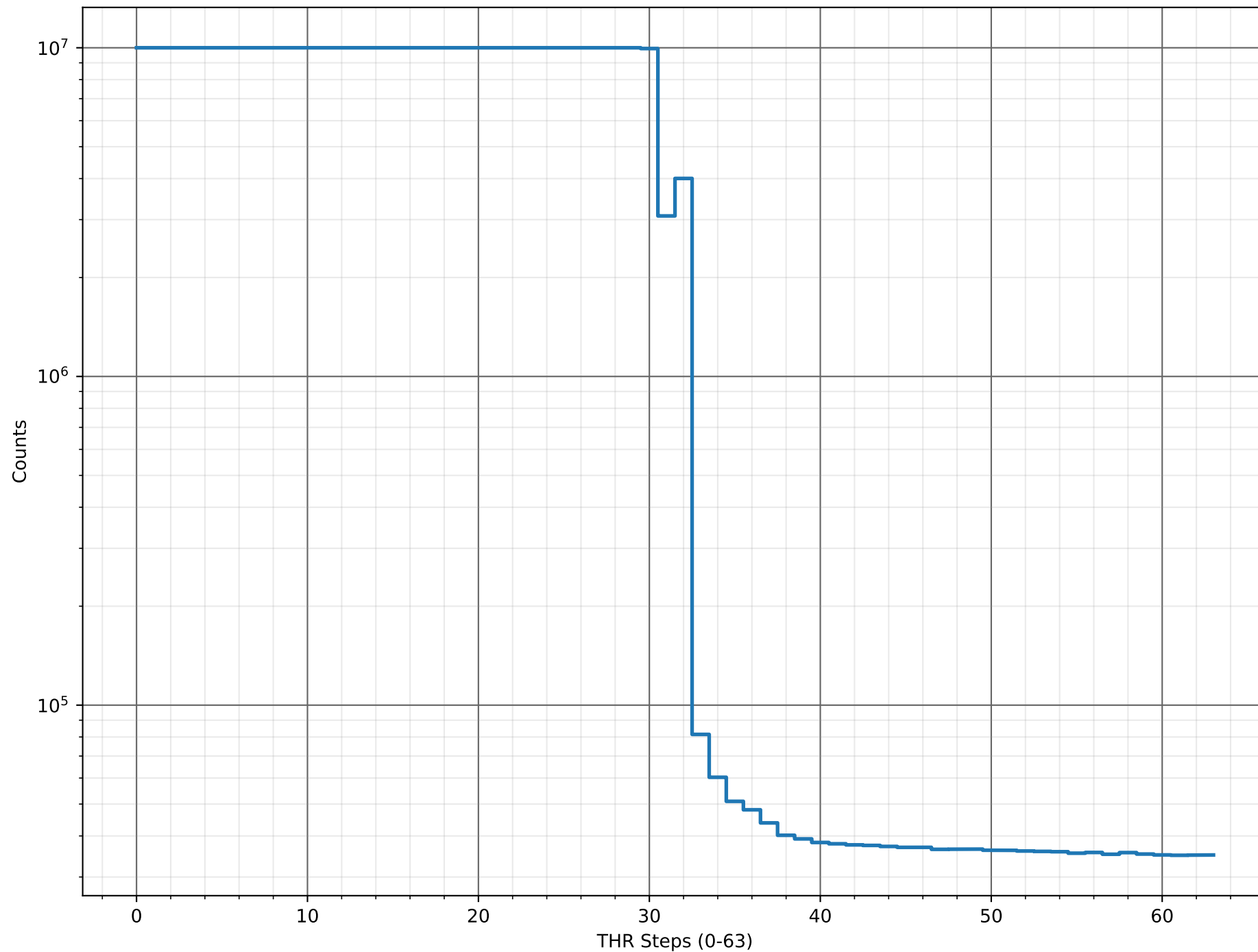
THR Distribution of PMT: D Anode: 20 (UART: 1 FEB: 1 CHIP: 3 CHANNEL: 0)



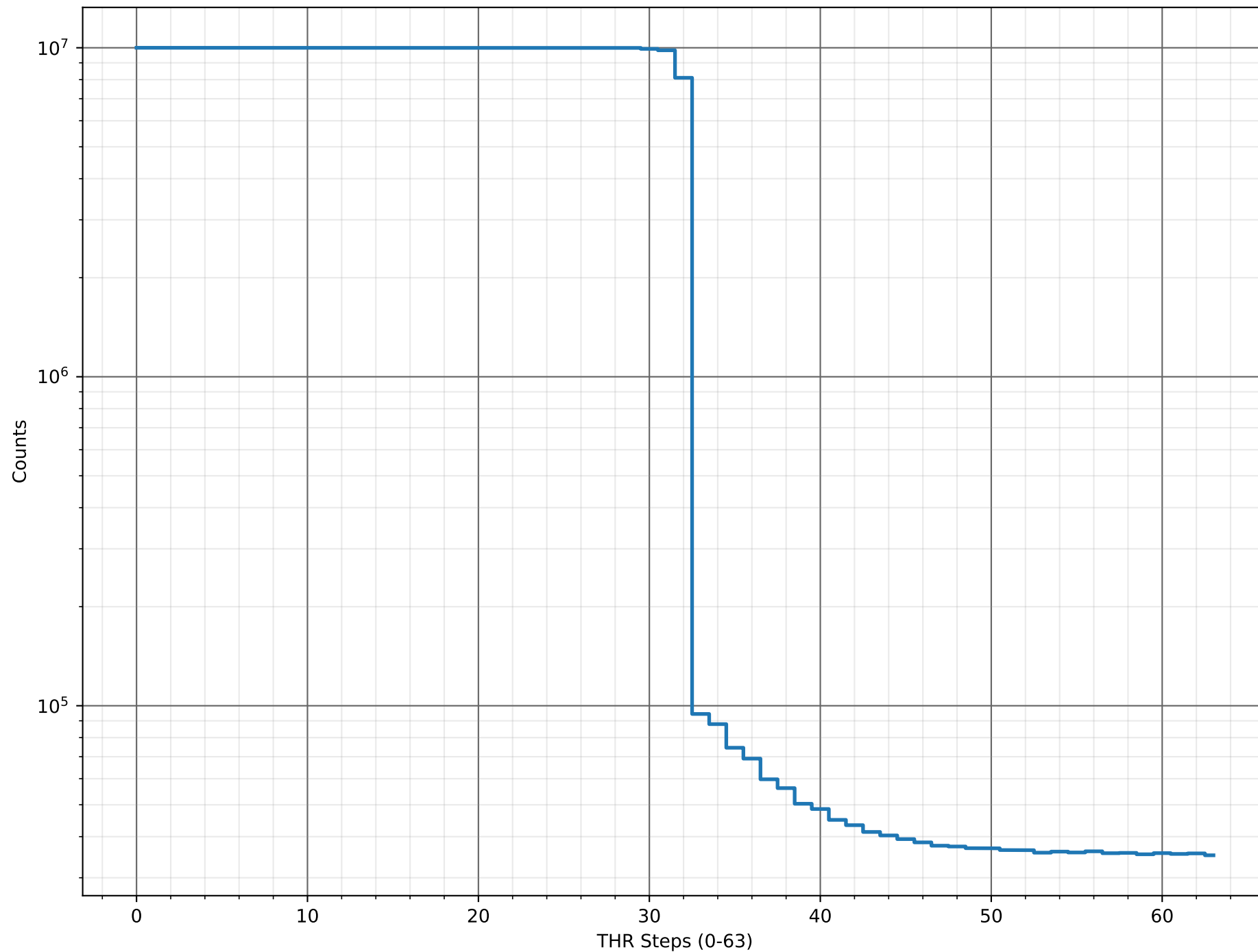
THR Distribution of PMT: D Anode: 21 (UART: 1 FEB: 1 CHIP: 1 CHANNEL: 6)



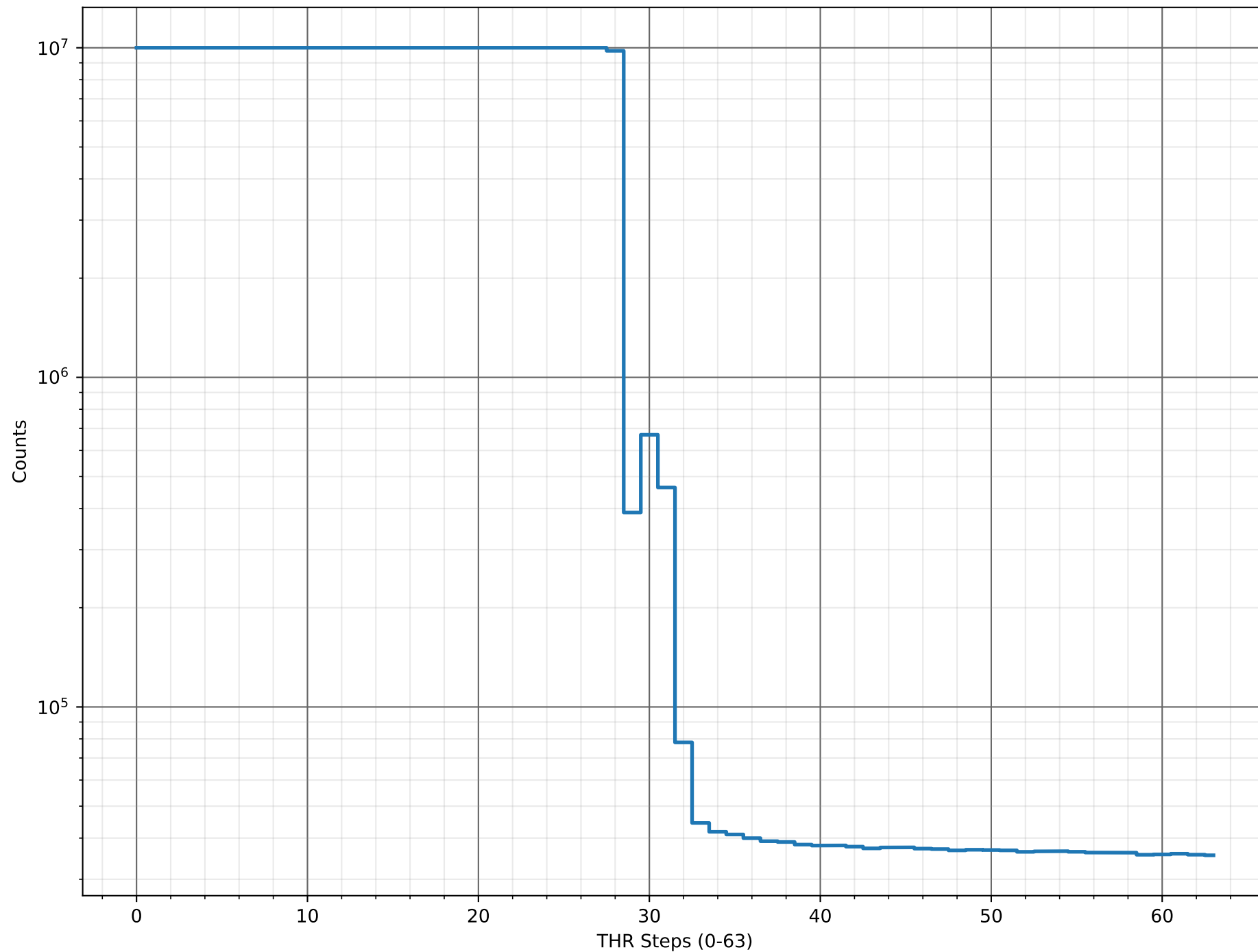
THR Distribution of PMT: D Anode: 22 (UART: 1 FEB: 1 CHIP: 1 CHANNEL: 4)



THR Distribution of PMT: D Anode: 23 (UART: 1 FEB: 1 CHIP: 1 CHANNEL: 2)

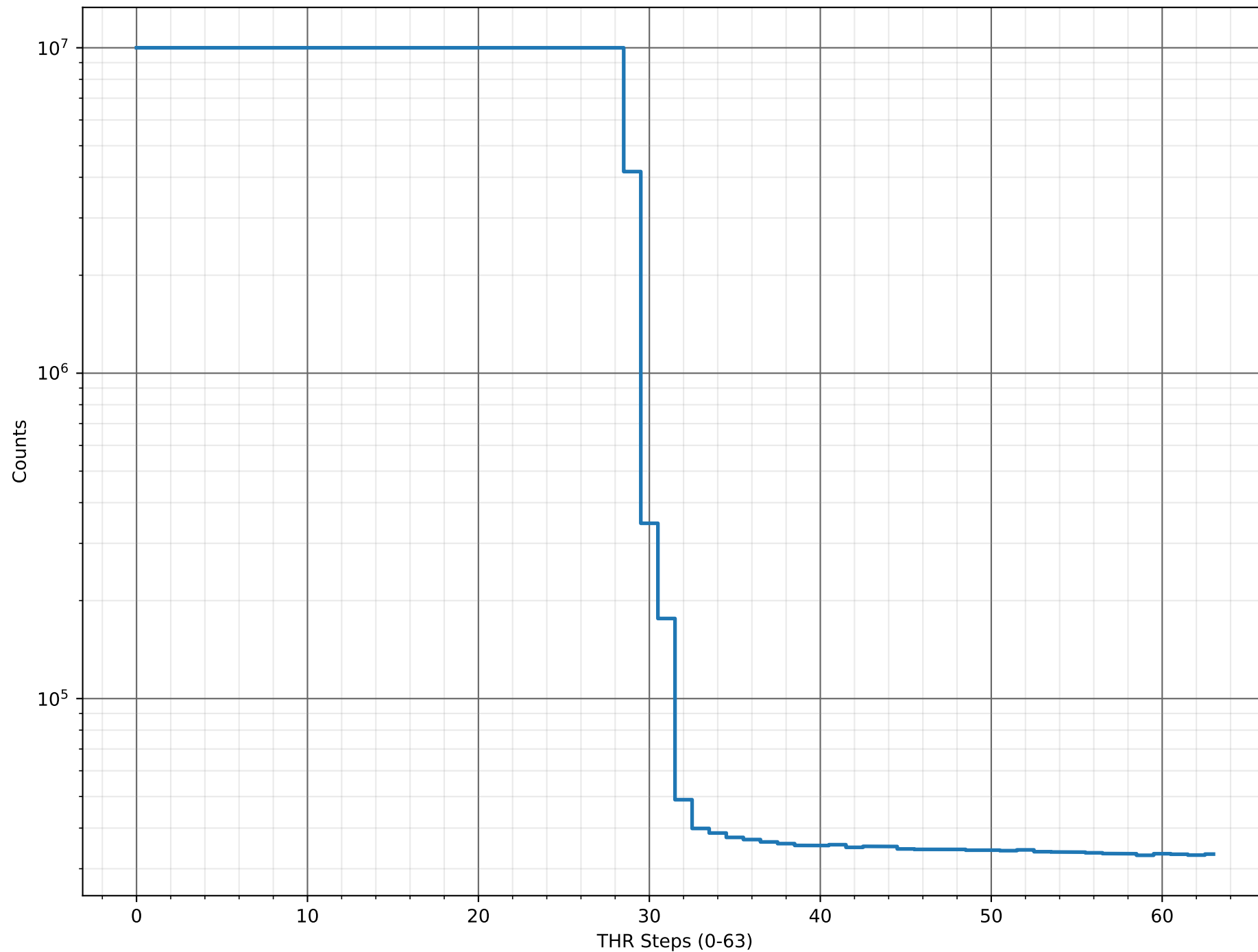


THR Distribution of PMT: D Anode: 24 (UART: 1 FEB: 1 CHIP: 1 CHANNEL: 0)

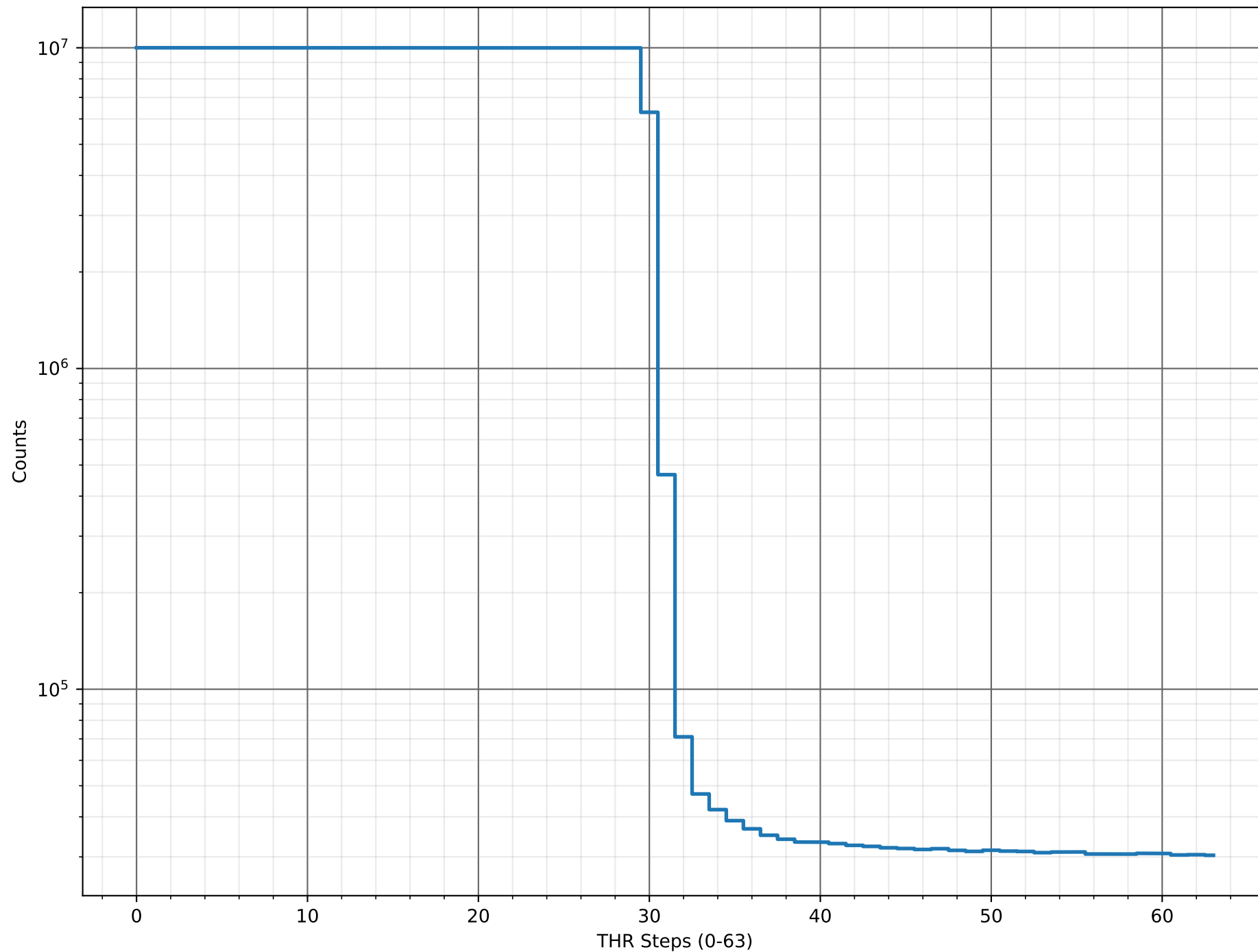




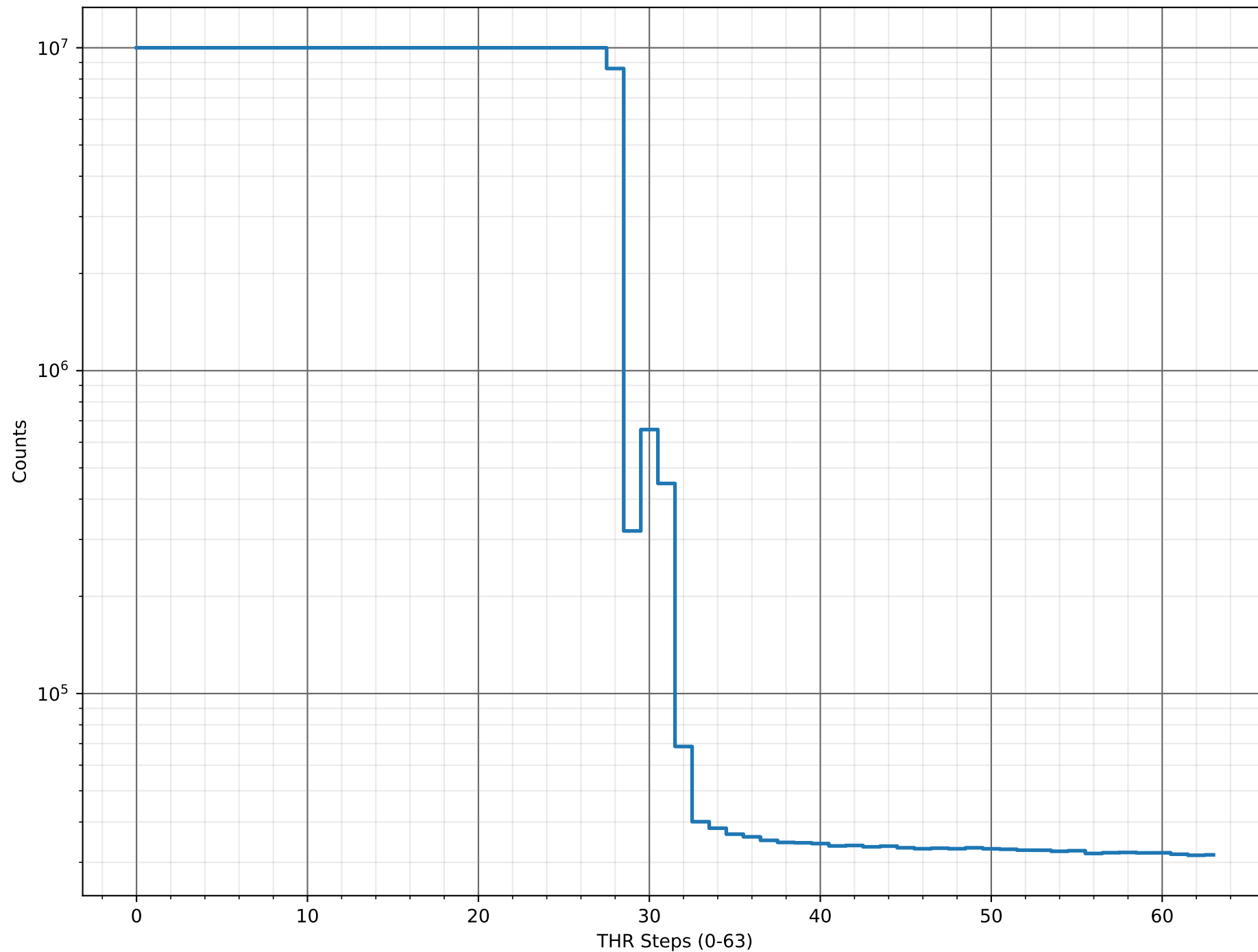
THR Distribution of PMT: D Anode: 25 (UART: 1 FEB: 1 CHIP: 3 CHANNEL: 6)



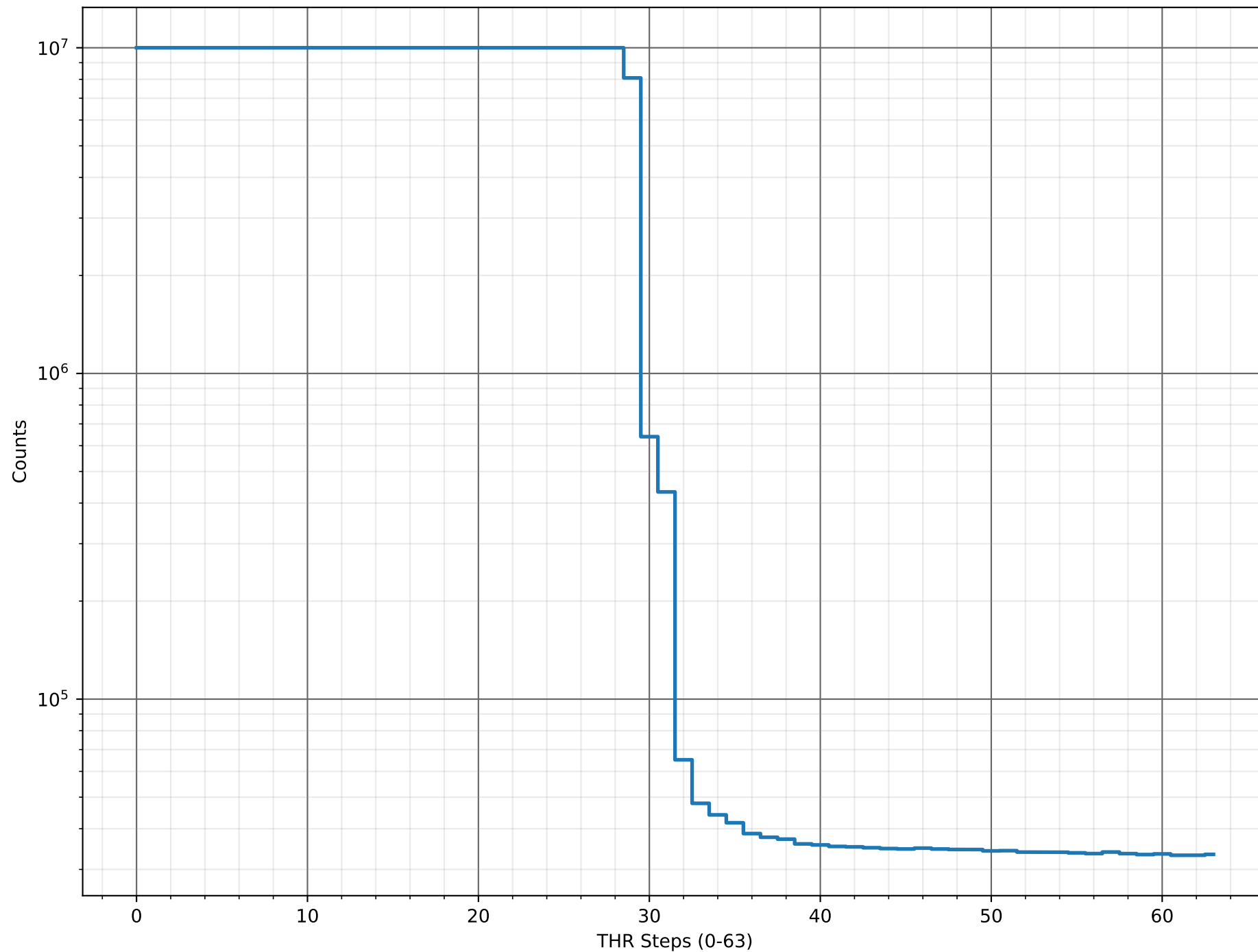
THR Distribution of PMT: D Anode: 26 (UART: 1 FEB: 1 CHIP: 3 CHANNEL: 4)



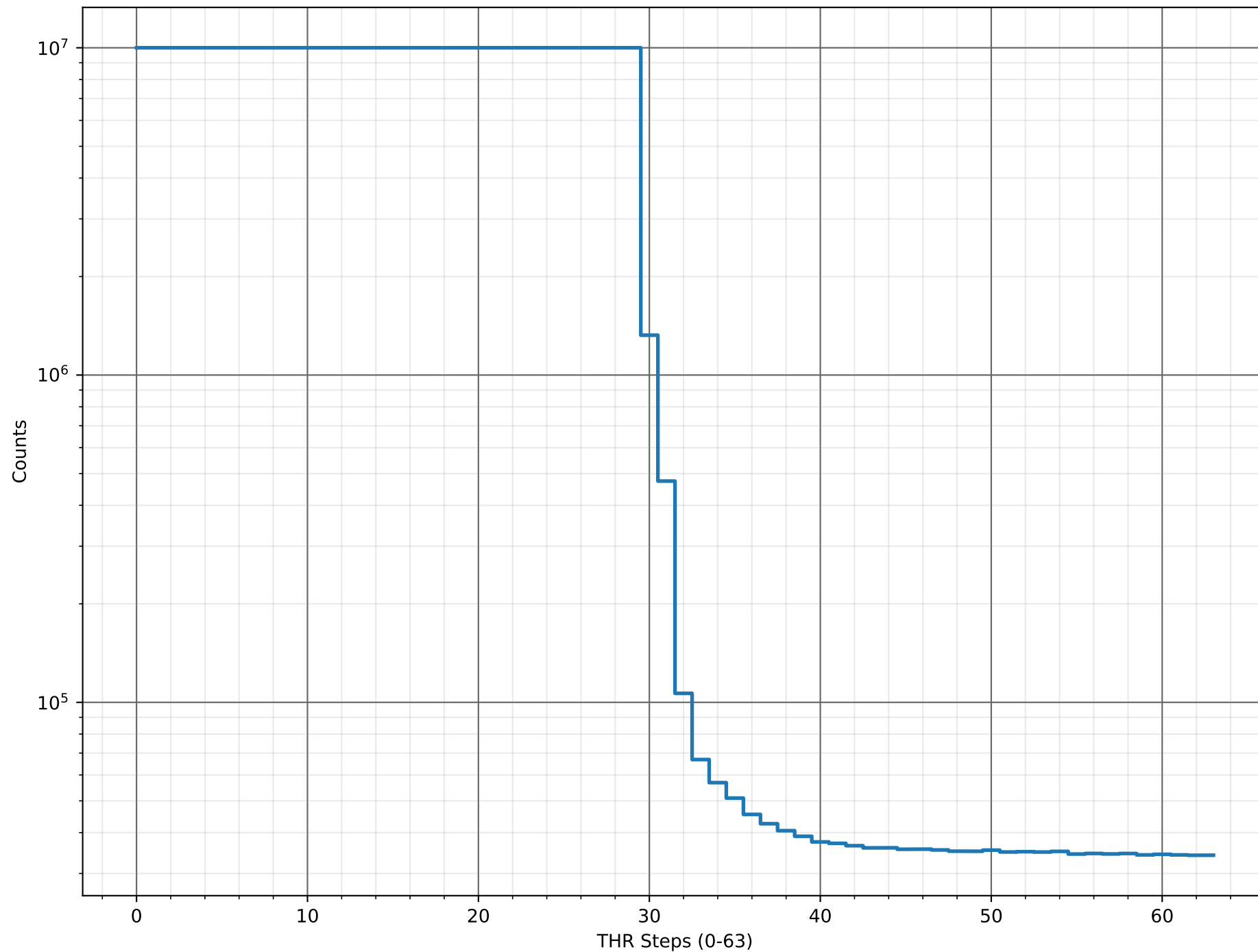
THR Distribution of PMT: D Anode: 27 (UART: 1 FEB: 1 CHIP: 3 CHANNEL: 2)



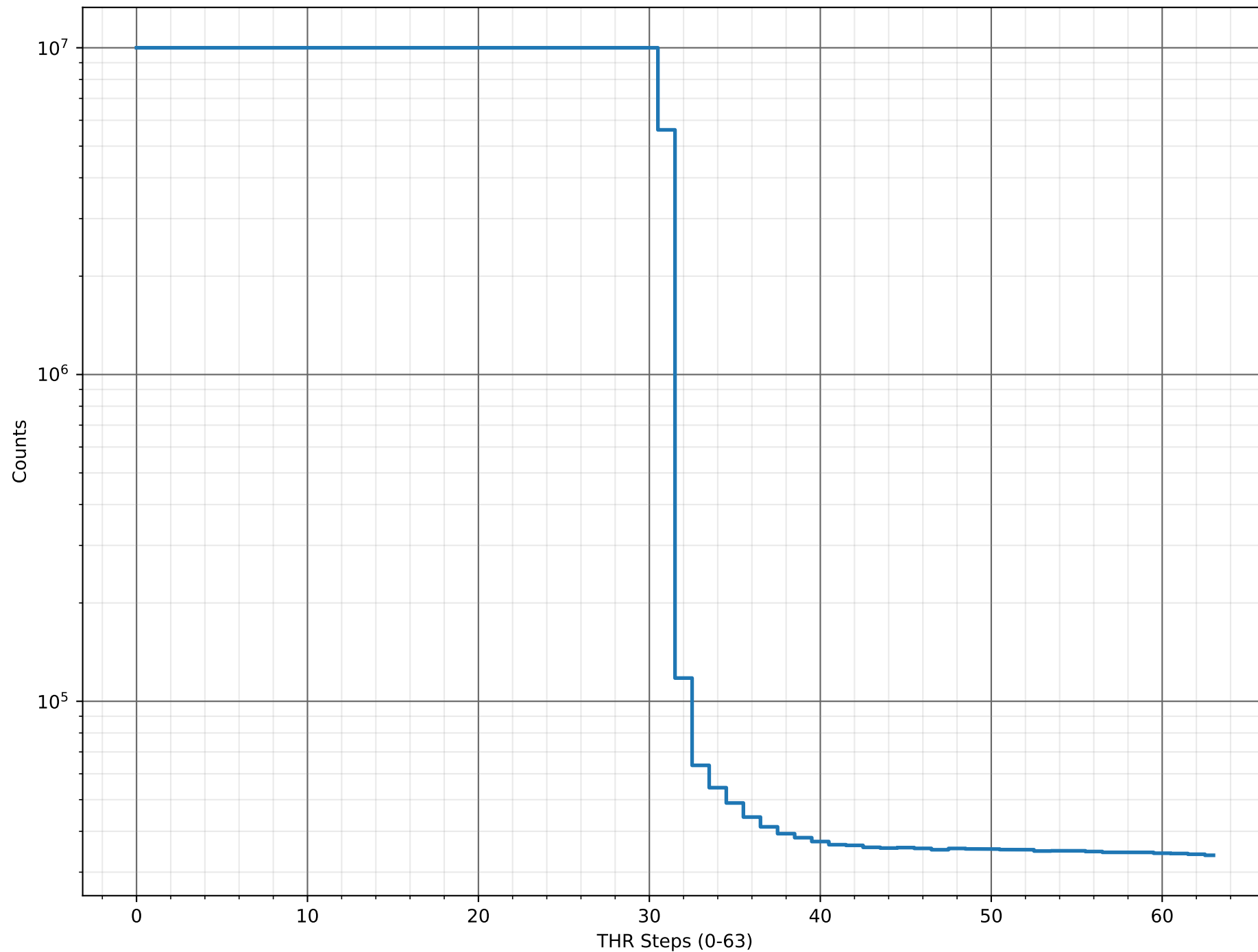
THR Distribution of PMT: D Anode: 28 (UART: 1 FEB: 1 CHIP: 3 CHANNEL: 1)



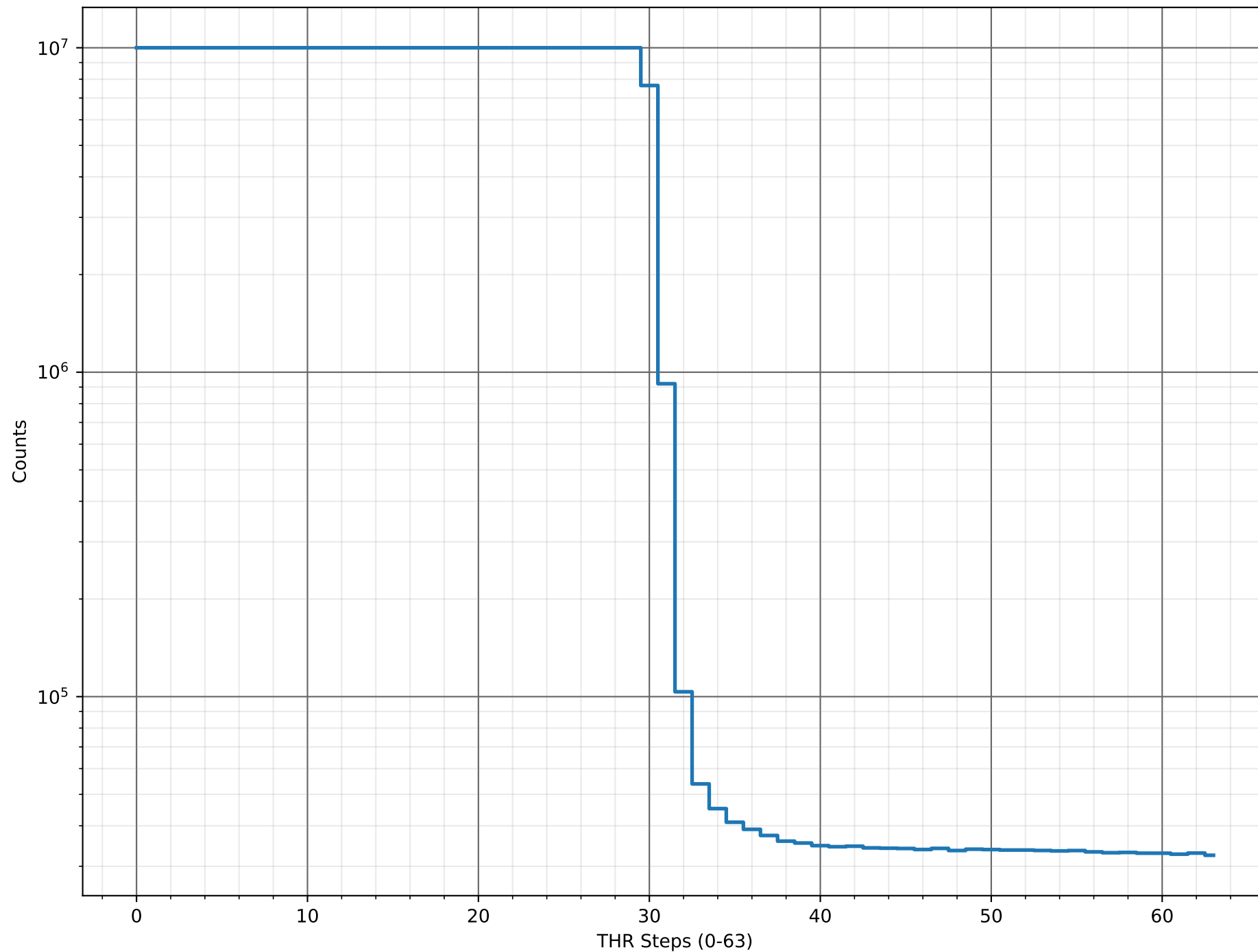
THR Distribution of PMT: D Anode: 29 (UART: 1 FEB: 1 CHIP: 1 CHANNEL: 7)



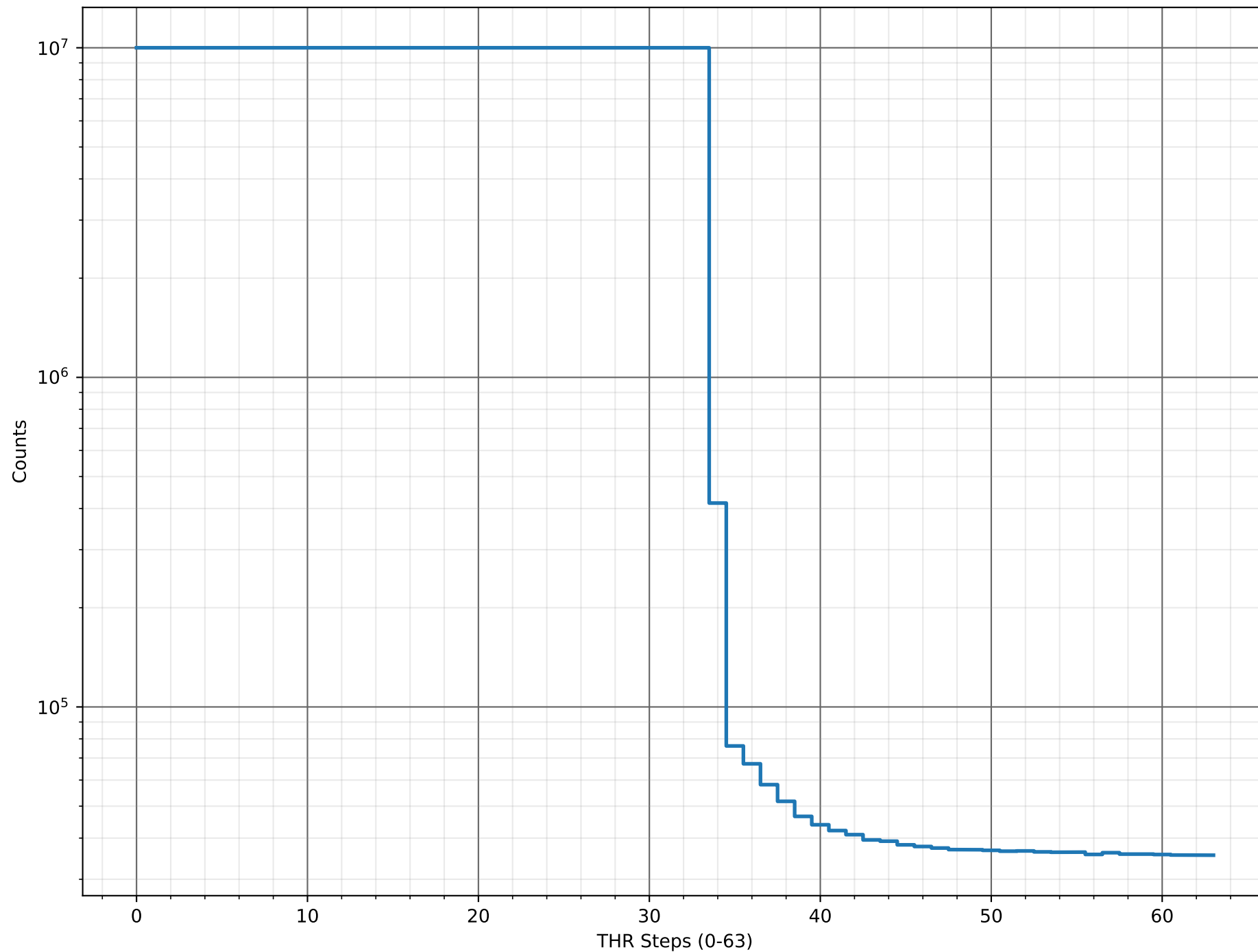
THR Distribution of PMT: D Anode: 30 (UART: 1 FEB: 1 CHIP: 1 CHANNEL: 5)



THR Distribution of PMT: D Anode: 31 (UART: 1 FEB: 1 CHIP: 1 CHANNEL: 3)

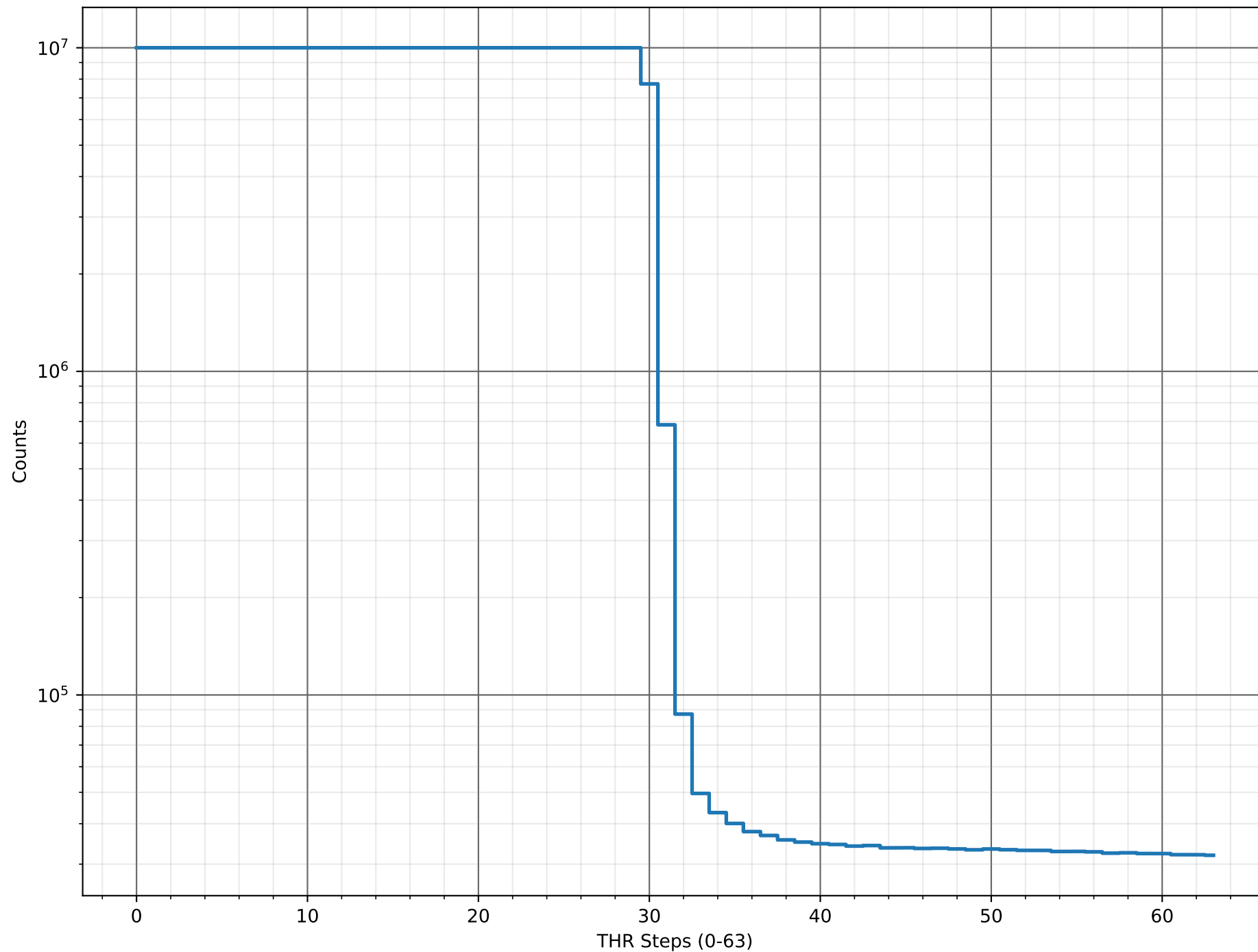


THR Distribution of PMT: D Anode: 32 (UART: 1 FEB: 1 CHIP: 1 CHANNEL: 1)

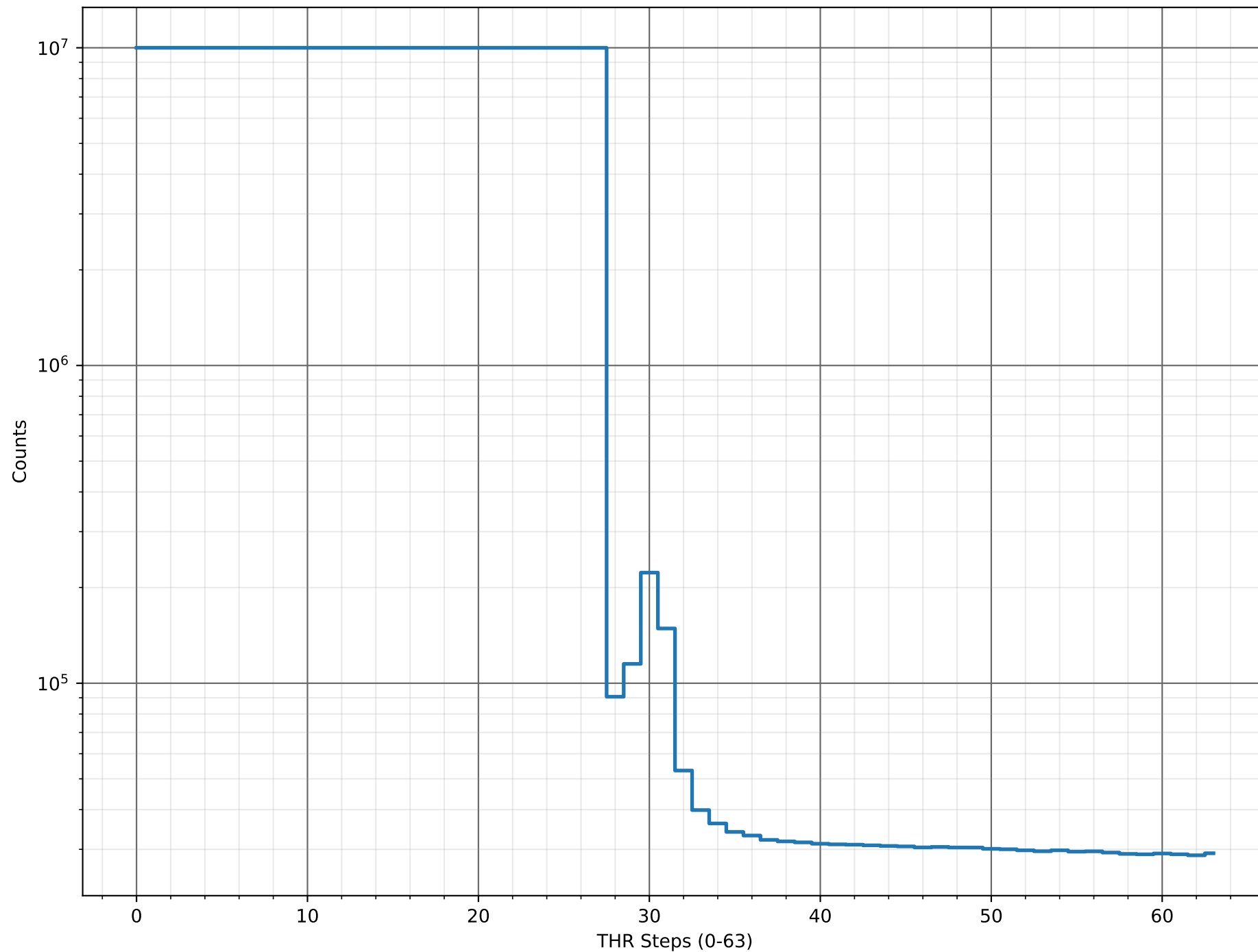




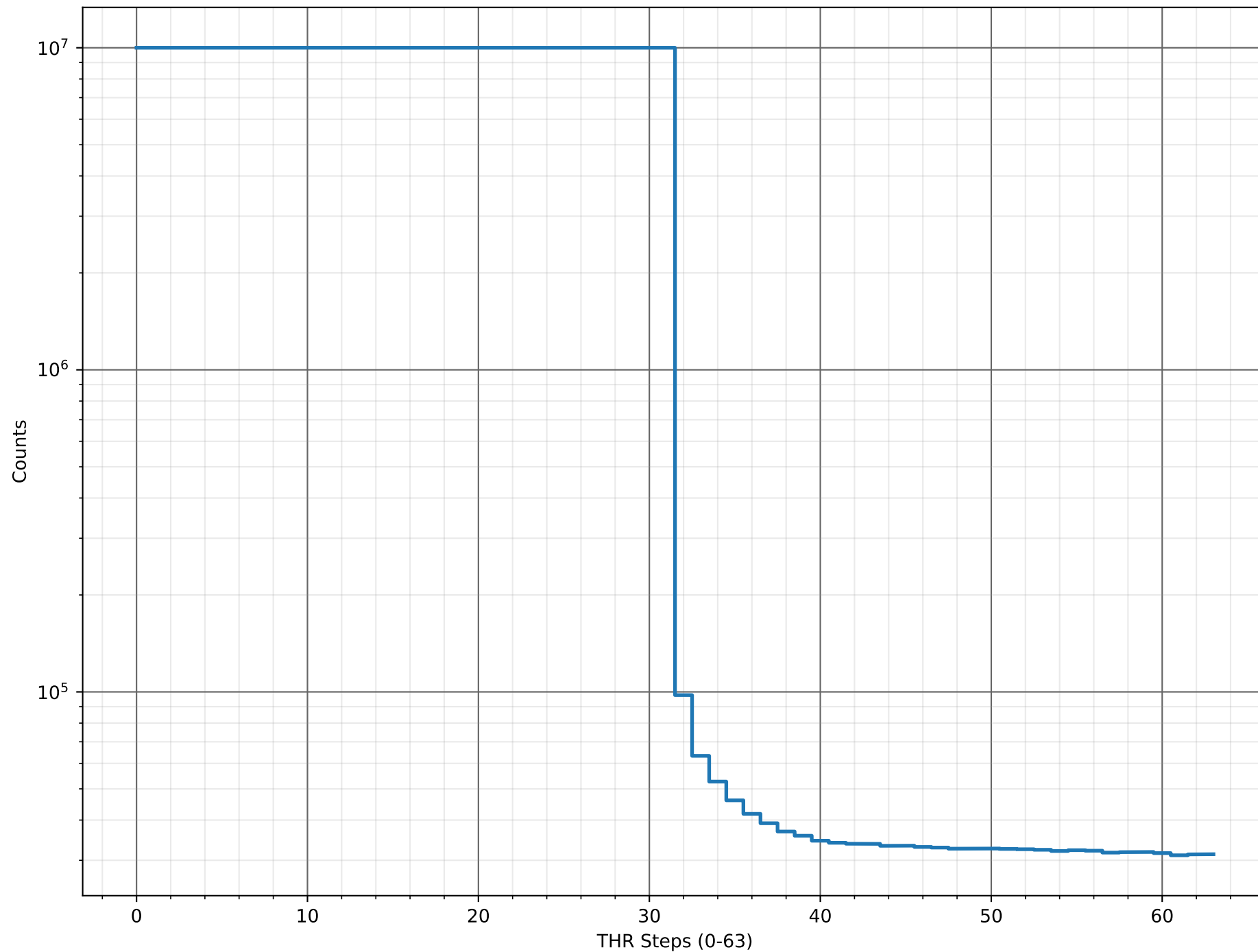
THR Distribution of PMT: D Anode: 33 (UART: 1 FEB: 0 CHIP: 2 CHANNEL: 1)



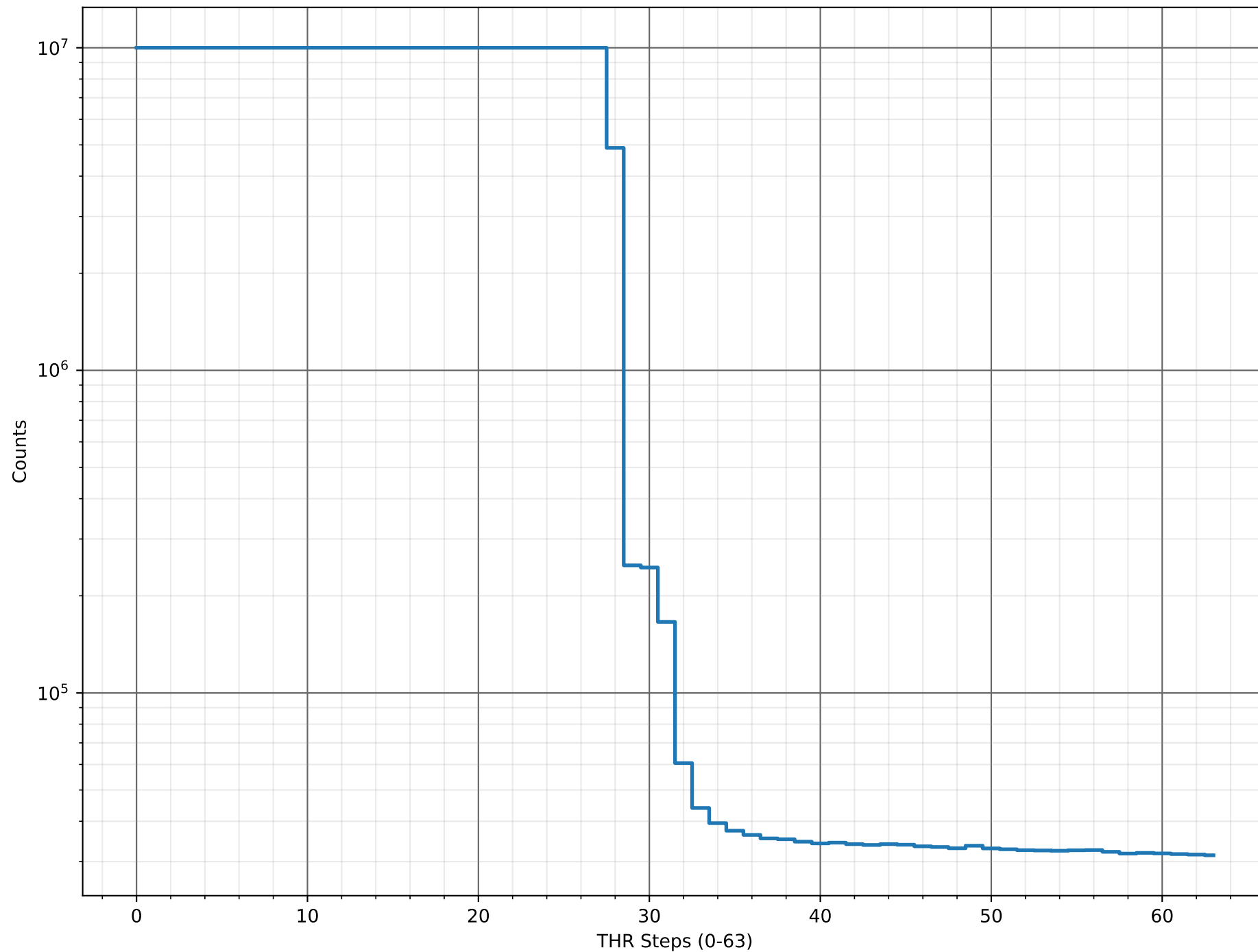
THR Distribution of PMT: D Anode: 34 (UART: 1 FEB: 0 CHIP: 2 CHANNEL: 3)



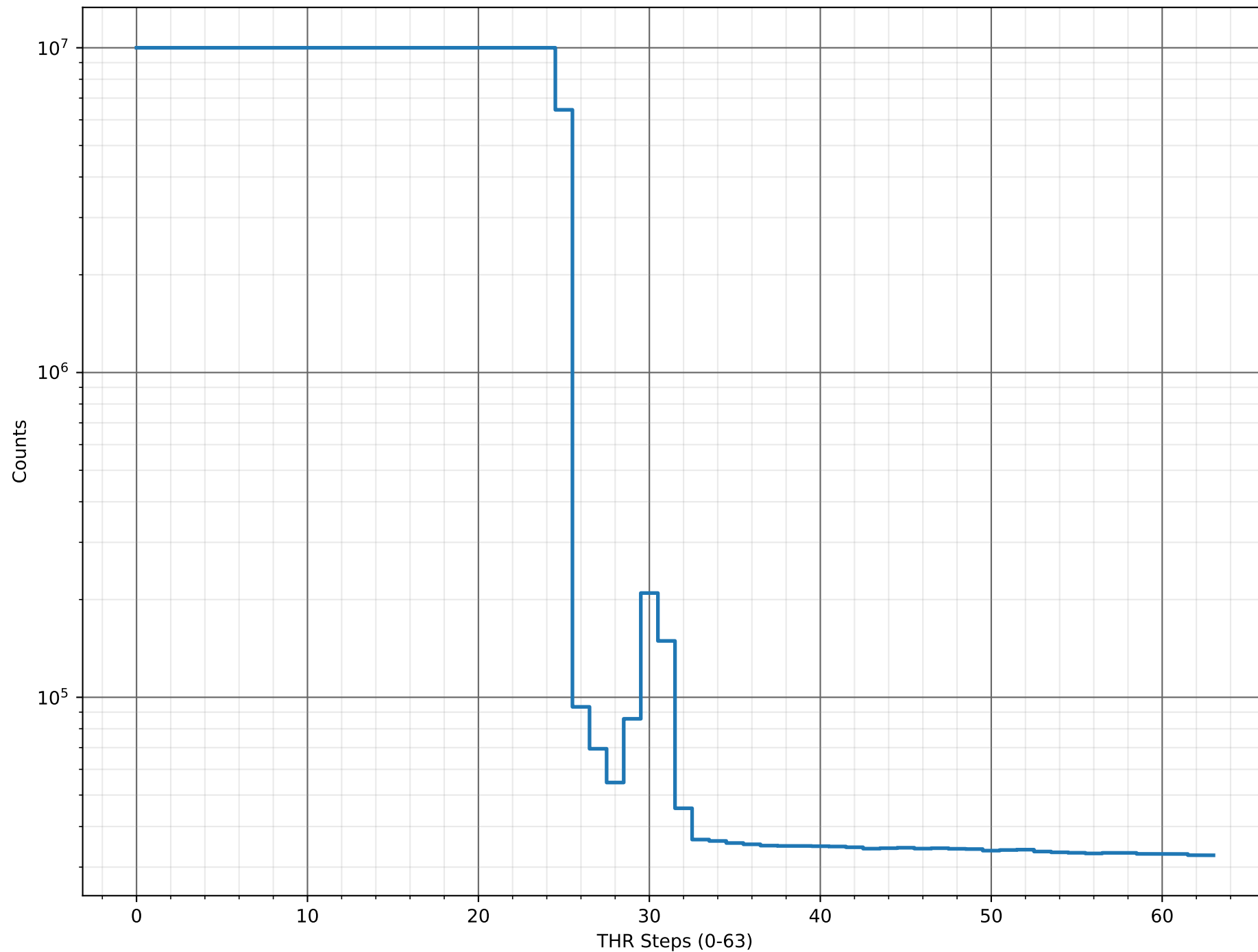
THR Distribution of PMT: D Anode: 35 (UART: 1 FEB: 0 CHIP: 2 CHANNEL: 5)



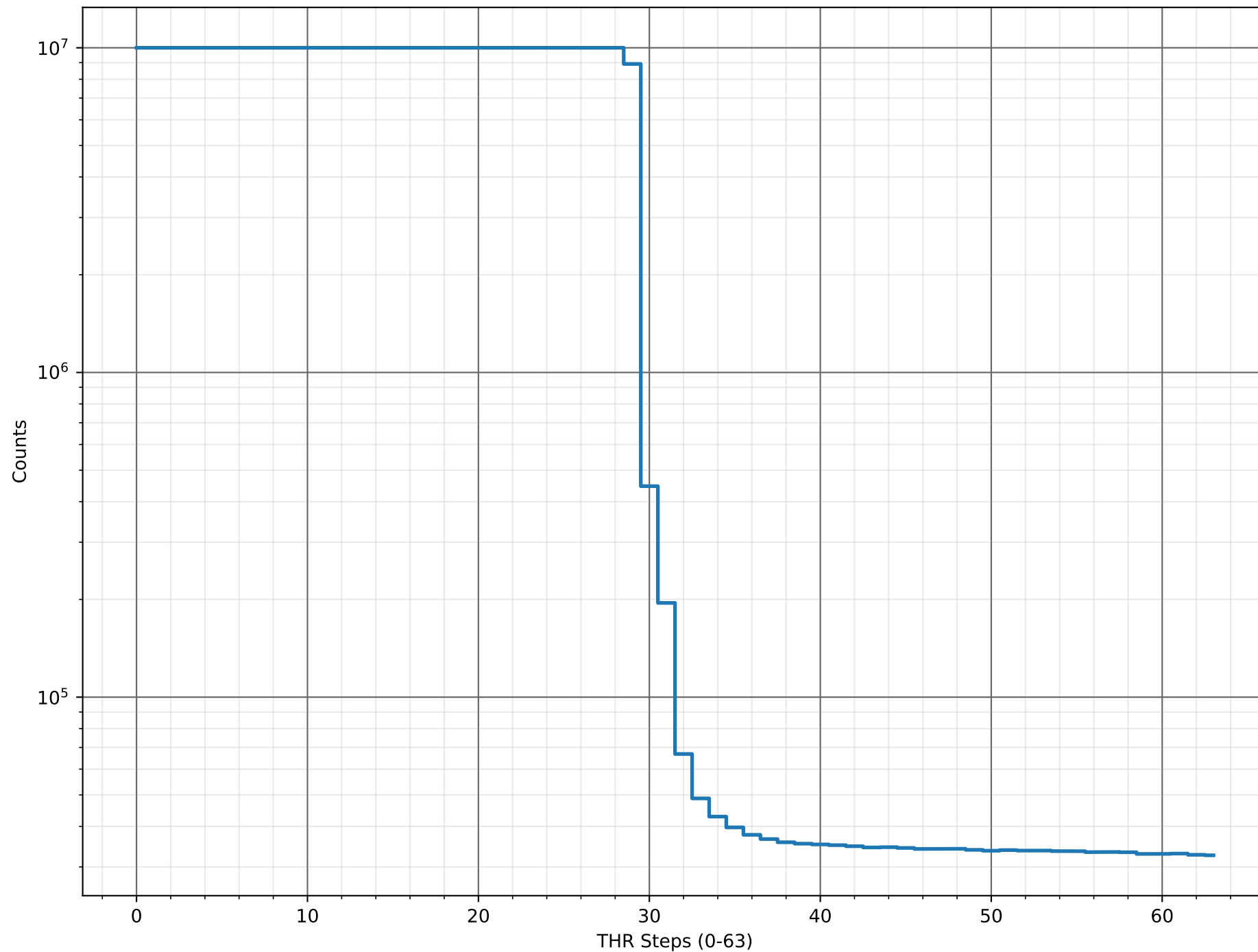
THR Distribution of PMT: D Anode: 36 (UART: 1 FEB: 0 CHIP: 2 CHANNEL: 6)



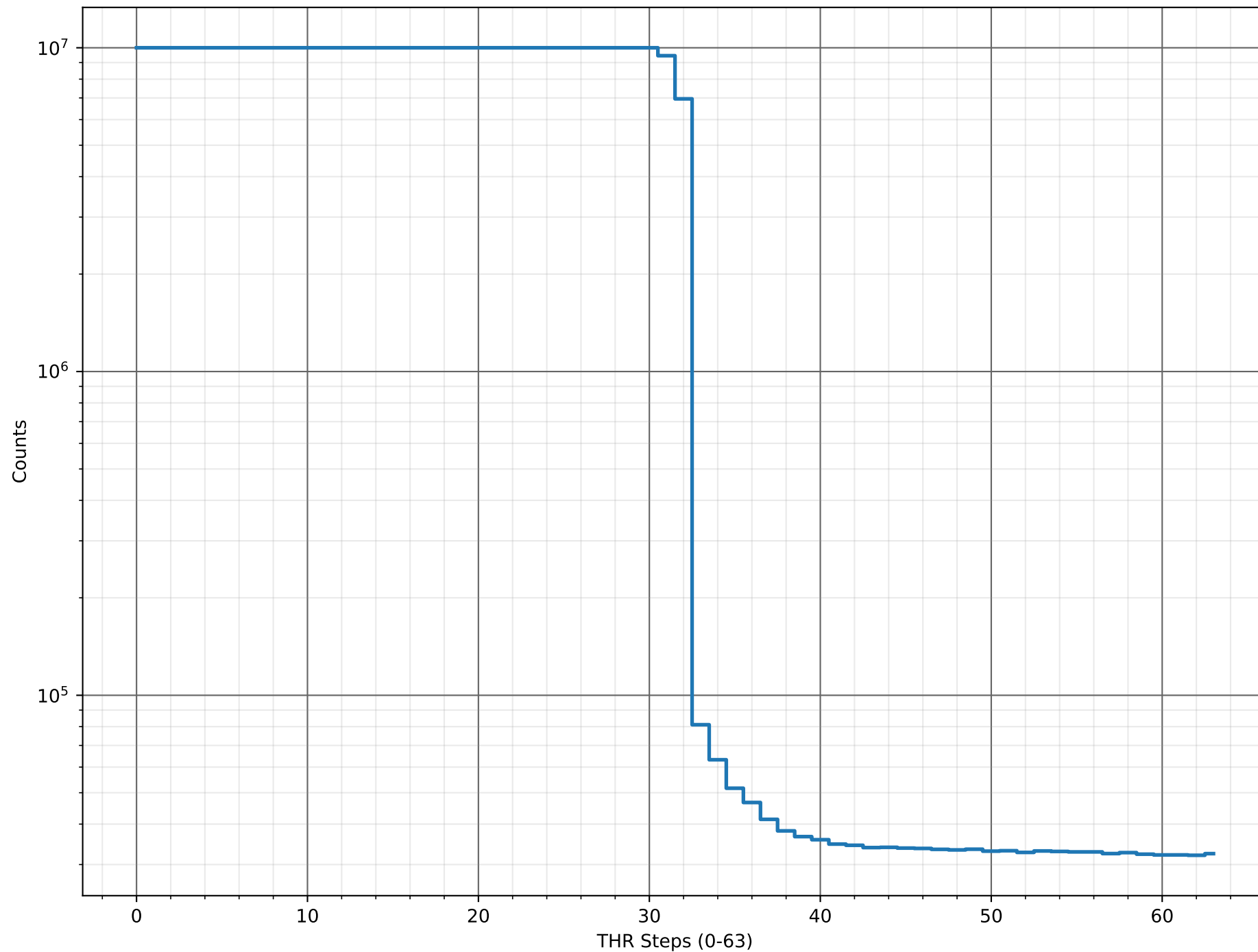
THR Distribution of PMT: D Anode: 37 (UART: 1 FEB: 0 CHIP: 0 CHANNEL: 0)



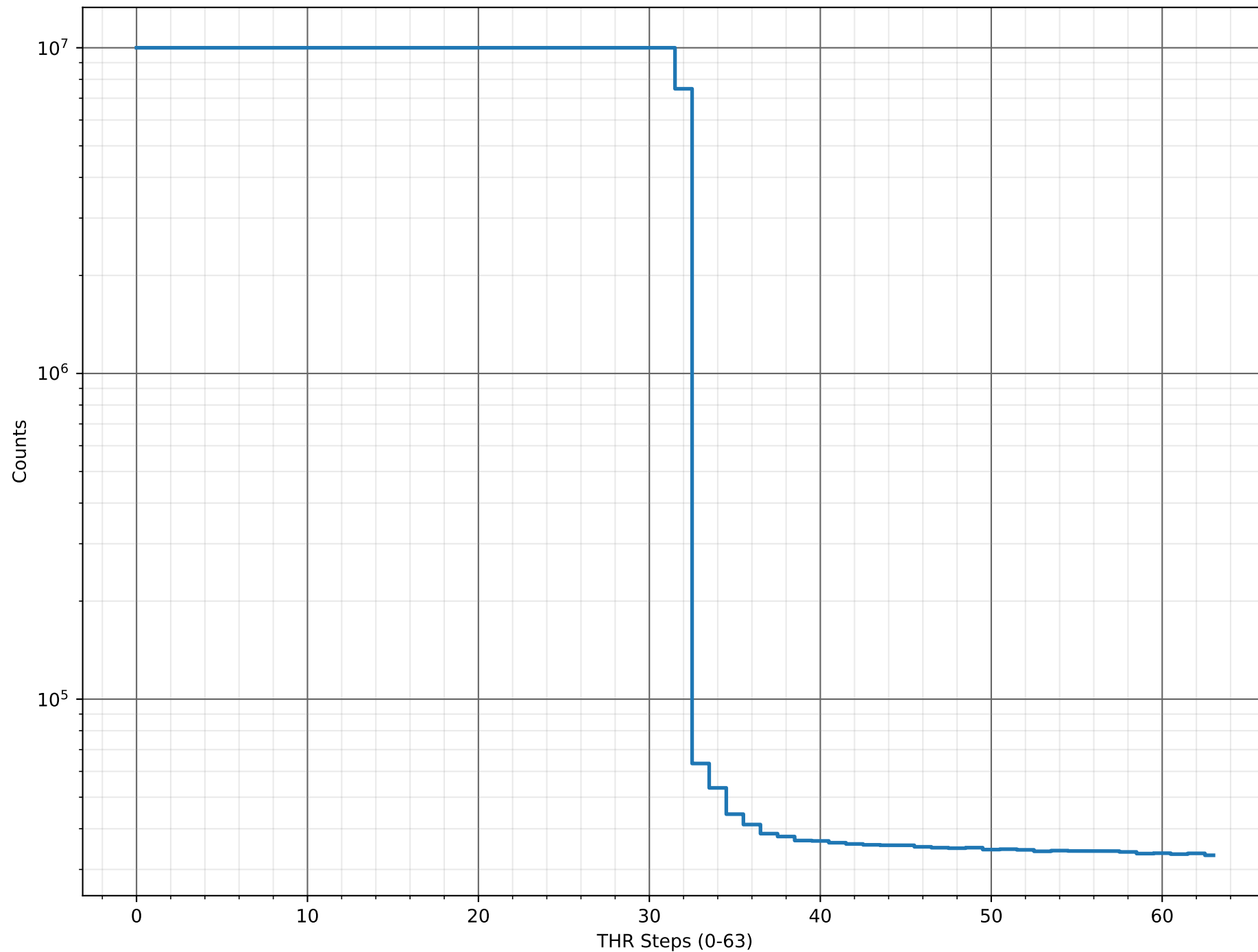
THR Distribution of PMT: D Anode: 38 (UART: 1 FEB: 0 CHIP: 0 CHANNEL: 2)



THR Distribution of PMT: D Anode: 39 (UART: 1 FEB: 0 CHIP: 0 CHANNEL: 4)

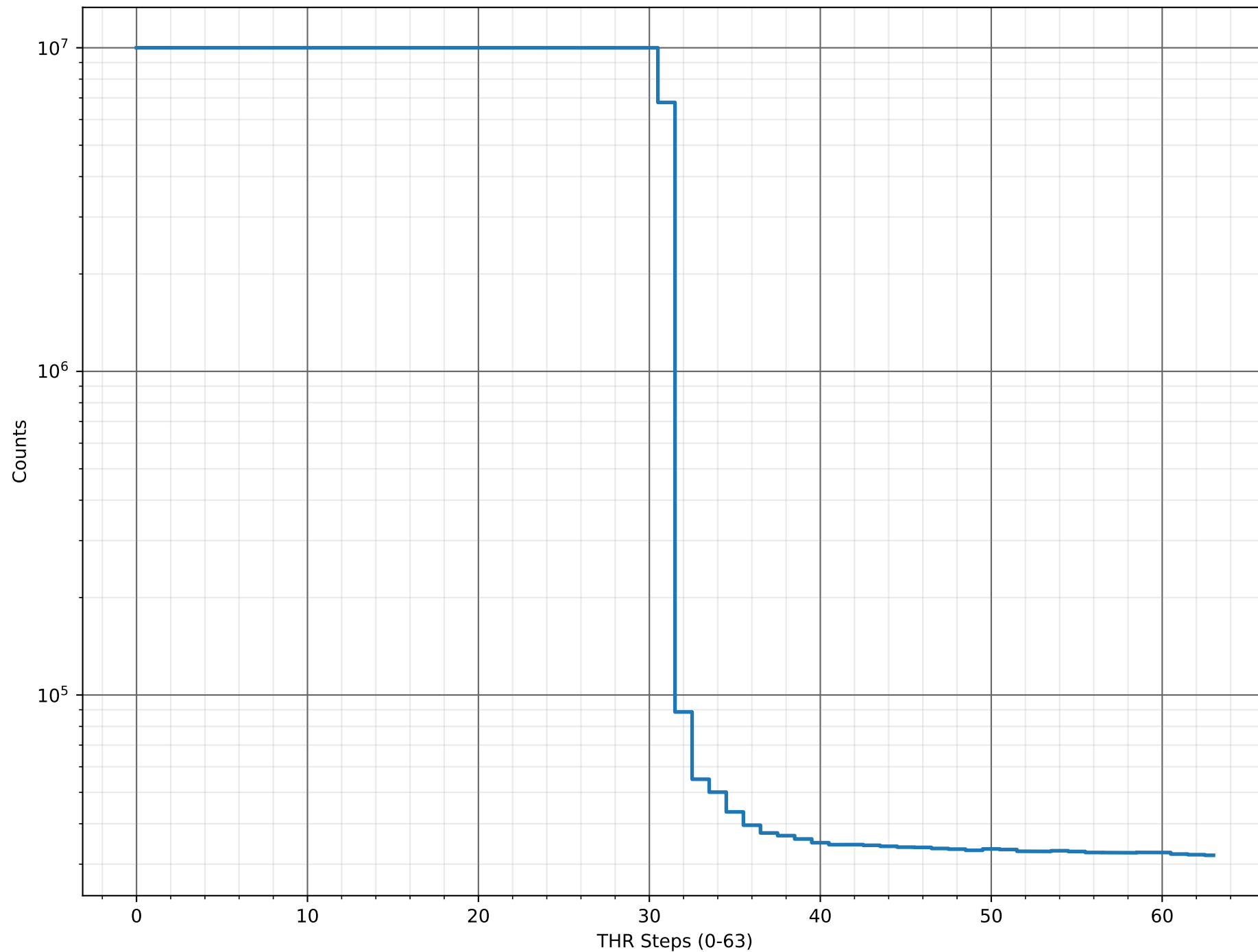


THR Distribution of PMT: D Anode: 40 (UART: 1 FEB: 0 CHIP: 0 CHANNEL: 6)

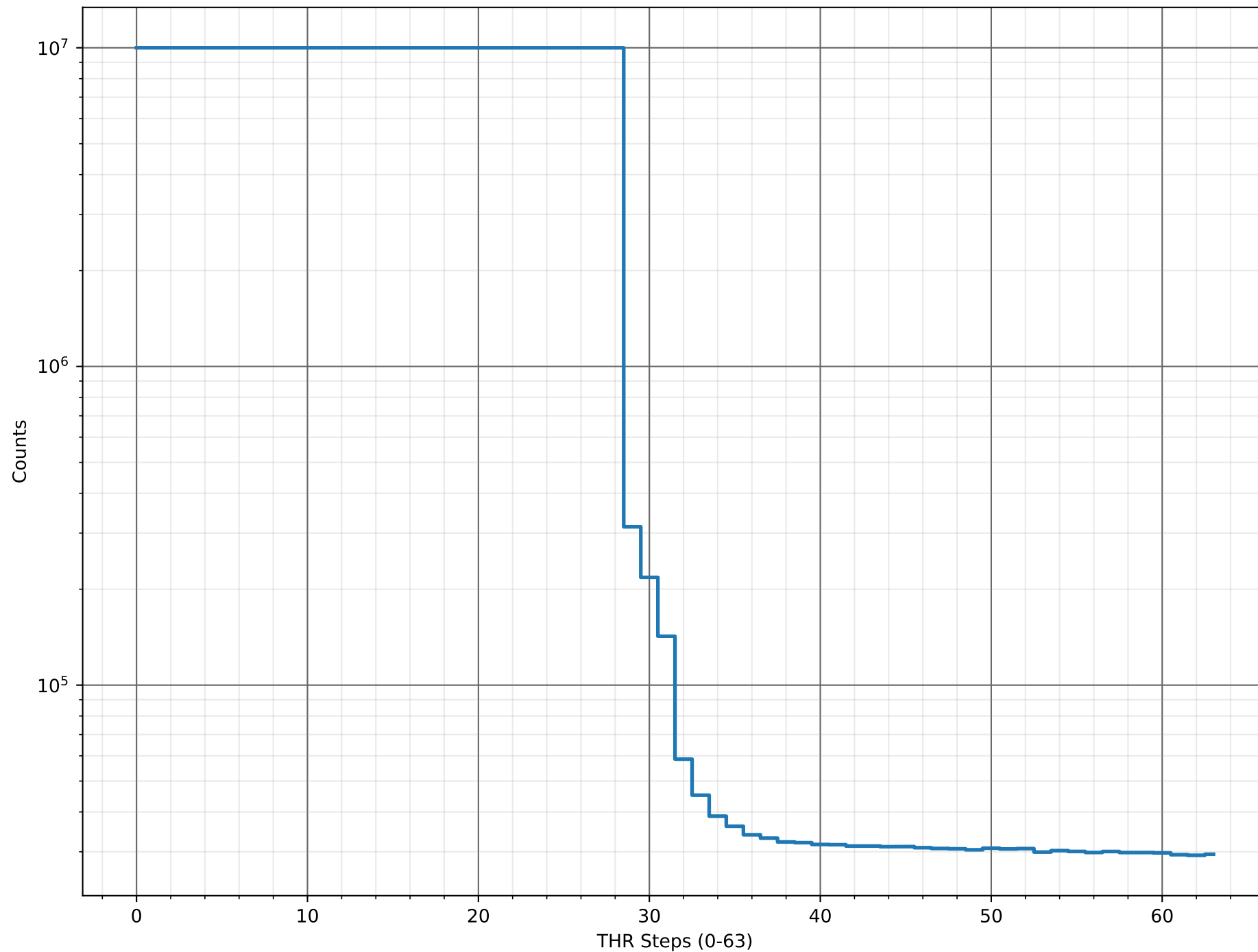




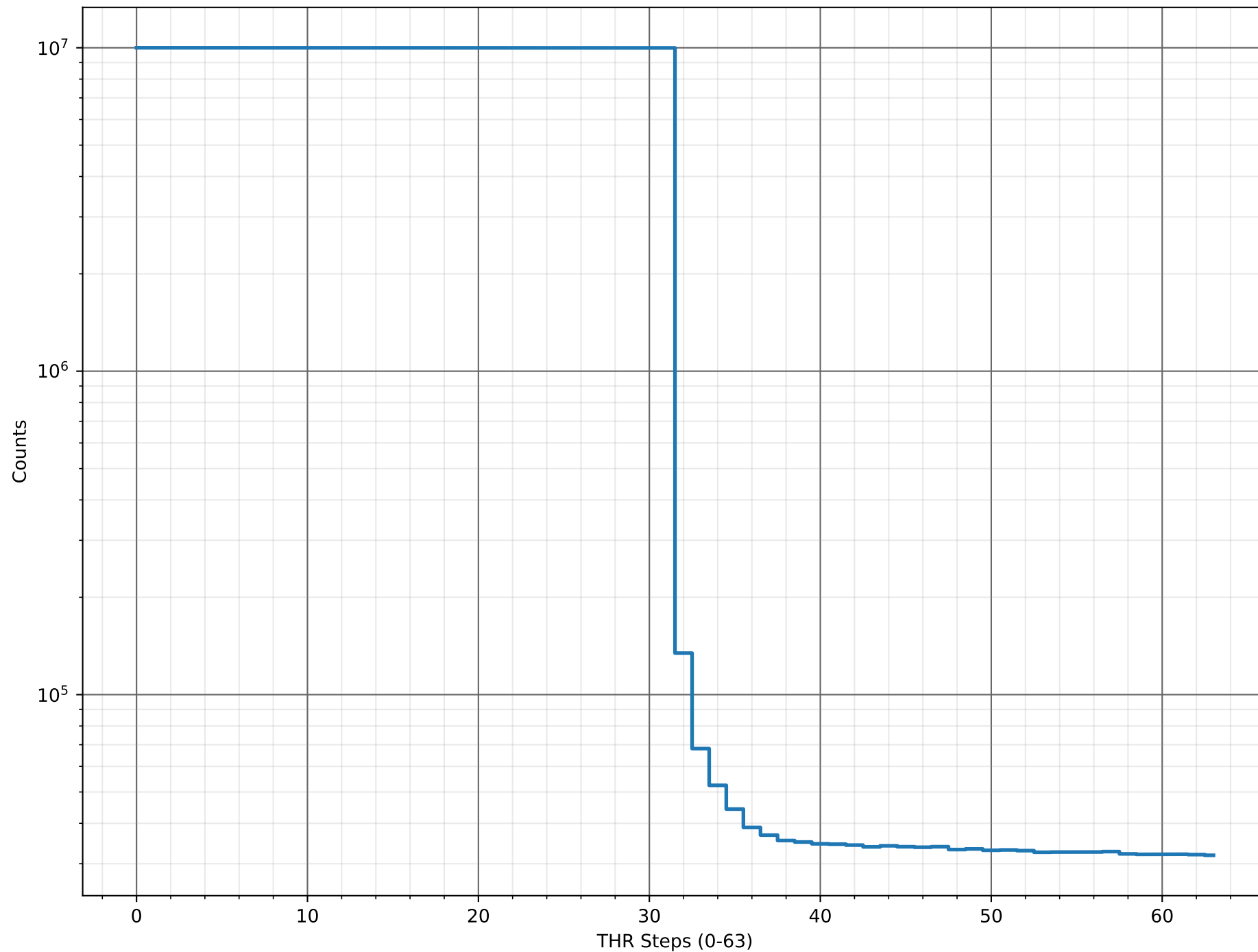
THR Distribution of PMT: D Anode: 41 (UART: 1 FEB: 0 CHIP: 2 CHANNEL: 0)



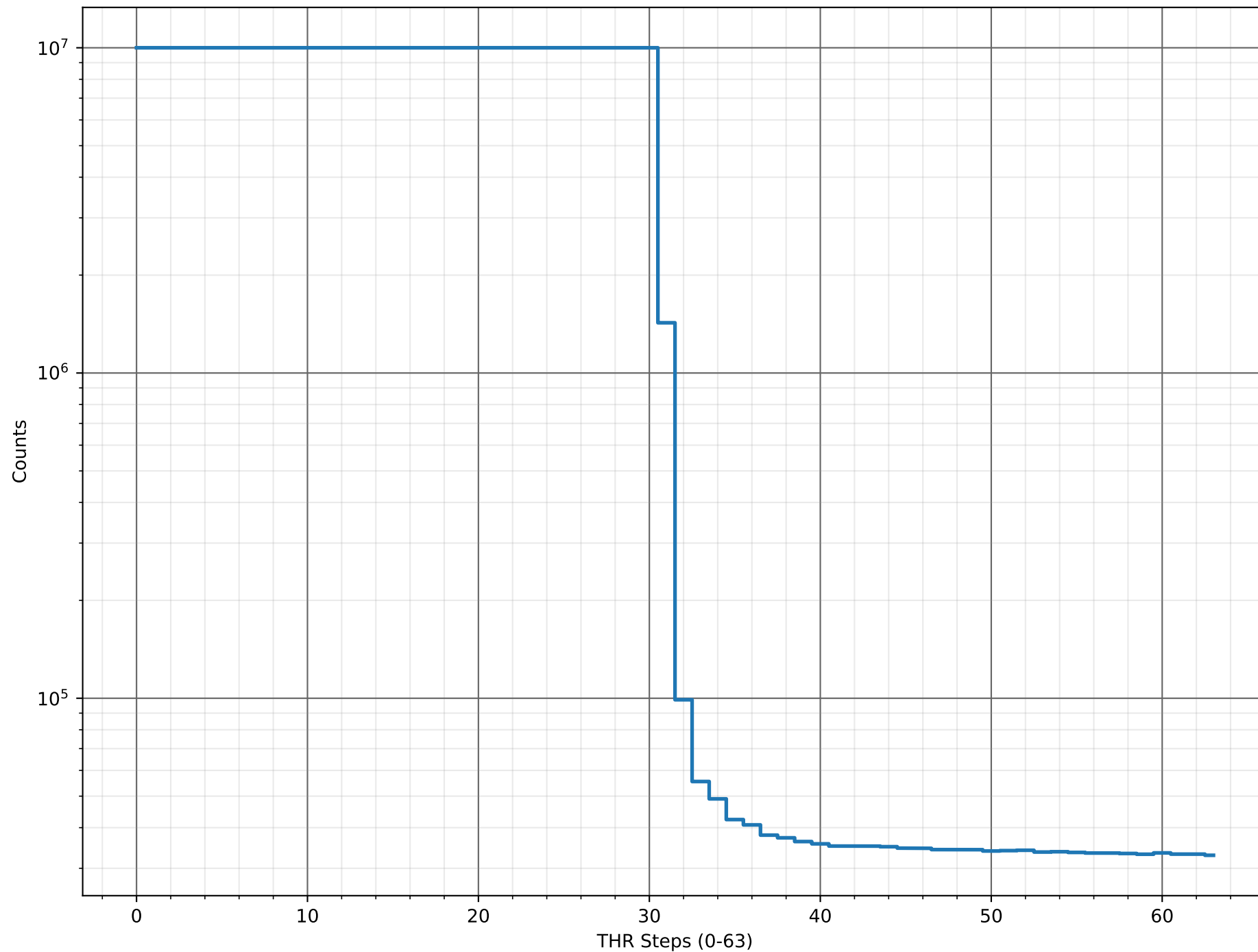
THR Distribution of PMT: D Anode: 42 (UART: 1 FEB: 0 CHIP: 2 CHANNEL: 2)



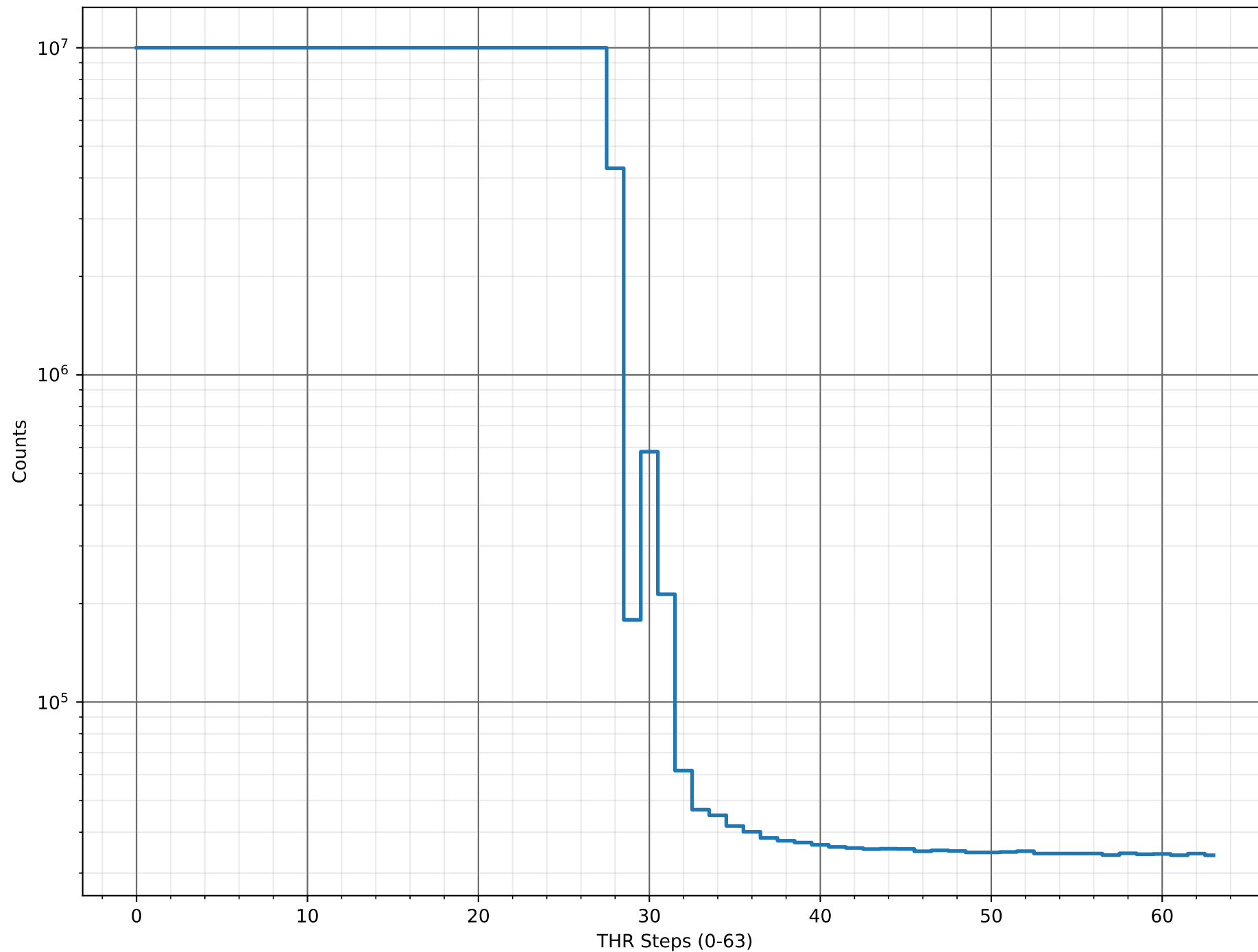
THR Distribution of PMT: D Anode: 43 (UART: 1 FEB: 0 CHIP: 2 CHANNEL: 4)



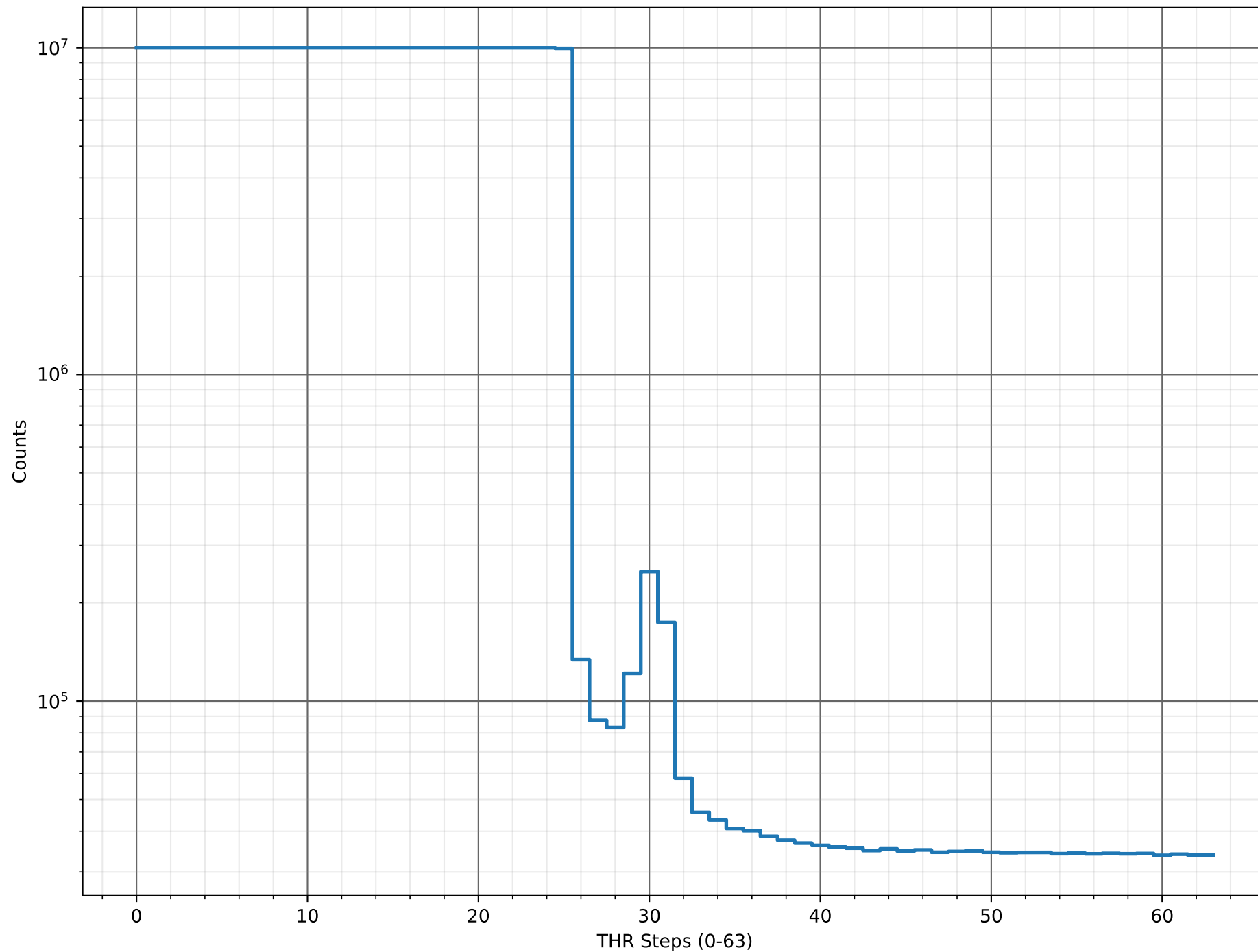
THR Distribution of PMT: D Anode: 44 (UART: 1 FEB: 0 CHIP: 2 CHANNEL: 7)



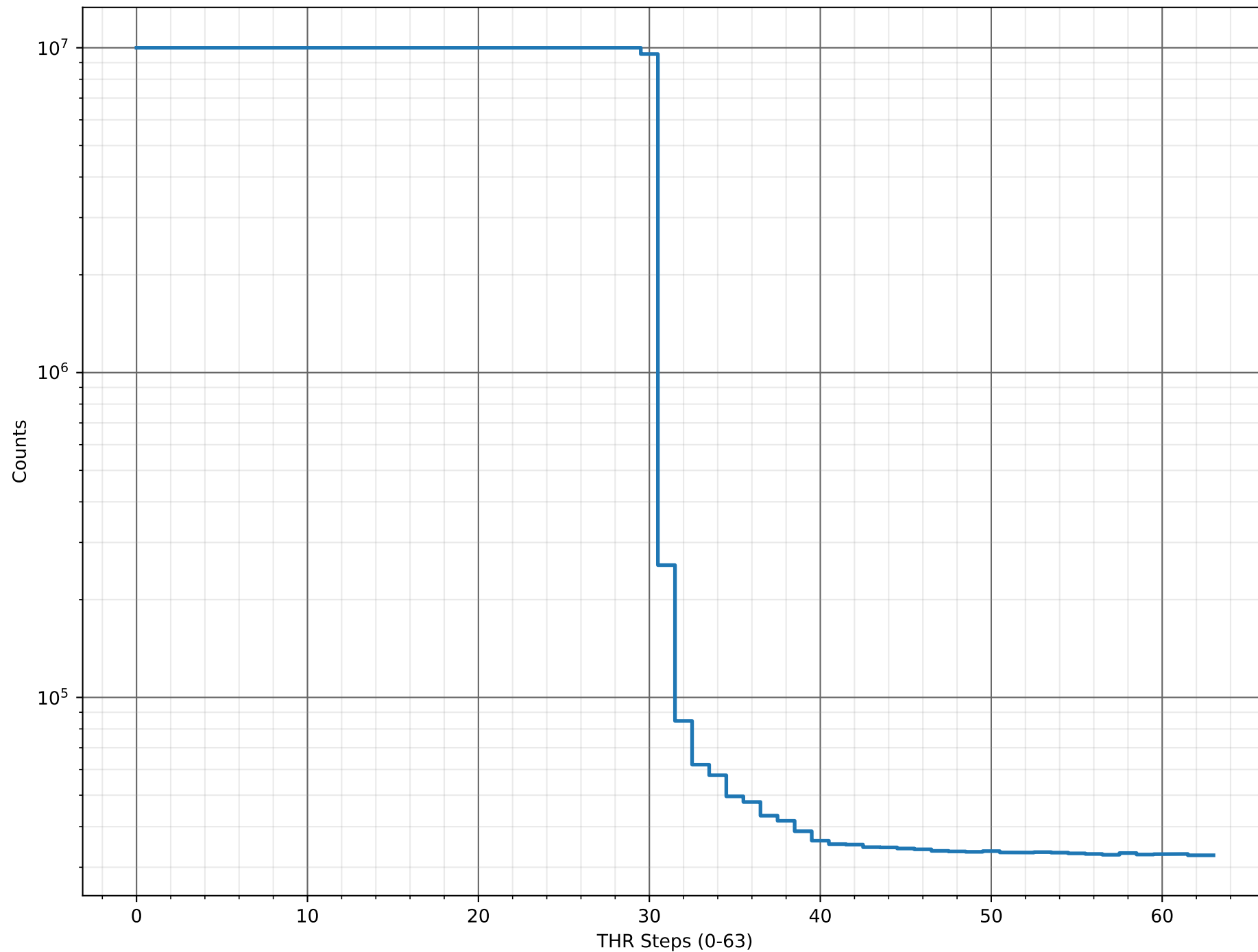
THR Distribution of PMT: D Anode: 45 (UART: 1 FEB: 0 CHIP: 0 CHANNEL: 1)



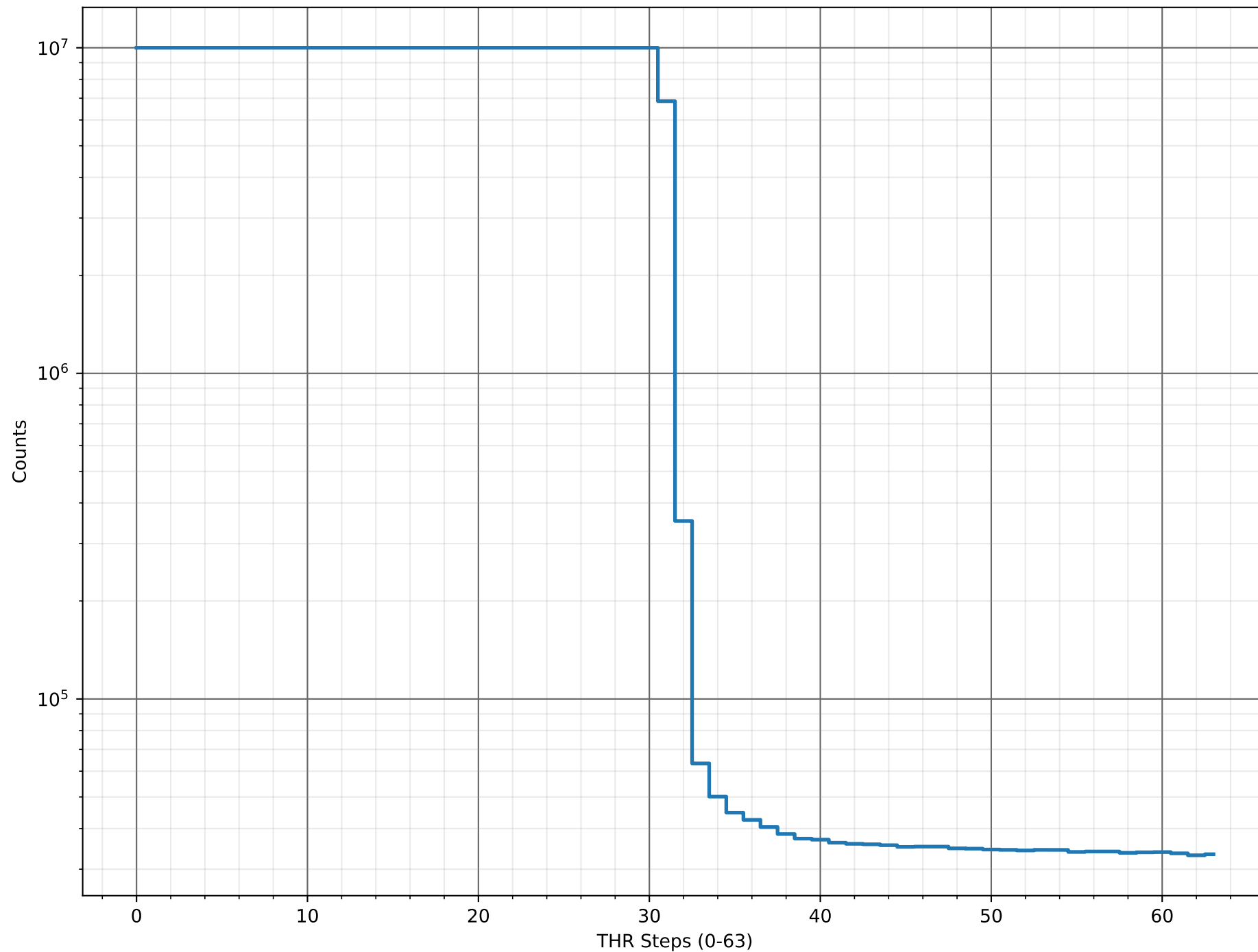
THR Distribution of PMT: D Anode: 46 (UART: 1 FEB: 0 CHIP: 0 CHANNEL: 3)



THR Distribution of PMT: D Anode: 47 (UART: 1 FEB: 0 CHIP: 0 CHANNEL: 5)

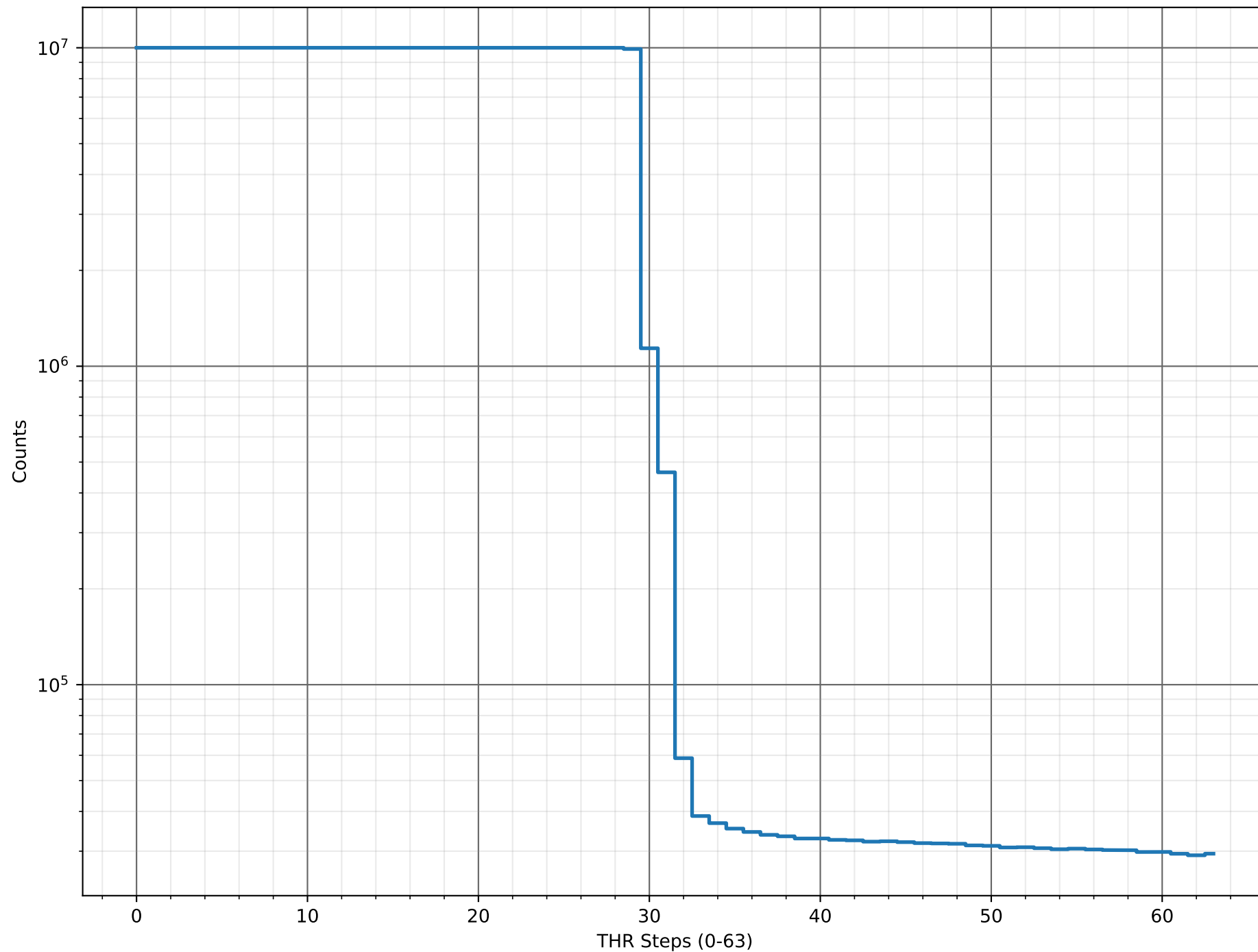


THR Distribution of PMT: D Anode: 48 (UART: 1 FEB: 0 CHIP: 0 CHANNEL: 7)

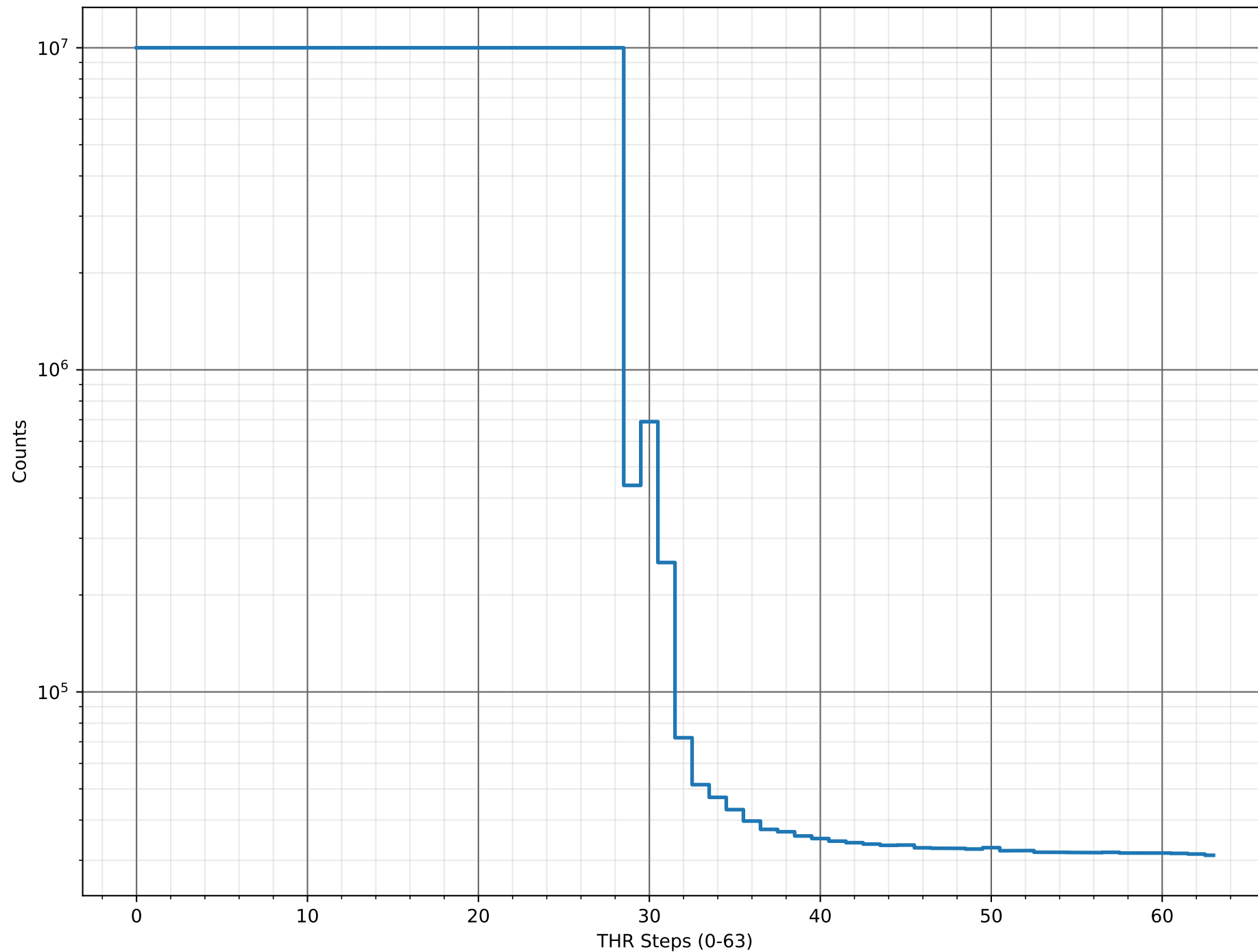




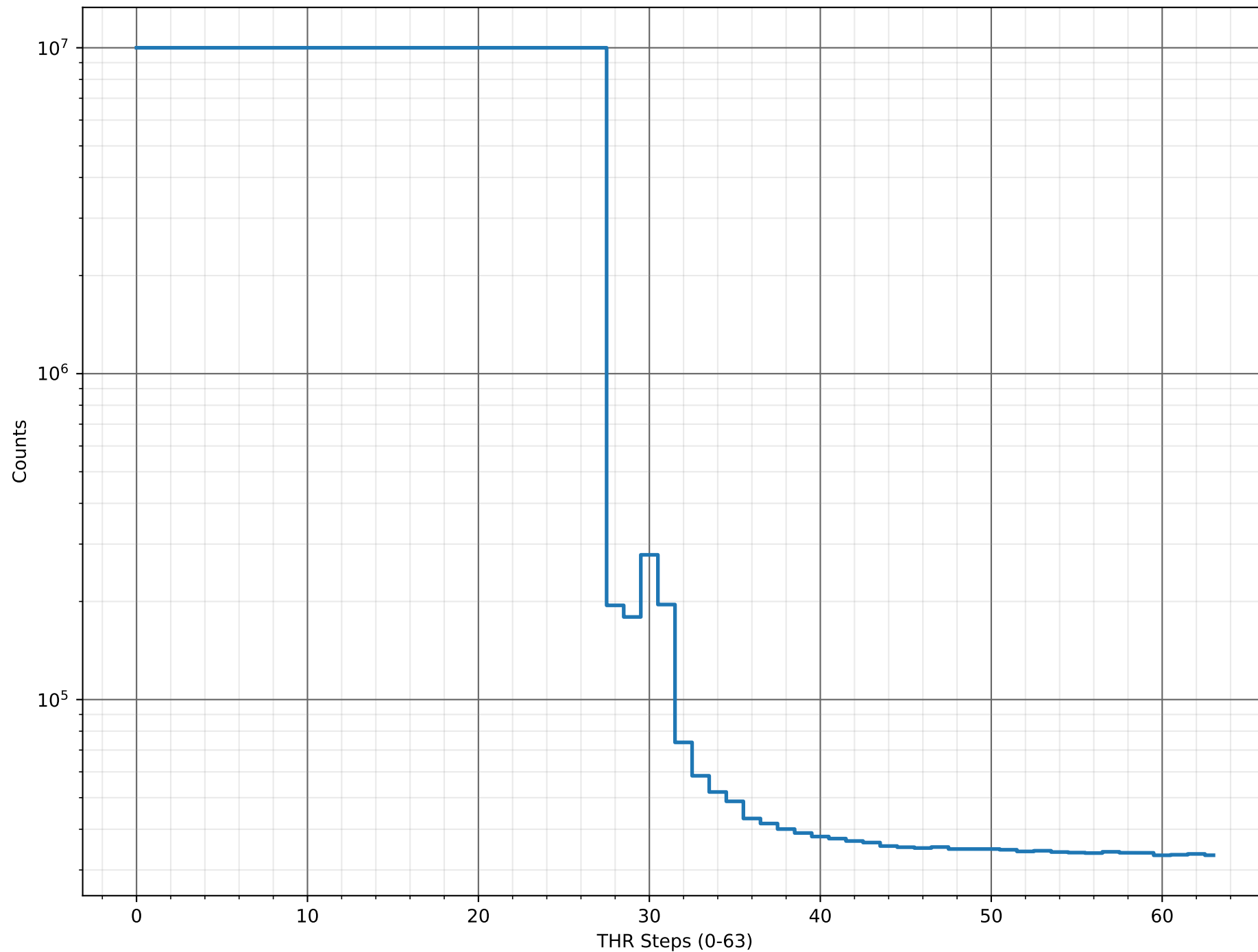
THR Distribution of PMT: D Anode: 49 (UART: 1 FEB: 0 CHIP: 3 CHANNEL: 6)



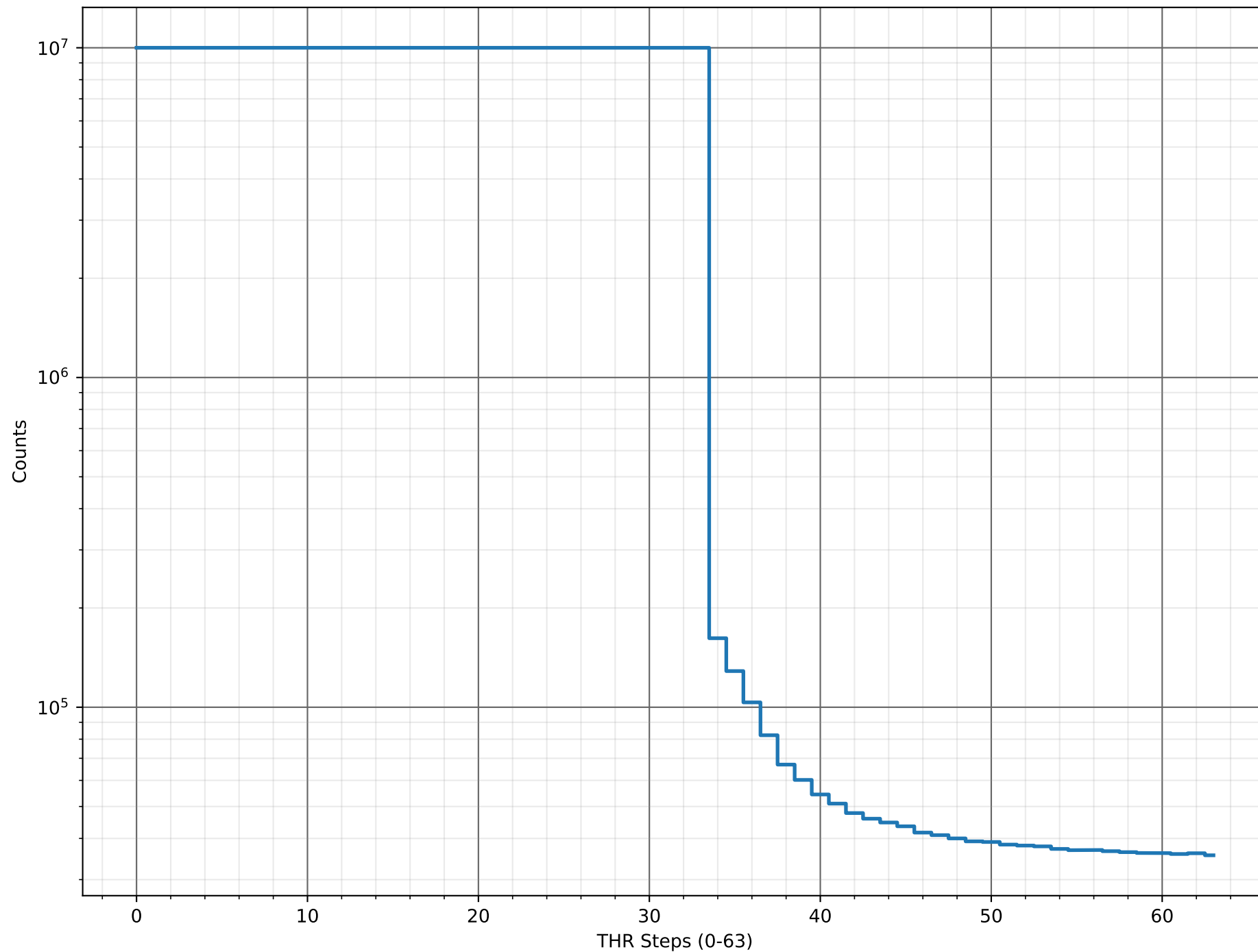
THR Distribution of PMT: D Anode: 50 (UART: 1 FEB: 0 CHIP: 3 CHANNEL: 4)



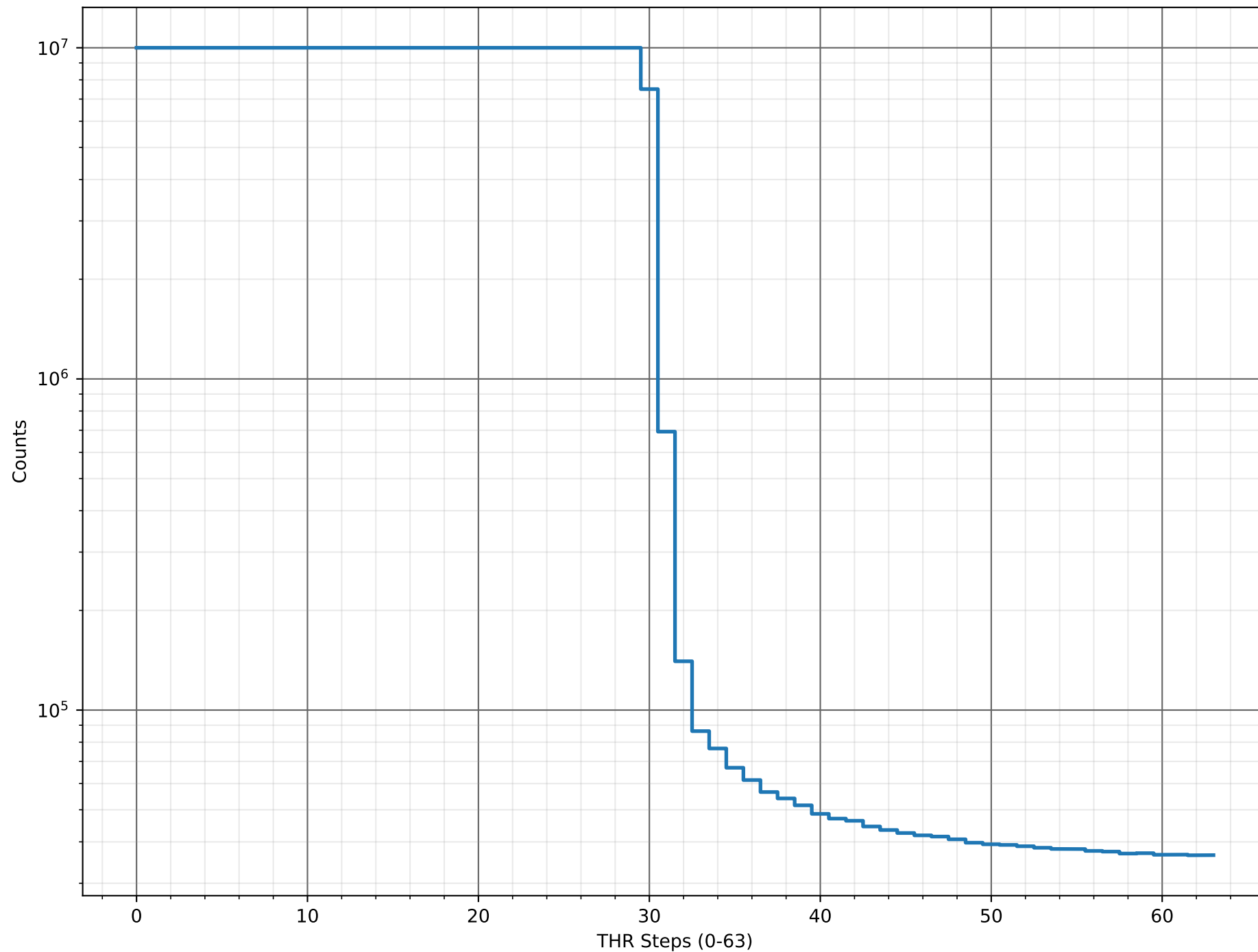
THR Distribution of PMT: D Anode: 51 (UART: 1 FEB: 0 CHIP: 3 CHANNEL: 2)



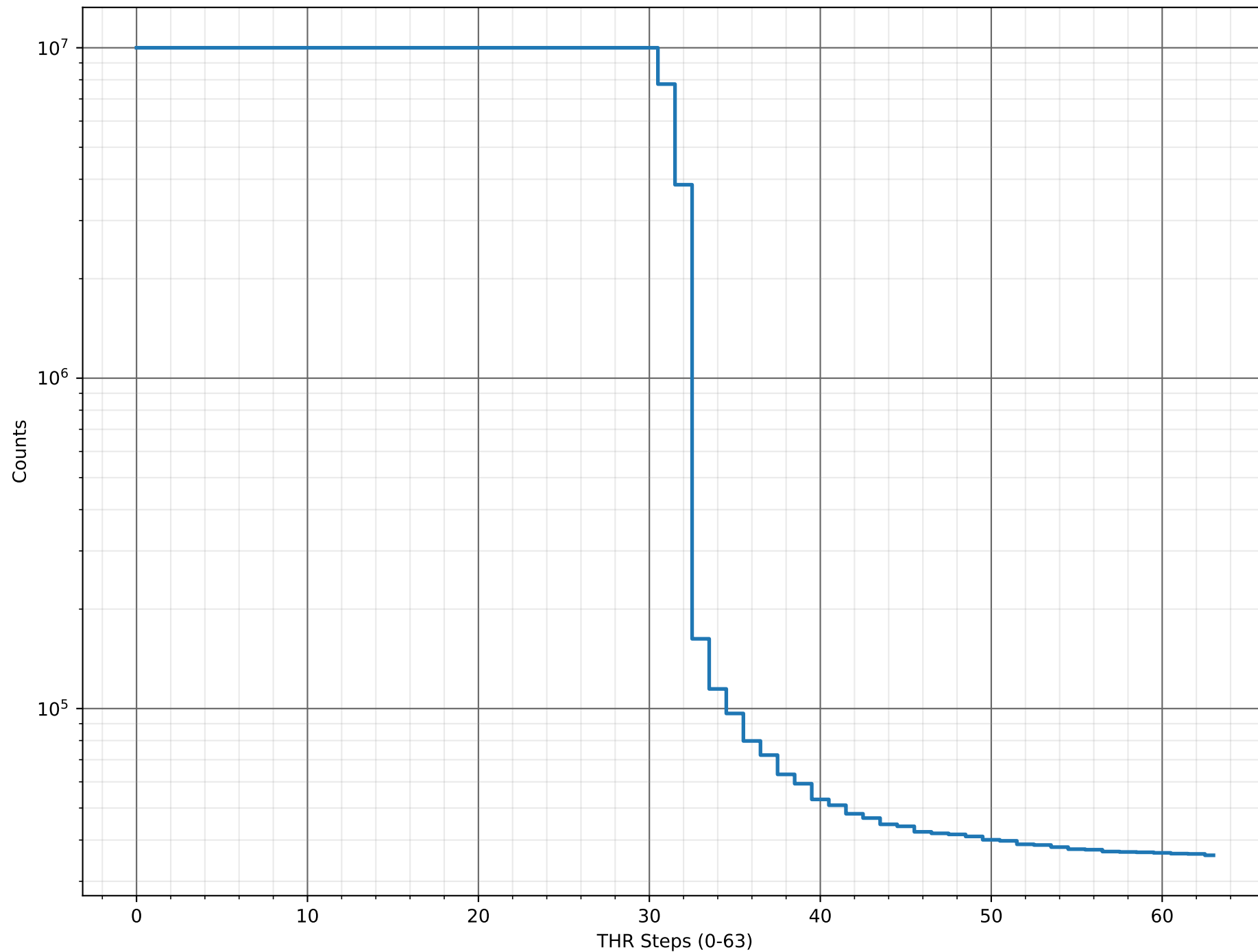
THR Distribution of PMT: D Anode: 52 (UART: 1 FEB: 0 CHIP: 3 CHANNEL: 0)



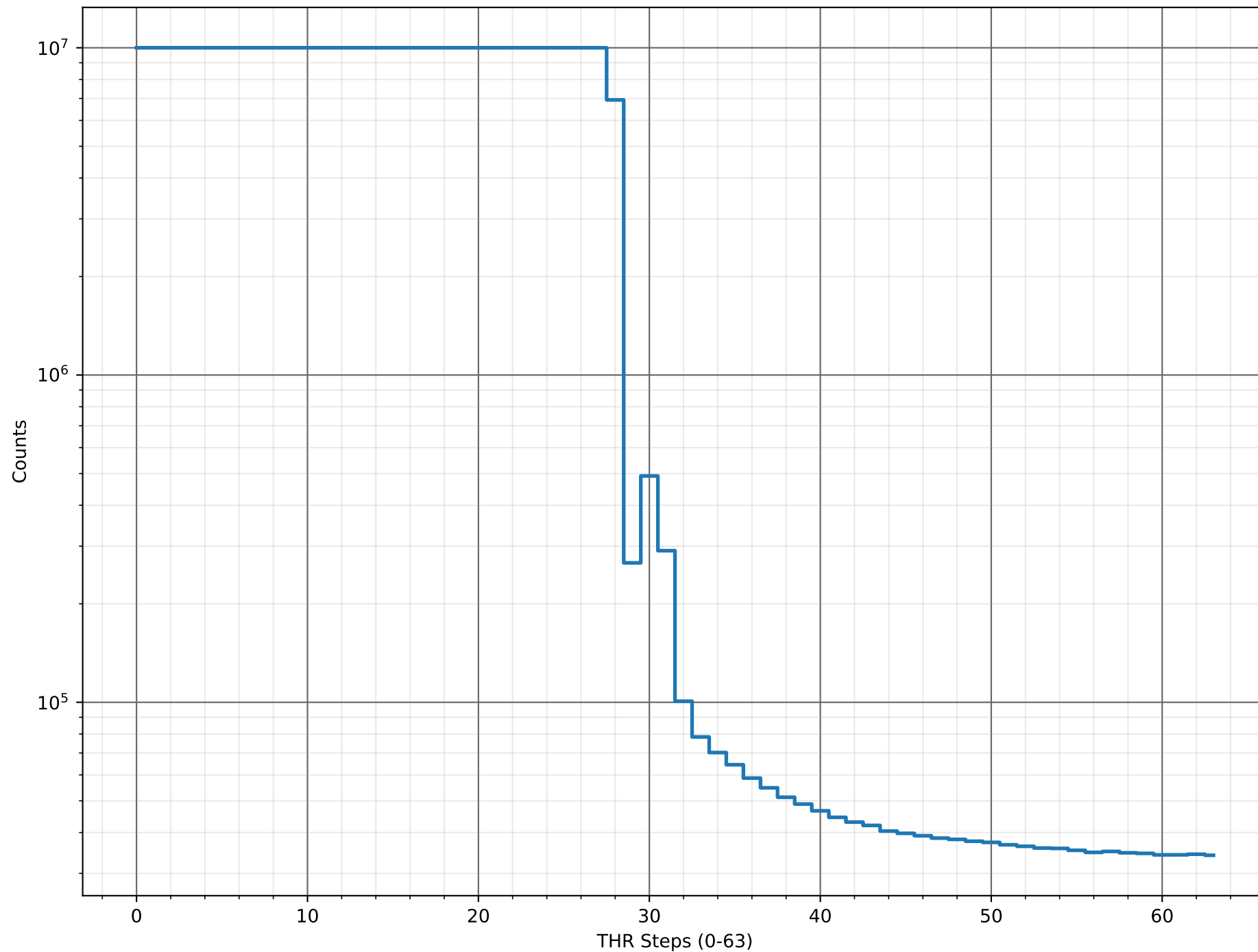
THR Distribution of PMT: D Anode: 53 (UART: 1 FEB: 0 CHIP: 1 CHANNEL: 7)



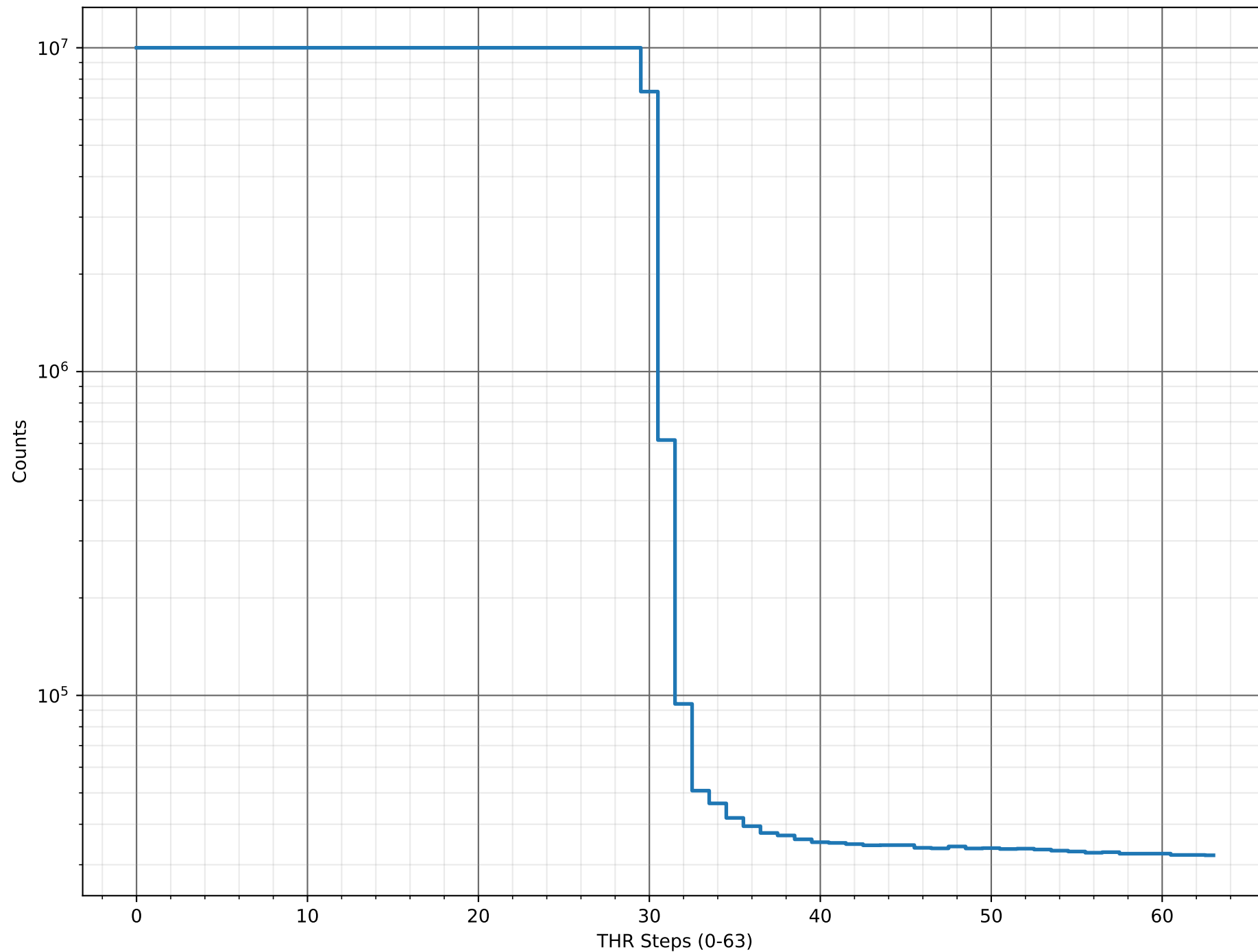
THR Distribution of PMT: D Anode: 54 (UART: 1 FEB: 0 CHIP: 1 CHANNEL: 5)



THR Distribution of PMT: D Anode: 55 (UART: 1 FEB: 0 CHIP: 1 CHANNEL: 3)

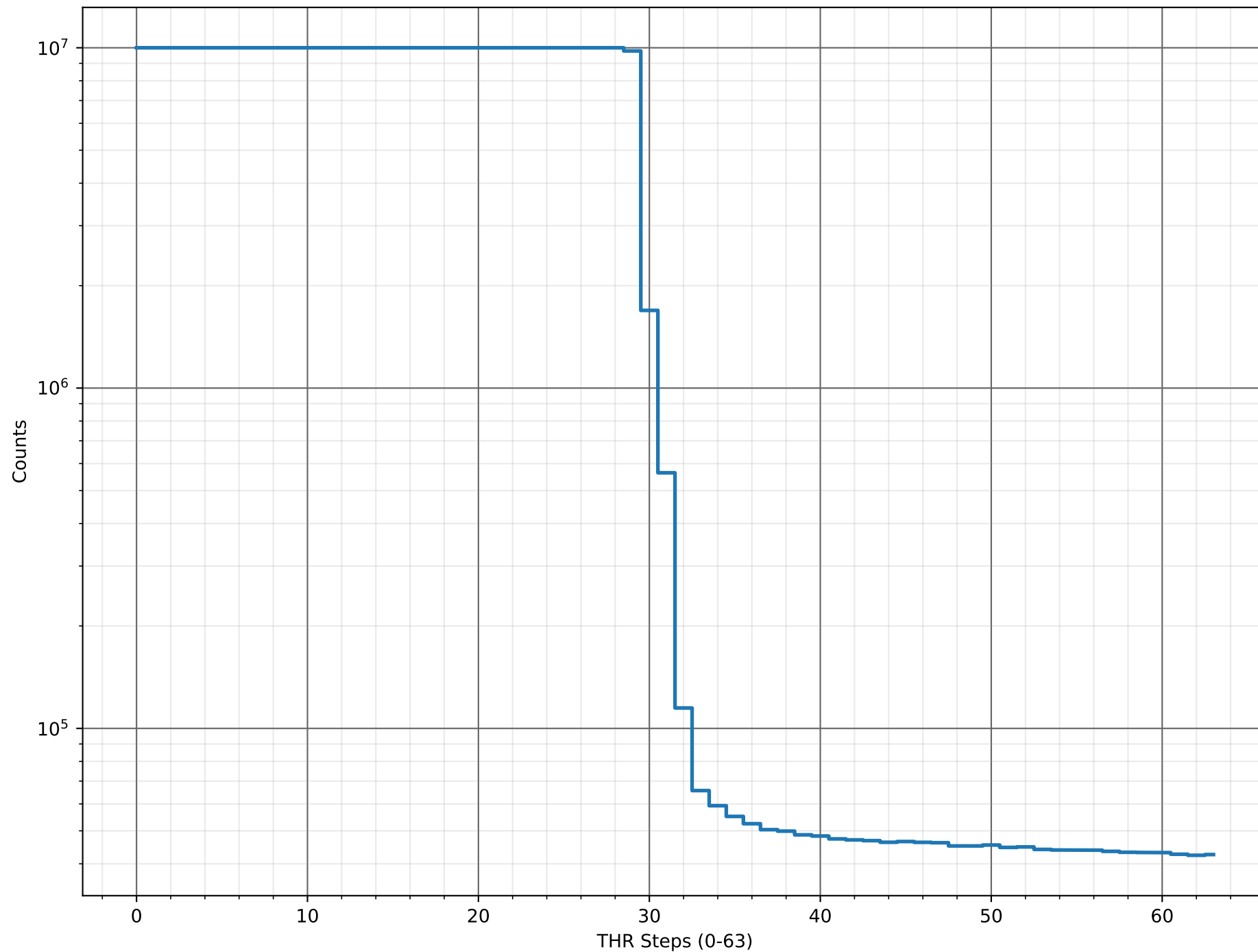


THR Distribution of PMT: D Anode: 56 (UART: 1 FEB: 0 CHIP: 1 CHANNEL: 1)

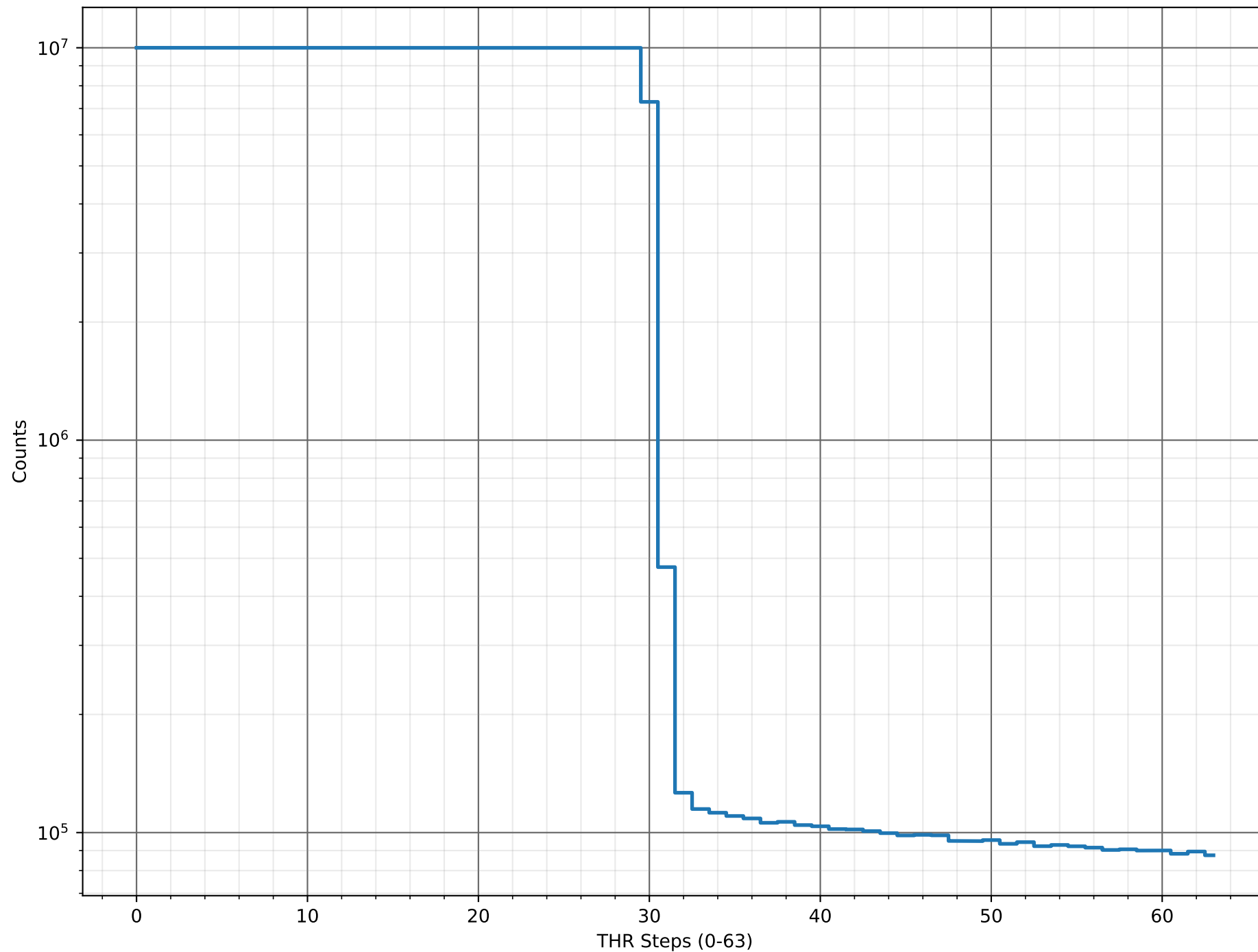




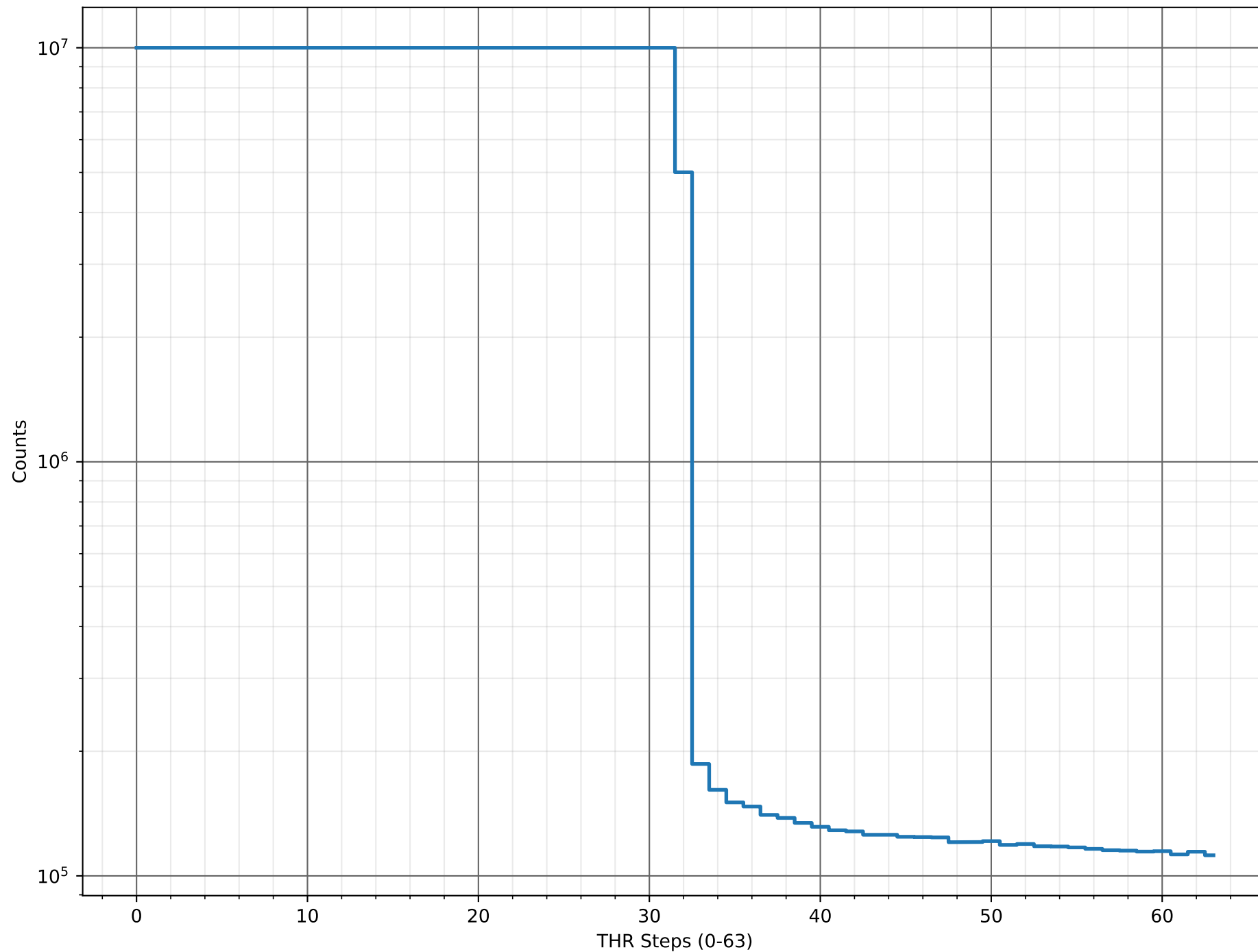
THR Distribution of PMT: D Anode: 57 (UART: 1 FEB: 0 CHIP: 3 CHANNEL: 7)



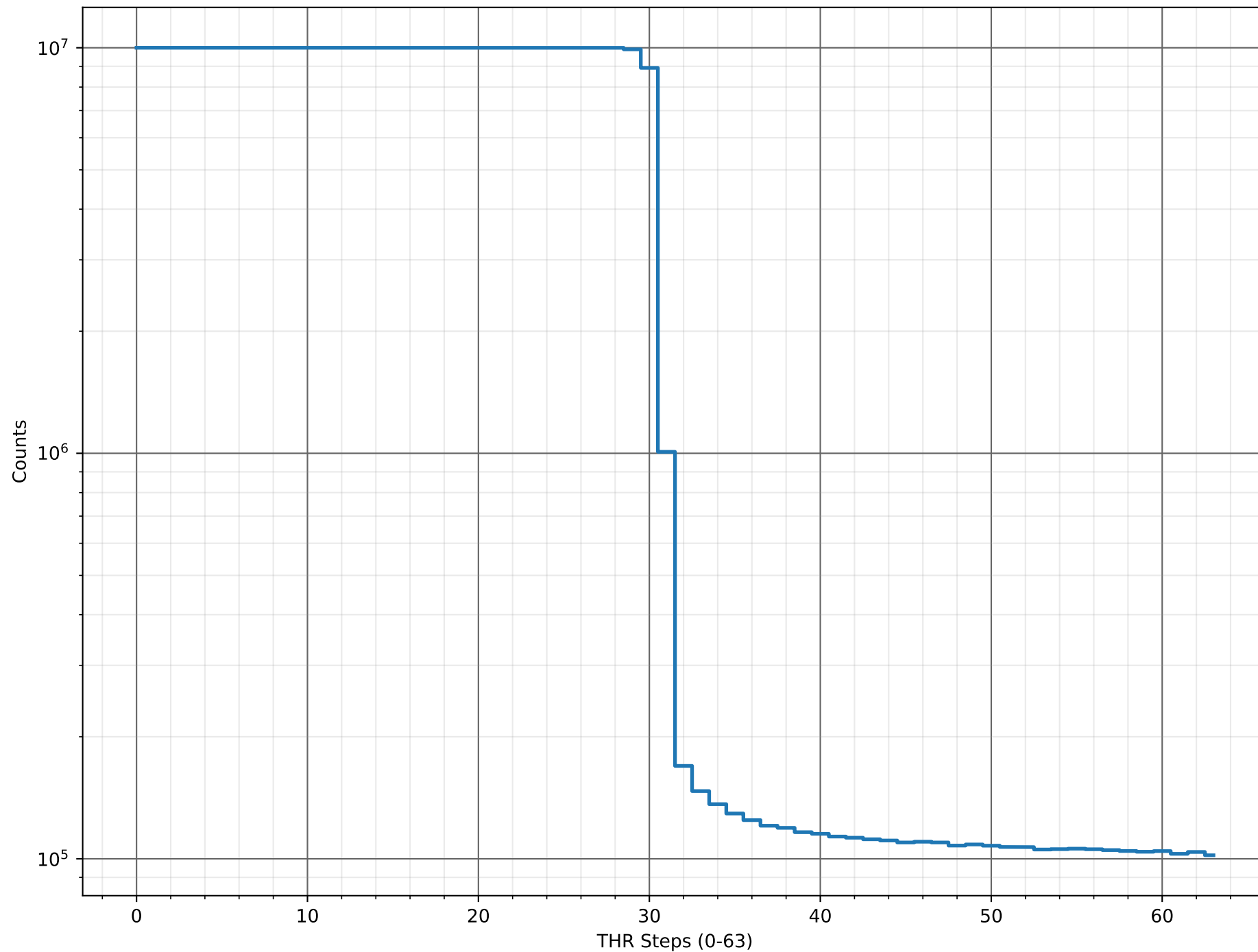
THR Distribution of PMT: D Anode: 58 (UART: 1 FEB: 0 CHIP: 3 CHANNEL: 5)



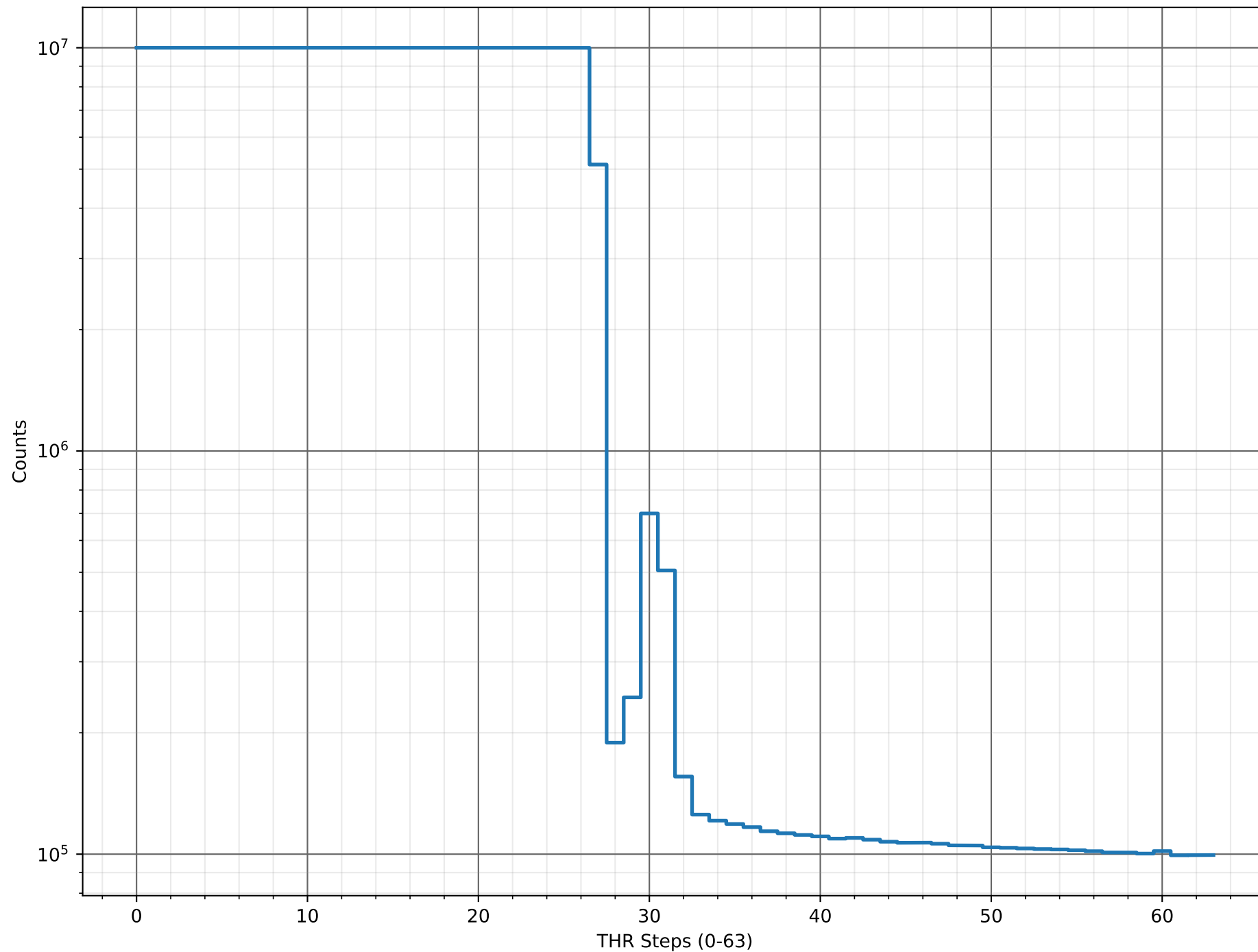
THR Distribution of PMT: D Anode: 59 (UART: 1 FEB: 0 CHIP: 3 CHANNEL: 3)



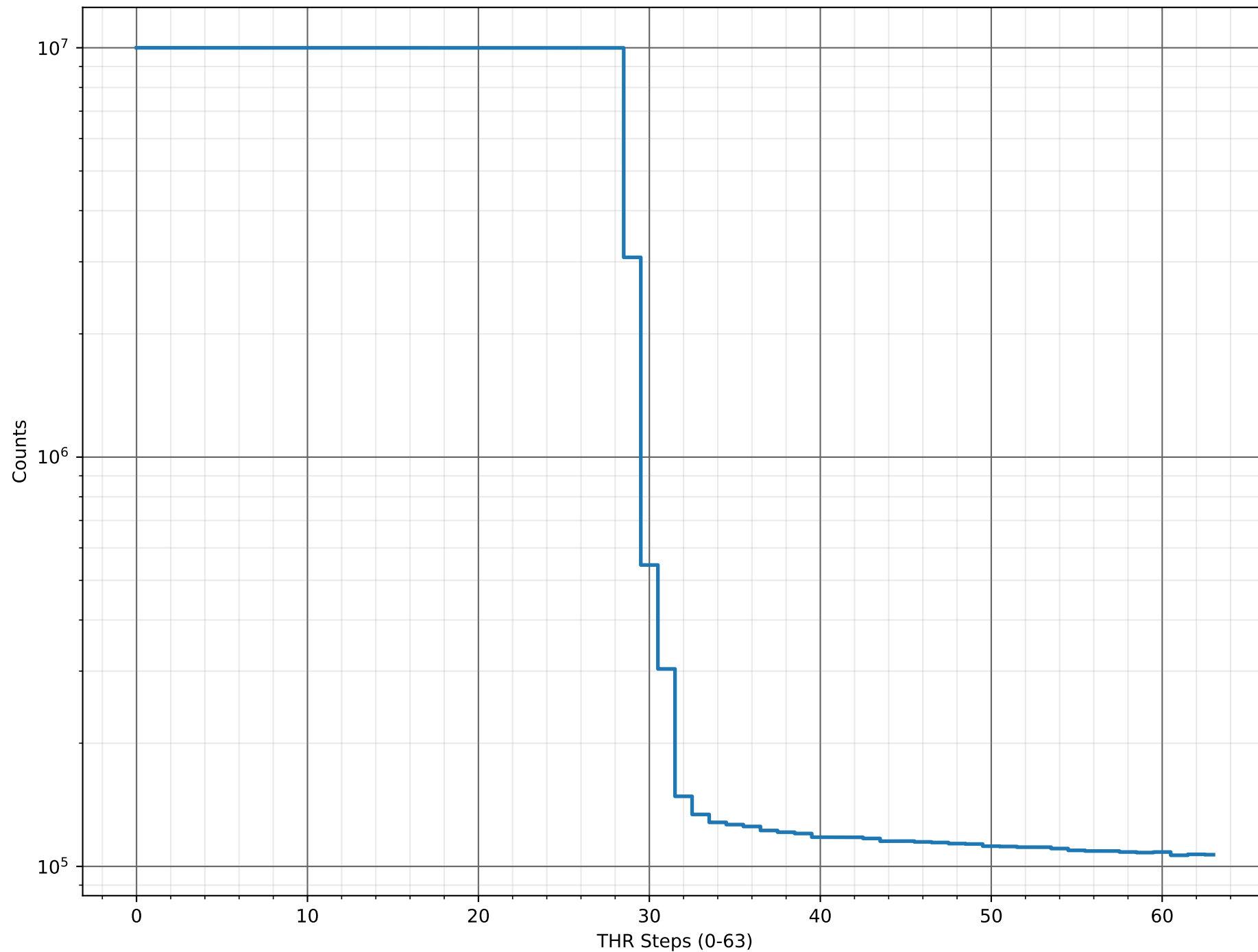
THR Distribution of PMT: D Anode: 60 (UART: 1 FEB: 0 CHIP: 3 CHANNEL: 1)



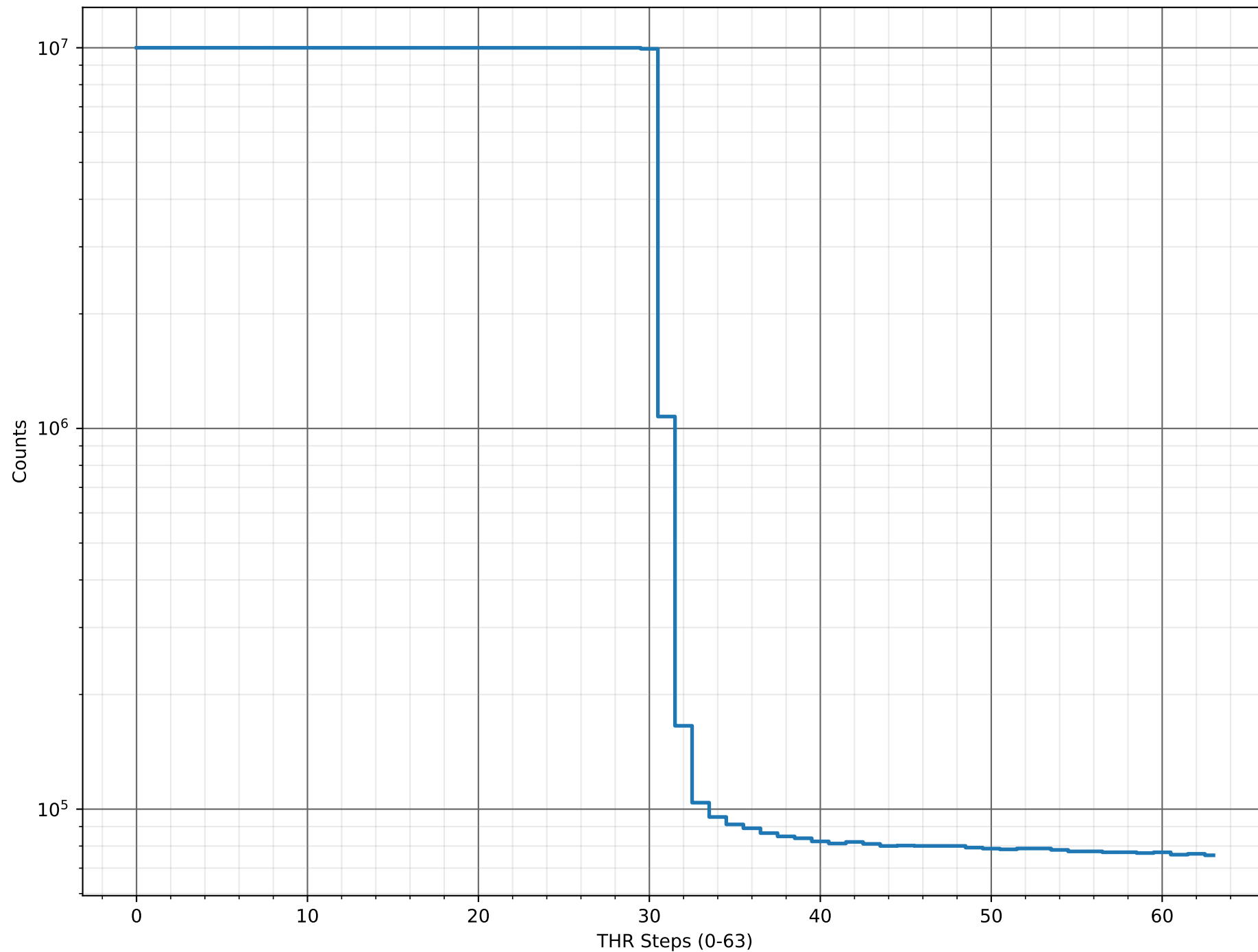
THR Distribution of PMT: D Anode: 61 (UART: 1 FEB: 0 CHIP: 1 CHANNEL: 6)



THR Distribution of PMT: D Anode: 62 (UART: 1 FEB: 0 CHIP: 1 CHANNEL: 4)



THR Distribution of PMT: D Anode: 63 (UART: 1 FEB: 0 CHIP: 1 CHANNEL: 2)



THR Distribution of PMT: D Anode: 64 (UART: 1 FEB: 0 CHIP: 1 CHANNEL: 0)

