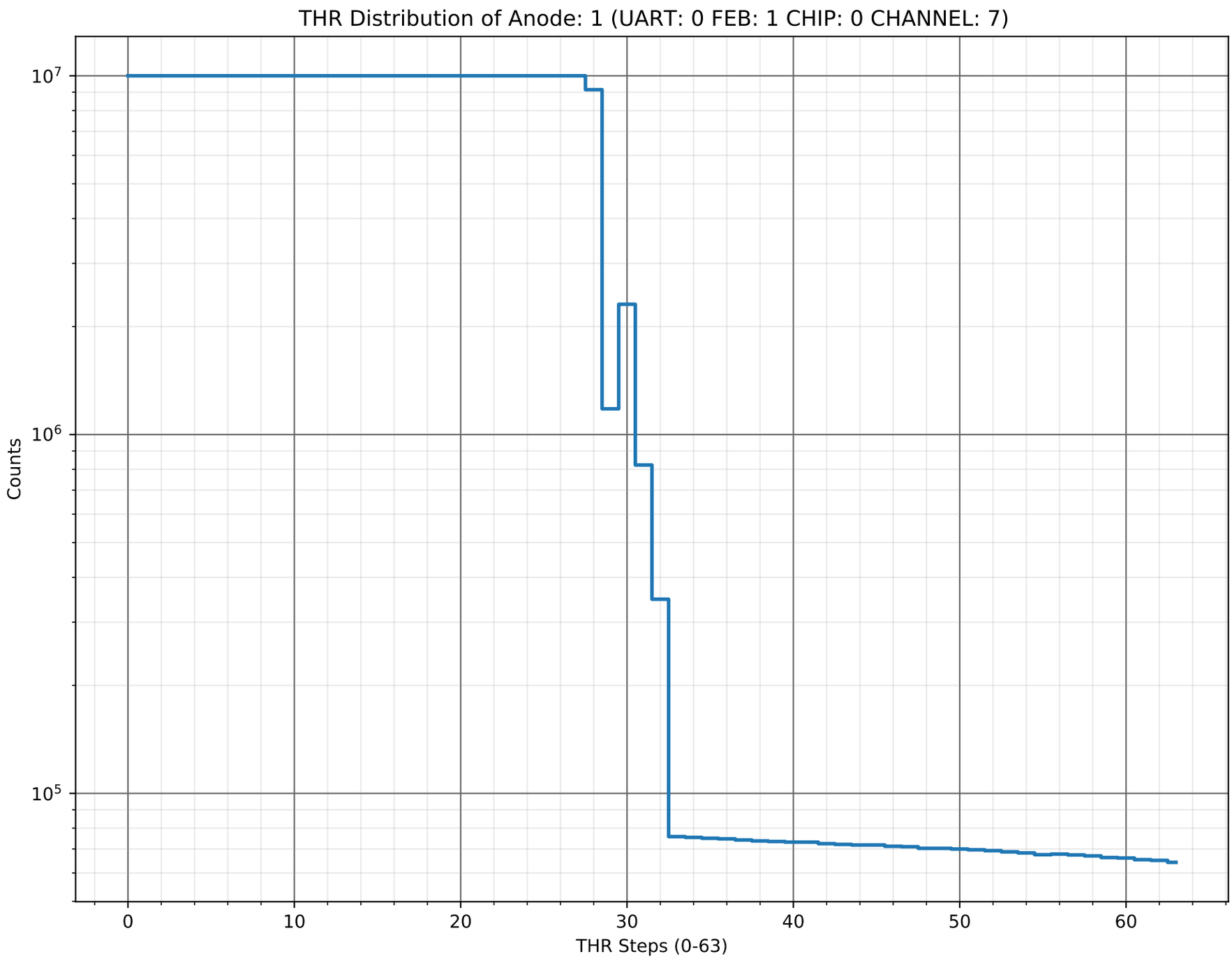
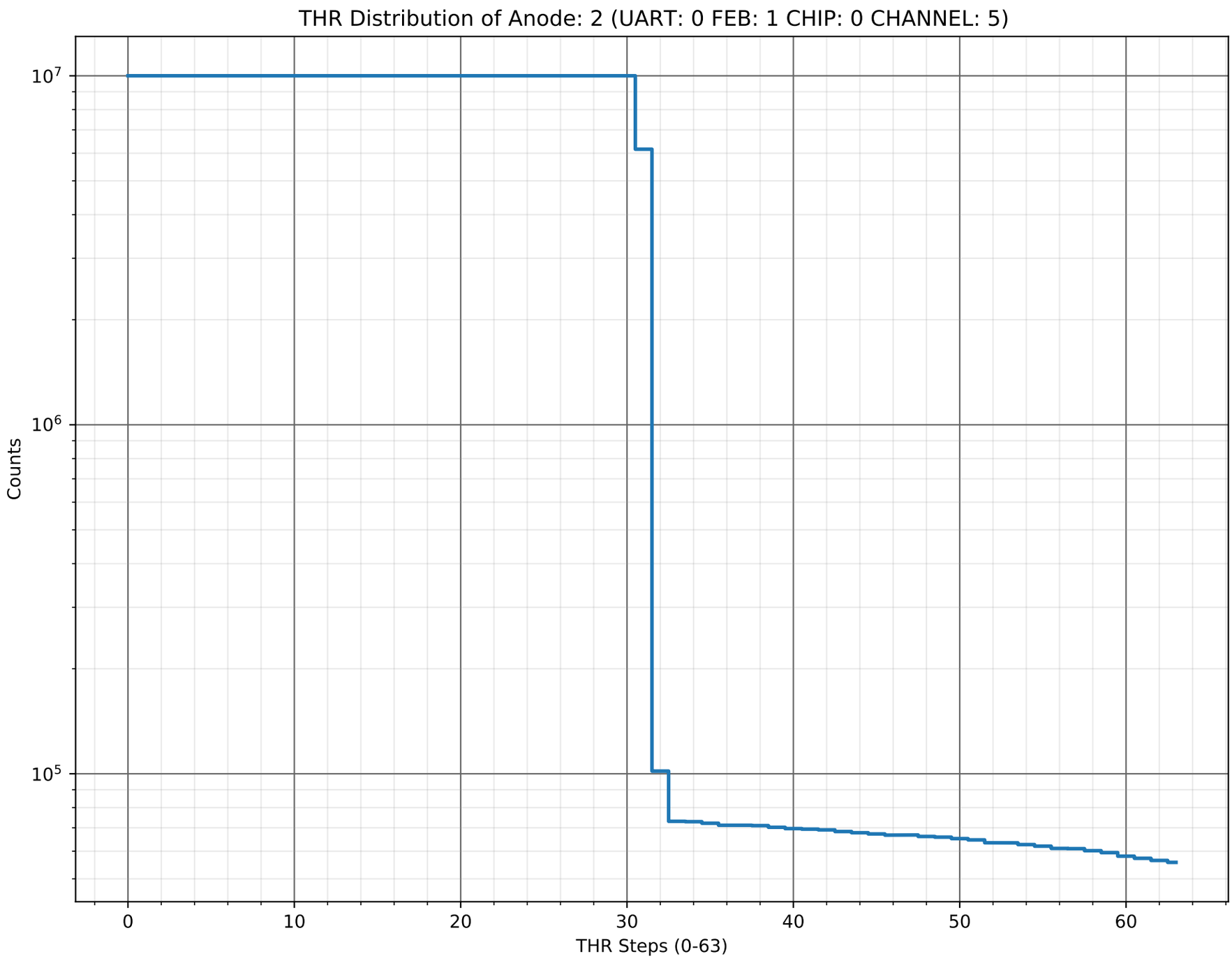
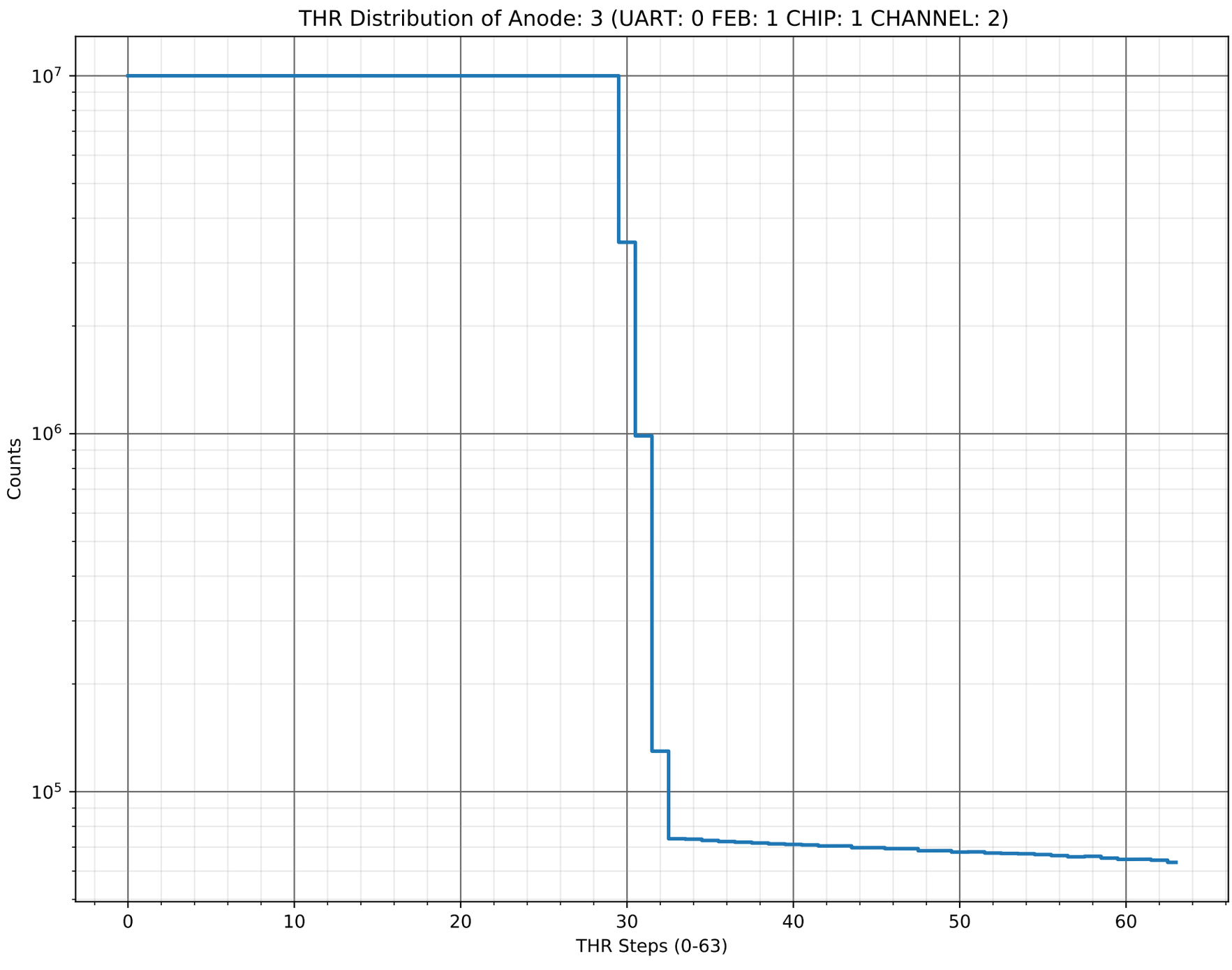
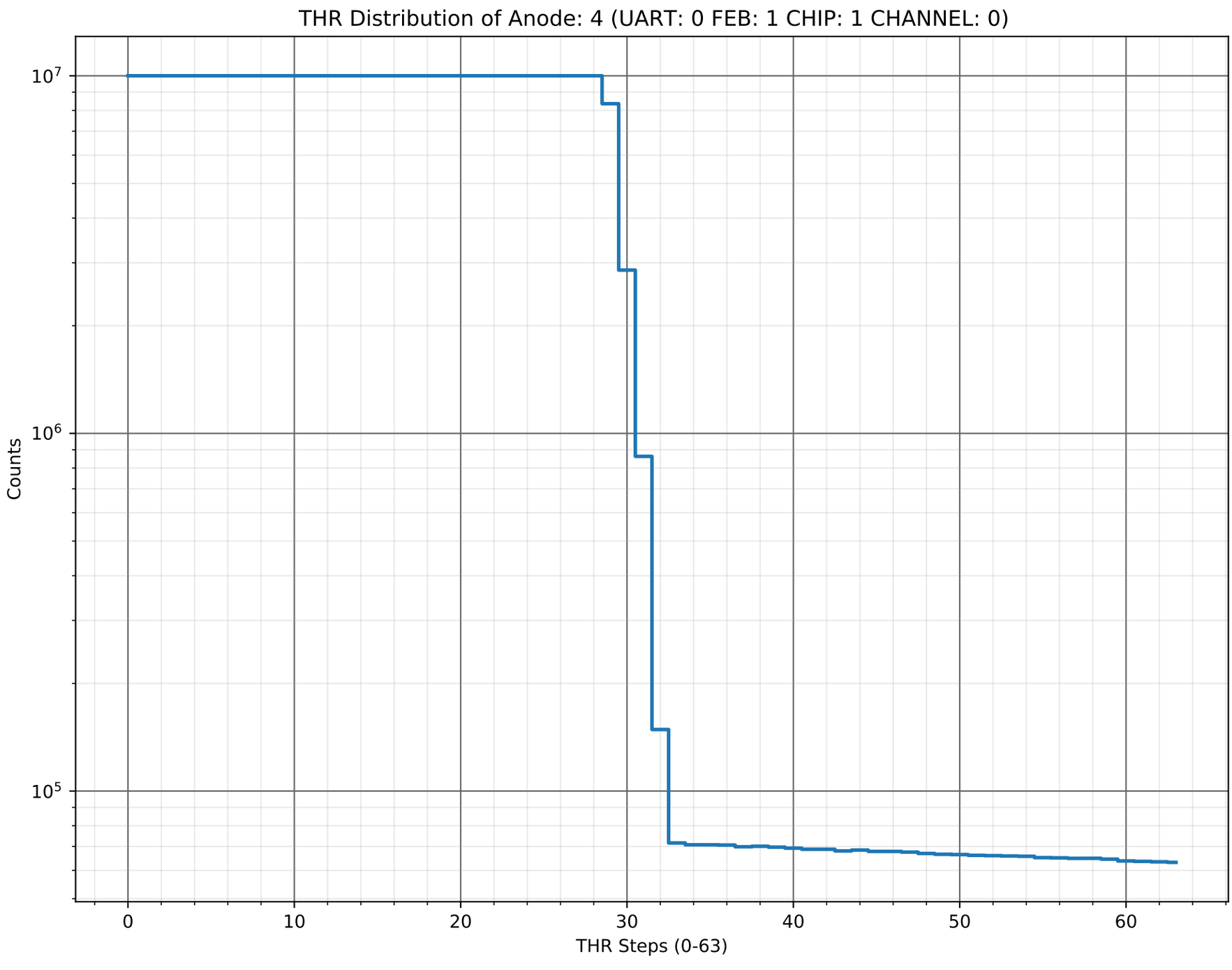
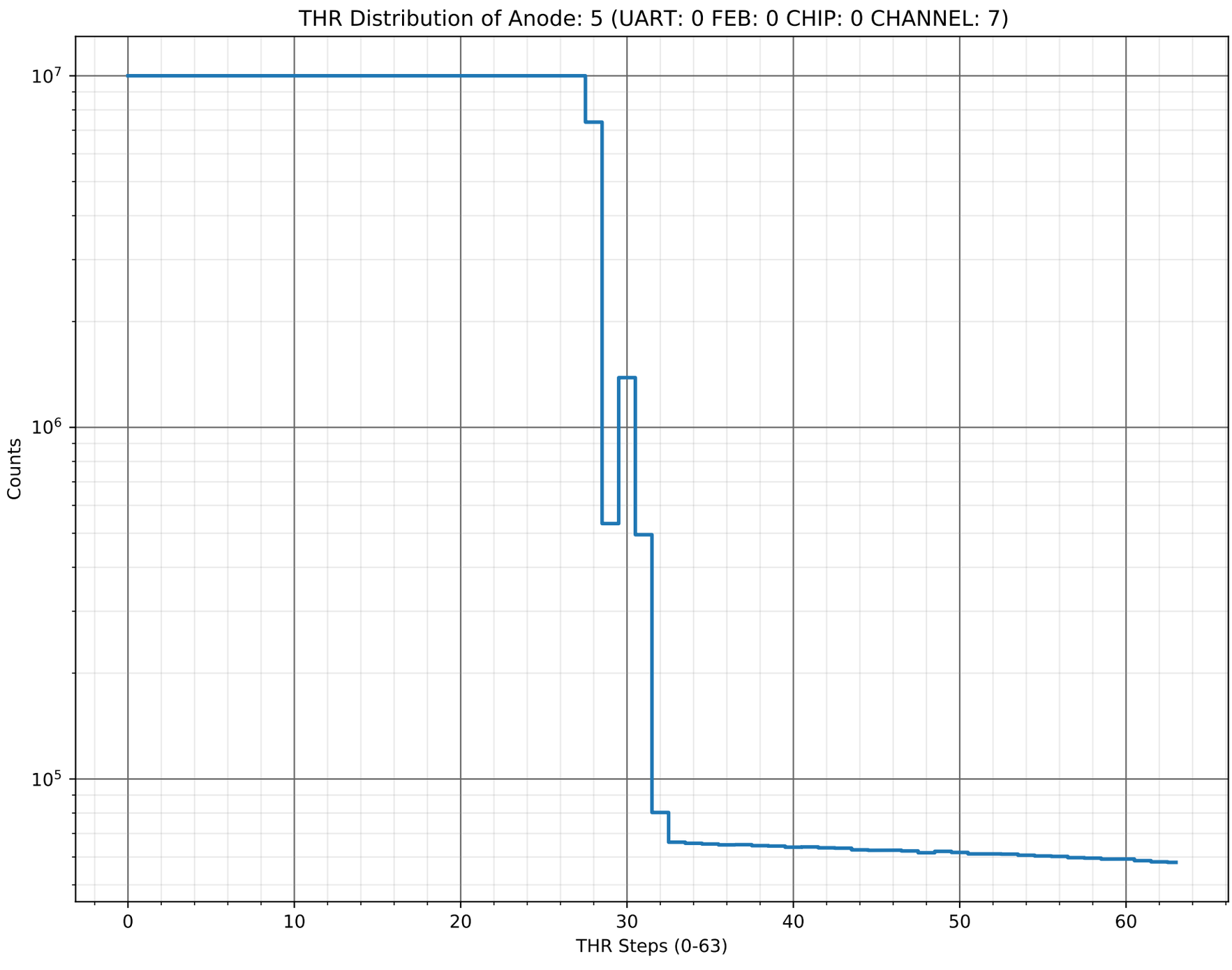
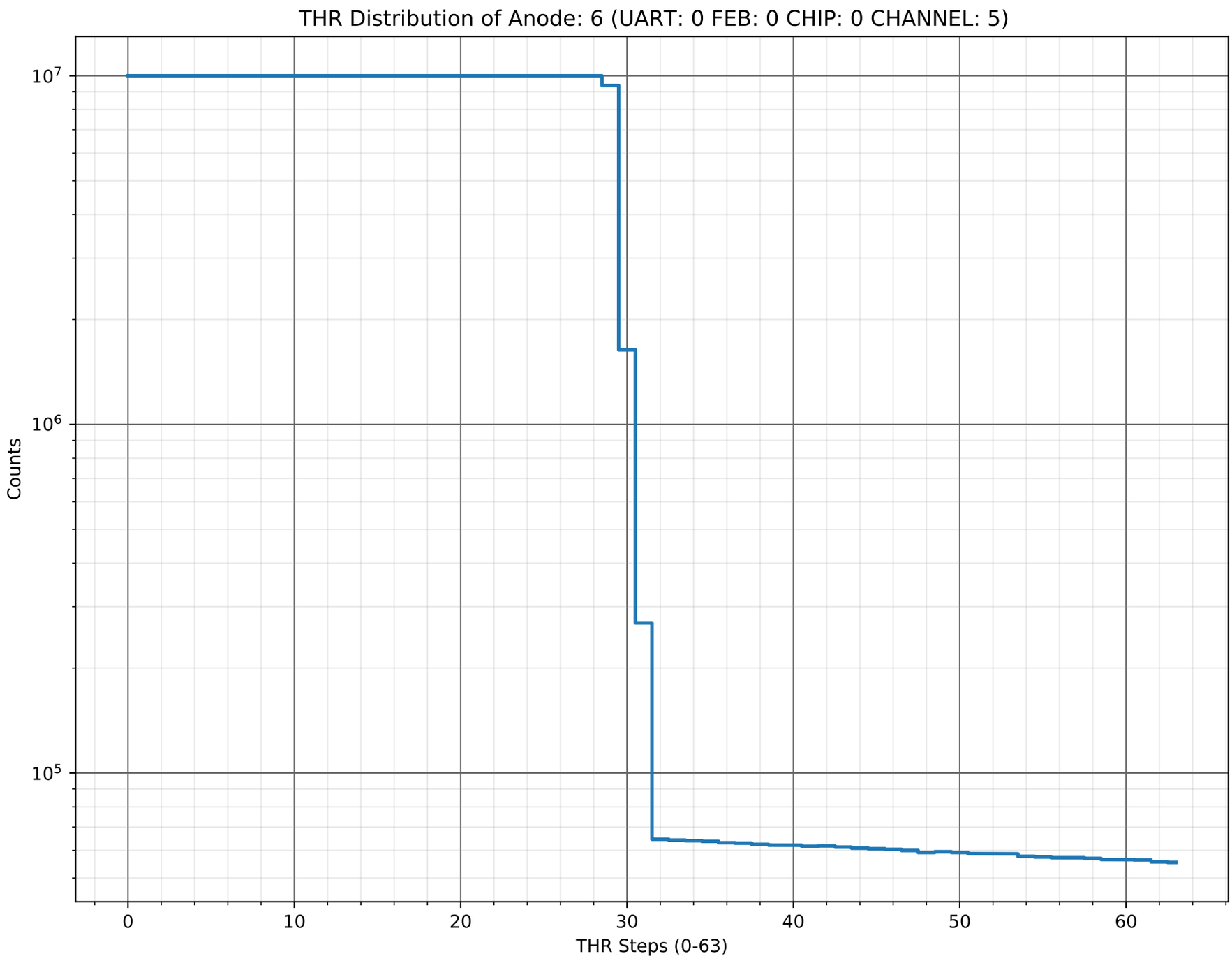

THR Distribution of Anode: 1 (UART: 0 FEB: 1 CHIP: 0 CHANNEL: 7)
10^7
10^6
10^5
Counts
0
10
20
30
40
50
60
THR Steps (0-63)

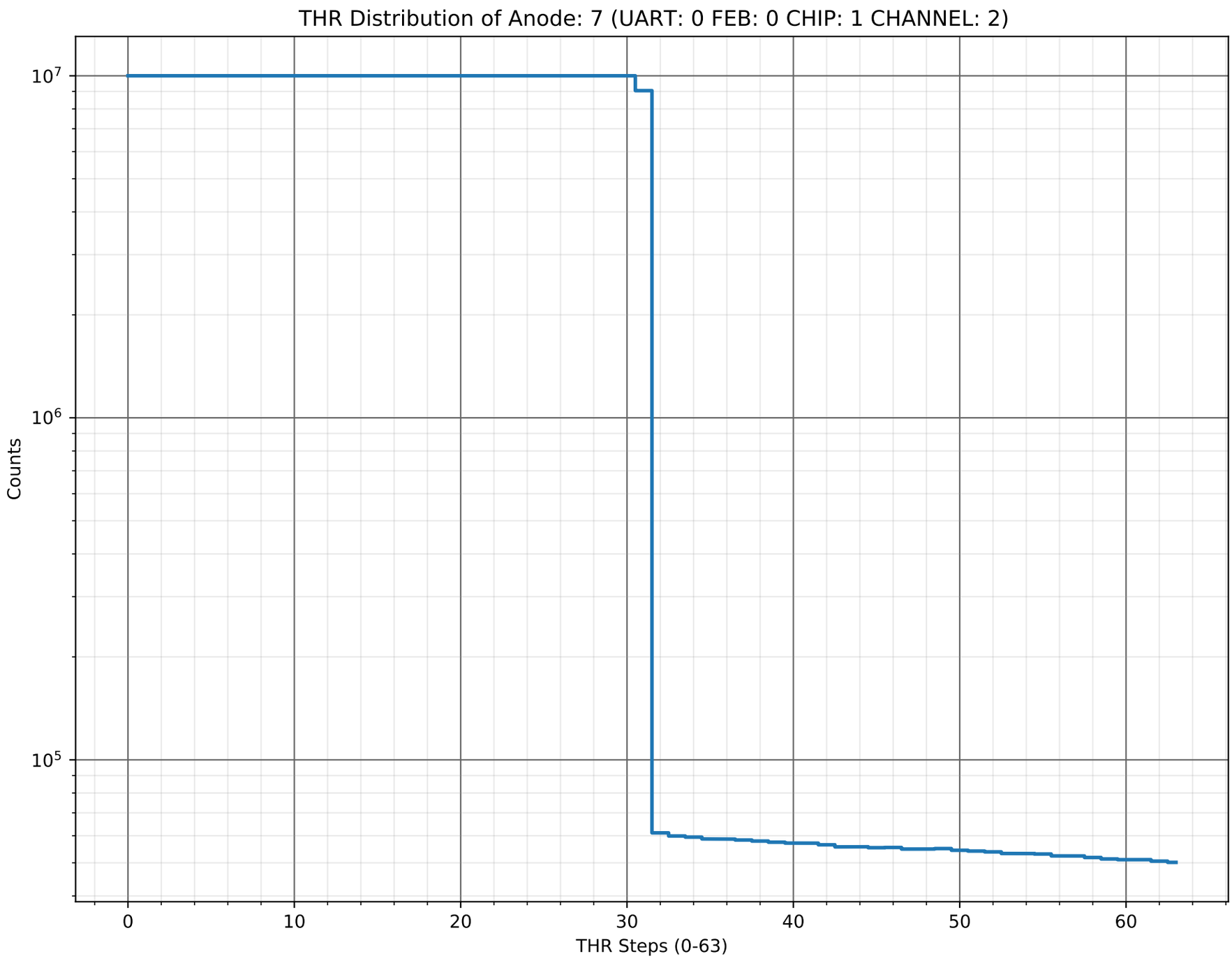

THR Distribution of Anode: 2 (UART: 0 FEB: 1 CHIP: 0 CHANNEL: 5)
10^7
10^6
10^5
Counts
0
10
20
30
40
50
60
THR Steps (0-63)

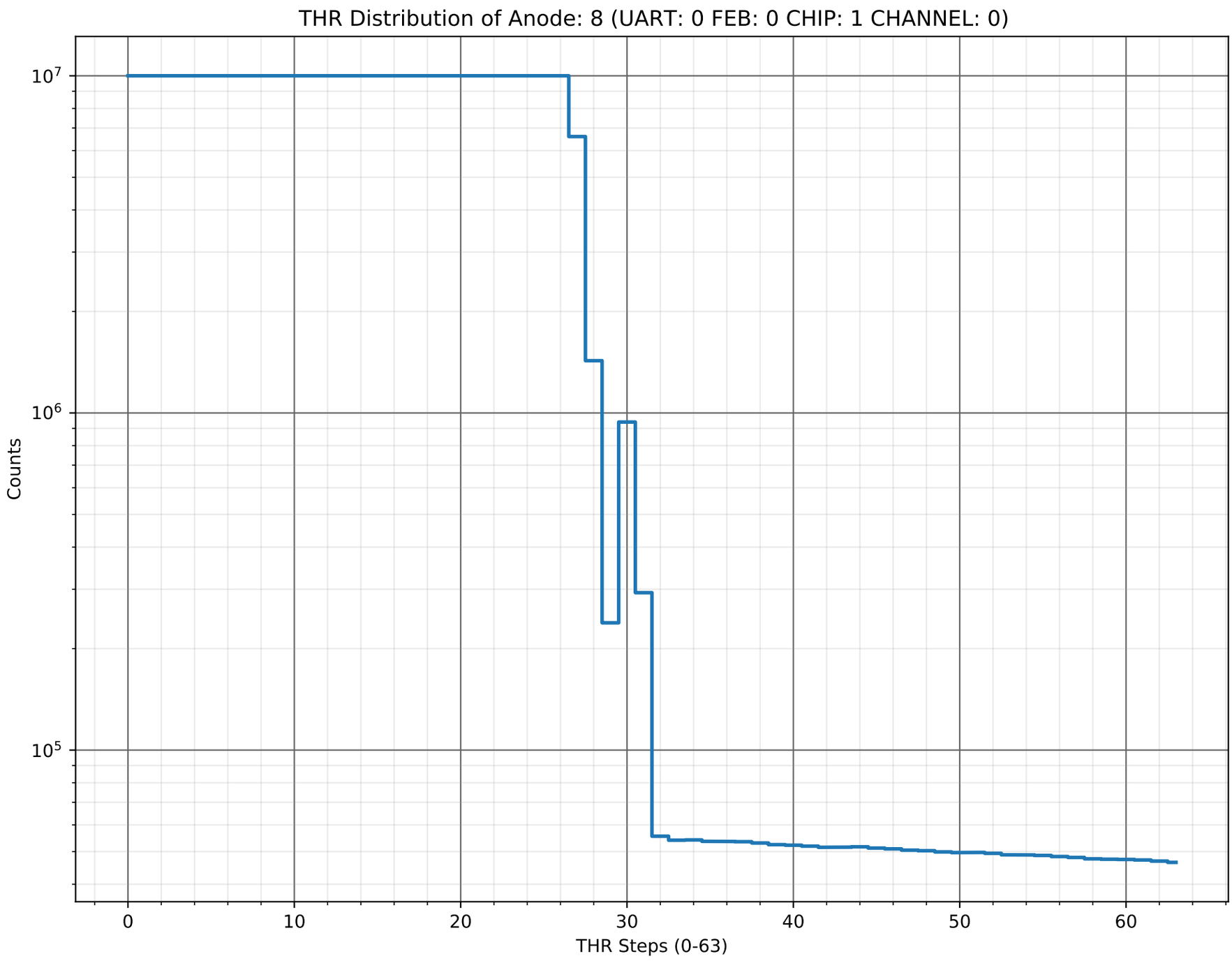

THR Distribution of Anode: 3 (UART: 0 FEB: 1 CHIP: 1 CHANNEL: 2)
10^7
10^6
10^5
Counts
0
10
20
30
40
50
60
THR Steps (0-63)

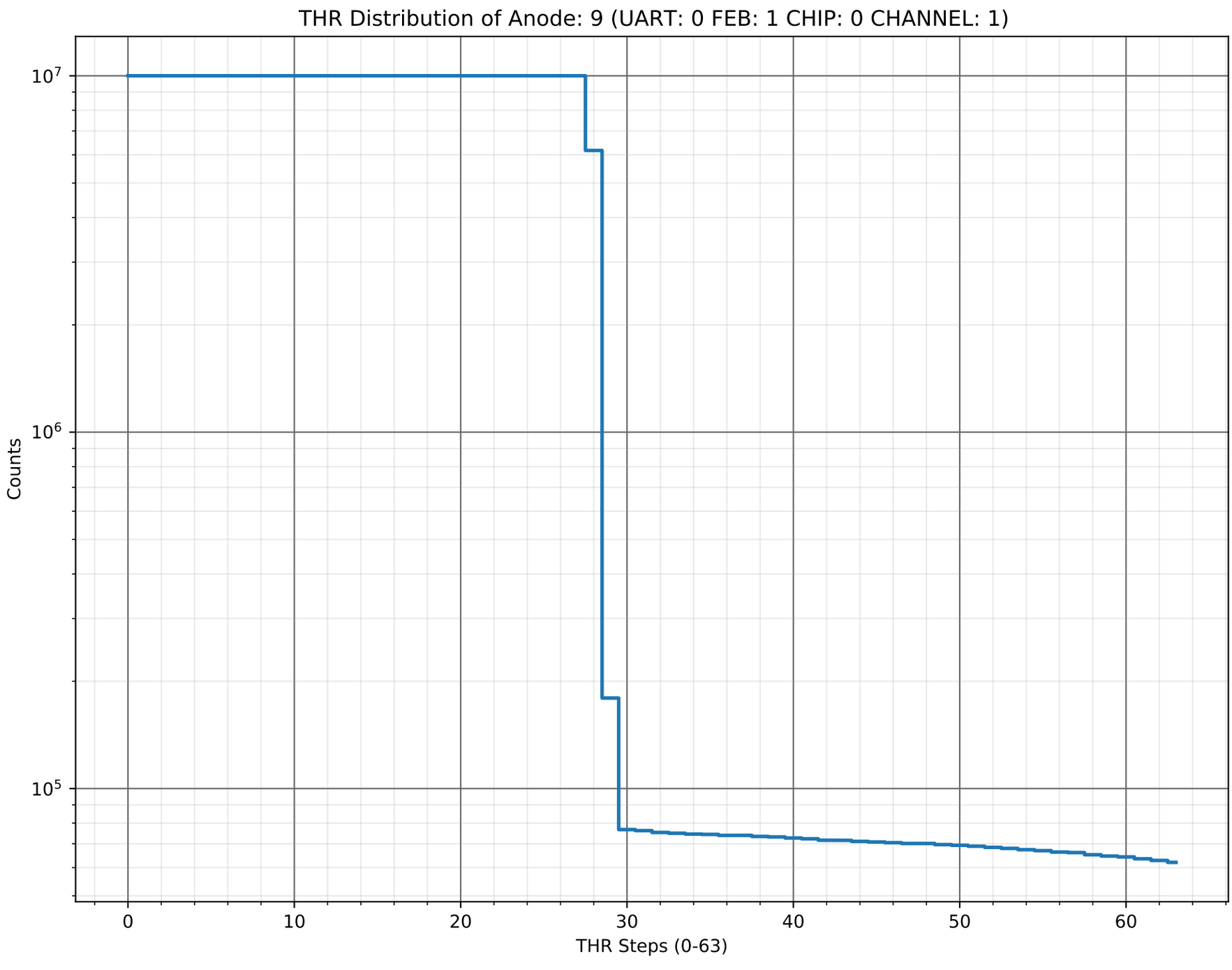

THR Distribution of Anode: 4 (UART: 0 FEB: 1 CHIP: 1 CHANNEL: 0)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

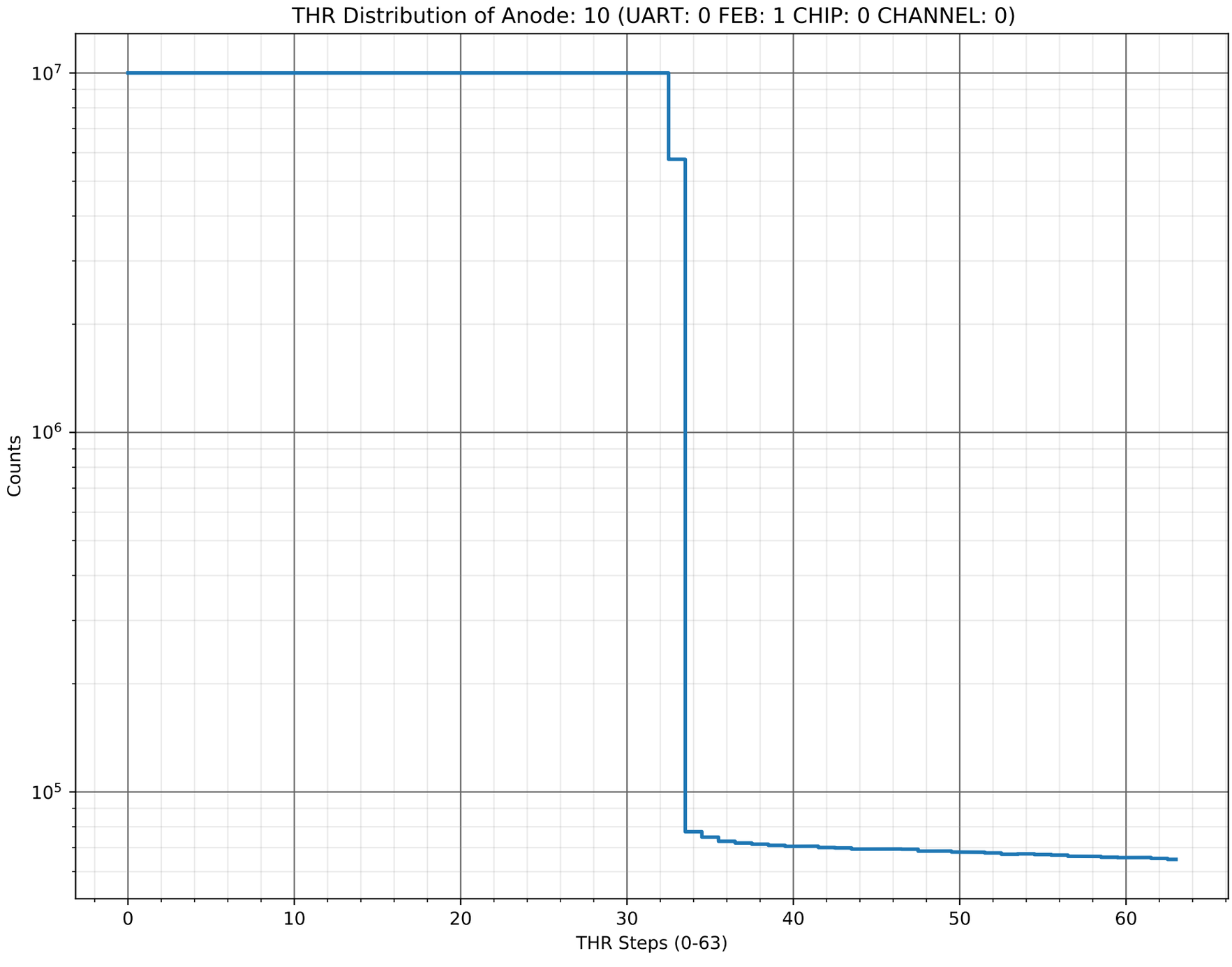

THR Distribution of Anode: 5 (UART: 0 FEB: 0 CHIP: 0 CHANNEL: 7)
10^7
10^6
10^5
Counts
0
10
20
30
40
50
60
THR Steps (0-63)

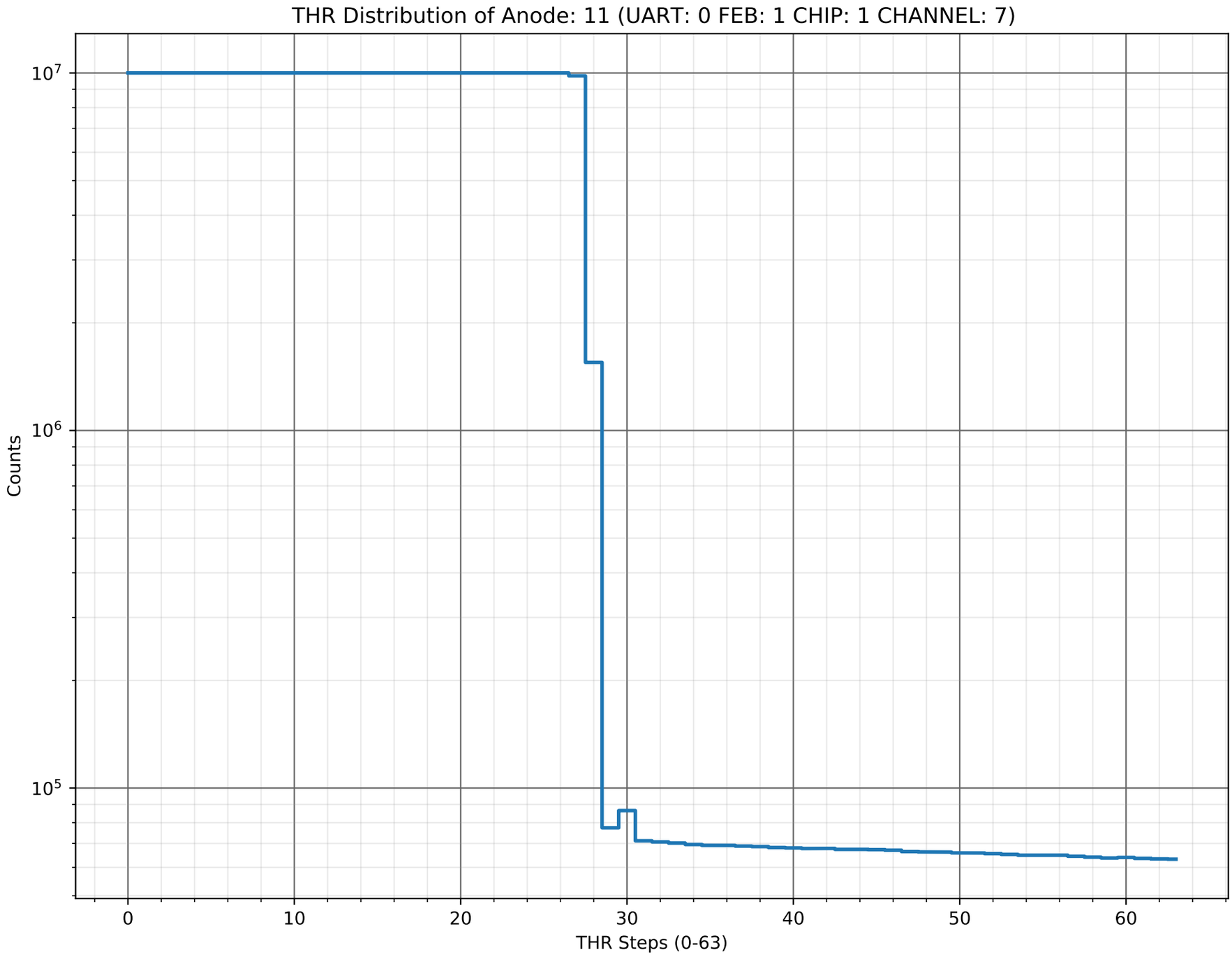

THR Distribution of Anode: 6 (UART: 0 FEB: 0 CHIP: 0 CHANNEL: 5)
10^7
10^6
10^5
Counts
0
10
20
30
40
50
60
THR Steps (0-63)

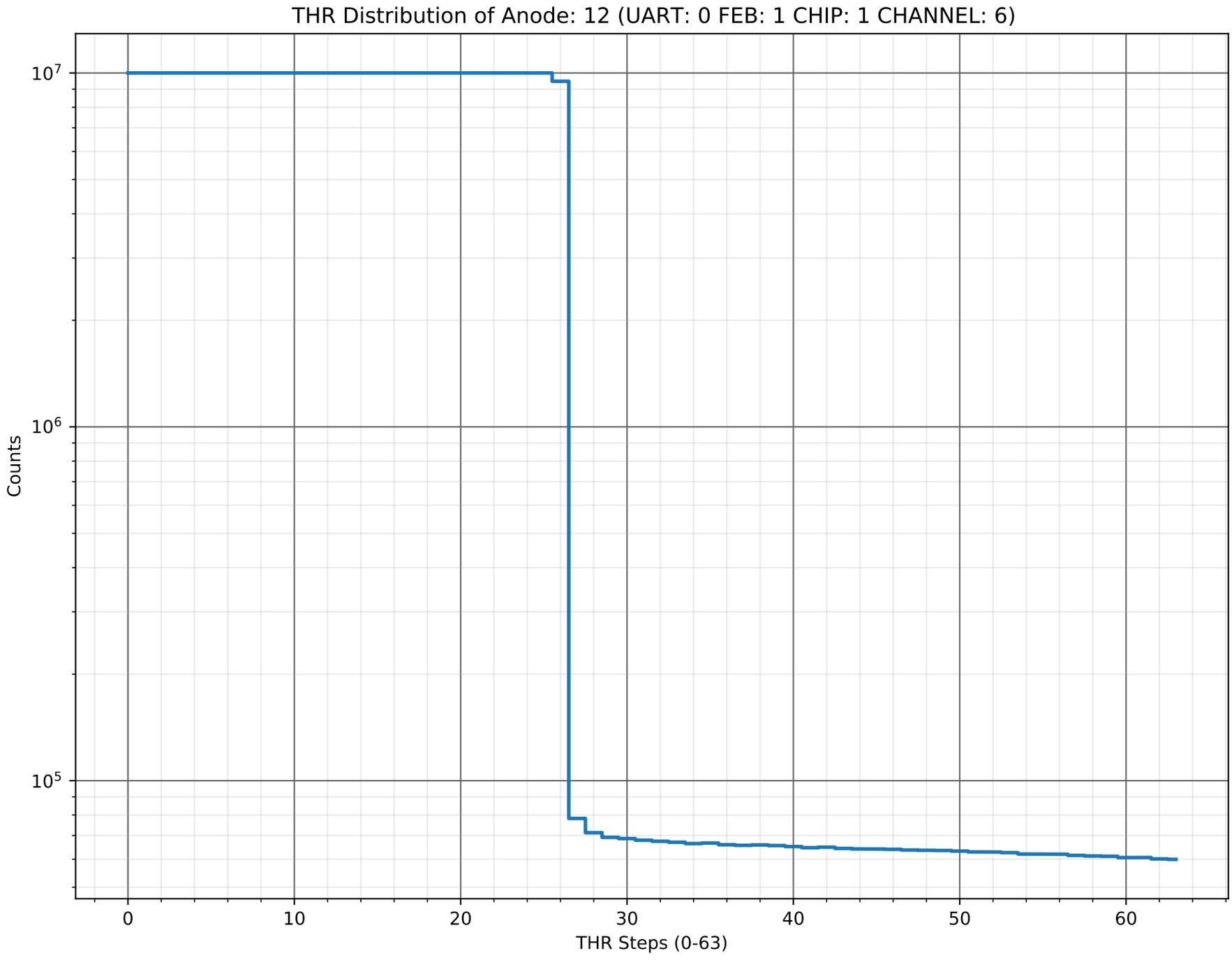

THR Distribution of Anode: 7 (UART: 0 FEB: 0 CHIP: 1 CHANNEL: 2)
10^7
10^6
10^5
Counts
0
10
20
30
40
50
60
THR Steps (0-63)

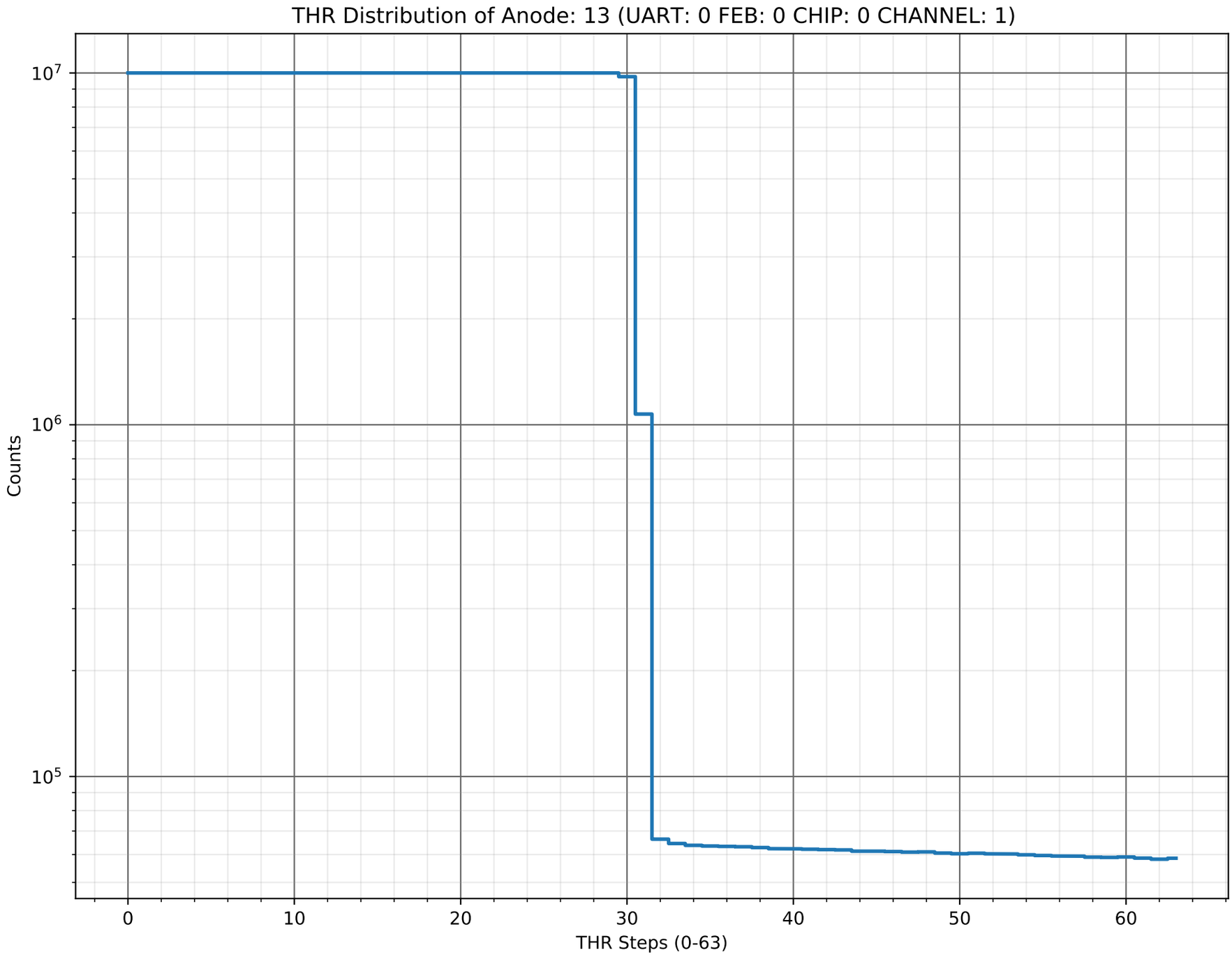

THR Distribution of Anode: 8 (UART: 0 FEB: 0 CHIP: 1 CHANNEL: 0)
10^7
10^6
10^5
Counts
0
10
20
30
40
50
60
THR Steps (0-63)

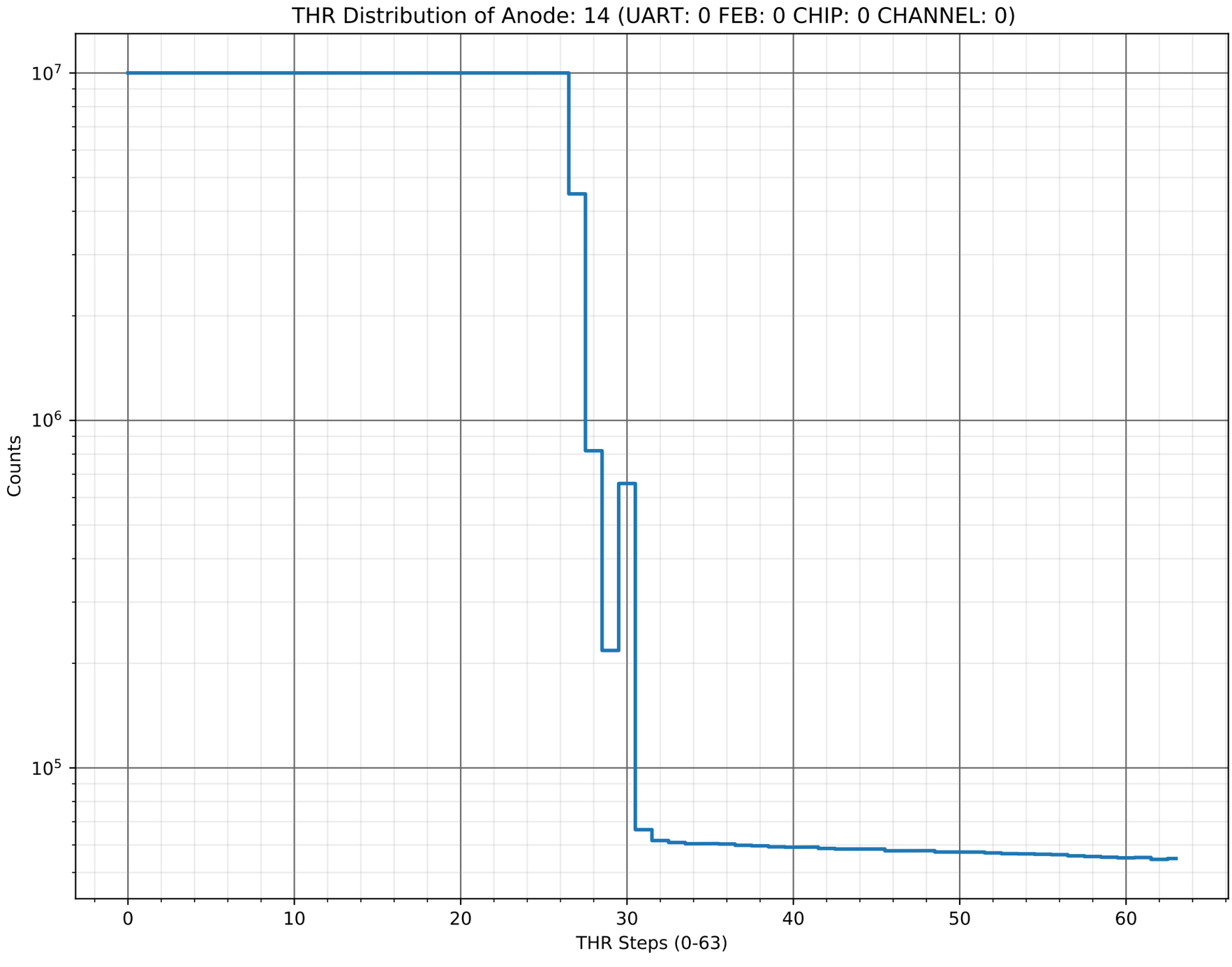

THR Distribution of Anode: 9 (UART: 0 FEB: 1 CHIP: 0 CHANNEL: 1)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

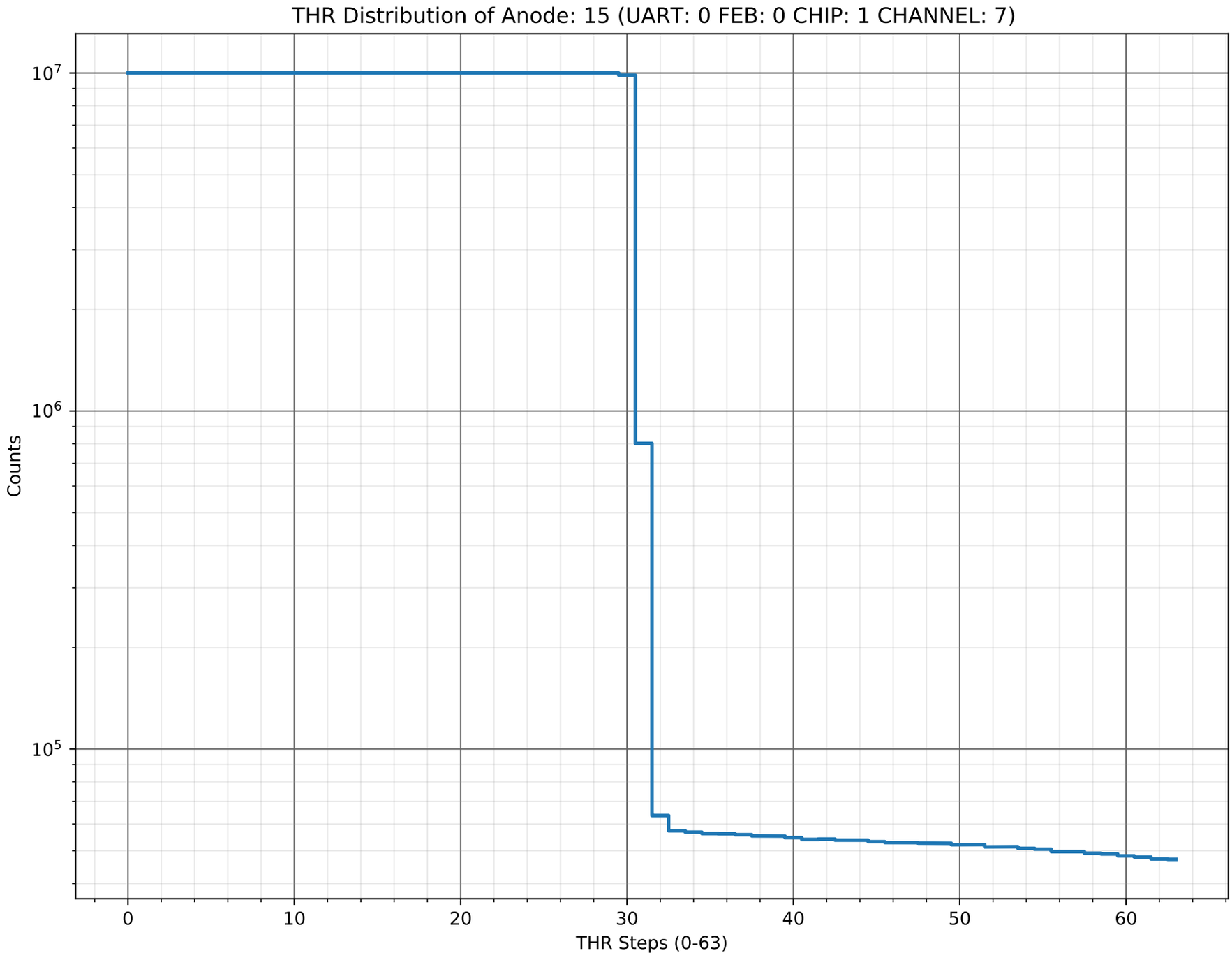

THR Distribution of Anode: 10 (UART: 0 FEB: 1 CHIP: 0 CHANNEL: 0)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

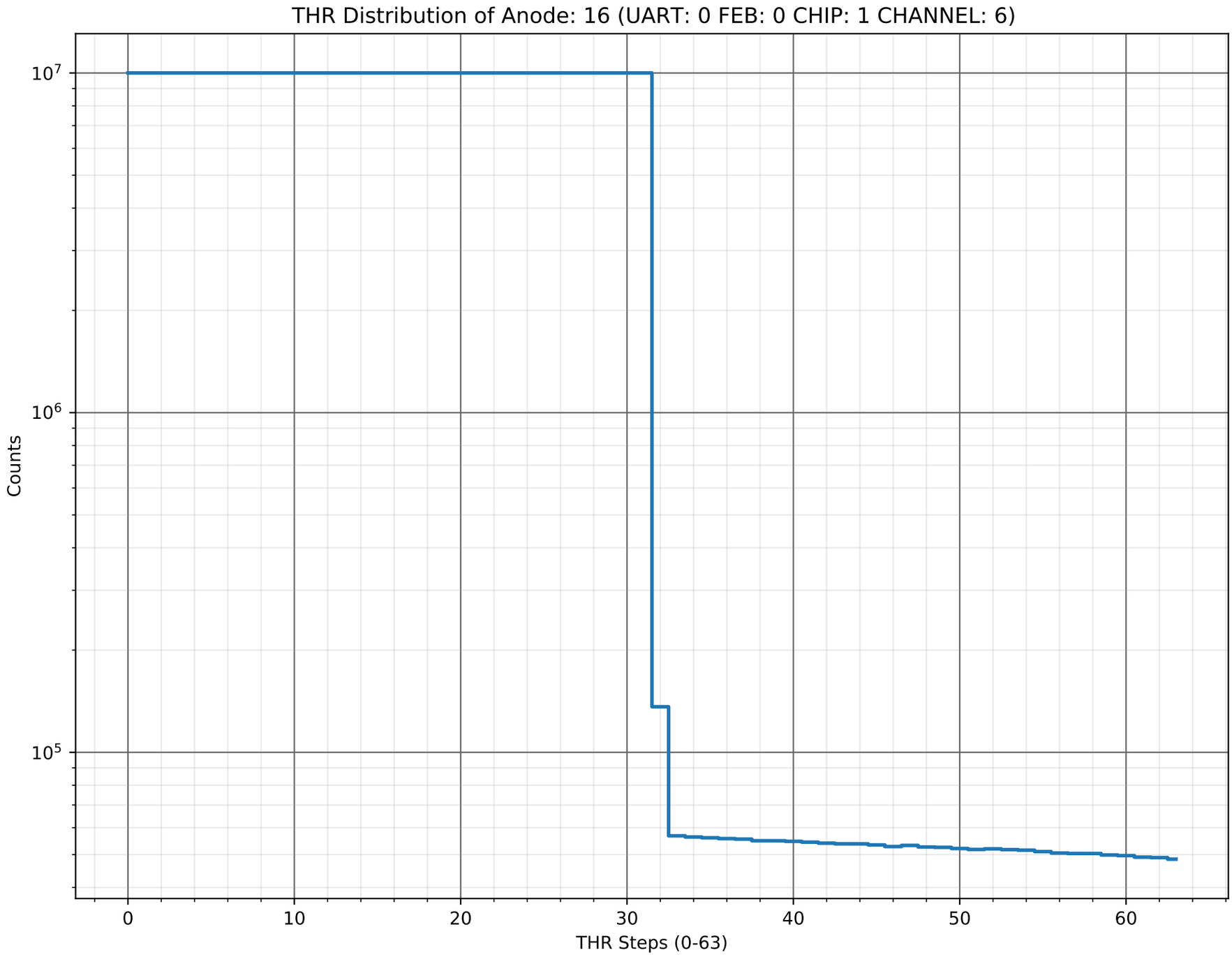

THR Distribution of Anode: 11 (UART: 0 FEB: 1 CHIP: 1 CHANNEL: 7)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

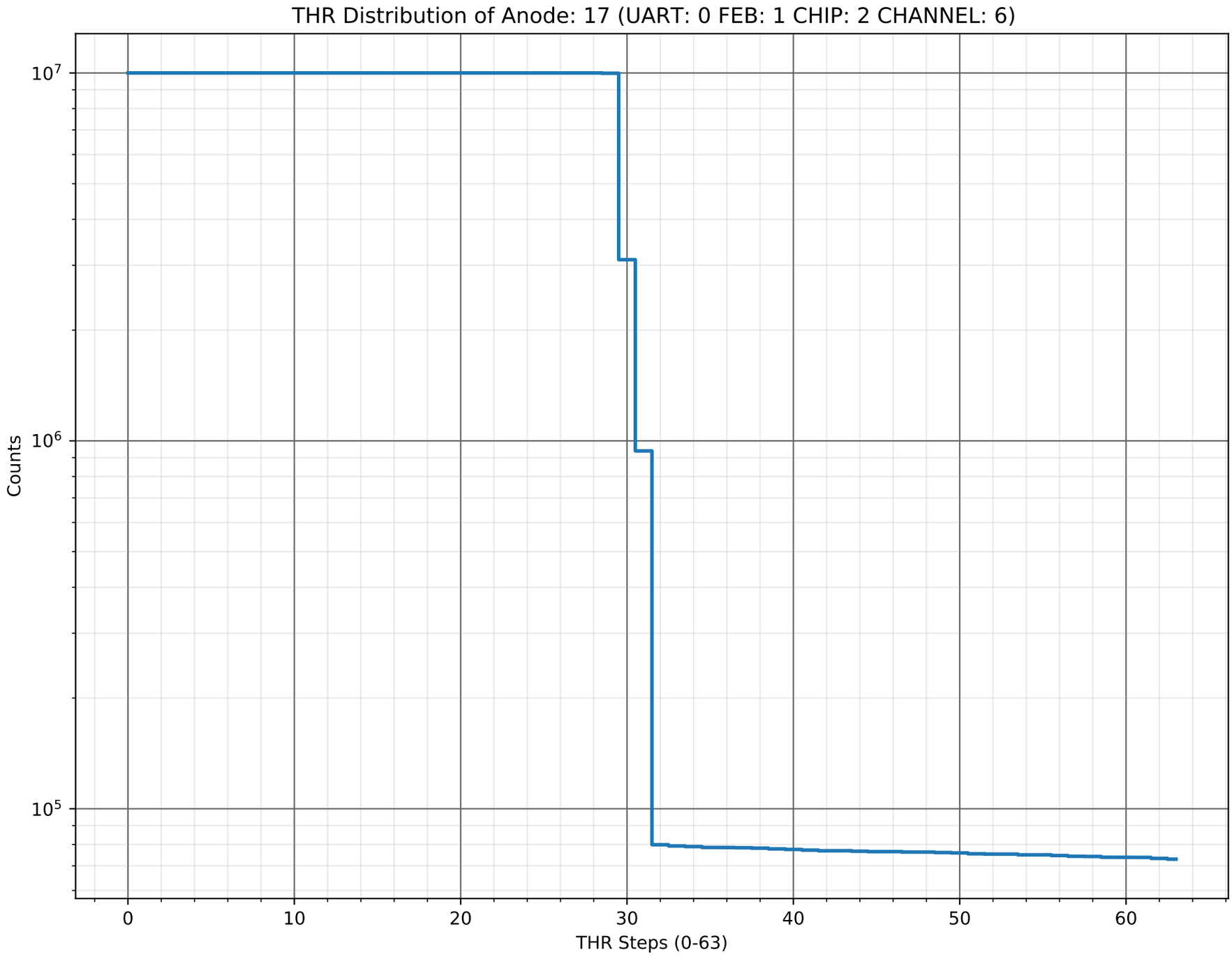

THR Distribution of Anode: 12 (UART: 0 FEB: 1 CHIP: 1 CHANNEL: 6)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

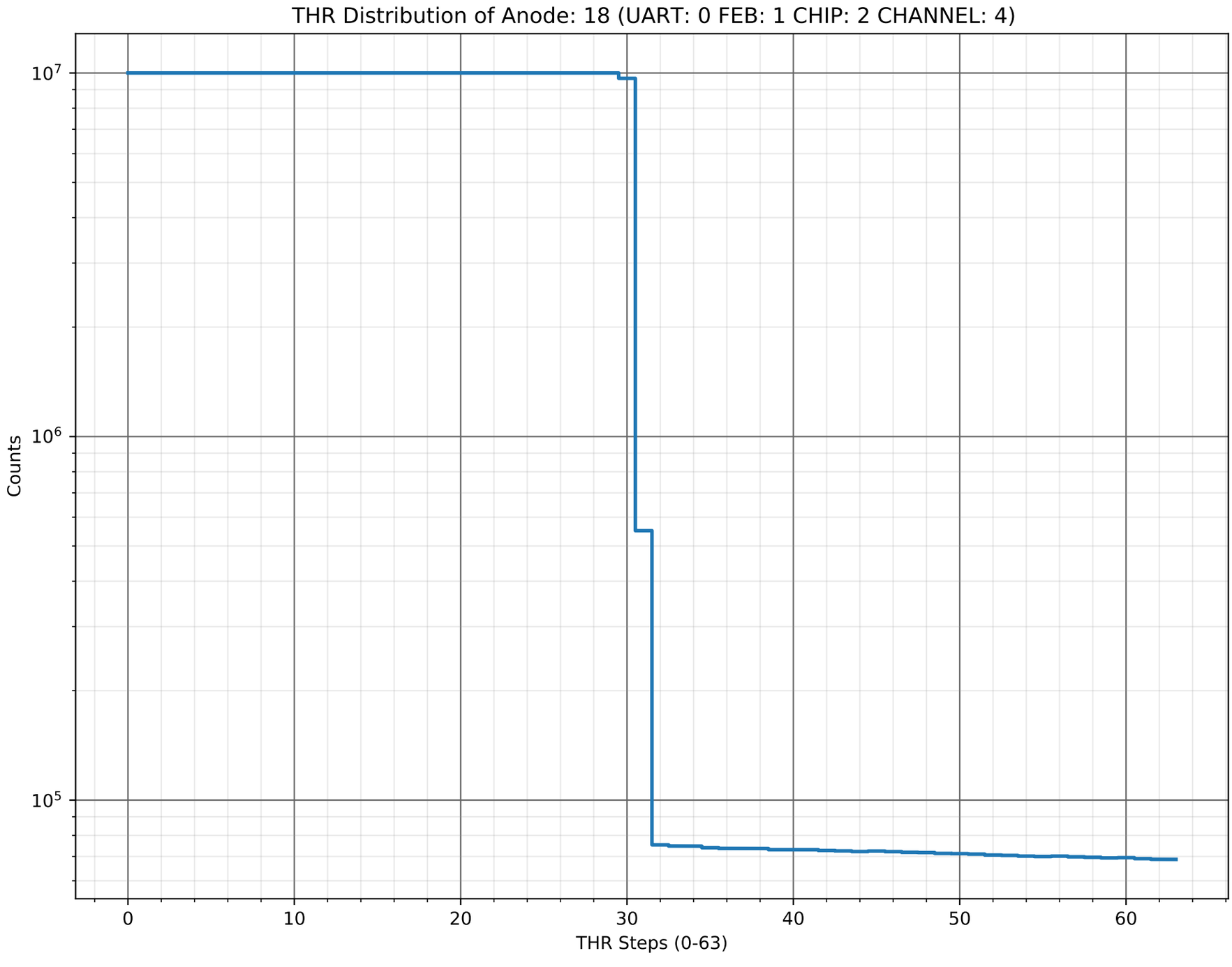

THR Distribution of Anode: 13 (UART: 0 FEB: 0 CHIP: 0 CHANNEL: 1)
10^7
10^6
10^5
Counts
0
10
20
30
40
50
60
THR Steps (0-63)

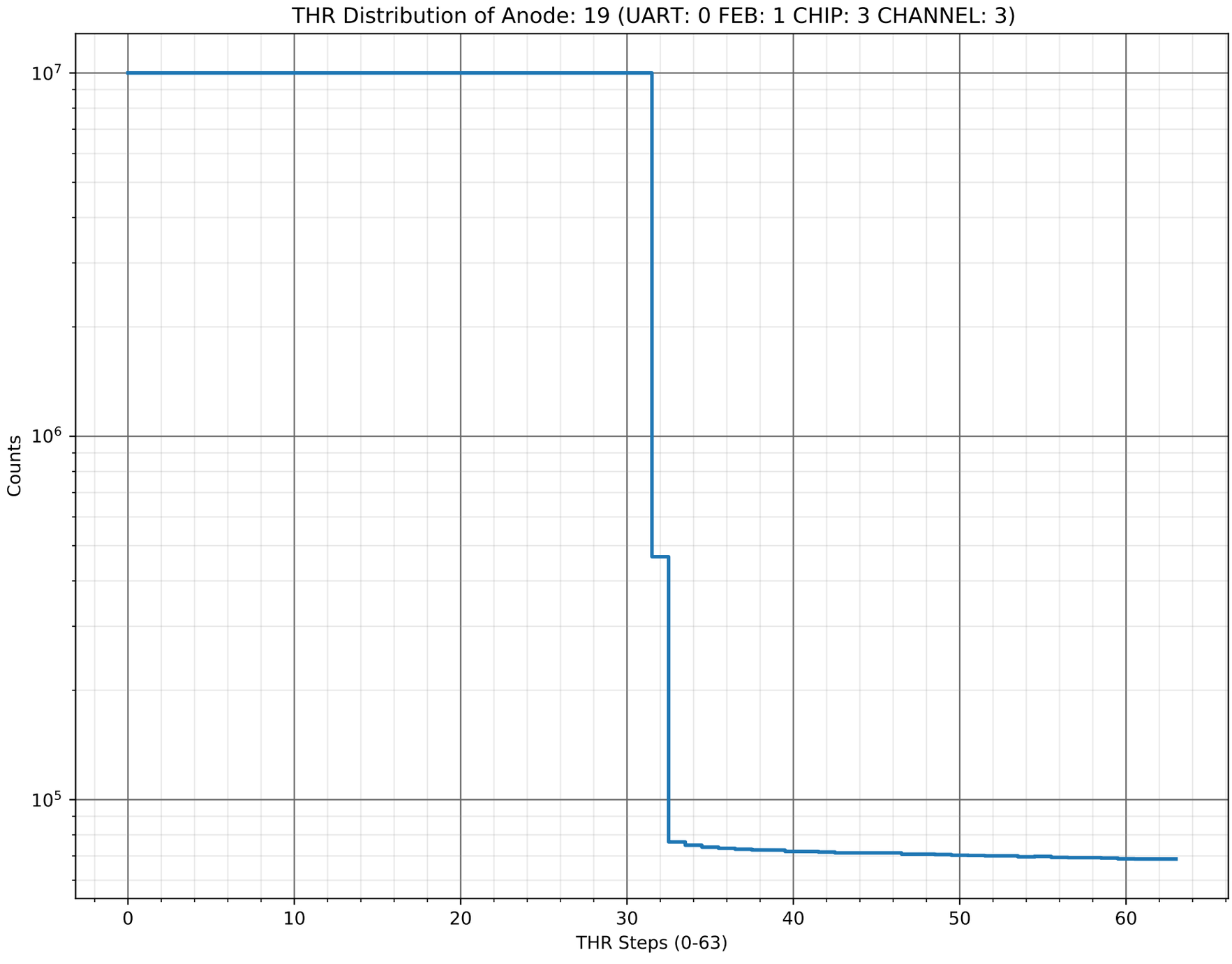

THR Distribution of Anode: 14 (UART: 0 FEB: 0 CHIP: 0 CHANNEL: 0)
10^7
10^6
10^5
Counts
0
10
20
30
40
50
60
THR Steps (0-63)

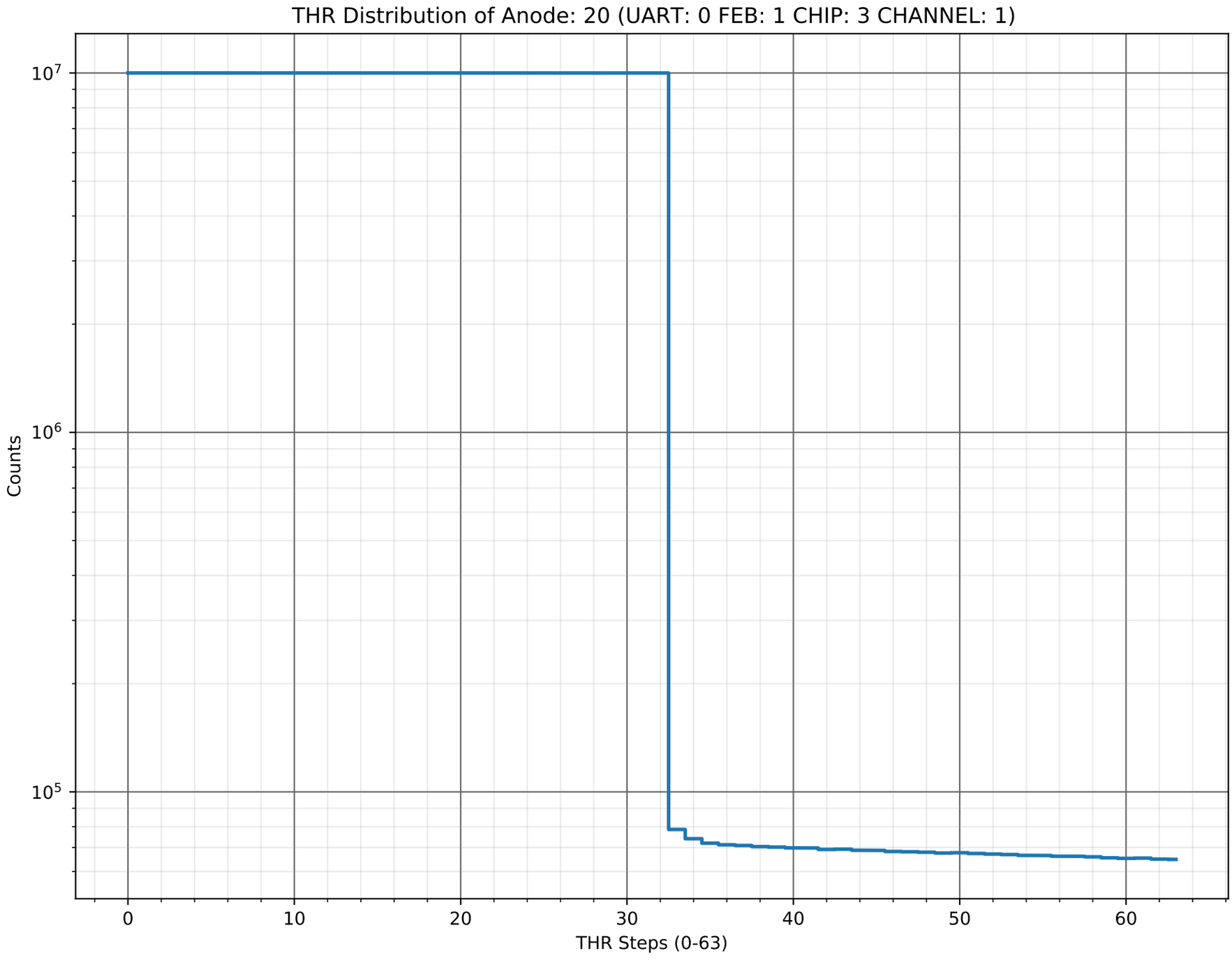

THR Distribution of Anode: 15 (UART: 0 FEB: 0 CHIP: 1 CHANNEL: 7)
10^7
10^6
10^5
Counts
0
10
20
30
40
50
60
THR Steps (0-63)

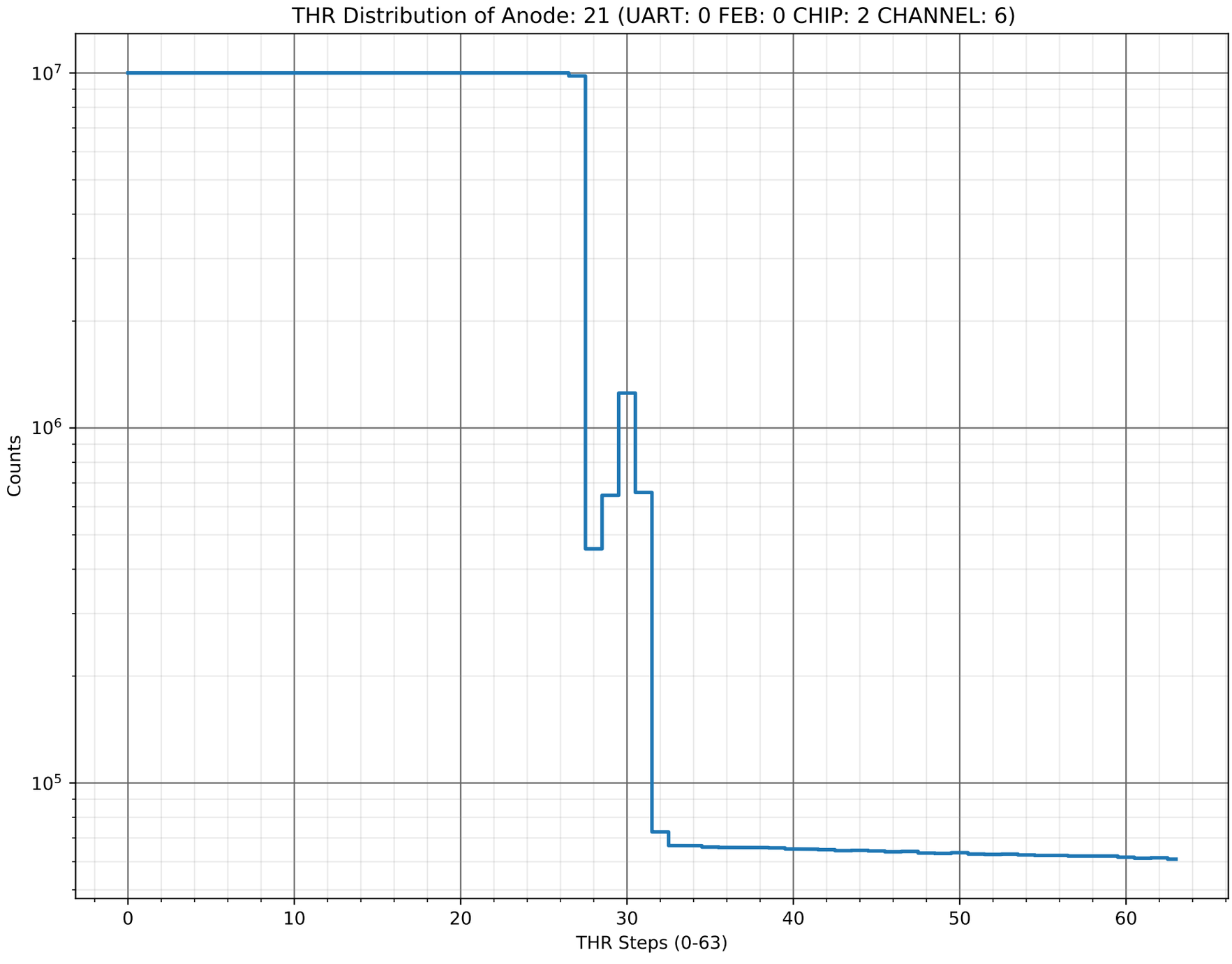

THR Distribution of Anode: 16 (UART: 0 FEB: 0 CHIP: 1 CHANNEL: 6)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

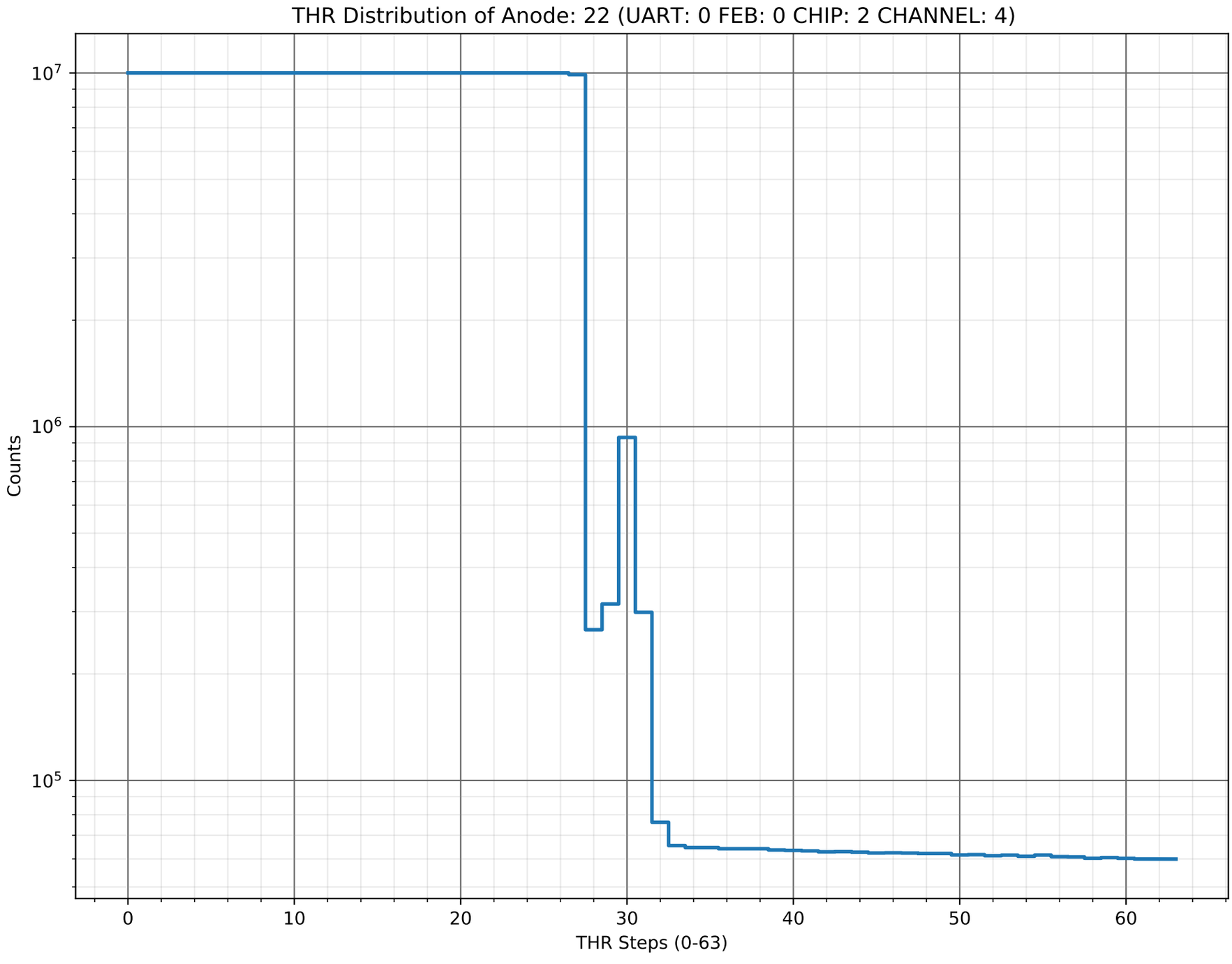

THR Distribution of Anode: 17 (UART: 0 FEB: 1 CHIP: 2 CHANNEL: 6)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

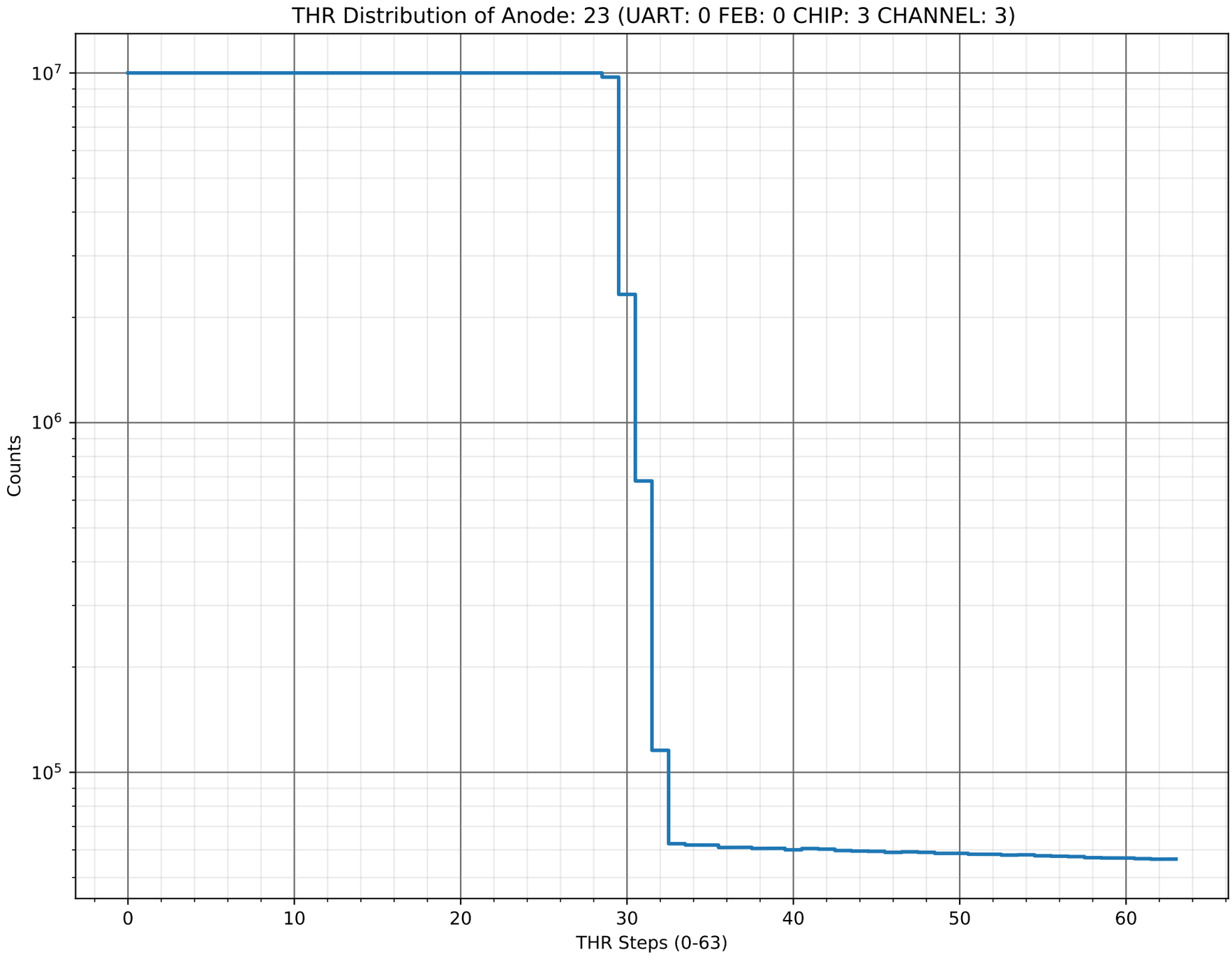

THR Distribution of Anode: 18 (UART: 0 FEB: 1 CHIP: 2 CHANNEL: 4)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

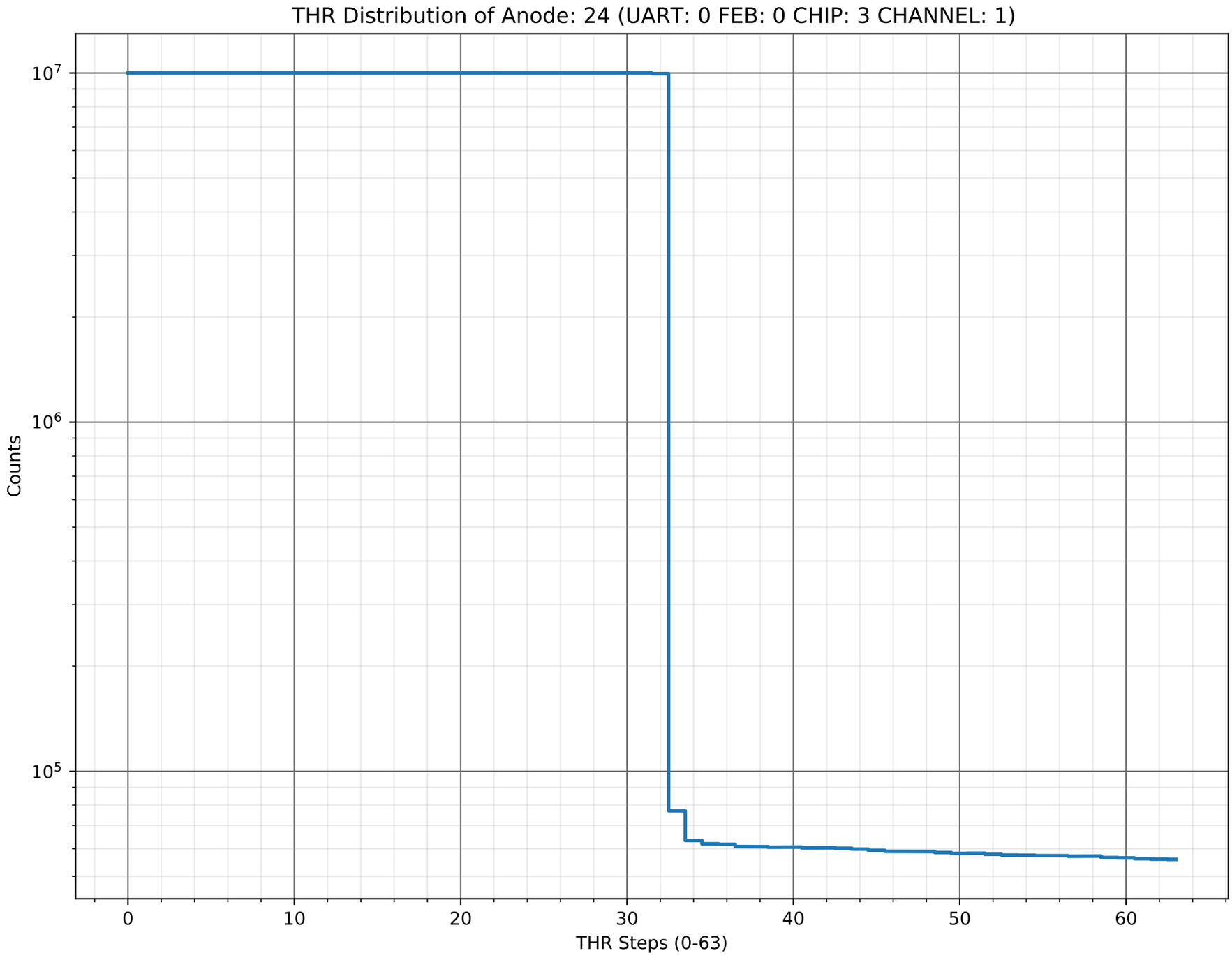

THR Distribution of Anode: 19 (UART: 0 FEB: 1 CHIP: 3 CHANNEL: 3)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

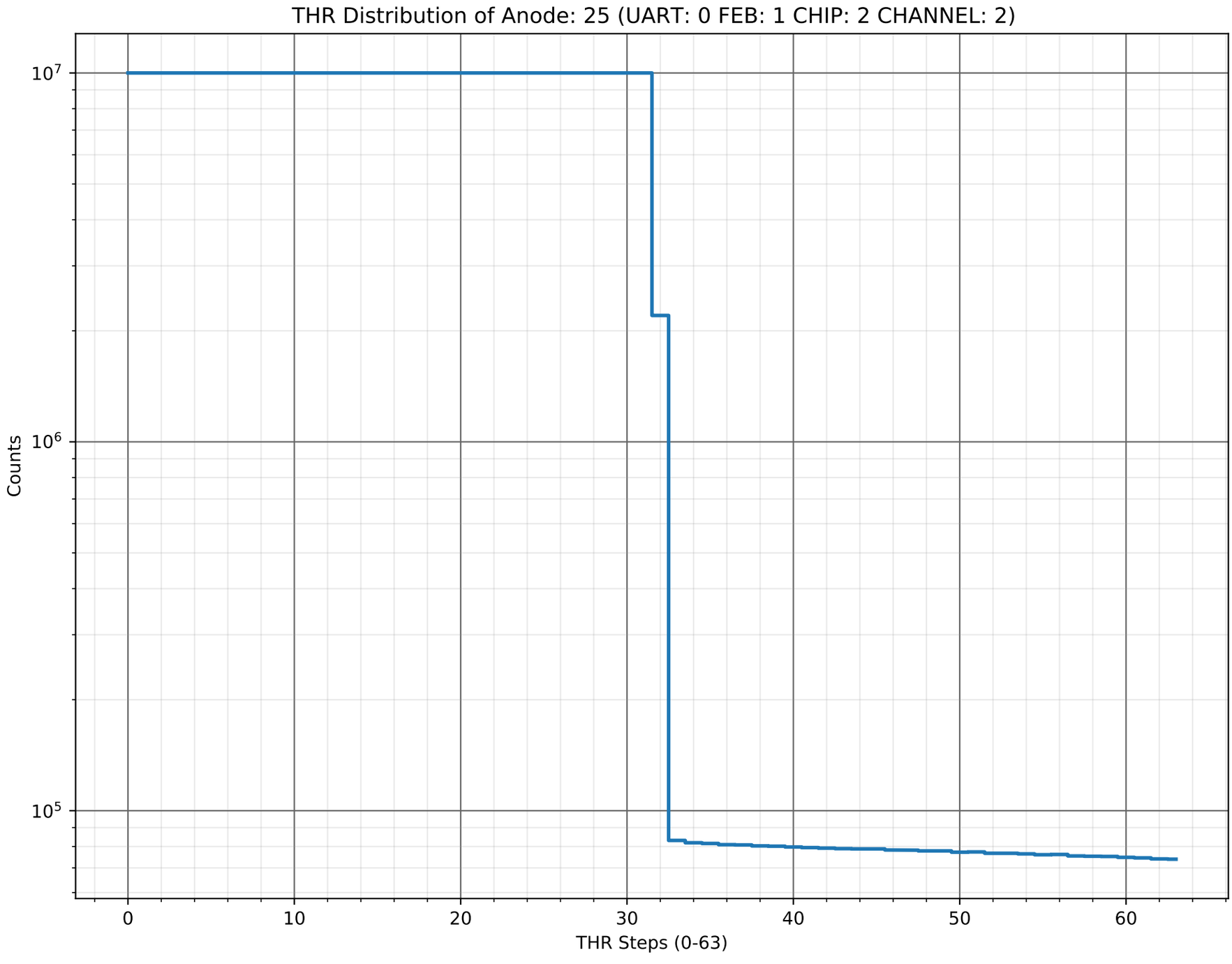

THR Distribution of Anode: 20 (UART: 0 FEB: 1 CHIP: 3 CHANNEL: 1)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

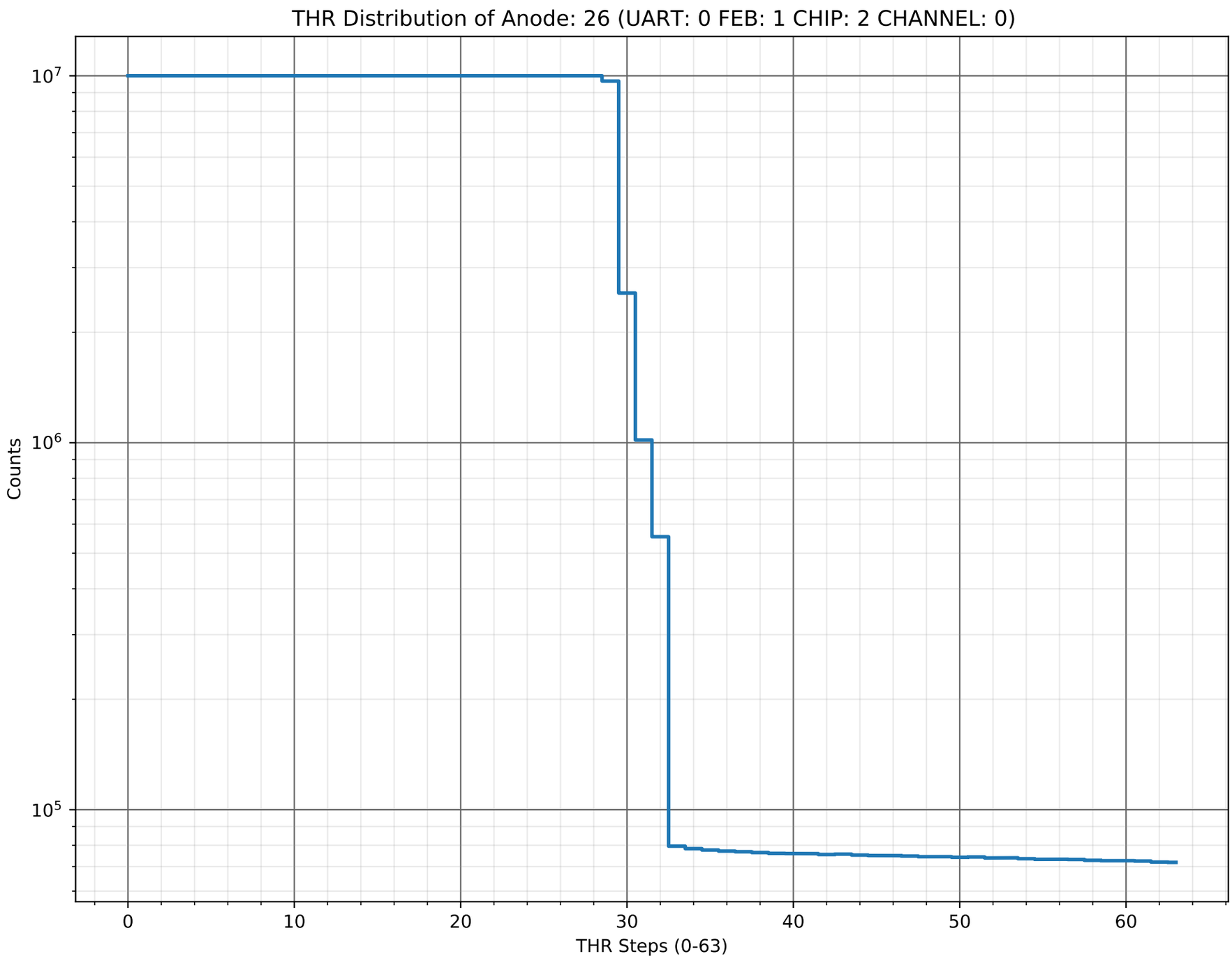

THR Distribution of Anode: 21 (UART: 0 FEB: 0 CHIP: 2 CHANNEL: 6)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

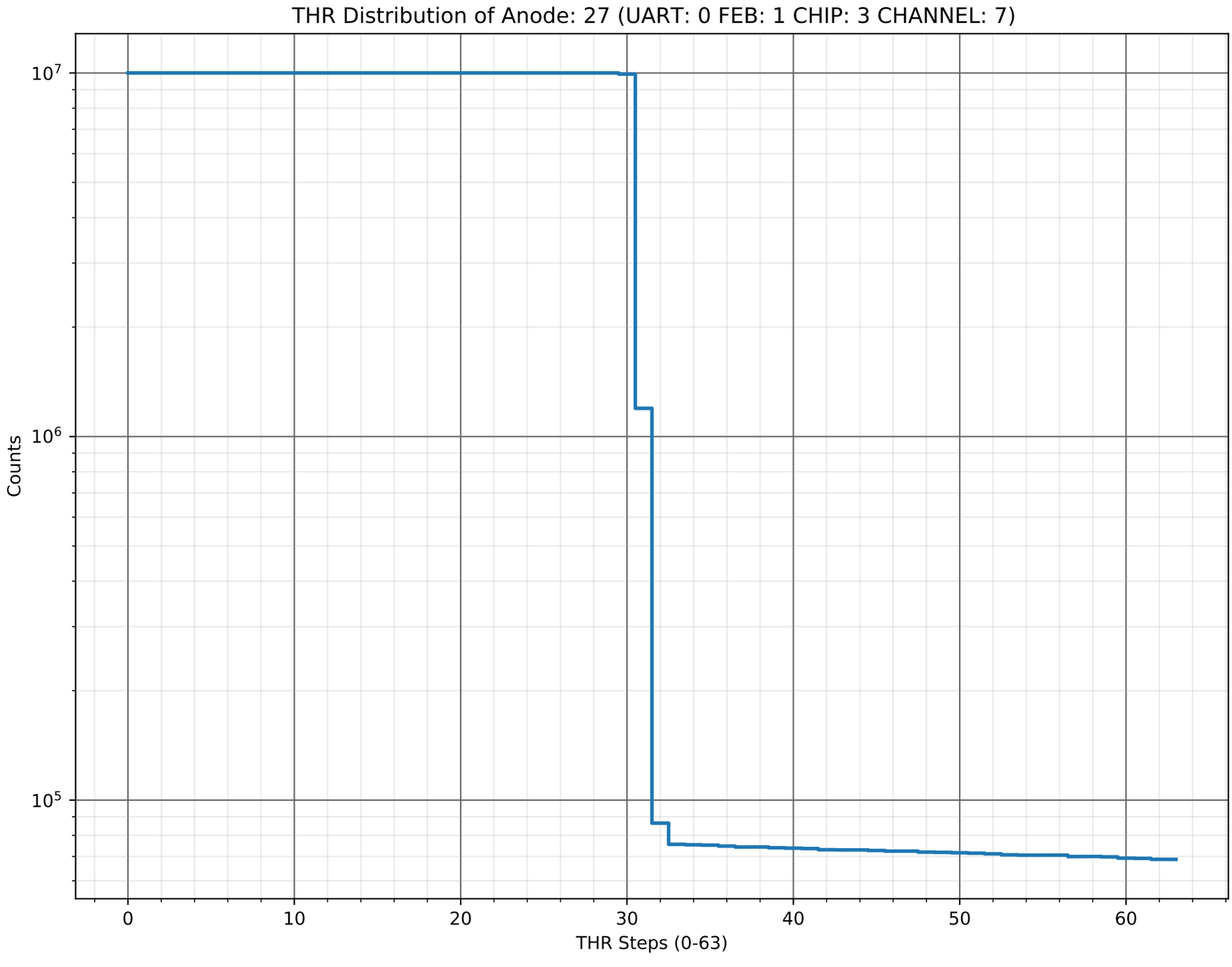

THR Distribution of Anode: 22 (UART: 0 FEB: 0 CHIP: 2 CHANNEL: 4)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

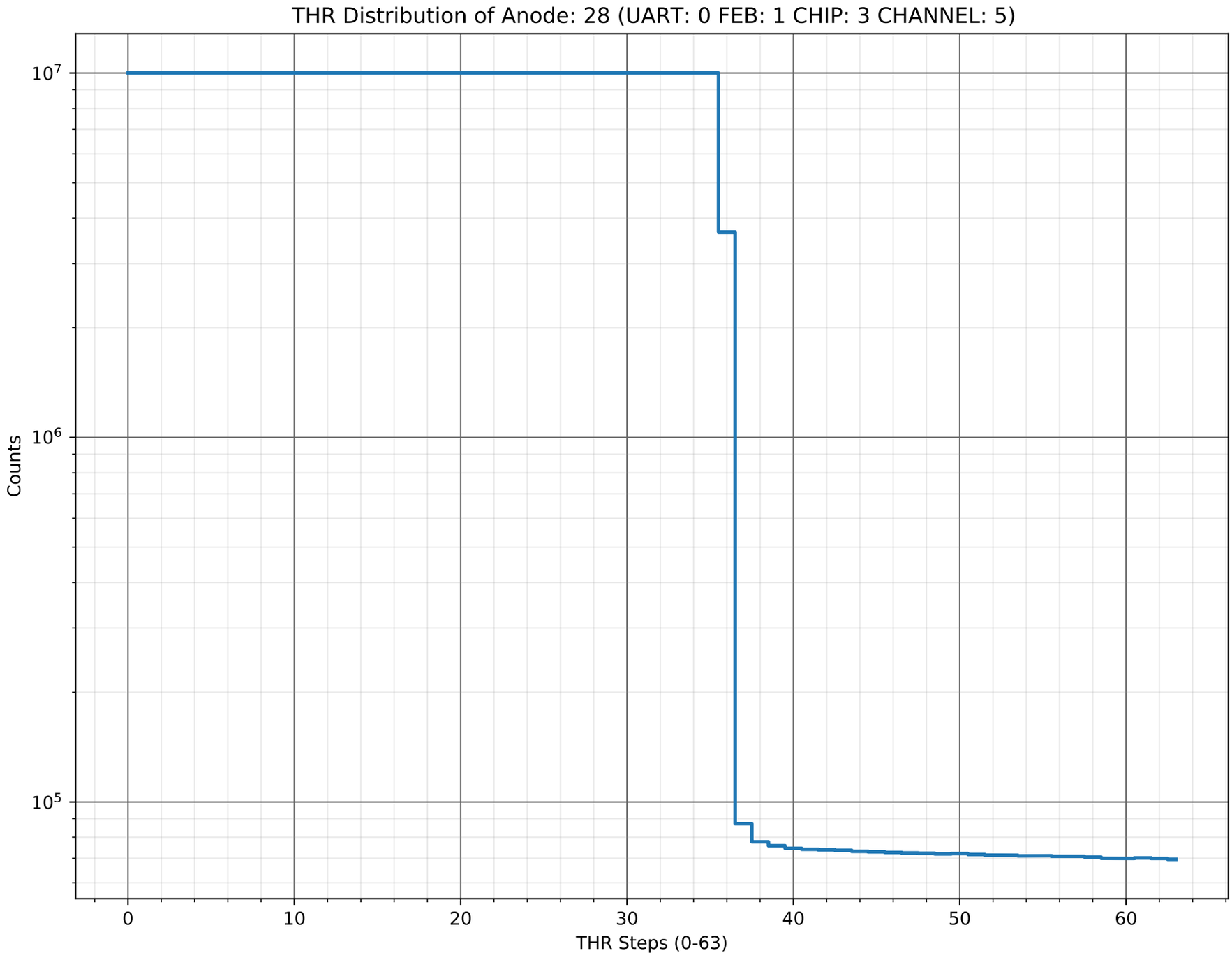

THR Distribution of Anode: 23 (UART: 0 FEB: 0 CHIP: 3 CHANNEL: 3)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

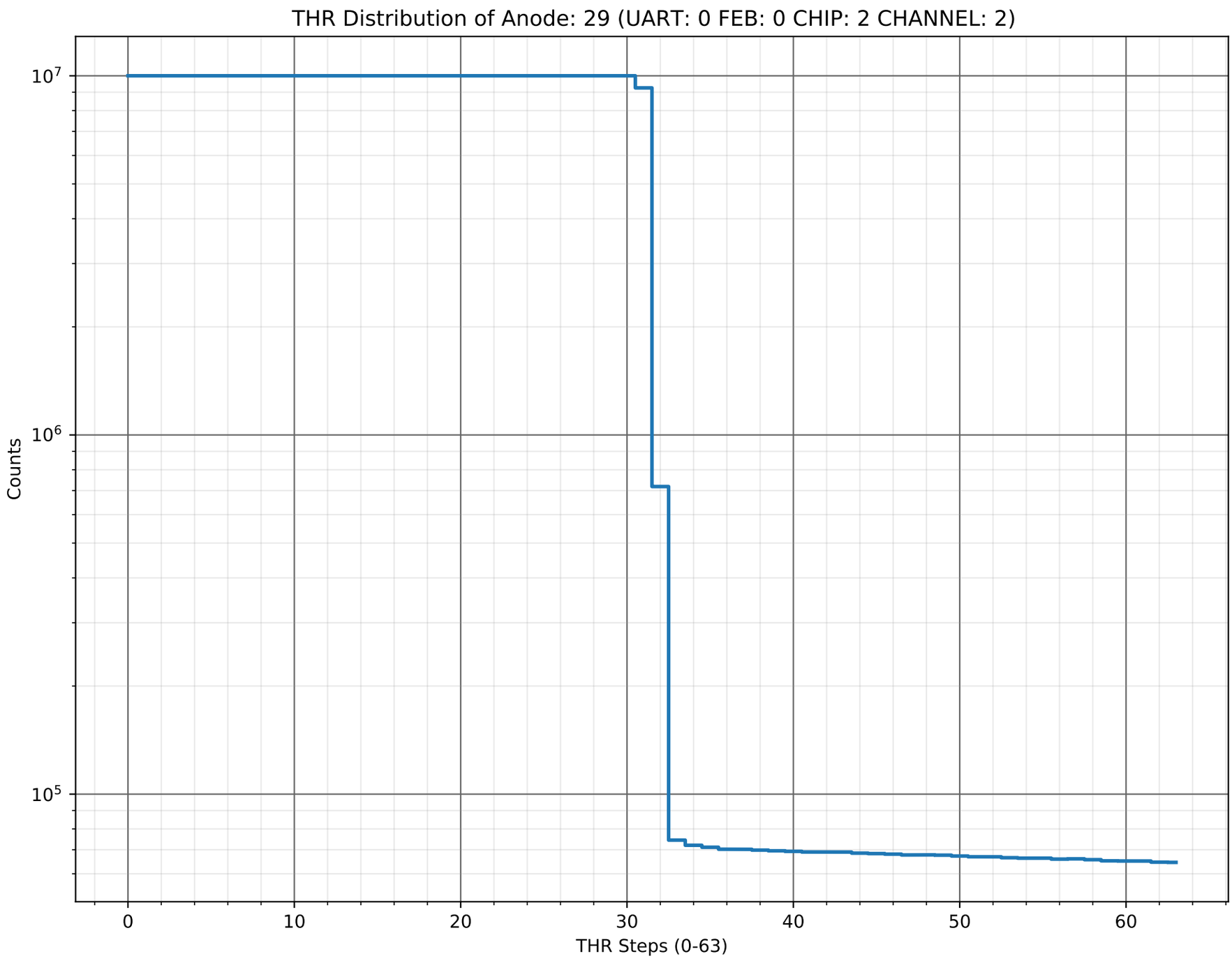

THR Distribution of Anode: 24 (UART: 0 FEB: 0 CHIP: 3 CHANNEL: 1)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

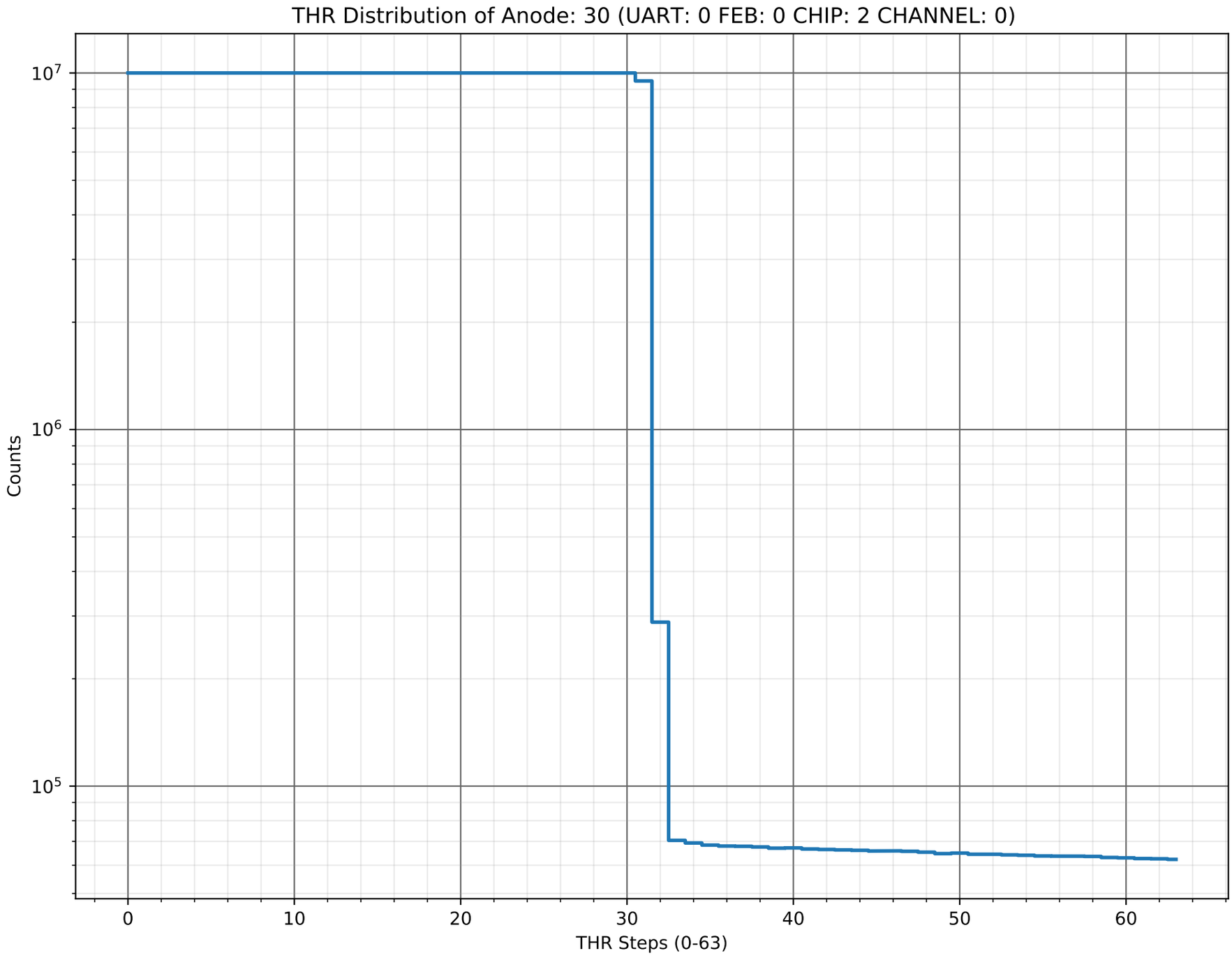

THR Distribution of Anode: 25 (UART: 0 FEB: 1 CHIP: 2 CHANNEL: 2)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

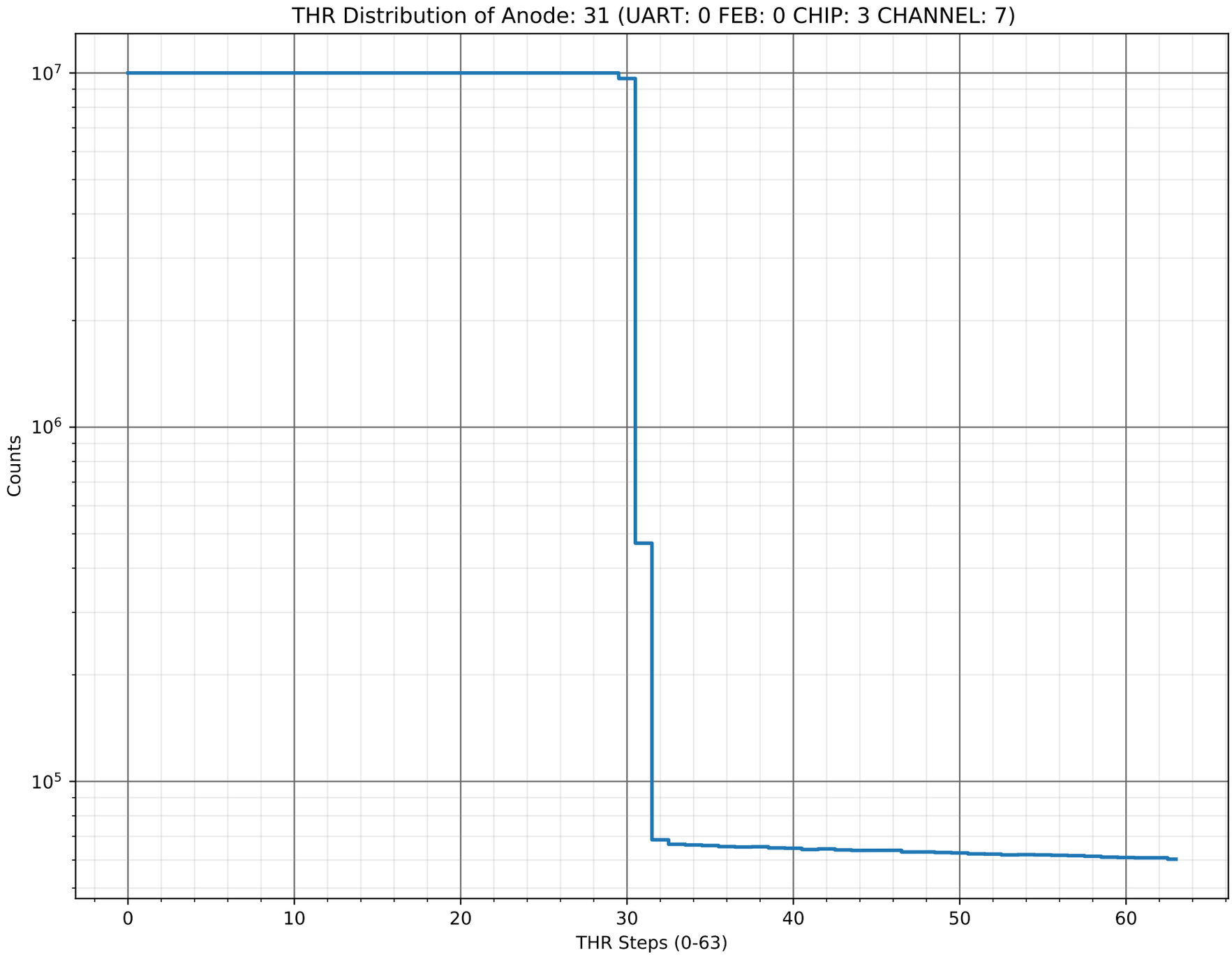

THR Distribution of Anode: 26 (UART: 0 FEB: 1 CHIP: 2 CHANNEL: 0)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

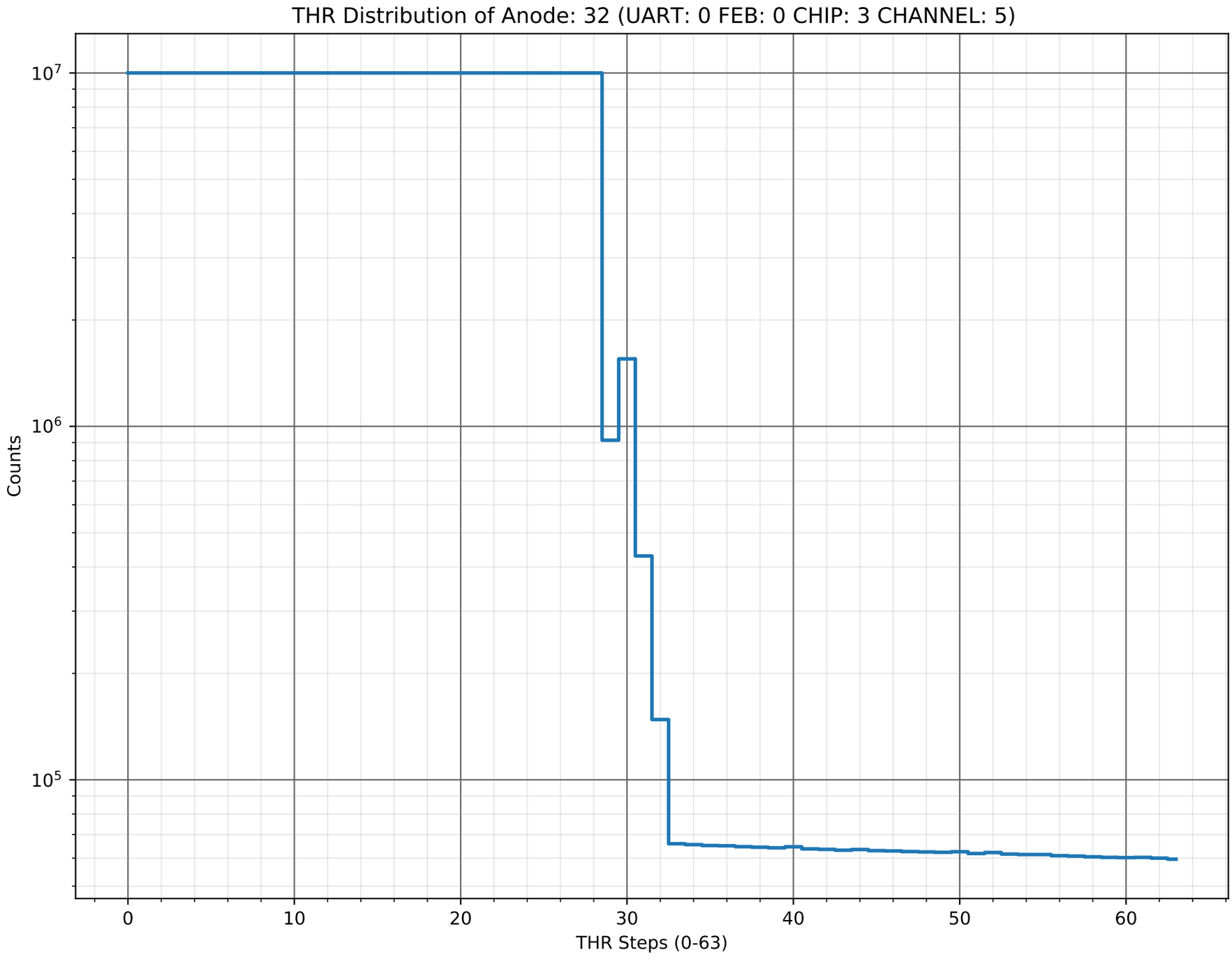

THR Distribution of Anode: 27 (UART: 0 FEB: 1 CHIP: 3 CHANNEL: 7)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

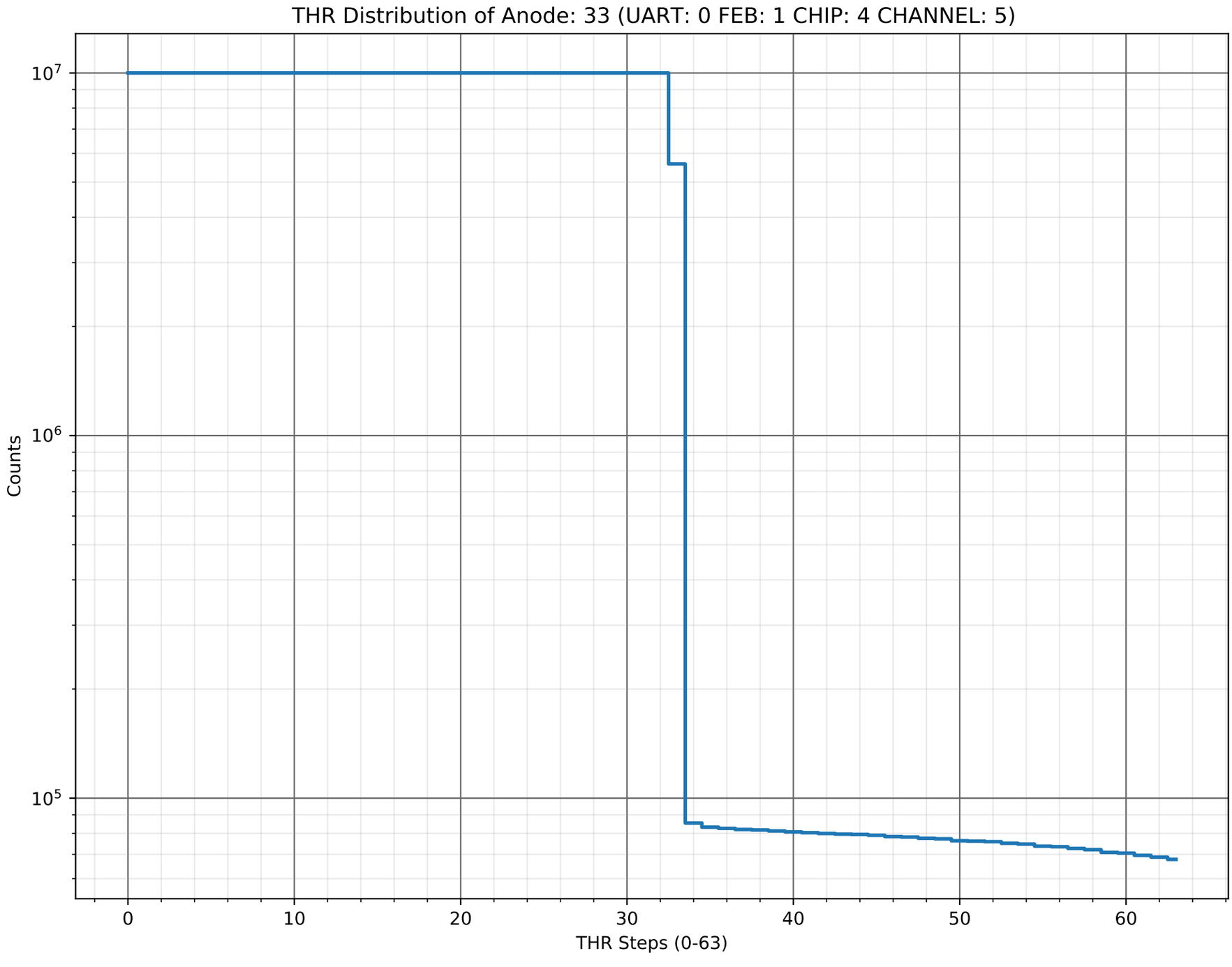

THR Distribution of Anode: 28 (UART: 0 FEB: 1 CHIP: 3 CHANNEL: 5)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

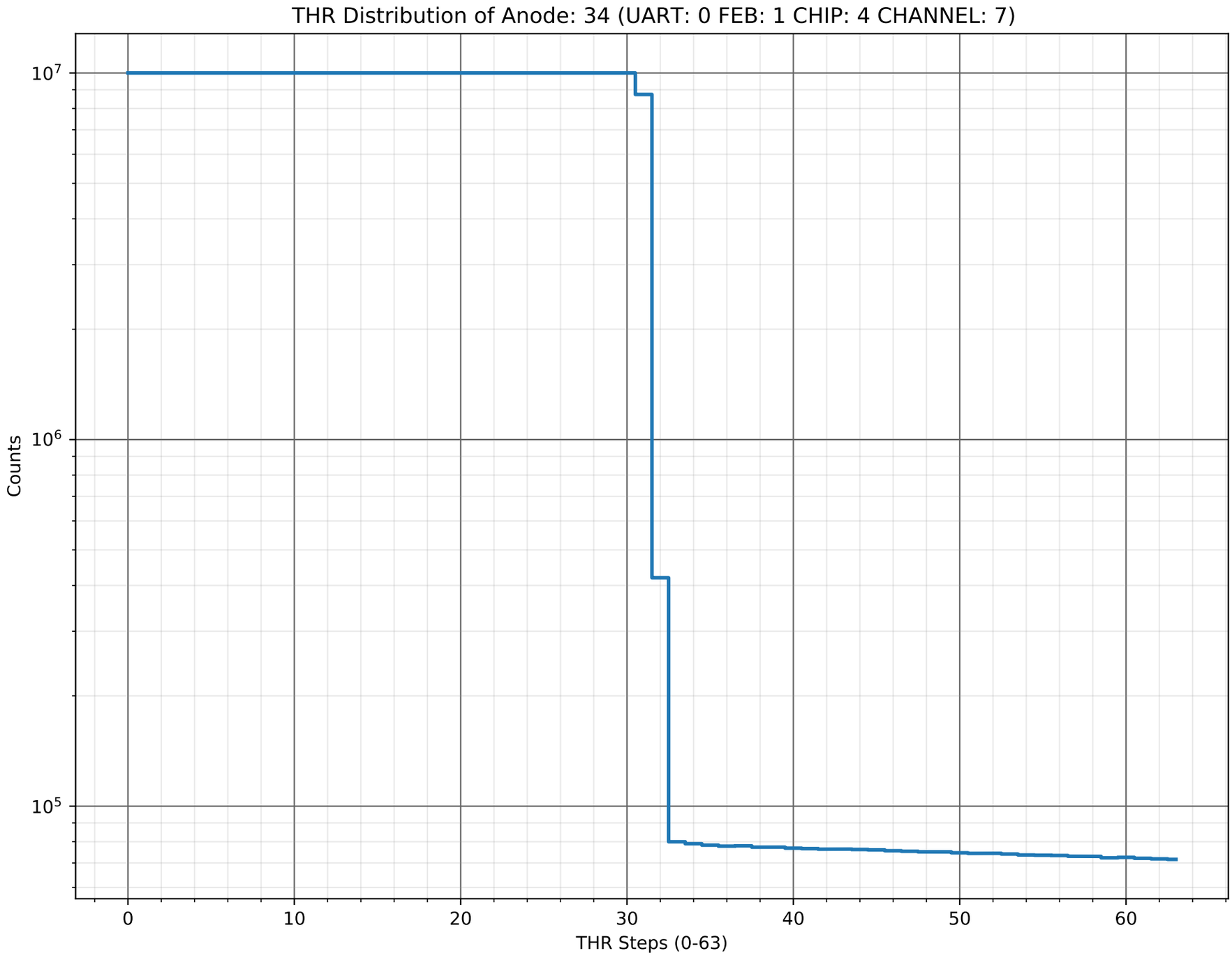

THR Distribution of Anode: 29 (UART: 0 FEB: 0 CHIP: 2 CHANNEL: 2)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

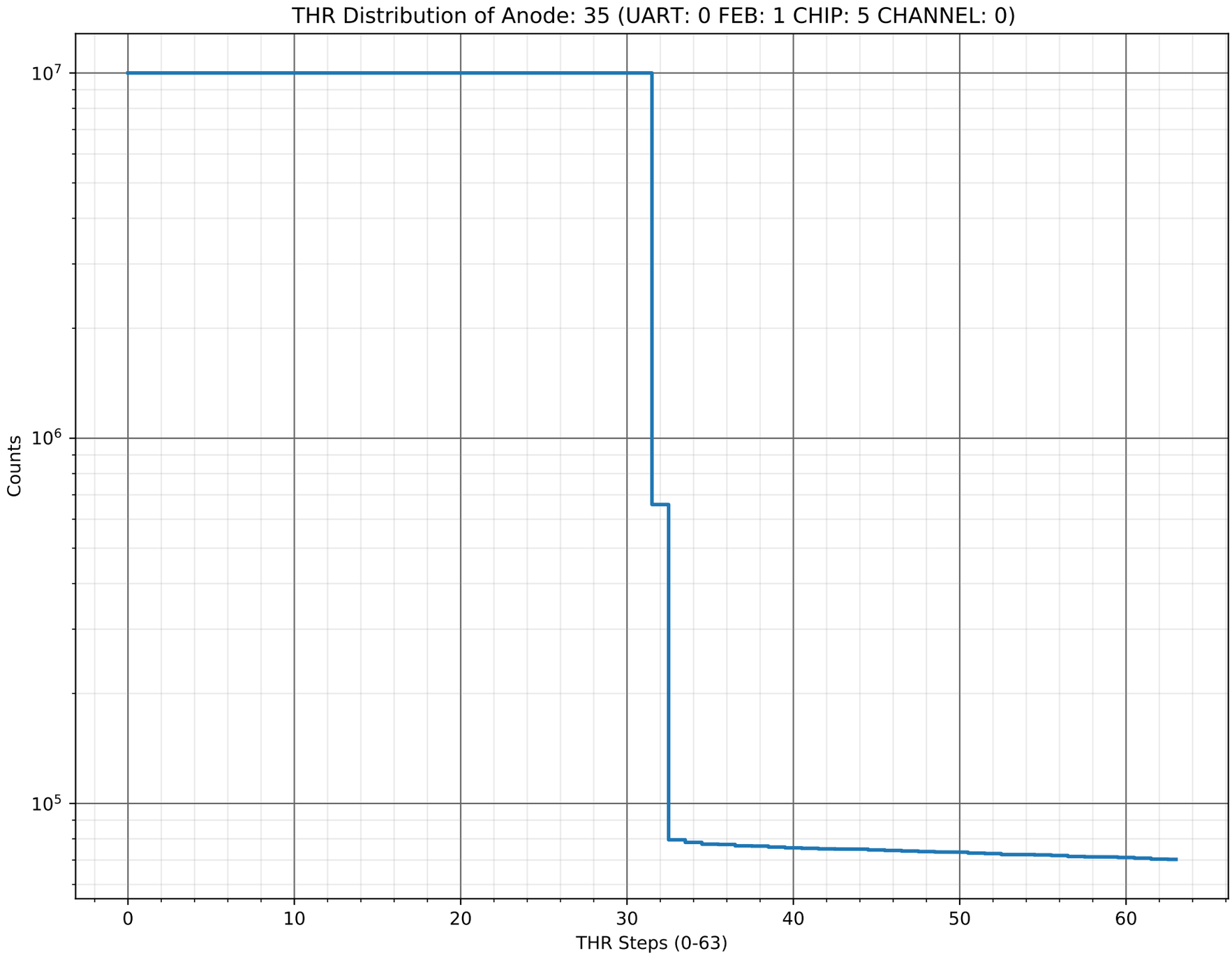

THR Distribution of Anode: 30 (UART: 0 FEB: 0 CHIP: 2 CHANNEL: 0)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

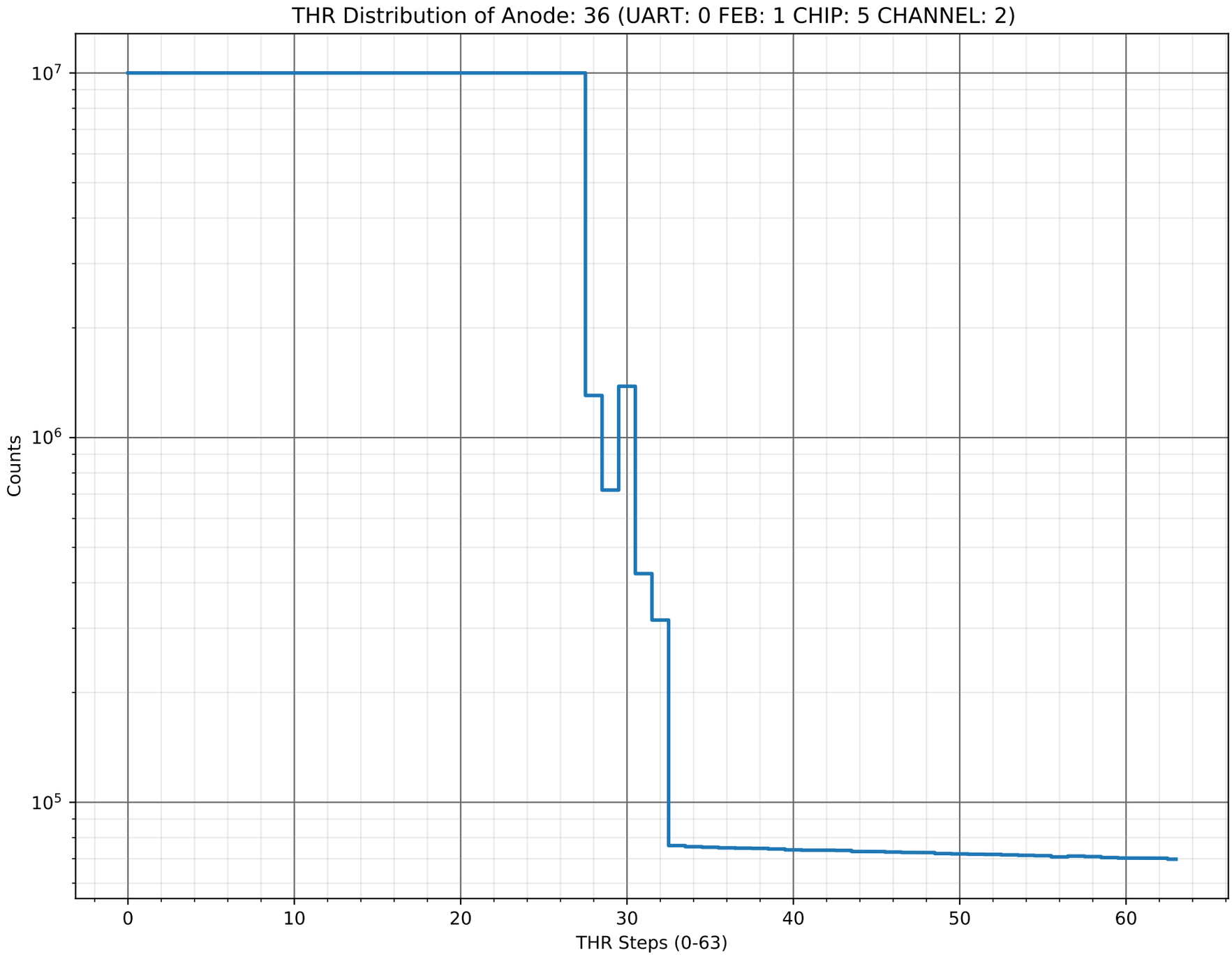

THR Distribution of Anode: 31 (UART: 0 FEB: 0 CHIP: 3 CHANNEL: 7)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

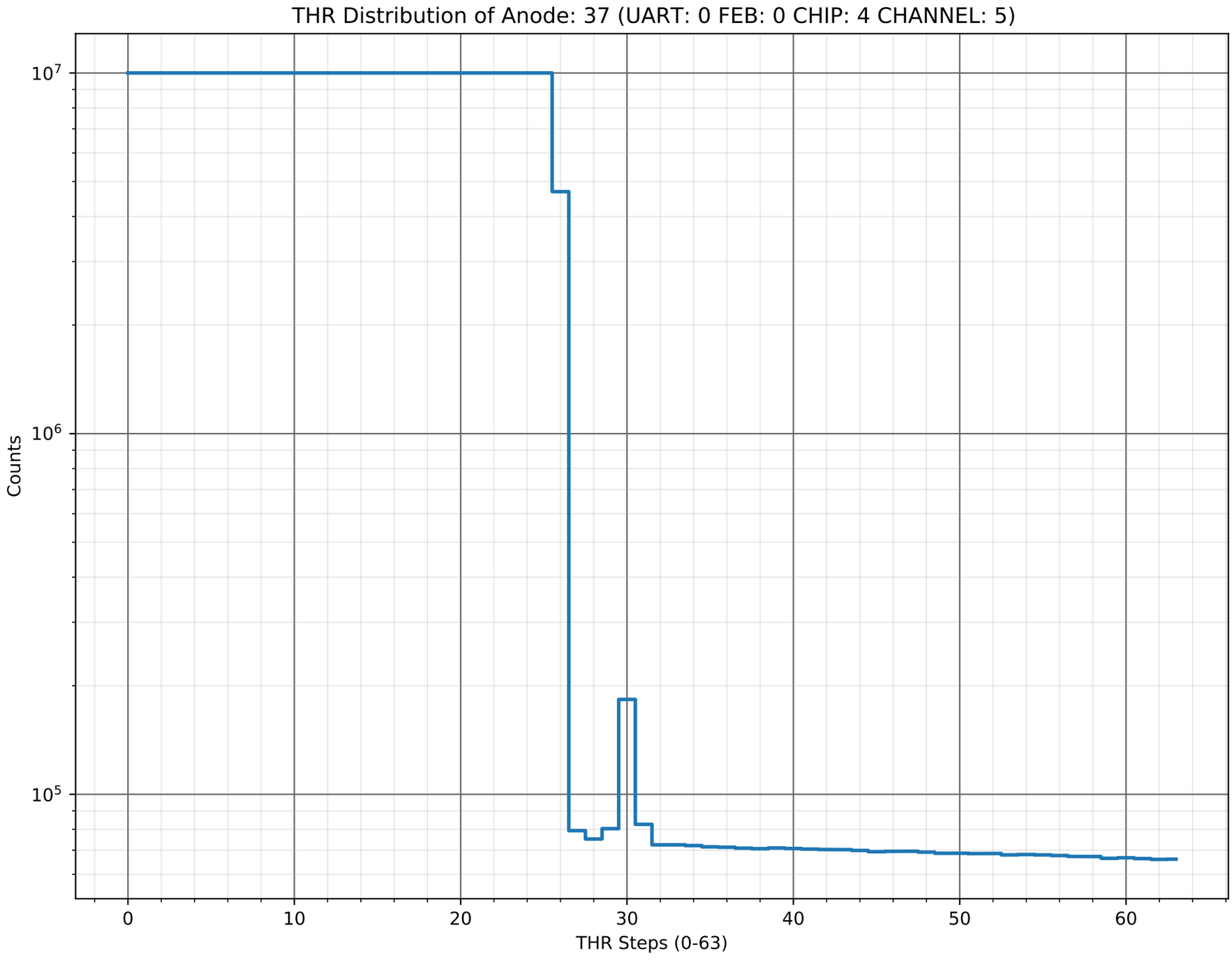

THR Distribution of Anode: 32 (UART: 0 FEB: 0 CHIP: 3 CHANNEL: 5)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

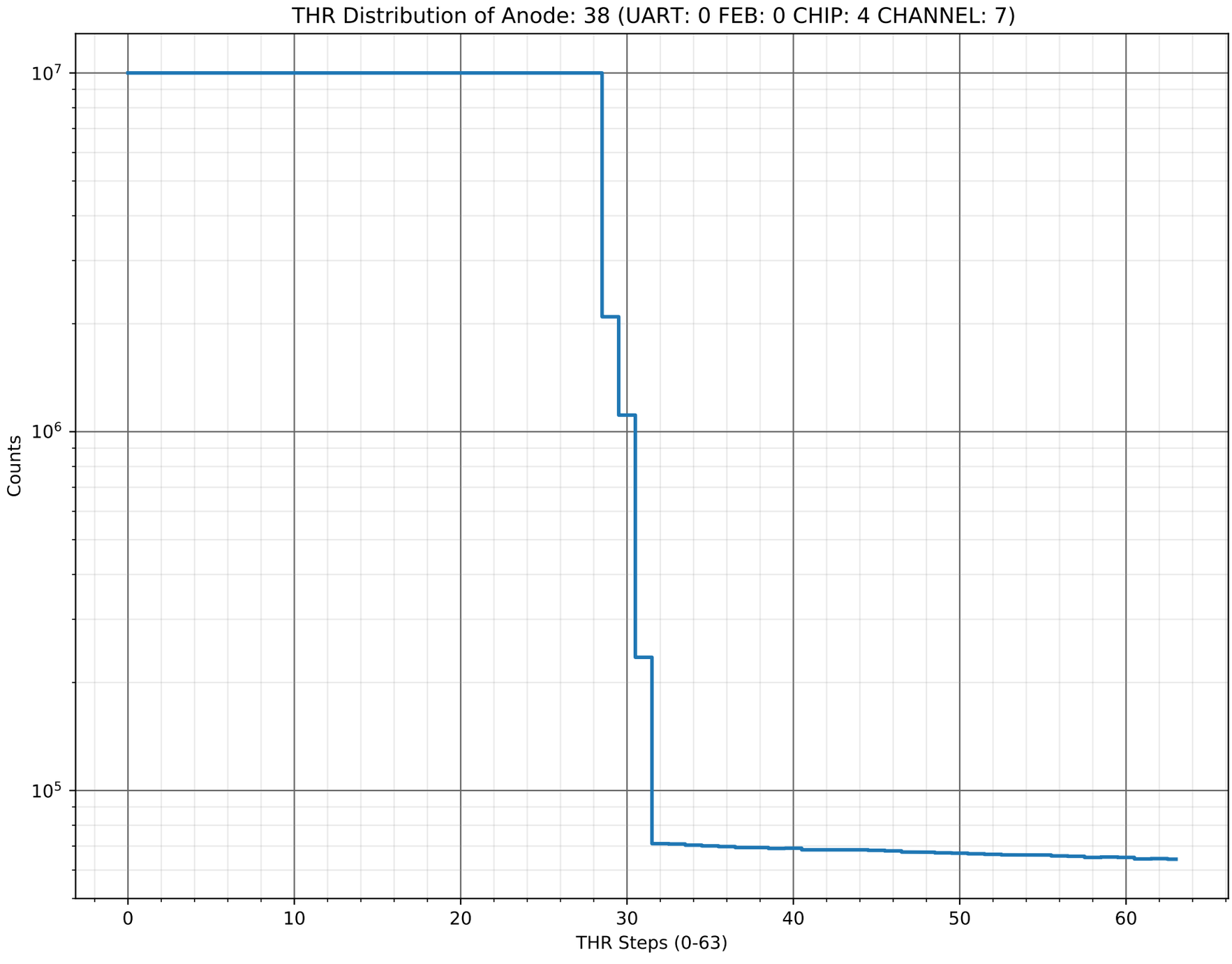

THR Distribution of Anode: 33 (UART: 0 FEB: 1 CHIP: 4 CHANNEL: 5)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

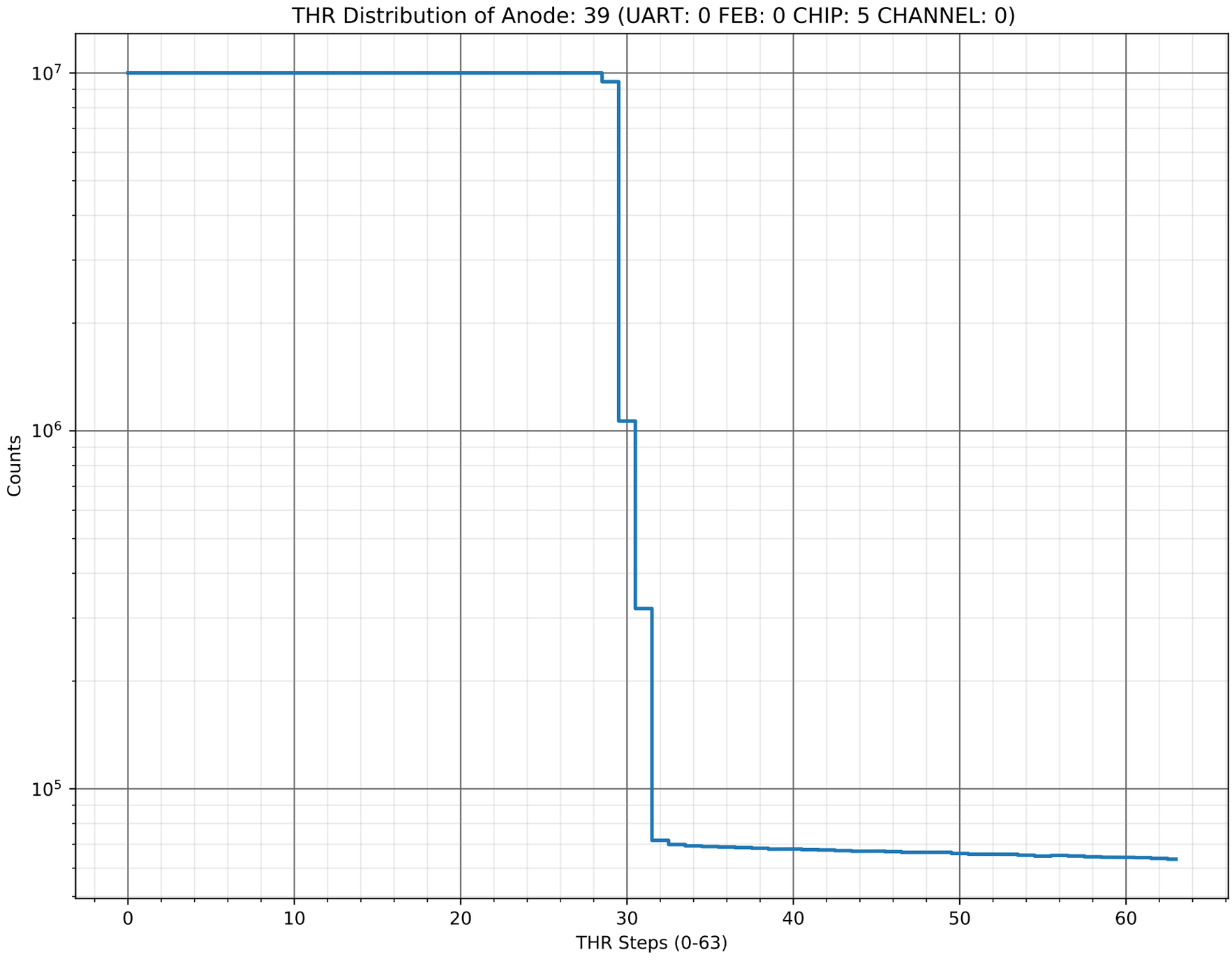

THR Distribution of Anode: 34 (UART: 0 FEB: 1 CHIP: 4 CHANNEL: 7)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

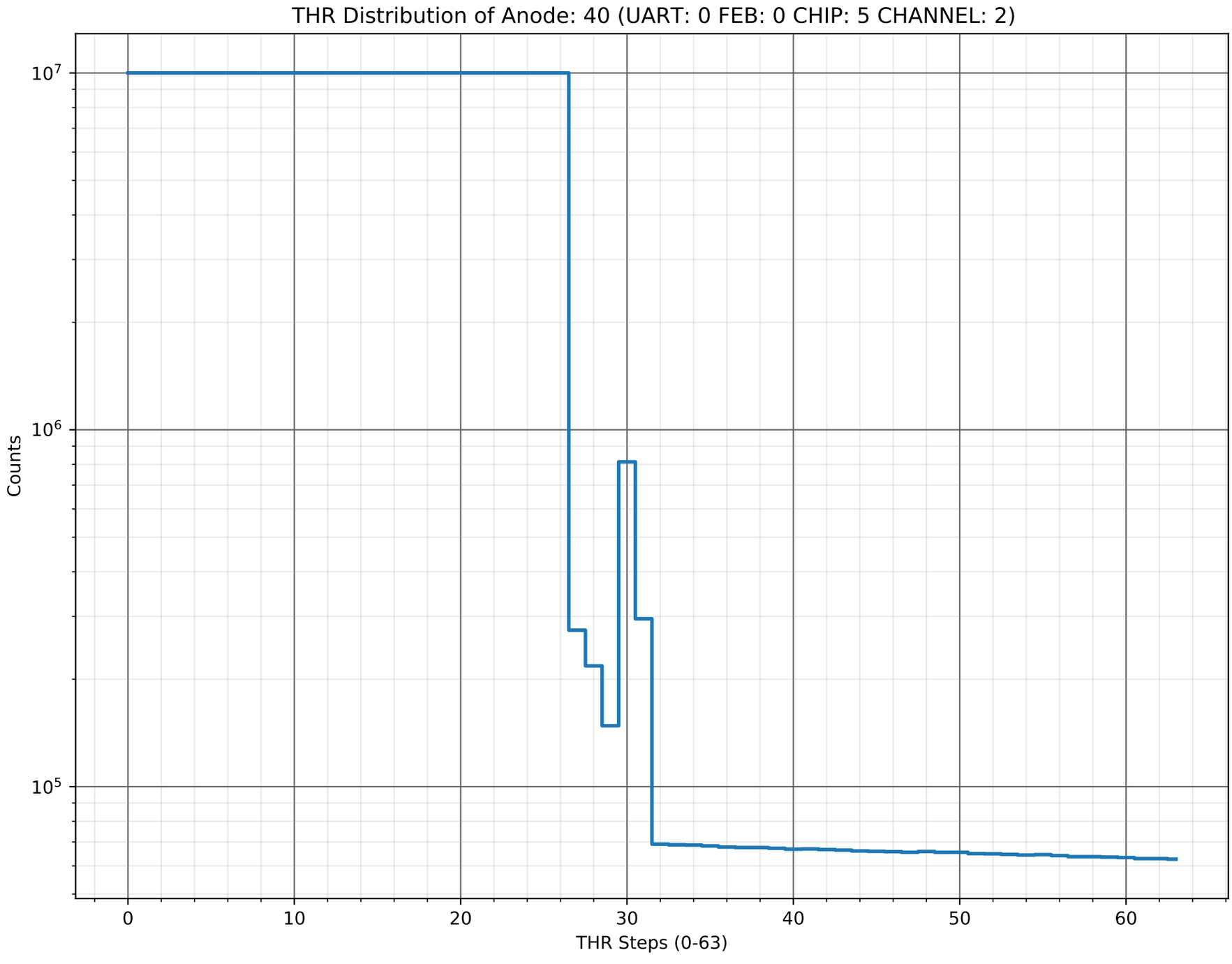

THR Distribution of Anode: 35 (UART: 0 FEB: 1 CHIP: 5 CHANNEL: 0)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

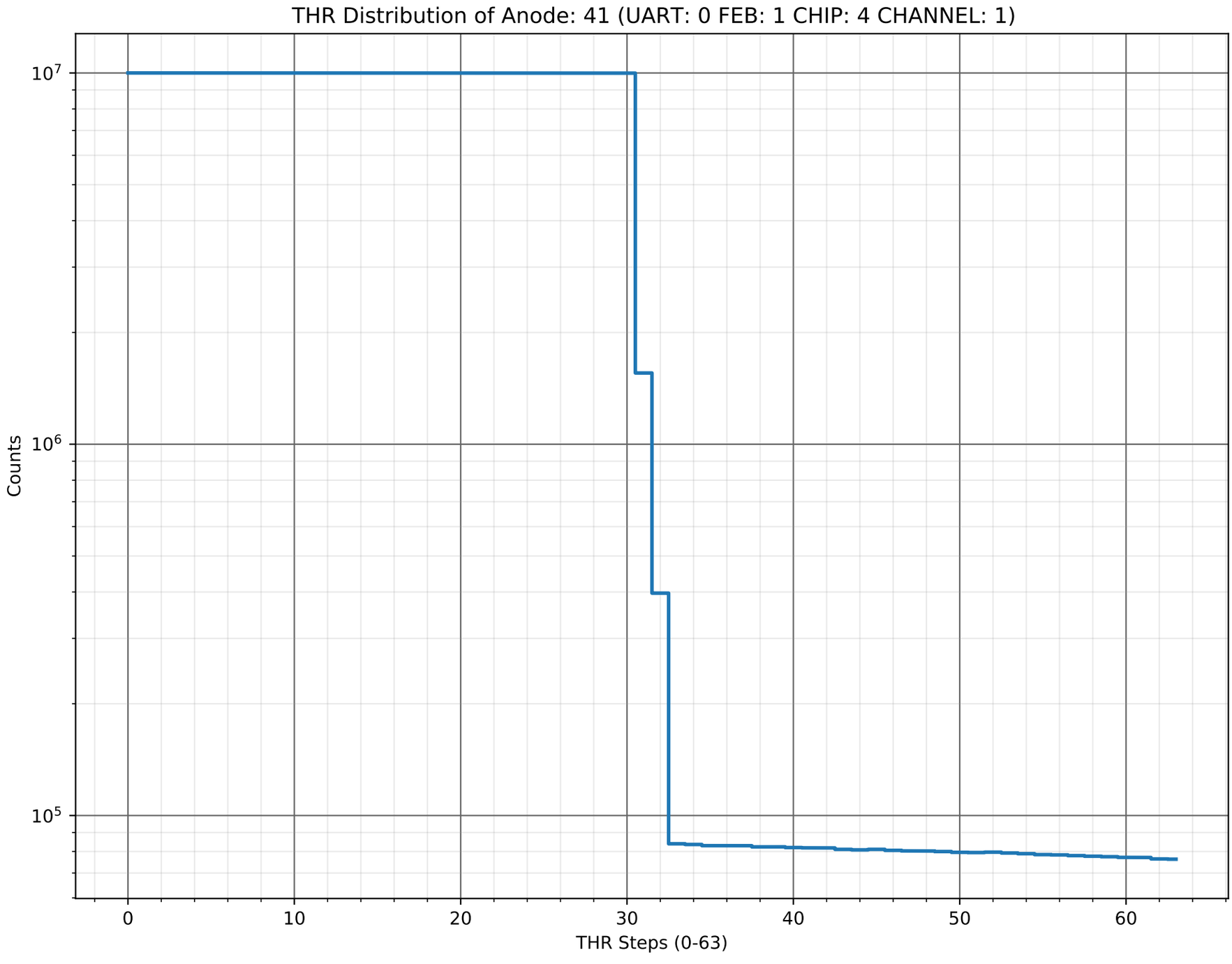

THR Distribution of Anode: 36 (UART: 0 FEB: 1 CHIP: 5 CHANNEL: 2)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

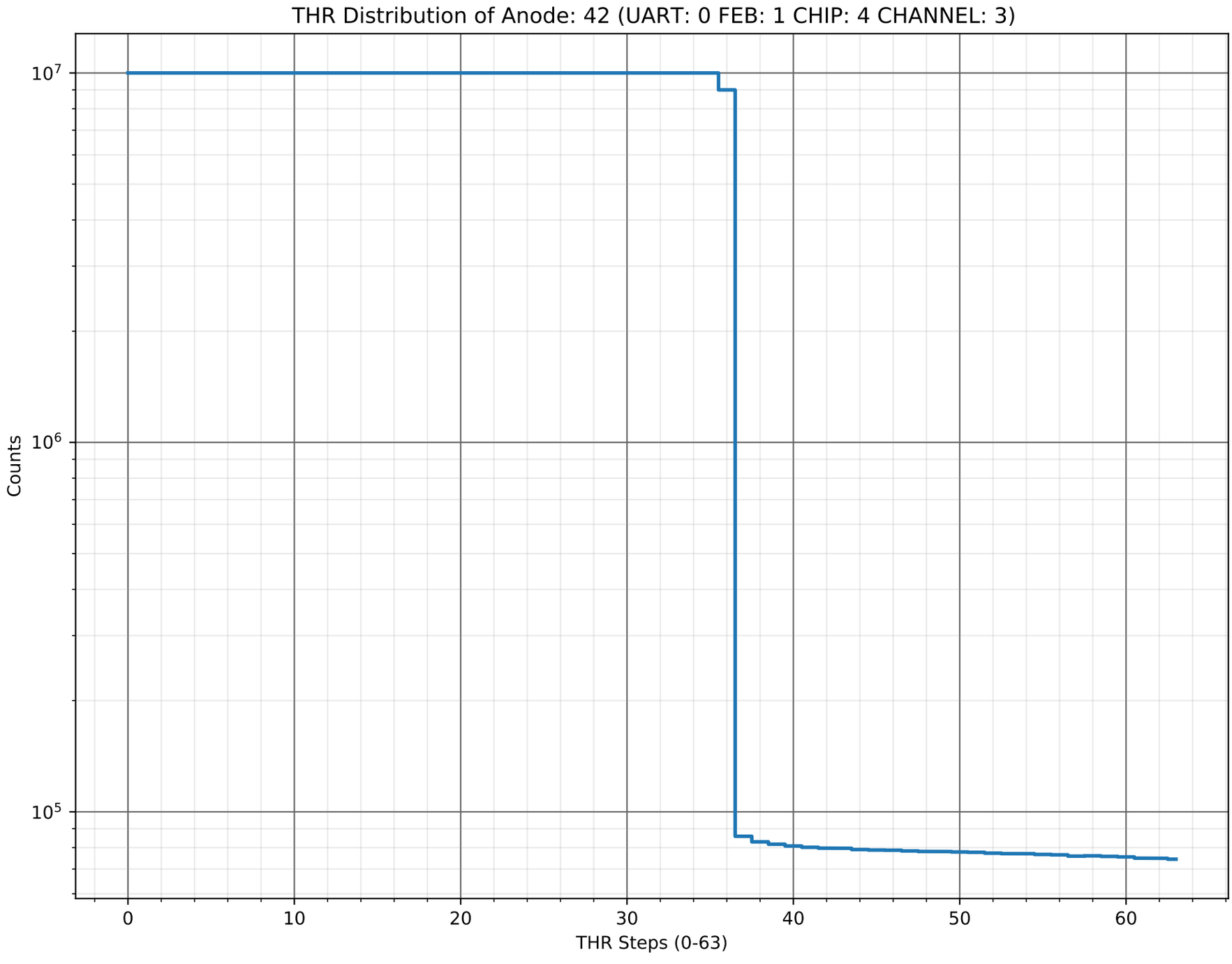

THR Distribution of Anode: 37 (UART: 0 FEB: 0 CHIP: 4 CHANNEL: 5)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

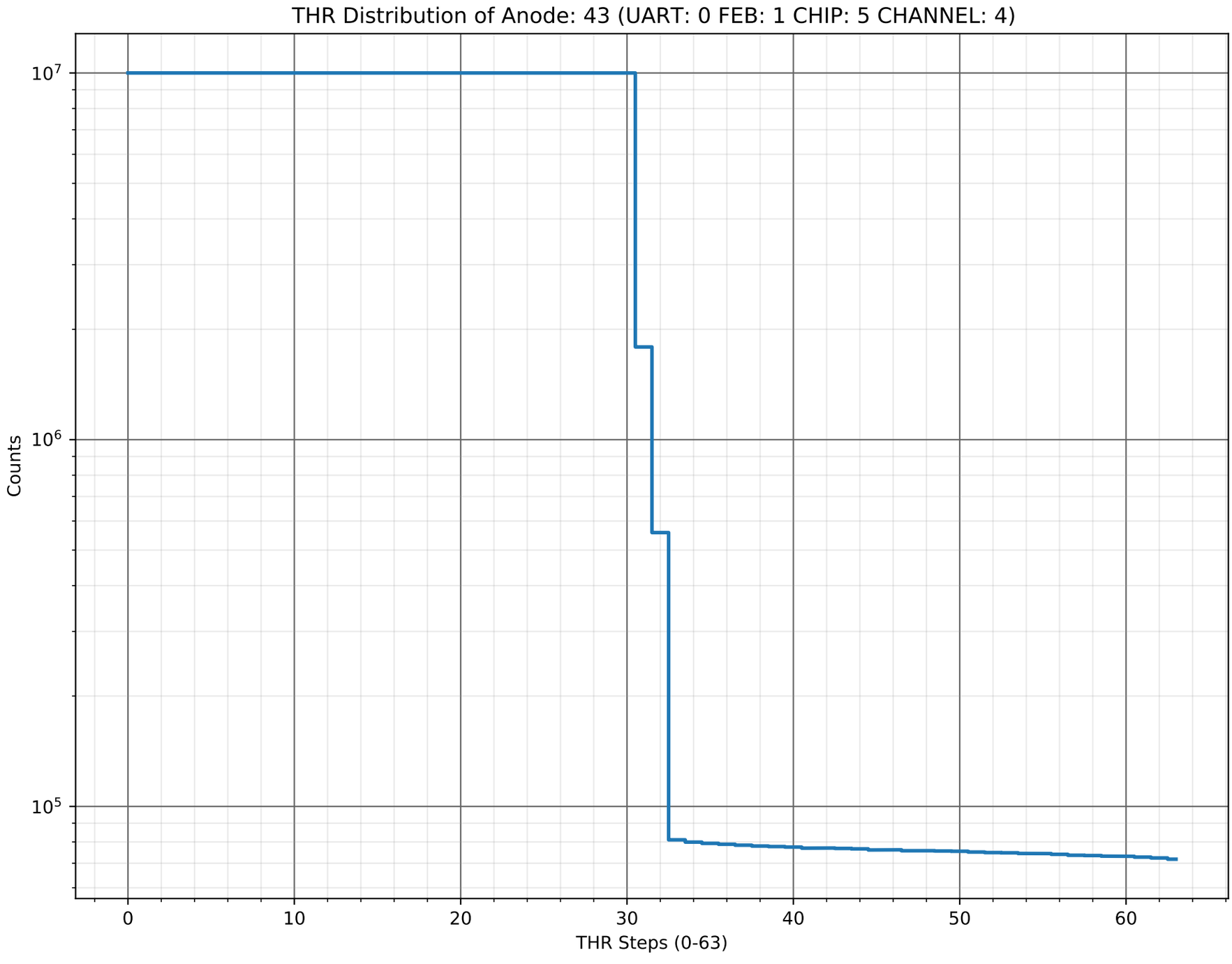

THR Distribution of Anode: 38 (UART: 0 FEB: 0 CHIP: 4 CHANNEL: 7)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

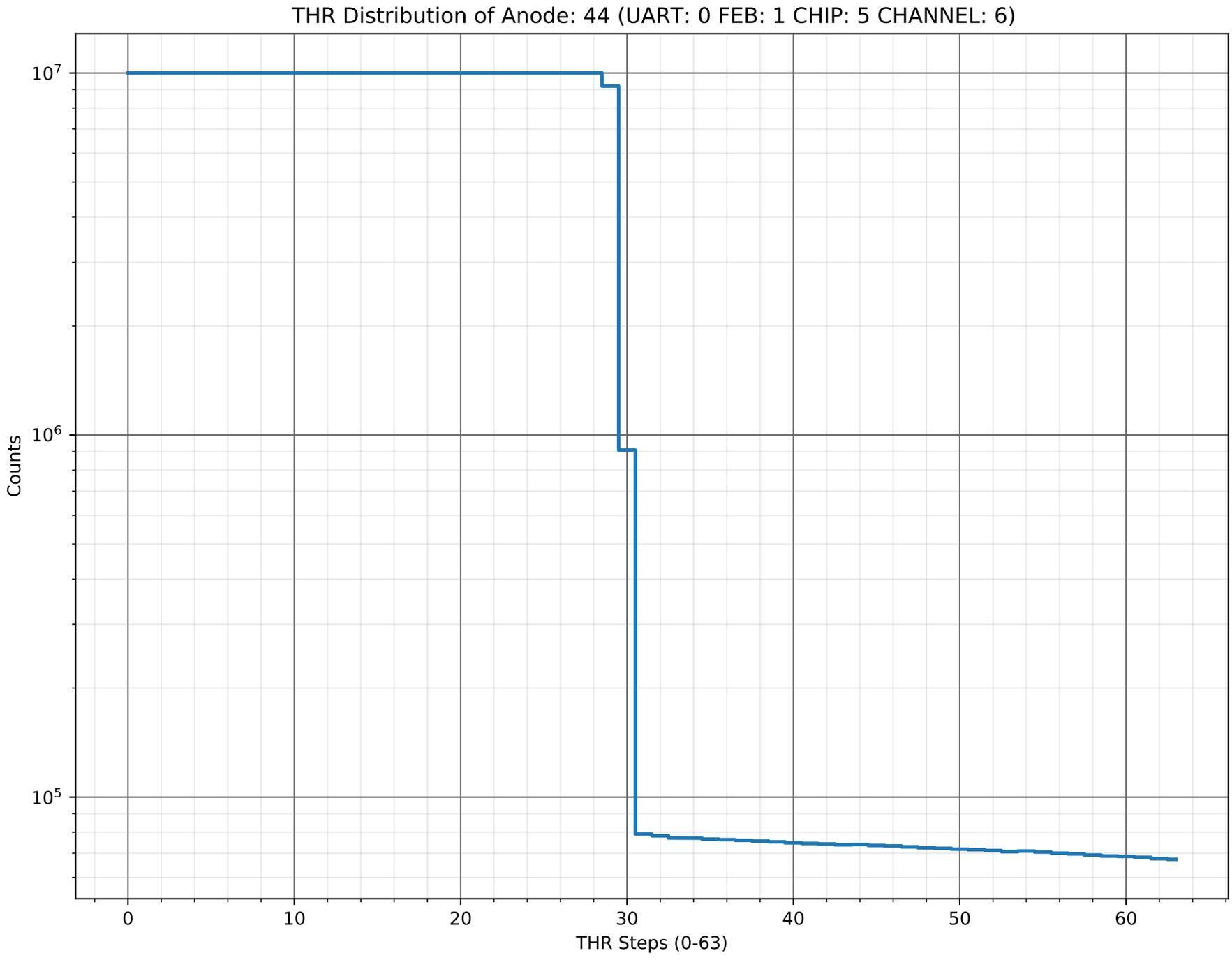

THR Distribution of Anode: 39 (UART: 0 FEB: 0 CHIP: 5 CHANNEL: 0)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

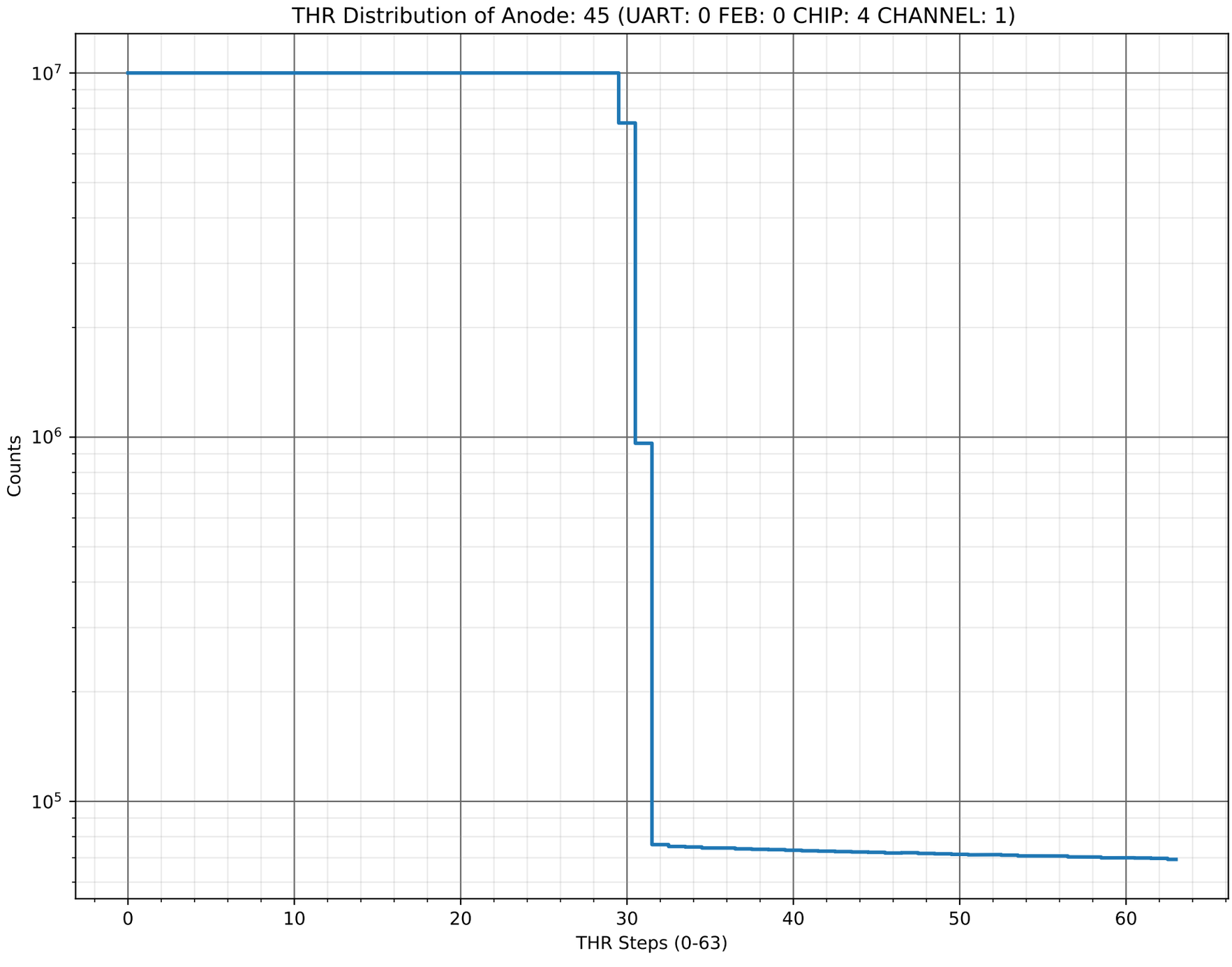

THR Distribution of Anode: 40 (UART: 0 FEB: 0 CHIP: 5 CHANNEL: 2)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

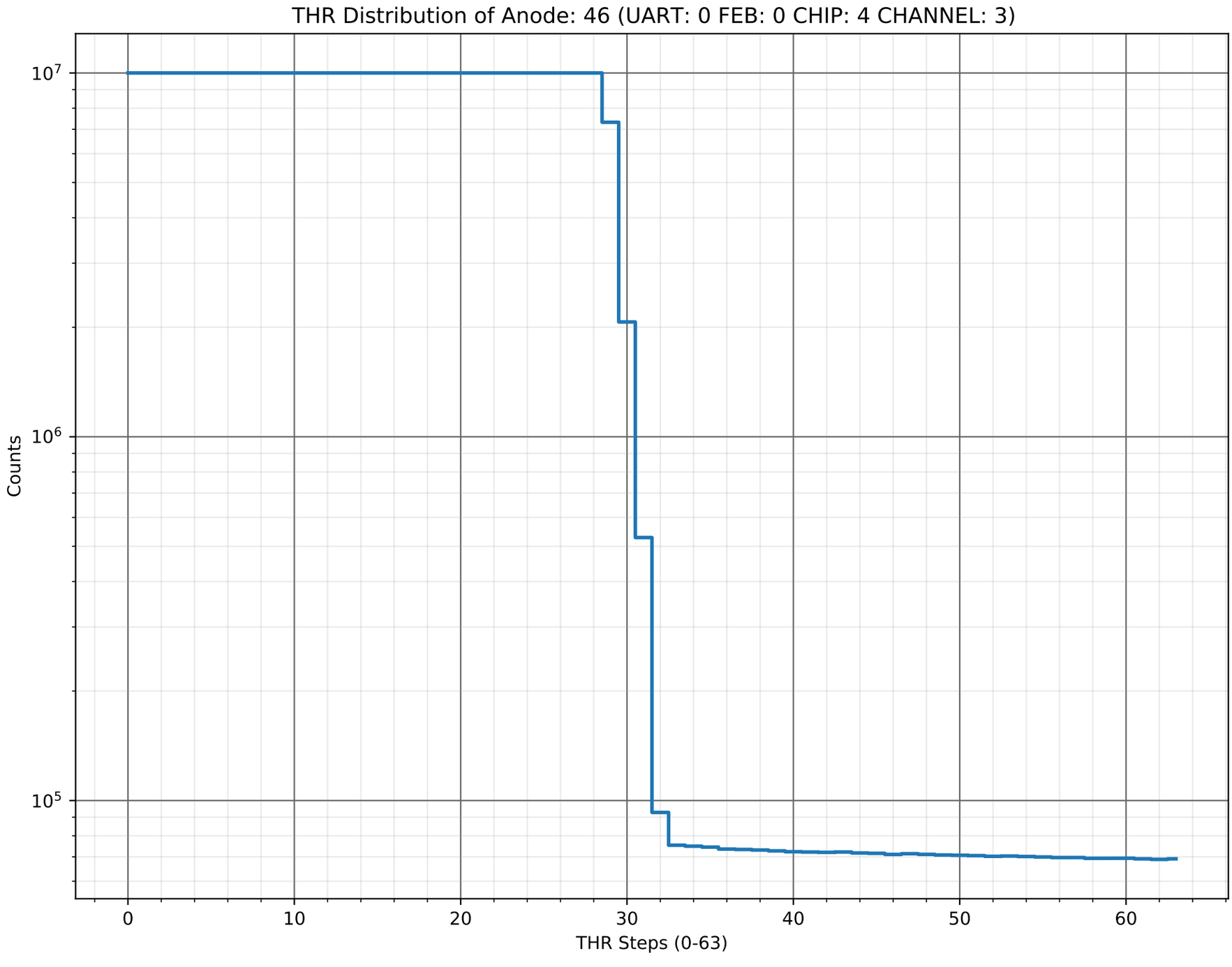

THR Distribution of Anode: 41 (UART: 0 FEB: 1 CHIP: 4 CHANNEL: 1)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

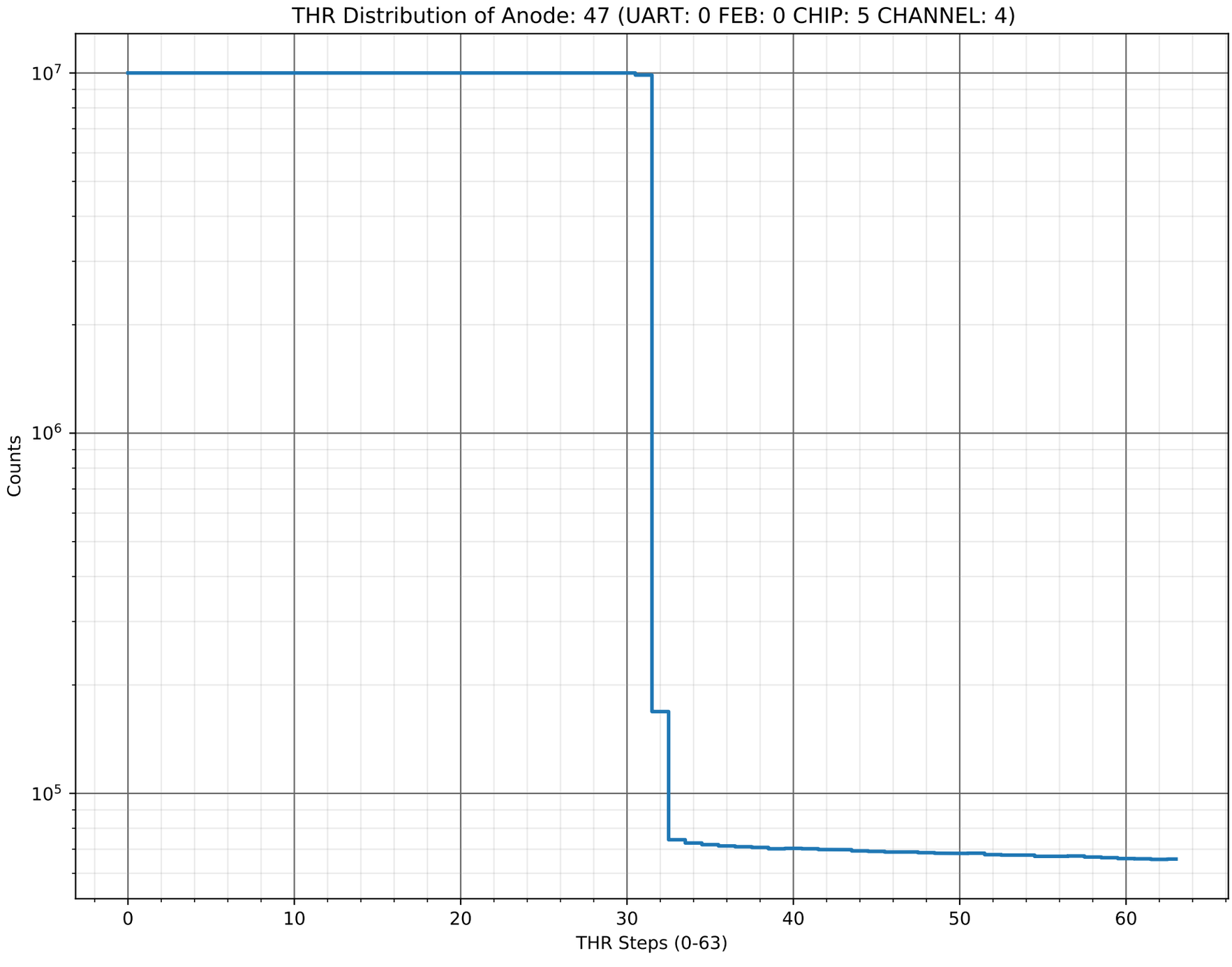

THR Distribution of Anode: 42 (UART: 0 FEB: 1 CHIP: 4 CHANNEL: 3)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

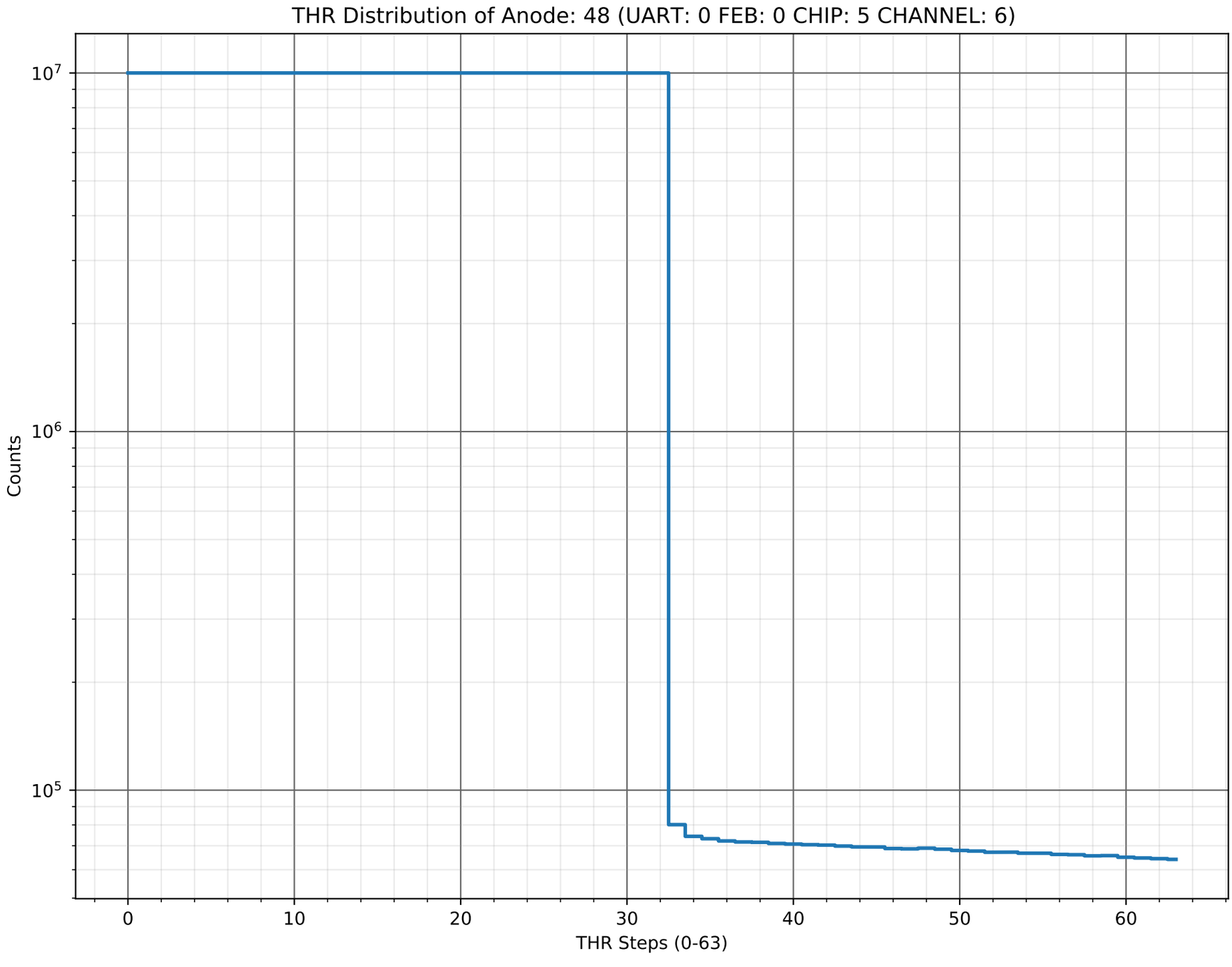

THR Distribution of Anode: 43 (UART: 0 FEB: 1 CHIP: 5 CHANNEL: 4)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

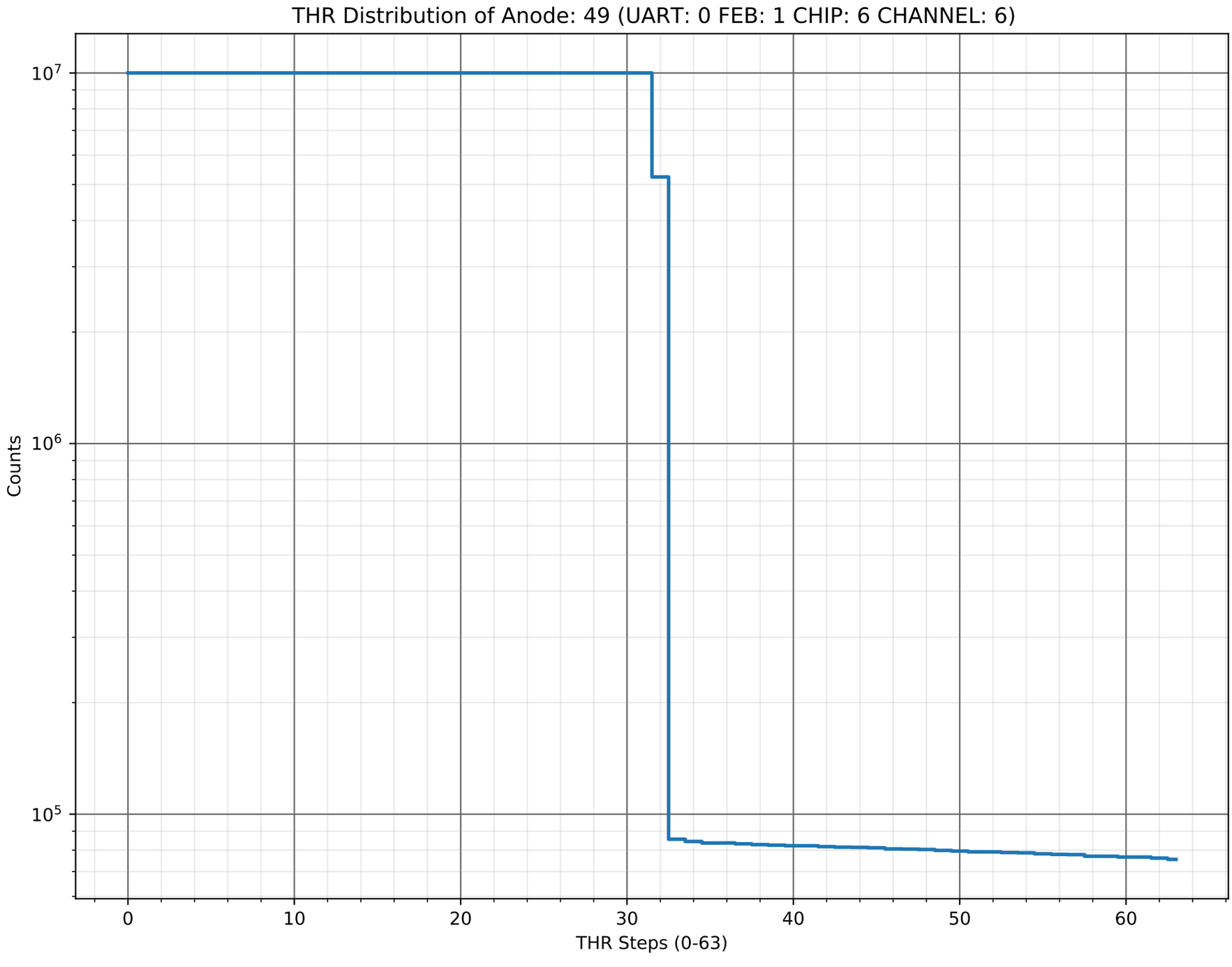

THR Distribution of Anode: 44 (UART: 0 FEB: 1 CHIP: 5 CHANNEL: 6)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

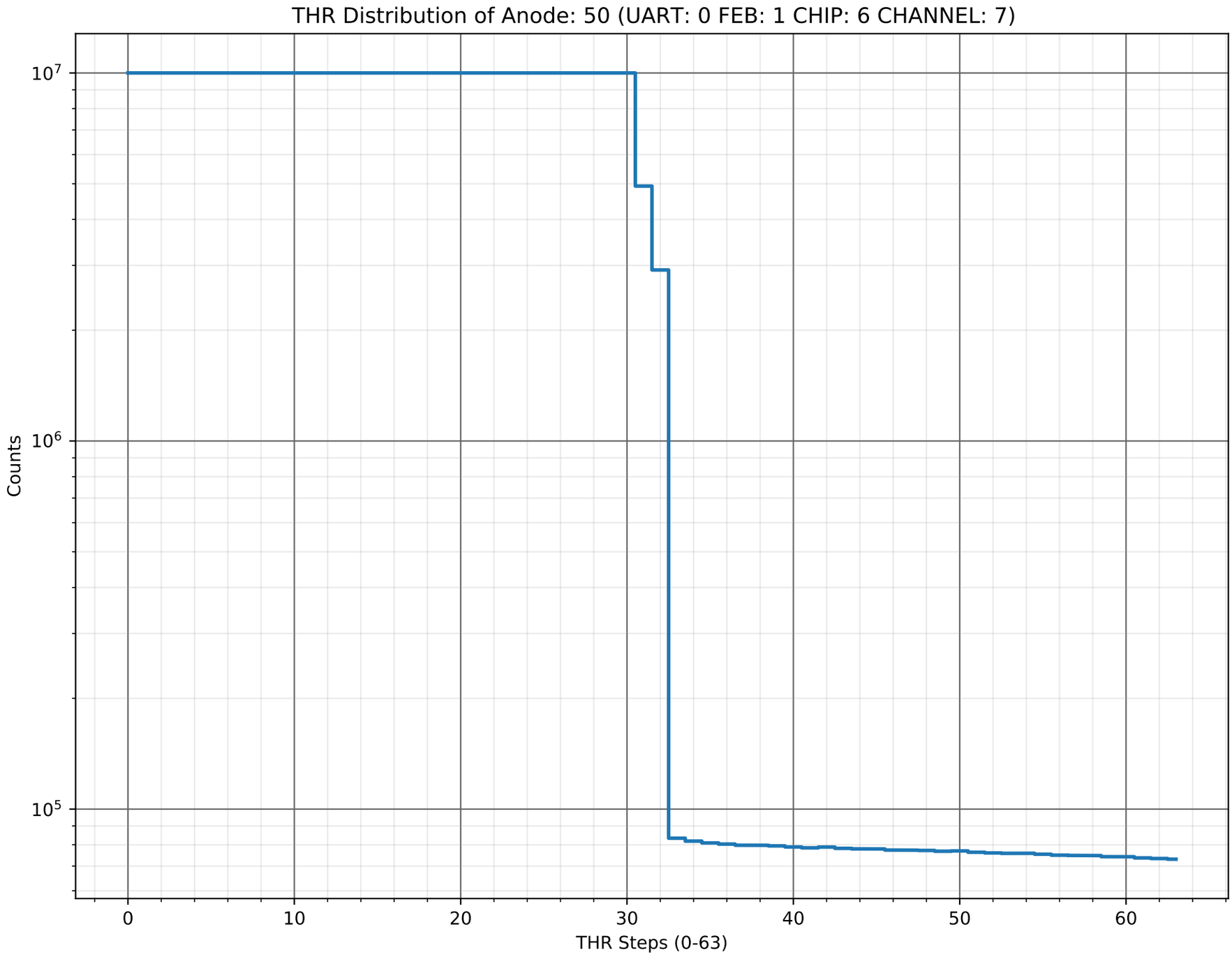

THR Distribution of Anode: 45 (UART: 0 FEB: 0 CHIP: 4 CHANNEL: 1)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

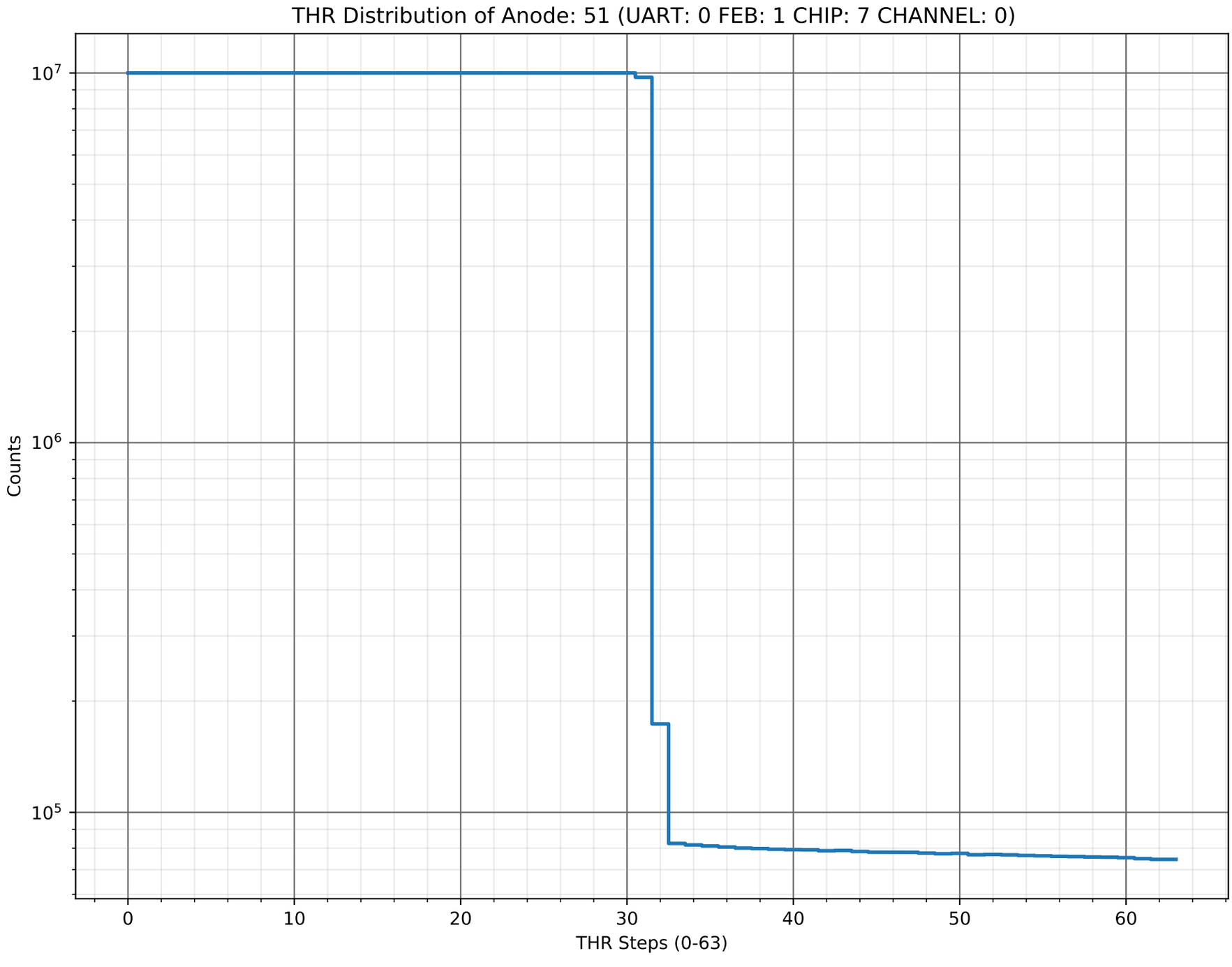

THR Distribution of Anode: 46 (UART: 0 FEB: 0 CHIP: 4 CHANNEL: 3)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

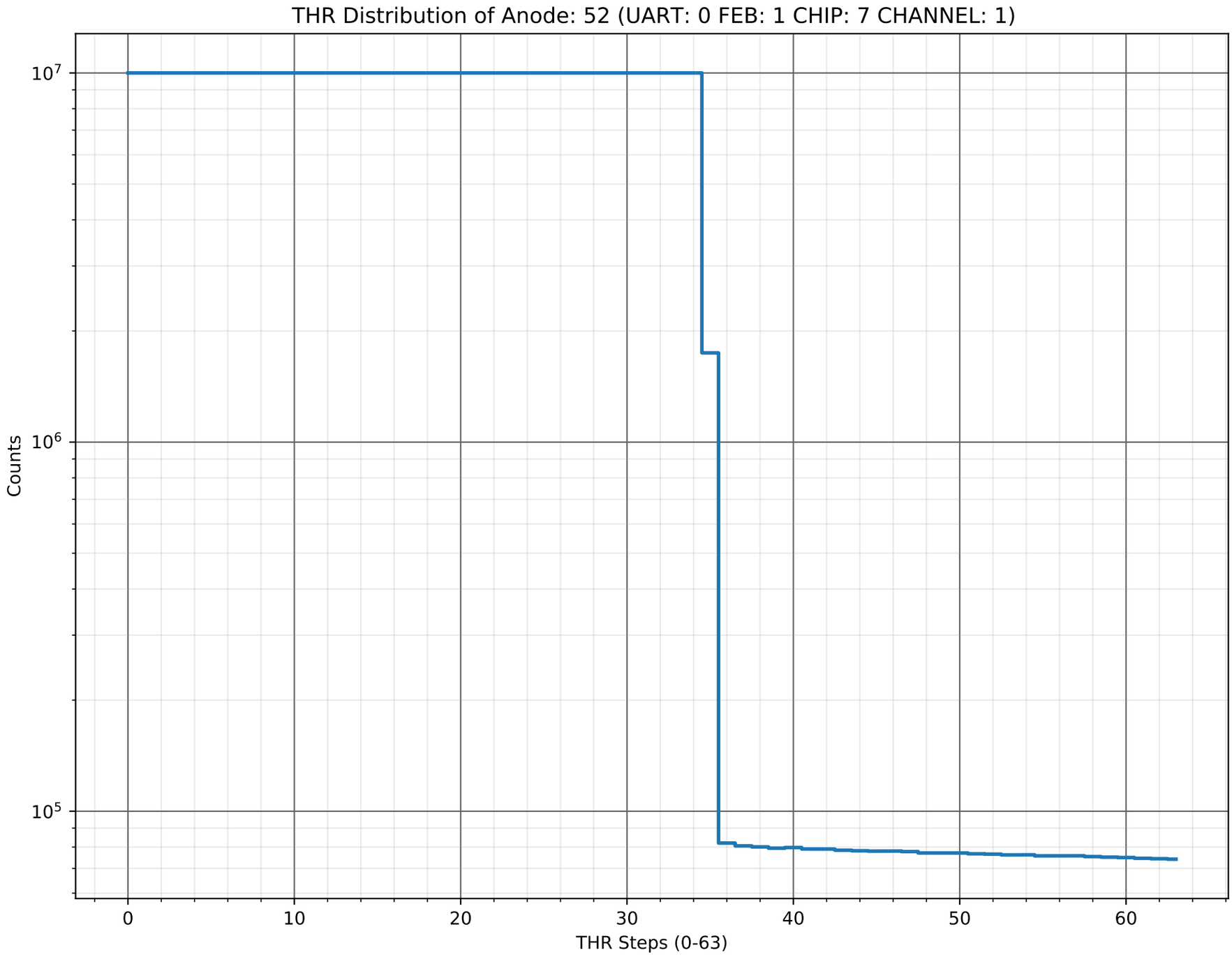

THR Distribution of Anode: 47 (UART: 0 FEB: 0 CHIP: 5 CHANNEL: 4)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

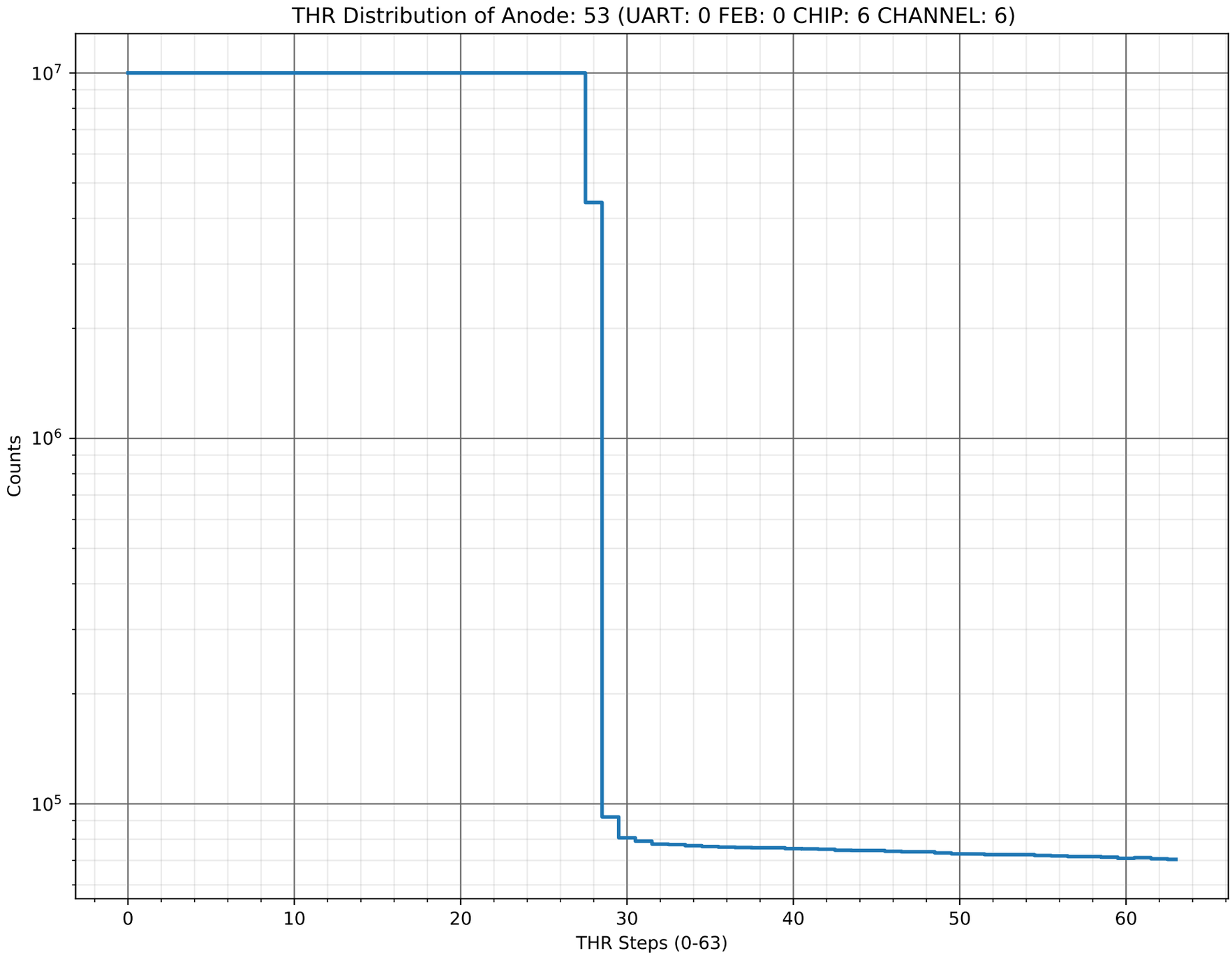

THR Distribution of Anode: 48 (UART: 0 FEB: 0 CHIP: 5 CHANNEL: 6)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

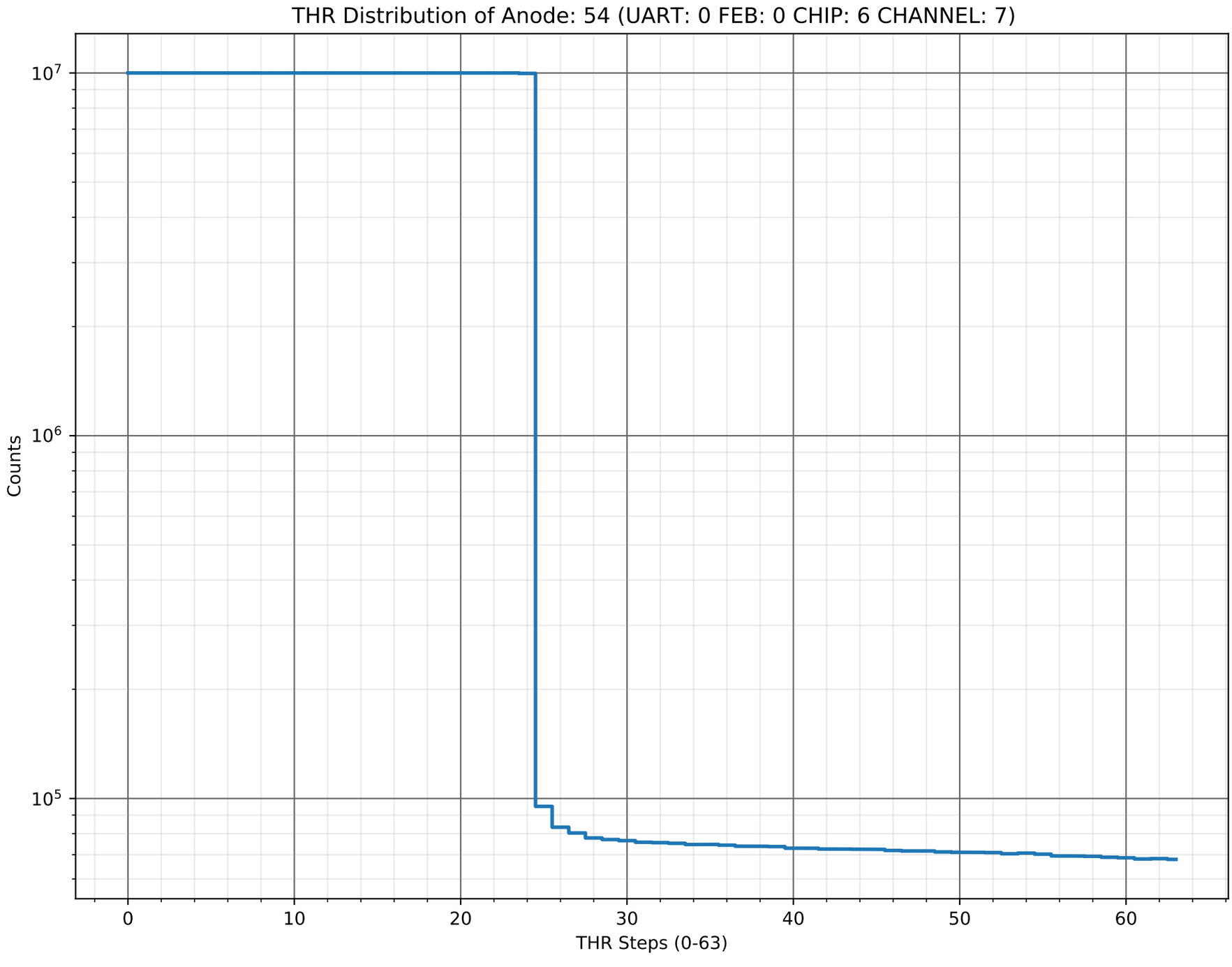

THR Distribution of Anode: 49 (UART: 0 FEB: 1 CHIP: 6 CHANNEL: 6)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

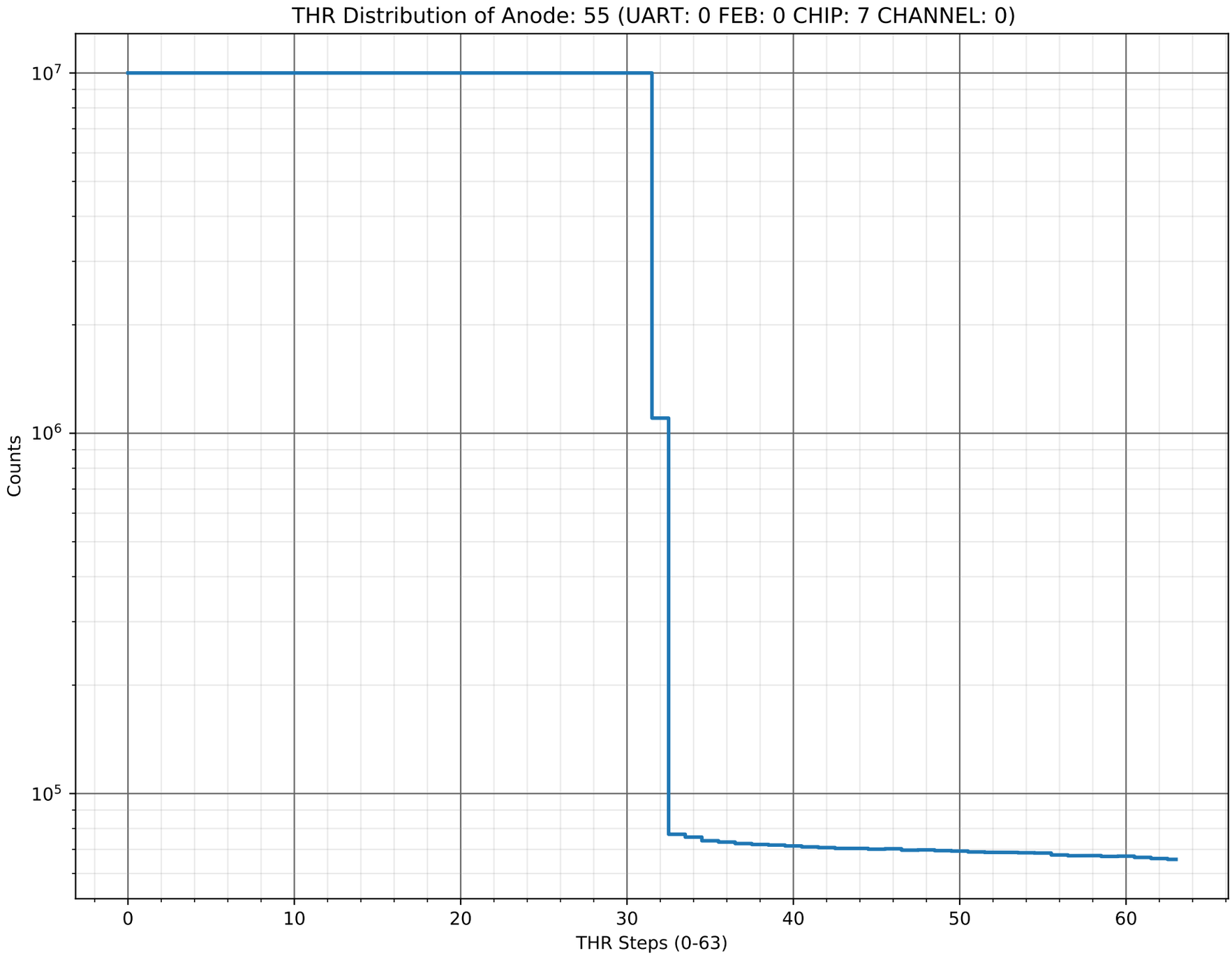

THR Distribution of Anode: 50 (UART: 0 FEB: 1 CHIP: 6 CHANNEL: 7)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

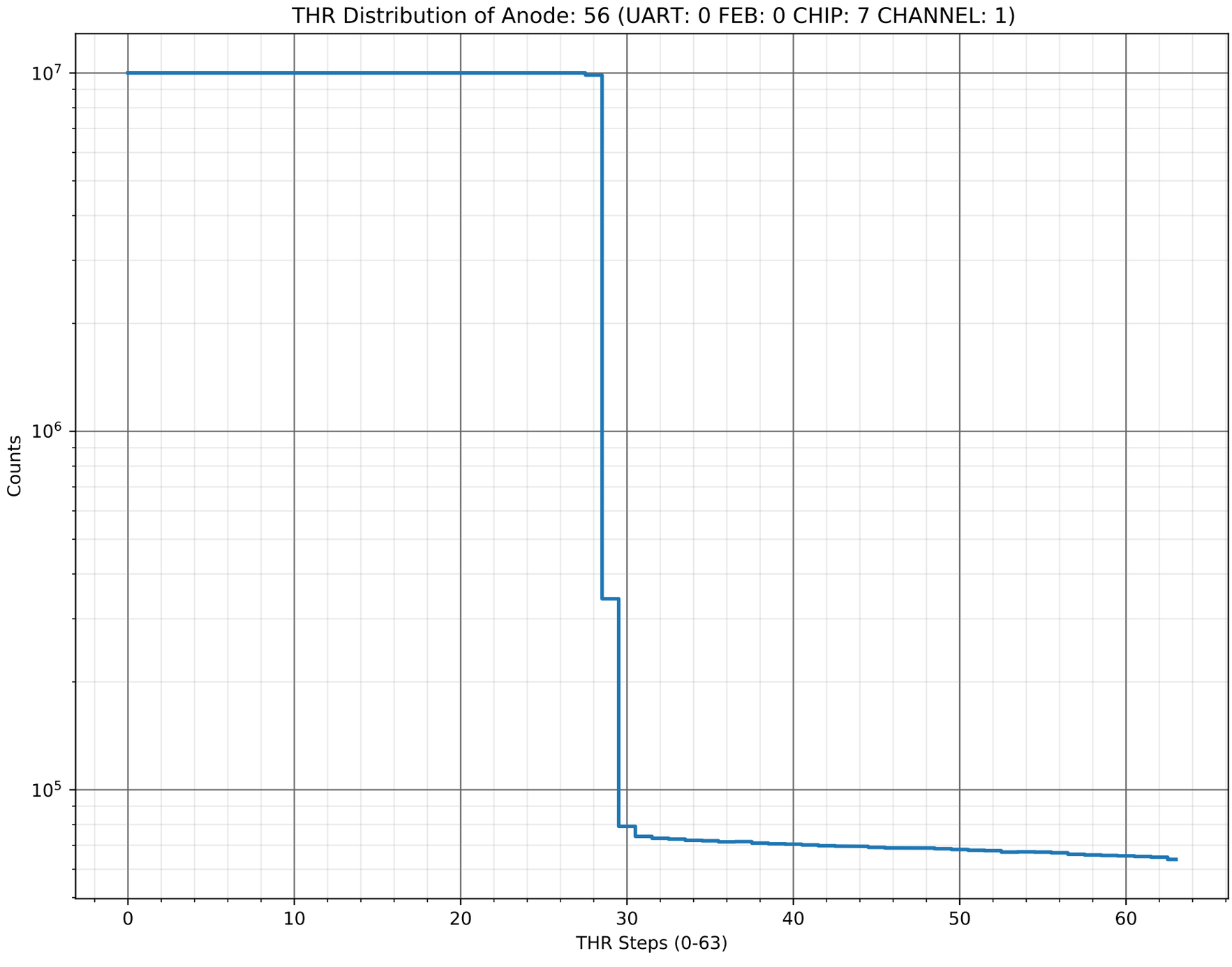

THR Distribution of Anode: 51 (UART: 0 FEB: 1 CHIP: 7 CHANNEL: 0)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

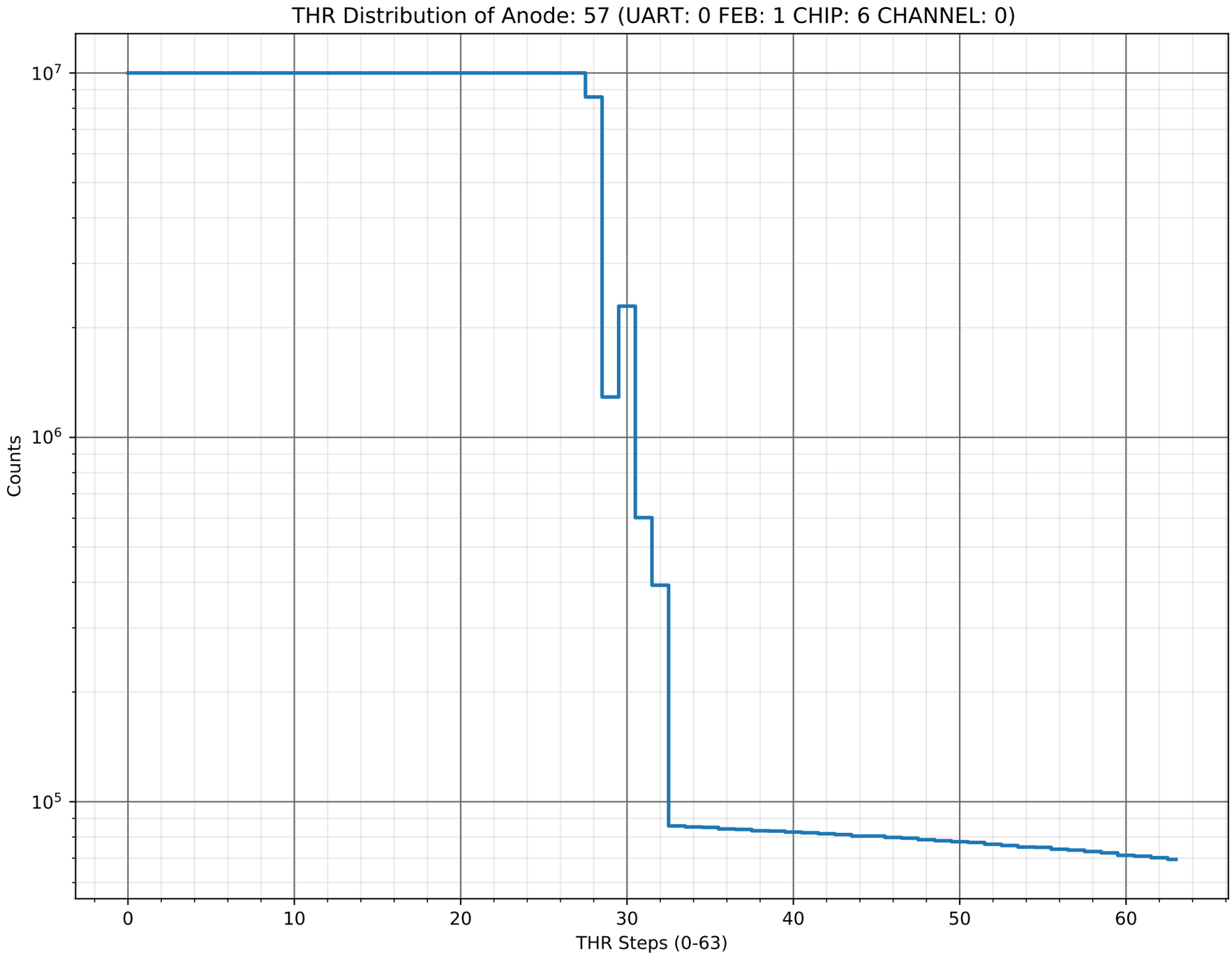

THR Distribution of Anode: 52 (UART: 0 FEB: 1 CHIP: 7 CHANNEL: 1)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

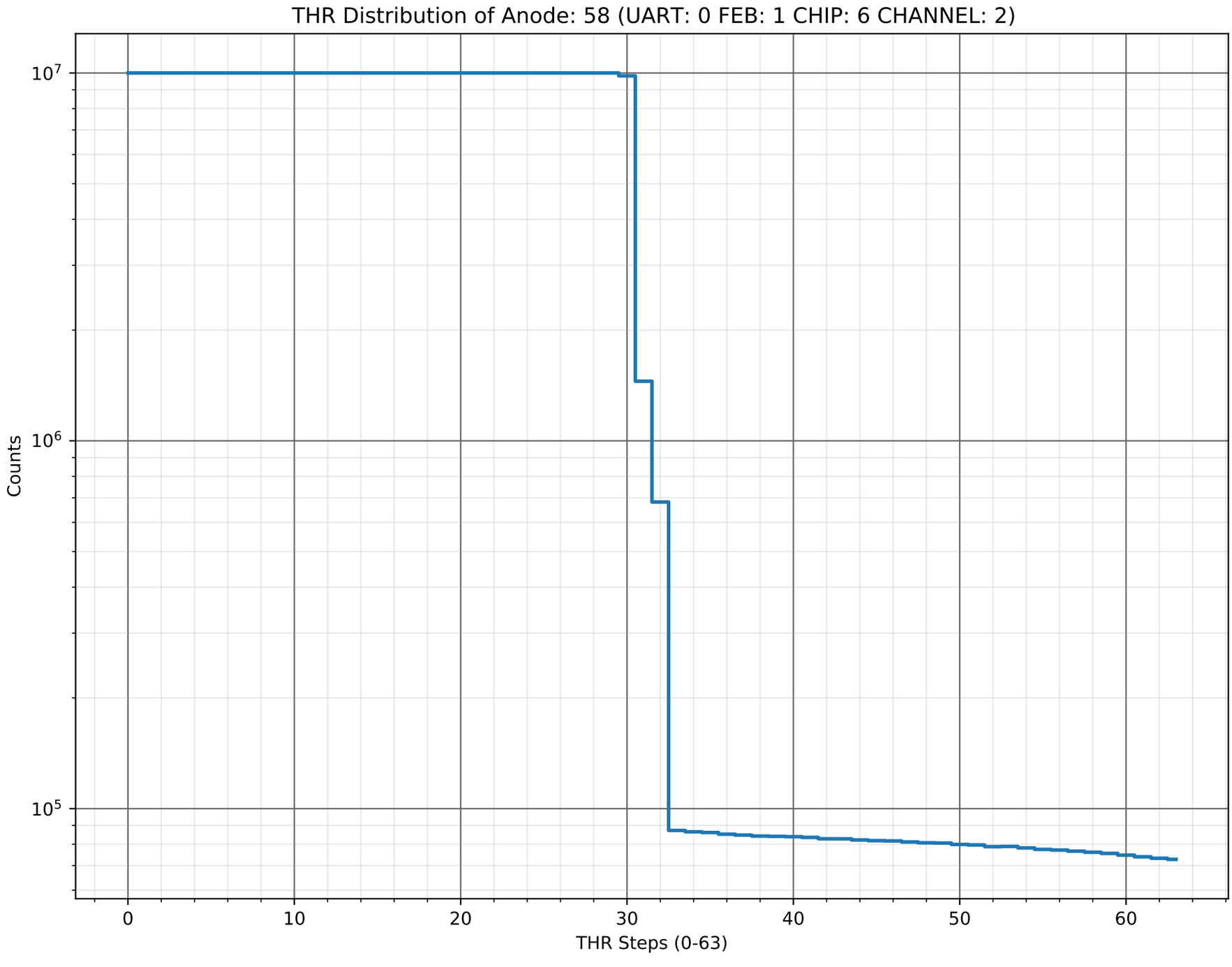

THR Distribution of Anode: 53 (UART: 0 FEB: 0 CHIP: 6 CHANNEL: 6)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

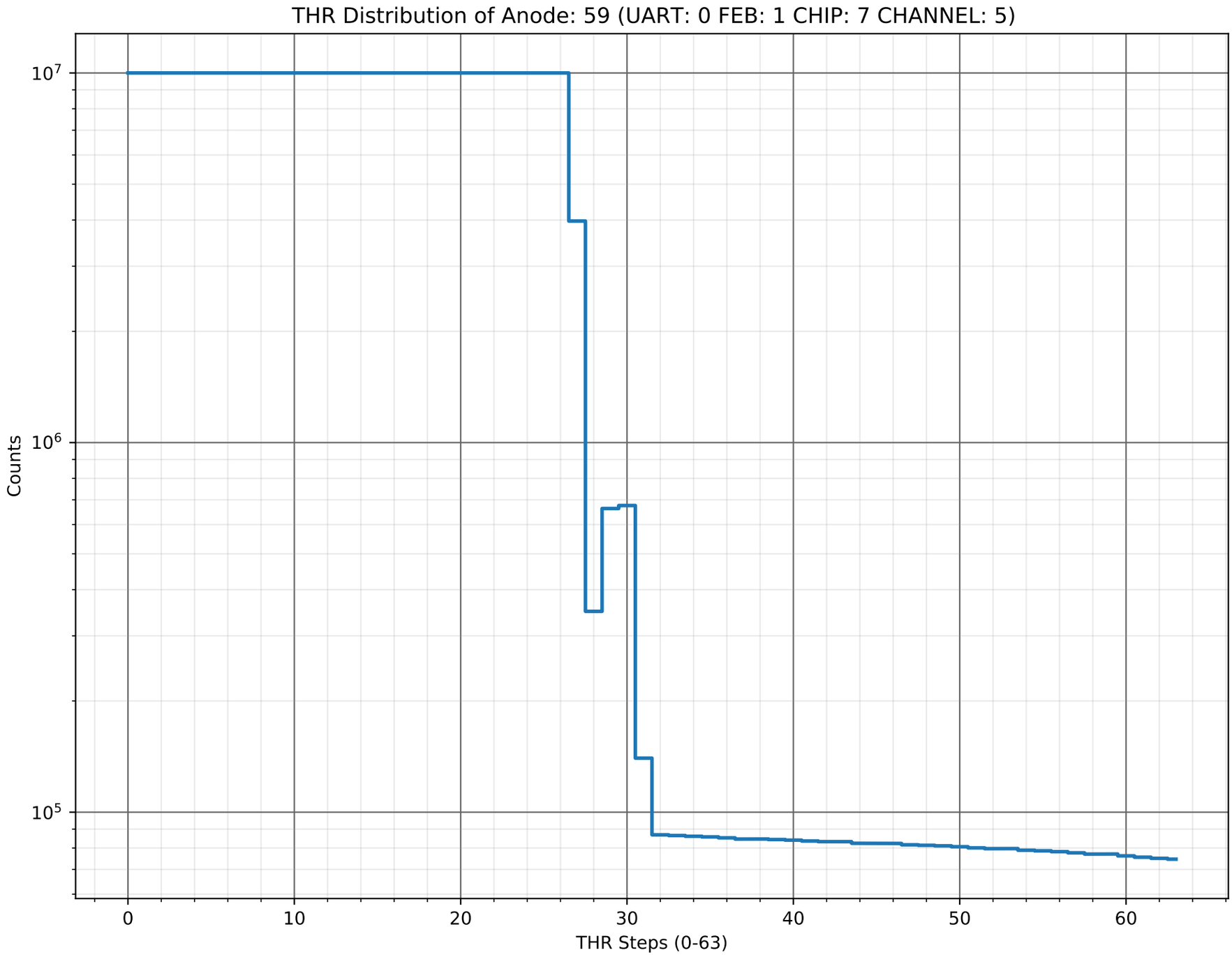

THR Distribution of Anode: 54 (UART: 0 FEB: 0 CHIP: 6 CHANNEL: 7)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

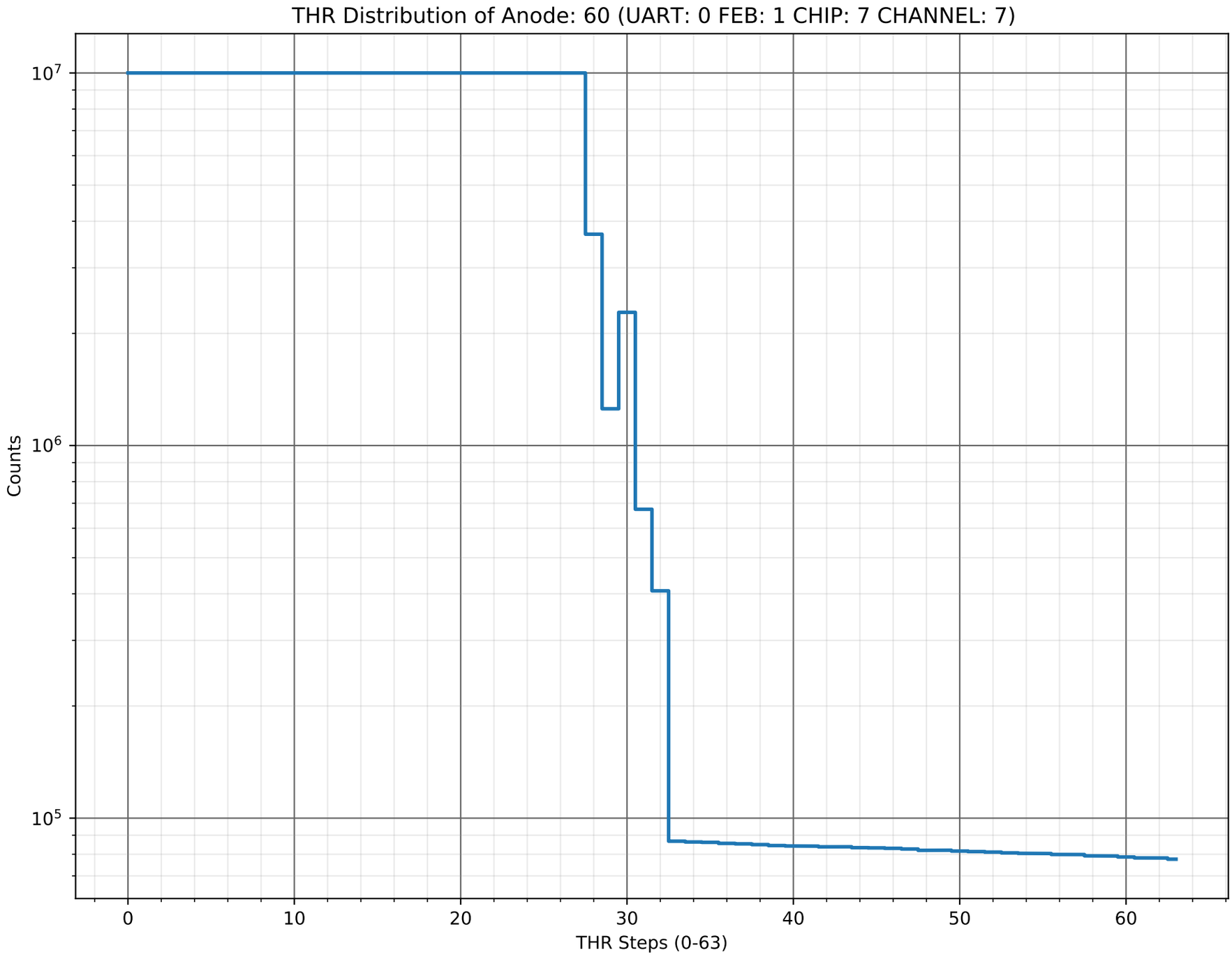

THR Distribution of Anode: 55 (UART: 0 FEB: 0 CHIP: 7 CHANNEL: 0)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

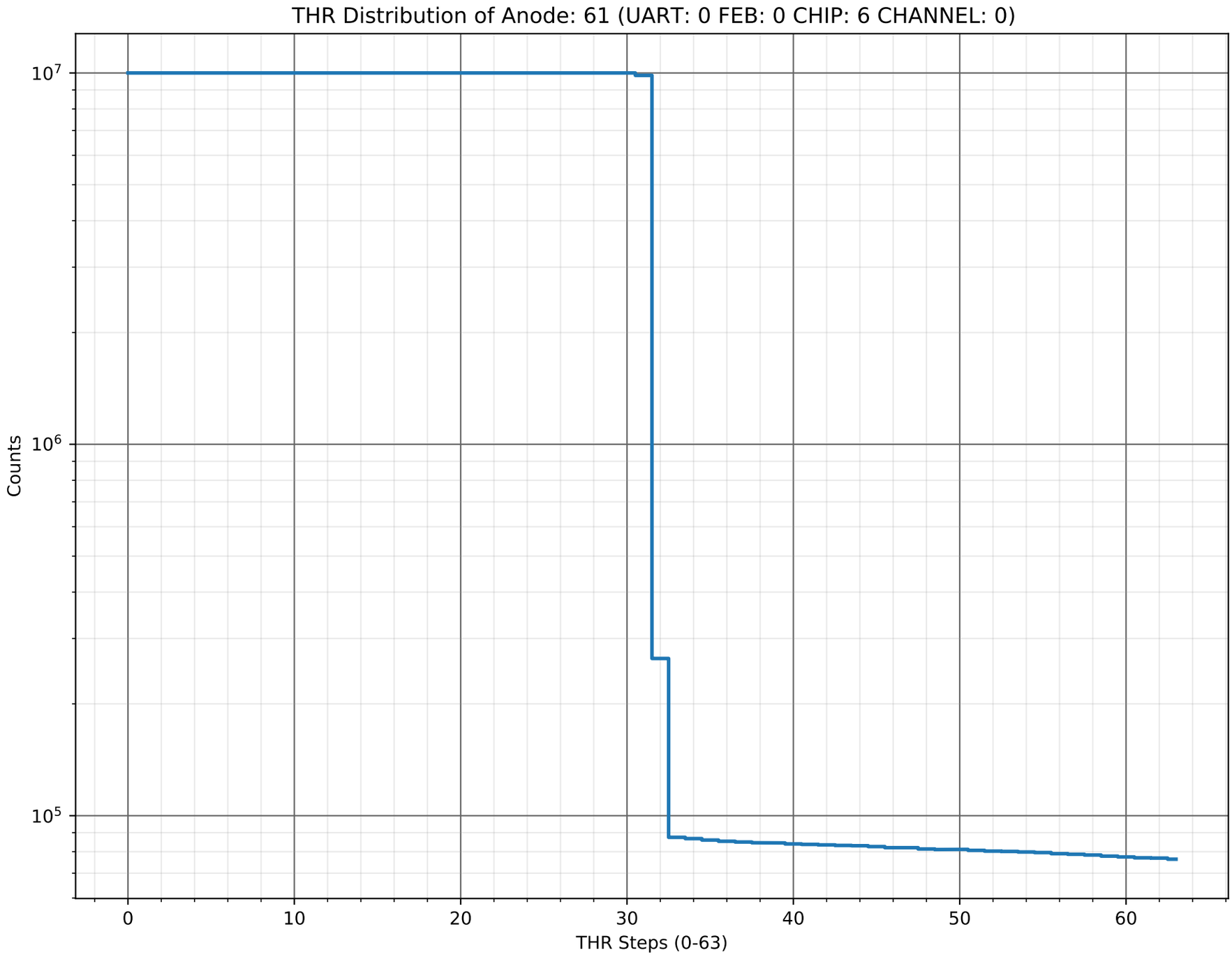

THR Distribution of Anode: 56 (UART: 0 FEB: 0 CHIP: 7 CHANNEL: 1)
10^7
10^6
10^5
Counts
0
10
20
30
40
50
60
THR Steps (0-63)

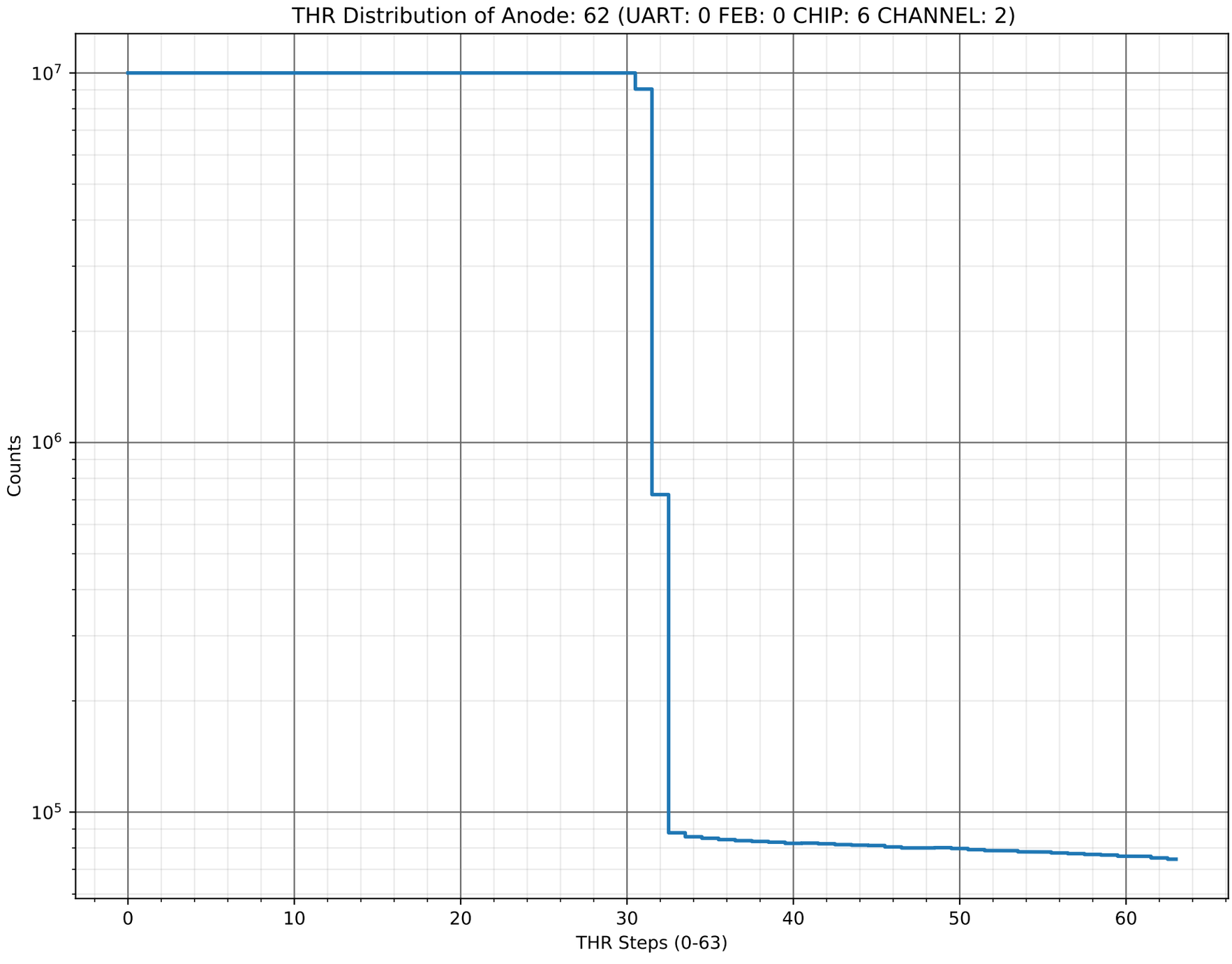

THR Distribution of Anode: 57 (UART: 0 FEB: 1 CHIP: 6 CHANNEL: 0)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

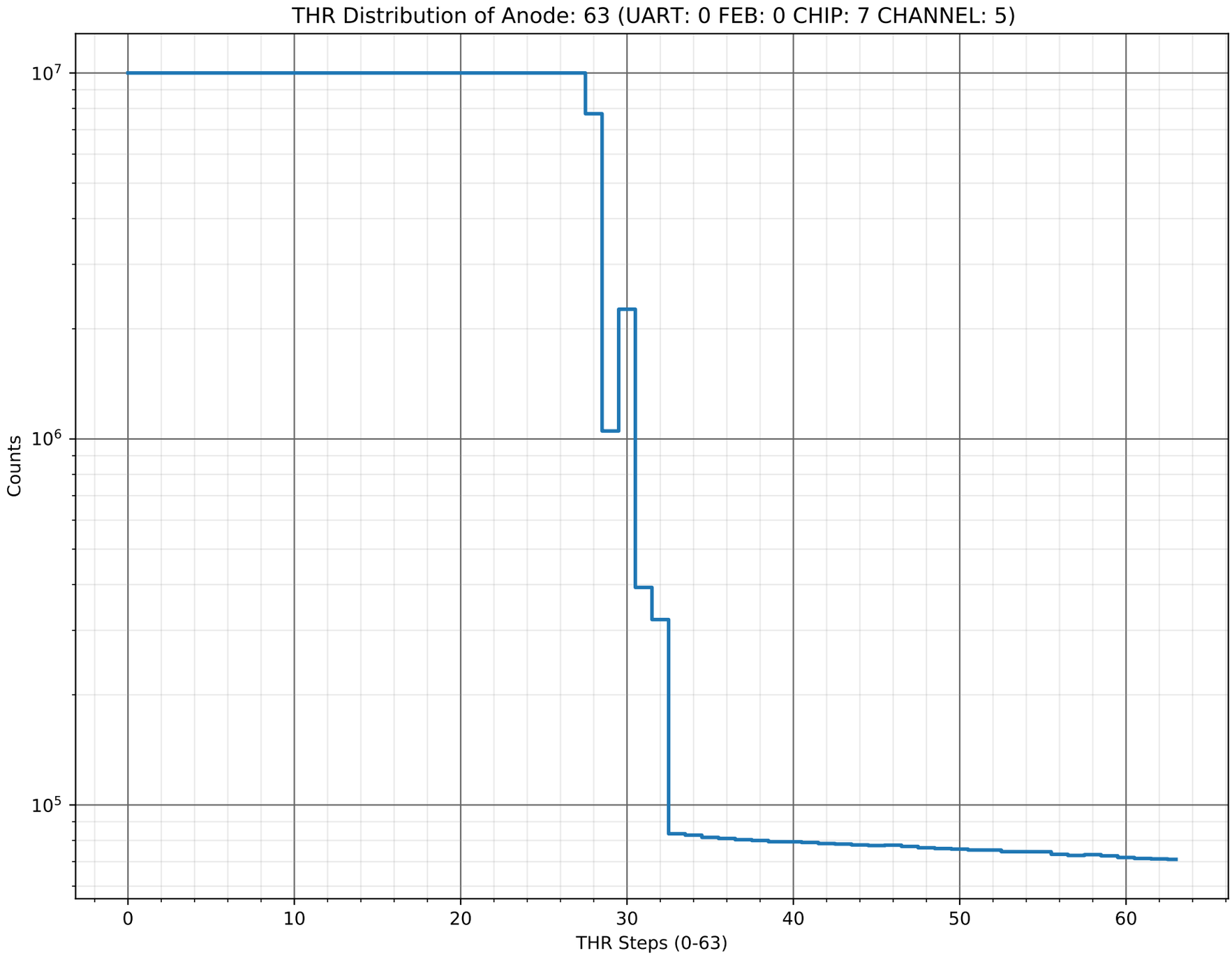

THR Distribution of Anode: 58 (UART: 0 FEB: 1 CHIP: 6 CHANNEL: 2)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)

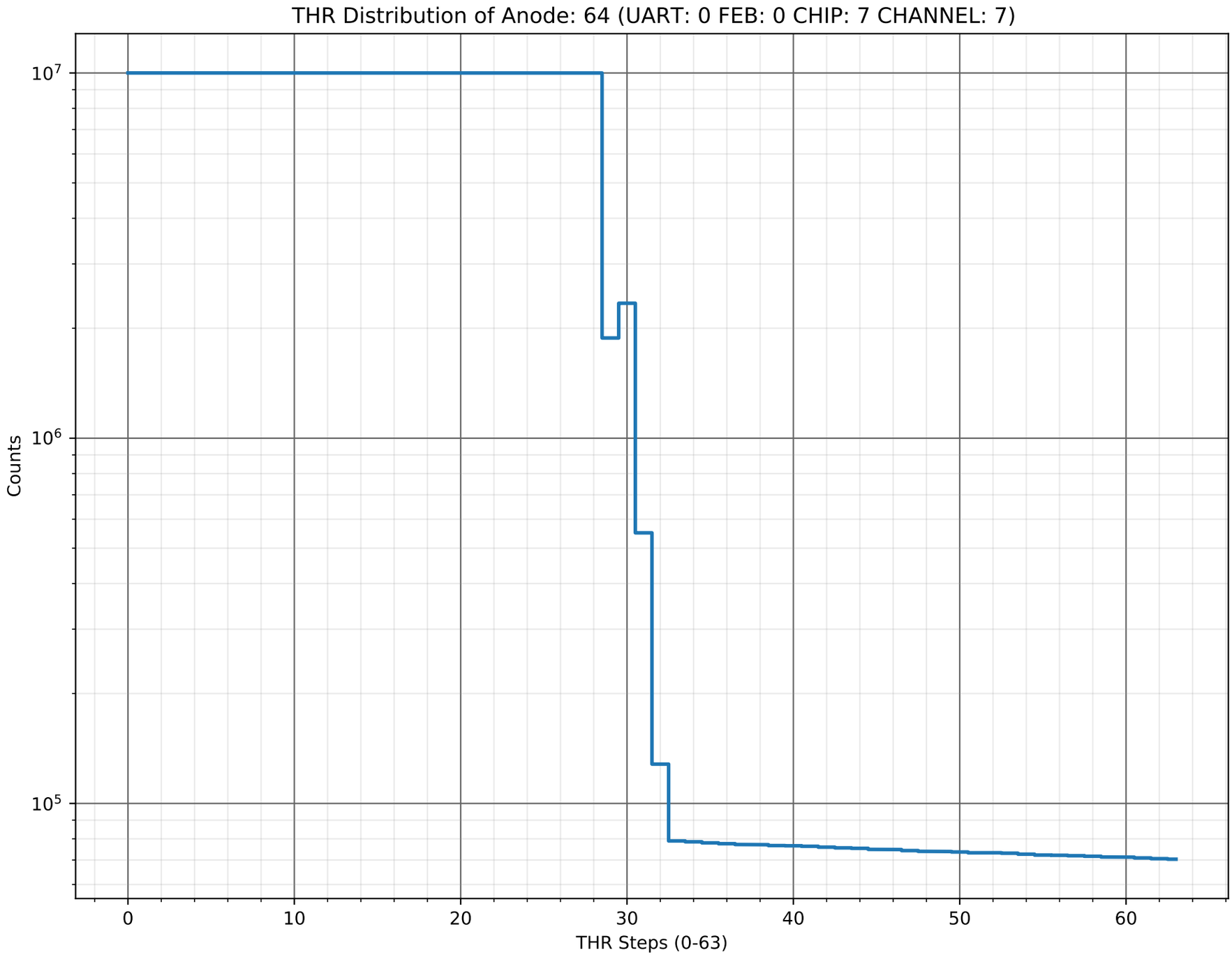

THR Distribution of Anode: 59 (UART: 0 FEB: 1 CHIP: 7 CHANNEL: 5)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)


THR Distribution of Anode: 60 (UART: 0 FEB: 1 CHIP: 7 CHANNEL: 7)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)


THR Distribution of Anode: 61 (UART: 0 FEB: 0 CHIP: 6 CHANNEL: 0)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)


THR Distribution of Anode: 62 (UART: 0 FEB: 0 CHIP: 6 CHANNEL: 2)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)


THR Distribution of Anode: 63 (UART: 0 FEB: 0 CHIP: 7 CHANNEL: 5)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)


THR Distribution of Anode: 64 (UART: 0 FEB: 0 CHIP: 7 CHANNEL: 7)
Counts
10^7
10^6
10^5
0
10
20
30
40
50
60
THR Steps (0-63)