1. Scope

The present specifications shall apply to an RL4Z.

2. Outline

Туре	Silicon Diode
Structure	Resin Molded
Applications	High Frequency Rectification

3. Flammability

UL94V-0(Equivalent)

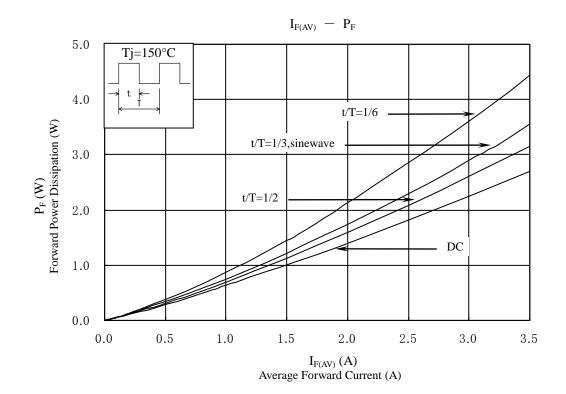
4. Absolute maximum ratings

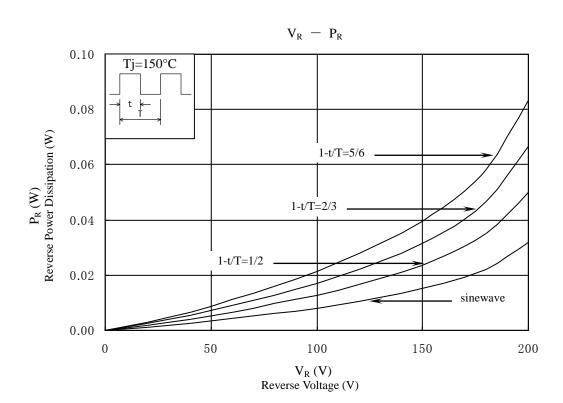
No.	Item	Symbol	Unit	Rating	Conditions
1	Transient Peak Reverse Voltage	V _{RSM}	V	200	
2	Peak Reverse Voltage	V_{RM}	V	200	
3	Average Forward Current	I _{F(AV)}	A	3.5	Refer to Derating of 7
4	Peak Surge Forward Current	I_{FSM}	A	80	10msec. Half sinewave, one shot
5	I ² t Limiting Value	I^2t	A^2s	32	1msec≤t≤10msec
6	Junction Temperature	T_{j}	°C	-40~+150	
7	Storage Temperature	T_{stg}	°C	-40~+150	

5. Electrical characteristics

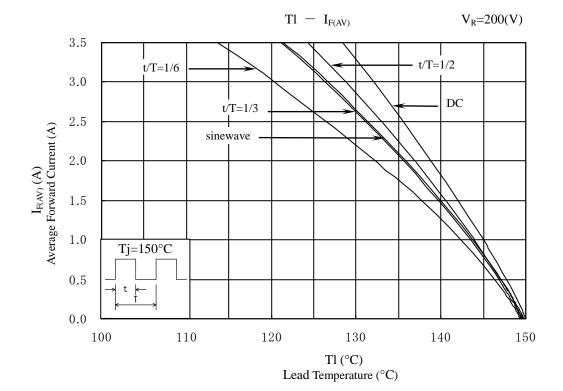
No ·	Item	Symbol	Unit	Value	Conditions
1	Forward Voltage Drop	V_{F}	V	0.95 max.	I _F =3.5A
2	Reverse Leakage Current	I_R	uA	150 max.	$V_R = V_{RM}$
3	Reverse Leakage Current Under High Temperature	$H \cdot I_R$	uA	500 max.	$V_R=V_{RM}, T_j=150^{\circ}C$
4	Payorsa Pagayami Tima	trr-1	ns	50 max.	I _F =I _{RP} =500mA 90% Recovery point, T _j =25°C
	Reverse Recovery Time	trr-2	ns	35 max.	I _F =500mA, I _{RP} =1A 75% Recovery point, T _j =25°C
5	Thermal Resistance	$R_{\text{th(j-l)}}$	°C/W	8.0 max.	Between Junction and Lead

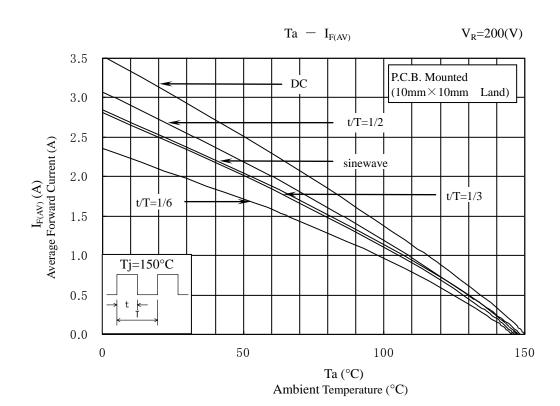
6. Characteristics







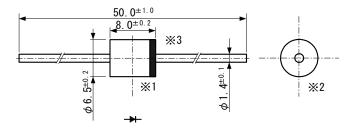




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8. Package information

8-1 Package type, physical dimensions and material



- *1 The allowance position of Body against the center of whole lead wire is 0.5mm(max.)
- $^{*}2$ The centric allowance of lead wire against center of physical body is 0.3mm(max.)
- *3 The burr may exit up to 2mm from the body of lead

Dimensions in mm

8-2 Appearance

The body shall be clean and shall not bear any stain, rust or flaw.

8-3 Marking

Cathode Band

(1)
(2)
(3)
(2)
(3)

- ① Type number RL4Z
- Lot number 1
 First digit: Last digit of Year
 Second digit: Month
 From 1 to 9 for Jan. to Sep.
 O for Oct., N for Nov., and D for Dec.
- 3 Lot number 2 (ten days)

 Top of the month

 Middle of month

 End of month