

July 2013

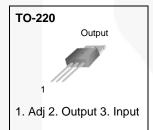
KA317 / LM317 3-Terminal Positive Adjustable Regulator

Features

- Output-Current In Excess of 1.5 A
- Output-Adjustable Between 1.2 V and 37 V
- Internal Thermal Overload Protection
- · Internal Short-Circuit Current Limiting
- Output-Transistor Safe Operating Area Compensation
- TO-220 Package

Description

This monolithic integrated circuit is an adjustable 3-terminal positive-voltage regulator designed to supply more than 1.5 A of load current with an output voltage adjustable over a 1.2 V to 37 V range. It employs internal current limiting, thermal shutdown, and safe area compensation.



Ordering Information

Product Number	Product Number Package		Operating Temperature	
LM317T	TO-220 (Single Gauge)	Rail	0°C to +125°C	
KA317TU	TO-220 (Dual Gauge)	Rail	0°C to +125°C	

Block Diagram

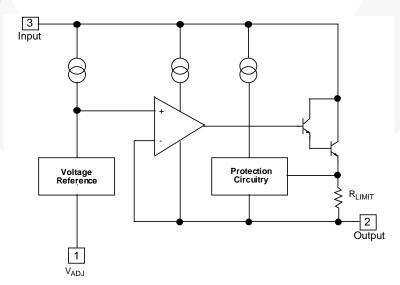


Figure 1. Block Diagram

1

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}\text{C}$ unless otherwise noted.

Symbol	Parameter	Value	Unit
V _I - V _O	Input-Output Voltage Differential	40	V
T _{LEAD}	Lead Temperature	230	°C
T_J	Operating Junction Temperature Range	0 to +125	°C
T _{STG}	Storage Temperature Range	-65 to +125	°C
$\Delta V_{O}/\Delta T$	Temperature Coefficient of Output Voltage	±0.02	%/°C

Thermal Characteristics

Symbol	Parameter	Value	Units
P_{D}	Power Dissipation	Internally Limited	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	80	°C/W
$R_{\theta JC}$	Thermal Resistance, Junction to Case	5	°C/W

Electrical Characteristics

 $V_I - V_O = 5 \text{ V}, \ I_O = 0.5 \text{ A}, \ 0^{\circ}\text{C} \leq T_J \leq +125^{\circ}\text{C}, \ I_{MAX} = 1.5 \text{ A}, \ P_{DMAX} = 20 \text{ W}, \ unless otherwise specified}.$

Symbol	Parameter	Conditions		Min.	Тур.	Max.	Unit	
D	Line Regulation ⁽¹⁾	$T_A = +25^{\circ}C, 3 \text{ V} \le V_I - V_O \le 40 \text{ V}$			0.01	0.04	%/ V	
R _{LINE}	Line Regulation	3 V ≤ V _I - V _O ≤ 40 V			0.02	0.07	%/ V	
R _{LOAD}	Load Regulation ⁽¹⁾	$T_{A} = +25^{\circ}C,$ $10mA \le I_{O} \le I_{MAX}$	V _O < 5 V		18	25	mV	
			V _O ≥ 5 V		0.4	0.5	%/V _O	
		10 mA \leq I _O \leq I _{MAX}	V _O < 5 V		40	70	mV	
			V _O ≥ 5 V		0.8	1.5	%/V _O	
I _{ADJ}	Adjustable Pin Current				46	100	μΑ	
Δl _{ADJ}	Adjustable Pin Current Change	$3 \text{ V} \le \text{V}_{\text{I}} - \text{V}_{\text{O}} \le 40 \text{ V},$ $10 \text{ mA} \le \text{I}_{\text{O}} \le \text{I}_{\text{MAX}}, \text{P}_{\text{D}} \le \text{P}_{\text{MAX}}$			2.0	5.0	μА	
V _{REF}	Reference Voltage	$3 \text{ V} \le \text{V}_{\text{IN}} - \text{V}_{\text{O}} \le 40 \text{ V},$ $10 \text{ mA} \le \text{I}_{\text{O}} \le \text{I}_{\text{MAX}}, \text{P}_{\text{D}} \le \text{P}_{\text{MAX}}$		1.20	1.25	1.30	V	
ST _T	Temperature Stability			_	0.7		%/V _O	
I _{L(MIN)}	Minimum Load Current to Maintain Regulation	V _I - V _O = 40 V			3.5	12.0	mA	
I _{O(MAX)}	Maximum Output Current	Maximum Output	T = 25°C	$V_I - V_O \le 15 V$, $P_D \le P_{MAX}$	1.5	2.2		А
		T _A = 25°C	$V_I - V_O \le 40 V$, $P_D \le P_{MAX}$		0.3		А	
e _N	RMS Noise,% of V _{OUT}	$T_A = +25^{\circ}C$, 10 Hz $\leq f \leq$ 10 kHz			0.003	0.010	%/V _O	
RR	Ripple Rejection	$V_O = 10 \text{ V}, \text{ f} = 120 \text{ Hz without } C_{ADJ}$			60		dB	
		$C_{ADJ} = 10 \mu F^{(2)}$		66	75		dB	
ST		T _A = +25°C for End Point Measurements, 1000 HR			0.3	1.0	%	

Notes:

- 1. Load and line regulation are specified at constant junction temperature. Change in V_D due to heating effects must be taken into account separately. Pulse testing with low duty is used ($P_{MAX} = 20 \text{ W}$).
- 2. C_{ADJ} , when used, is connected between the adjustment pin and ground.

Typical Performance Characteristics

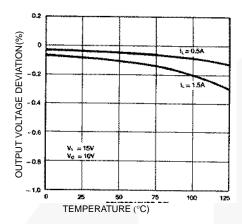


Figure 2. Load Regulation

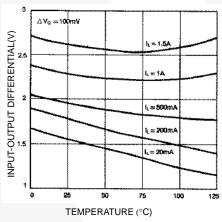


Figure 4. Dropout Voltage

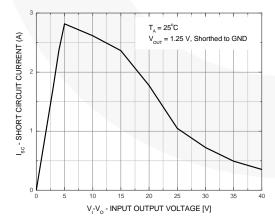


Figure 6. Short Circuit vs. Input-Output Voltage

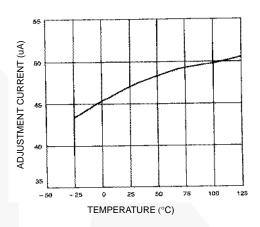


Figure 3. Adjustment Current

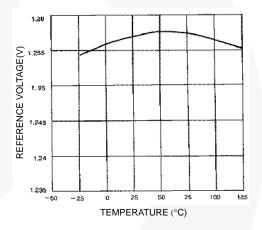
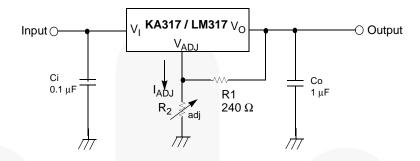


Figure 5. Reference Voltage

Typical Application(3)



$$V_0 = 1.25 \text{ V} (1 + R_2 / R_1) + I_{ADJ}R_2$$

Figure 7. Typical Application

Note:

3. C_I is required when the regulator is located an appreciable distance from power supply filter. C_O is not needed for stability; however, it does improve transient response. Since I_{ADJ} is controlled to less than 100 μ A, the error associated with this term is negligible in most applications.

Physical Dimensions

TO-220 [SINGLE GAUGE]

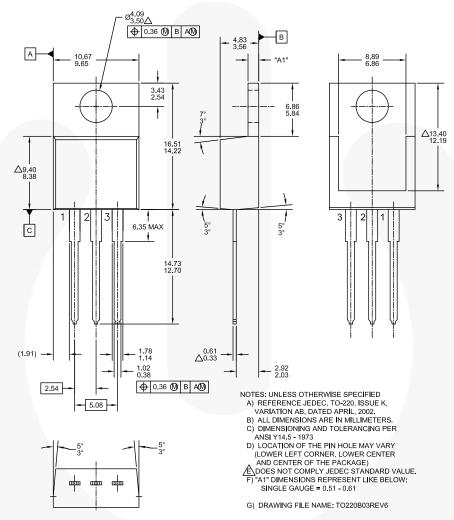


Figure 8. TO-220, MOLDED, 3-LEAD, JEDEC, VARIATION AB

Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a Fairchild Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.

Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings: http://www.fairchildsemi.com/dwg/TO/TO220B03.pdf.

For current tape and reel specifications, visit Fairchild Semiconductor's online packaging area: http://www.fairchildsemi.com/packing_dwg/PKG-TO220B03.pdf.

Physical Dimensions (continued)

TO-220 [DUAL GAUGE]

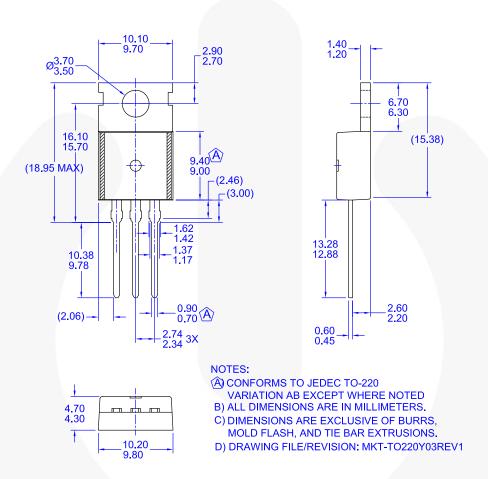


Figure 9. TO220, MOLDED, 3-LEAD, NON-JEDEC, VARIATION AB

Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a Fairchild Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.

Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings: http://www.fairchildsemi.com/dwg/TO/TO220Y03.pdf.

For current tape and reel specifications, visit Fairchild Semiconductor's online packaging area: http://www.fairchildsemi.com/packing_dwg/PKG-TO220Y03.pdf.





TRADEMARKS

CorePOWER™

The following includes registered and unregistered trademarks and service marks, owned by Fairchild Semiconductor and/or its global subsidiaries, and is not intended to be an exhaustive list of all such trademarks.

FPS™ AccuPower™ F-PFS™ AX-CAP® FRFET® BitSiC™ Global Power ResourceSM GreenBridge™ Build it Now™ CorePLUS™ Green FPS™

Gmax™ CROSSVOLT™ CTL™ GTO™ Current Transfer Logic™ IntelliMAX™ ISOPLANAR™ DEUXPEED®

Making Small Speakers Sound Louder Dual Cool™

EcoSPARK® and Better™ EfficientMax™ MegaBuck™ $\mathsf{ESBC}^{\mathsf{TM}}$ MICROCOUPLER™ ® MicroFET™ MicroPak™

Fairchild® MicroPak2™ Fairchild Semiconductor® MillerDrive™ FACT Quiet Series™ MotionMax™ FACT mWSaver™ FAST® OptoHiT™ FastvCore™ OPTOLOGIC® FETBench™ OPTOPLANAR® PowerTrench® PowerXS™

Programmable Active Droop™

OFET QS™ Quiet Series™ RapidConfigure™

Saving our world, 1mW/W/kW at a time™

SignalWise™ SmartMax™ SMART START™

Solutions for Your Success™

SPM® STEAL TH™ SuperFET[®] SuperSOT™-3 SuperSOT™-6 SuperSOT™-8 SupreMOS® SyncFET™

SYSTEM SERVERAL

TinyBoost™ TinyBuck™ TinyCalc™ TinyLogic[®] TINYOPTO™ TinyPower™ TinyPWM™ TinyWire™ TranSiC™ TriFault Detect™ TRUECURRENT®*

UHC Ultra FRFET™ UniFFT™ **VCX™** VisualMax™ VoltagePlus™

uSerDes™

Green FPS™ e-Series™

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
- 2. A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

ANTI-COUNTERFEITING POLICY

Fairchild Semiconductor Corporation's Anti-Counterfeiting Policy. Fairchild's Anti-Counterfeiting Policy is also stated on our external website, www.fairchildsemi.com,

Counterfeiting of semiconductor parts is a growing problem in the industry. All manufacturers of semiconductor products are experiencing counterfeiting of their parts. Customers who inadvertently purchase counterfeit parts experience many problems such as loss of brand reputation, substandard performance, failed applications, and increased cost of production and manufacturing delays. Fairchild is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts. Fairchild strongly encourages customers to purchase Fairchild parts either directly from Fairchild or from Authorized Fairchild Distributors who are listed by country on our web page cited above. Products customers buy either from Fairchild directly or from Authorized Fairchild Distributors are genuine parts, have full traceability, meet Fairchild's quality standards for handling and storage and provide access to Fairchild's full range of up-to-date technical and product information. Fairchild and our Authorized Distributors will stand behind all warranties and will appropriately address any warranty issues that may arise. Fairchild will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources. Fairchild is committed to combat this global problem and encourage our customers to do their part in stopping this practice by buying direct or from authorized distributors

PRODUCT STATUS DEFINITIONS

Definition of Terms				
Datasheet Identification	Product Status	Definition		
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.		
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.		
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.		
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.		

Rev. 164

^{*} Trademarks of System General Corporation, used under license by Fairchild Semiconductor.