

BTA/BTB06 Series

6A TRIACs

SNUBBERLESS™, LOGIC LEVEL & STANDARD

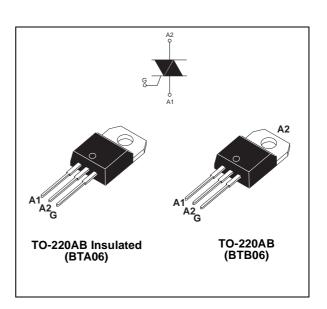
MAIN FEATURES:

Symbol	Value	Unit
I _{T(RMS)}	6	А
V _{DRM} /V _{RRM}	600 and 800	V
I _{G (Q₁)}	5 to 50	mA

DESCRIPTION

Suitable for AC switching operations, the BTA/BTB06 series can be used as an ON/OFF function in applications such as static relays, heating regulation, induction motor starting circuits... or for phase control in light dimmers, motor speed controllers,...

The snubberless and logic level versions (BTA/BTB...W) are specially recommended for use on inductive loads, thanks to their high commutation performances. By using an internal ceramic pad, the BTA series provides voltage insulated tab (rated at 2500V RMS) complying with UL standards (File ref.: E81734)



ABSOLUTE MAXIMUM RATINGS

Symbol	Param		Value	Unit	
I _{T(RMS)}	RMS on-state current (full sine wave)	TO-220AB	Tc = 110°C	6	Α
		TO-220AB Ins.	Tc = 105°C	0	
ITSM	Non repetitive surge peak on-state F = 50 Hz t = 20 ms		60	Α	
	current (full cycle, Tj initial = 25° C) F = 60 Hz t = 16.7 ms		63		
l ² t	I ² t Value for fusing	tp = 10 r	ns	21	A ² s
dl/dt	Critical rate of rise of on-state current $I_G = 2 \times I_{GT}$, tr $\leq 100 \text{ ns}$	F = 120 Hz	F = 120 Hz Tj = 125°C		A/µs
I _{GM}	Peak gate current	tp = 20 μs	Tj = 125°C	4	Α
P _{G(AV)}	Average gate power dissipation Tj = 125°C		1	W	
T _{stg} T _j	Storage junction temperature range Operating junction temperature range			- 40 to + 150 - 40 to + 125	°C

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ELECTRICAL CHARACTERISTICS (Tj = 25°C, unless otherwise specified)

■ SNUBBERLESS™ and LOGIC LEVEL (3 Quadrants)

Symbol	Test Conditions	Quadrant		BTA/BTB06				Unit
				TW	SW	CW	BW	01
I _{GT} (1)	$V_D = 12 \text{ V}$ $R_1 = 30 \Omega$	1 - 11 - 111	MAX.	5	10	35	50	mA
V _{GT}	- VD - 12 V	1 - 11 - 111	MAX.		1	.3	•	V
$V_{\sf GD}$	$V_D = V_{DRM}$ $R_L = 3.3 \text{ k}\Omega$ Tj = 125°C	1 - 11 - 111	MIN.	0.2				V
I _H (2)	I _T = 100 mA	•	MAX.	10	15	35	50	mA
ΙL	I _G = 1.2 I _{GT}	I - III	MAX.	10	25	50	70	mA
		II		15	30	60	80	
dV/dt (2)	$V_D = 67 \text{ %V}_{DRM}$ gate open Tj = 125°C		MIN.	20	40	400	1000	V/µs
(dl/dt)c (2)	$(dV/dt)c = 0.1 V/\mu s$ Tj =	125°C	MIN.	2.7	3.5	-	-	A/ms
	(dV/dt)c = 10 V/µs Tj = 125°C			1.2	2.4	-	-	1
	Without snubber Tj =	125°C		-	-	3.5	5.3	1

■ STANDARD (4 Quadrants)

Symbol	Test Conditions	Quadrant		BTA/BTB06		Unit
				С	В	0
I _G (1)	$V_D = 12 \text{ V}$ $R_L = 30 \Omega$	I - II - III IV	MAX.	25 50	50 100	mA
V _{GT}		ALL	MAX.	1.	.3	V
V _{GD}	$V_D = V_{DRM}$ $R_L = 3.3 \text{ k}\Omega$ $Tj = 125^{\circ}\text{C}$	ALL	MIN.	0.2		V
I _H (2)	I _T = 500 mA		MAX.	25	50	mA
ΙL	I _G = 1.2 I _{GT}	I - III - IV	MAX.	40	50	mA
		II		80	100	
dV/dt (2)	$V_D = 67 \% V_{DRM}$ gate open Tj = 125°C		MIN.	200	400	V/µs
(dV/dt)c (2)	$(dI/dt)c = 2.7 \text{ A/ms}$ $Tj = 125^{\circ}C$		MIN.	5	10	V/µs

STATIC CHARACTERISTICS

Symbol	Test Conditions			Value	Unit
V _T (2)	$I_{TM} = 5.5 \text{ A}$ tp = 380 µs	Tj = 25°C	MAX.	1.55	V
V _{to} (2)	Threshold voltage	Tj = 125°C	MAX.	0.85	V
R _d (2)	Dynamic resistance	Tj = 125°C	MAX.	60	mΩ
I _{DRM}	$V_{DRM} = V_{RRM}$	Tj = 25°C	MAX.	5	μΑ
I _{RRM}		Tj = 125°C	IVIAA.	1	mA

Note 1: minimum IGT is guaranted at 5% of IGT max.

Note 2: for both polarities of A2 referenced to A1

2/6

THERMAL RESISTANCES

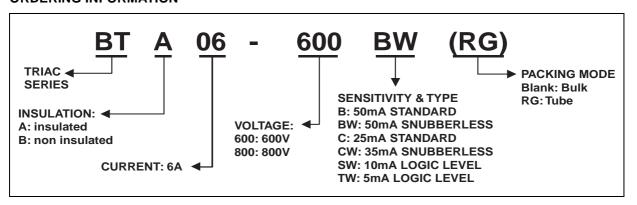
Symbol	Parameter		Value	Unit
R _{th(j-c)}	Junction to case (AC)	TO-220AB	1.8	°C/W
		TO-220AB Insulated	2.7	
R _{th(j-a)}	Junction to ambient	TO-220AB TO-220AB Insulated	60	°C/W

PRODUCT SELECTOR

Part Number	Voltag	Voltage (xxx)		Type	Package
	600 V	800 V	Sensitivity	,,,	
BTA/BTB06-xxxB	Х	Х	50 mA	Standard	TO-220AB
BTA/BTB06-xxxBW	Х	Х	50 mA	Snubberless	TO-220AB
BTA/BTB06-xxxC	Х	Х	25 mA	Standard	TO-220AB
BTA/BTB06-xxxCW	Х	Х	35 mA	Snubberless	TO-220AB
BTA/BTB06-xxxSW	Х	Х	10 mA	Logic level	TO-220AB
BTA/BTB06-xxxTW	Х	Х	5 mA	Logic level	TO-220AB

BTB: non insulated TO-220AB package

ORDERING INFORMATION



OTHER INFORMATION

Part Number	Marking	Weight	Base quantity	Packing mode
BTA/BTB06-xxxyz	BTA/BTB06-xxxyz	2.3 g	250	Bulk
BTA/BTB06-xxxyzRG	BTA/BTB06-xxxyz	2.3 g	50	Tube

Note: xxx = voltage, y = sensitivity, z = type

Fig. 1: Maximum power dissipation versus RMS on-state current (full cycle).

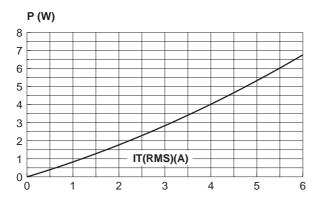


Fig. 3: Relative variation of thermal impedance versus pulse duration.

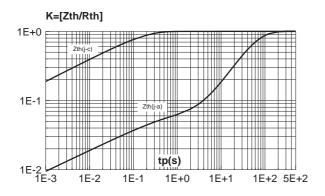


Fig. 5: Surge peak on-state current versus number of cycles.

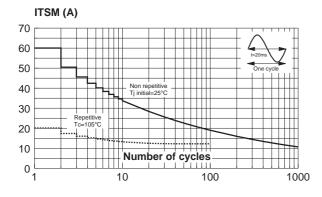


Fig. 2: RMS on-state current versus case temperature (full cycle).

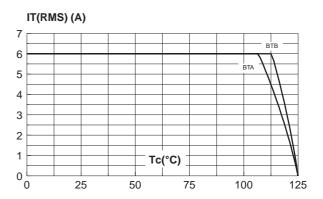


Fig. 4: On-state characteristics (maximum values).

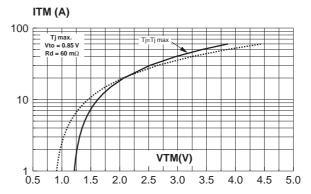
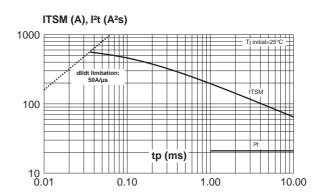


Fig. 6: Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp < 10ms, and corresponding value of l^2t .



4/6

Fig. 7: Relative variation of gate trigger current, holding current and latching current versus junction temperature (typical values).

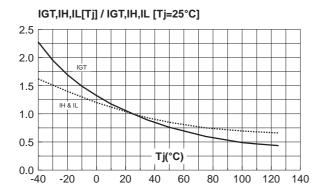
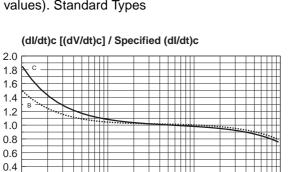


Fig. 8-2: Relative variation of critical rate of decrease of main current versus (dV/dt)c (typical values). Standard Types



(dV/dt)c (V/µs)

10.0

100.0

1.0

Fig. 8-1: Relative variation of critical rate of decrease of main current versus (dV/dt)c (typical values). Snubberless & Logic Level Types

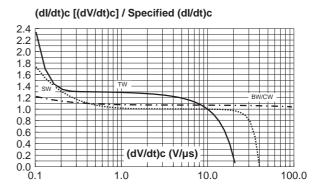
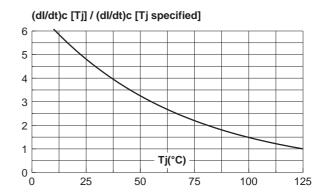


Fig. 9: Relative variation of critical rate of decrease of main current versus junction temperature.

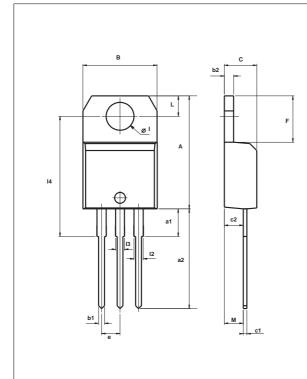


0.2

0.0 -

PACKAGE MECHANICAL DATA

TO-220AB / TO-220AB Ins.



	DIMENSIONS					
REF.	Millimeters		rs			
	Min.	Тур.	Max.	Min.	Тур.	Max.
Α	15.20		15.90	0.598		0.625
a1		3.75			0.147	
a2	13.00		14.00	0.511		0.551
В	10.00		10.40	0.393		0.409
b1	0.61		0.88	0.024		0.034
b2	1.23		1.32	0.048		0.051
С	4.40		4.60	0.173		0.181
c1	0.49		0.70	0.019		0.027
c2	2.40		2.72	0.094		0.107
е	2.40		2.70	0.094		0.106
F	6.20		6.60	0.244		0.259
I	3.75		3.85	0.147		0.151
14	15.80	16.40	16.80	0.622	0.646	0.661
L	2.65		2.95	0.104		0.116
I2	1.14		1.70	0.044		0.066
13	1.14		1.70	0.044		0.066
M		2.60			0.102	

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57

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