

6. Procedure:

- a. Collect the components necessary to accomplish this experiment.
- b. Plug the IC chip into the breadboard.
- c. Connect the supply voltage and ground lines to the chips. PIN7 = Ground and PIN14 = +5V.
- d. Make connections as shown in the respective circuit diagram.
- e. Connect the inputs of the gate to the input switches of the LED.
- f. Connect the output of the gate to the output LEDs.
- g. Once all connections have been done, turn on the power switch of the breadboard
- h. Operate the switches and fill in the truth table (Write "1" if LED is ON and "0" if L1 is OFF apply the various combinations of inputs according to the truth table and observe the condition of Output LEDs.
- 7. Observation Table: Input Variable: ABCD

Output Variable: W X Y Z LED ON: RED Light: Logic 1 LED OFF: Green Light: Logic 0

| | INPUTS(LED) | | | OUTPUTS(LED) | | | | |
|---|-------------|---|---|--------------|---|---|---|--|
| A | В | C | D | W | X | Y | Z | |
| | | | | , | | | | |
| | | | | | | | | |
| | | | | | | | | |
| | _ | | | | | | | |
| | | | | | | | | |
| | | | | | | | | |

Calculation:

K-map

Boolean Expression: W =

X =

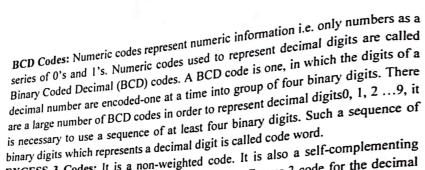
Y =

Z =

- 8. Result and Discussions: Excess-3 code is a 4-bit un-weighted code and can be obtained from the corresponding value of BCD code by adding three to each coded number. Excess-3 code is self complementing in nature because 1's complement of the coded number yields 9's complement of number itself.
- 9. Conclusion: BCD to Excess-3 code converter has been designed using basic logic gates and its truth table verified.

10. Self assessment:

- a. Why Excess-3 code is known as self complementary code?
- To Design and verify the truth table of code conversion from Excess-3 to BCD using basic Logic Gates.



EXCESS 3 Codes: It is a non-weighted code. It is also a self-complementing BCD code used in decimal arithmetic units. . The Excess-3 code for the decimal number is performed in the same manner as BCD except that decimal number 3 is added to the each decimal unit before encoding it to binary.

Truth Table for BCD to Excess -3 Code Conversion:

| 11 | INPUTS(BCD) | | | | OUTPUTS(Excess- | | | |
|----|-------------|---|---|---|-----------------|---|---|--|
| | | | | | 3) | | | |
| A | В | C | D | W | X | Y | Z | |
| 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | |
| 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | |
| 0 | 0 | 1 | 1 | 0 | 1 | 0 | 1 | |
| 0 | 0 | 1 | 0 | 0 | 1 | 1 | 0 | |
| 0 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | |
| 0 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | |
| 0 | 1 | 0 | 1 | 1 | 0 | 0 | 1 | |
| 0 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | |
| 1 | 1 | 0 | 0 | 1 | 0 | 1 | 1 | |
| 1 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | |

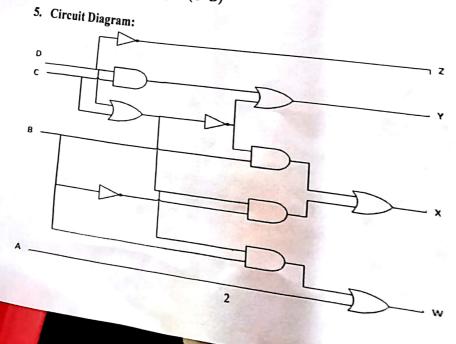
Expression:

Z = D

Y = CD+C'D'=CD(C+D)'

X = B'C+B'D+BC'D' = B'(C+D) +BC'D

W = B'(C+D) + B(C+D)'



Performance Evaluation:

| Name | | | | | | |
|---------------|---------------|---------------------|--------------|-------------------|--|--|
| Performing on | | First Submission | | Second Submission | | |
| Extra | regular | | | | | |
| Grade and F | Remarks by | the Tutor | | | | |
| 1. Cla | rity about th | ne objective of th | e experiment | | | |
| 2. Cla | rity about th | ie problem statei | nent | | | |
| 3. Sub | mitted the v | vork in desired for | rmat | | | |
| | | ty to solve the pr | | | | |
| | | the team work. | | | | |
| Others: | | , | o dis | | | |
| | | | | | | |
| | | | | | | |
| Grade | AB | C D F | | signatur | | |

Experiment - 5

- TITLE: To Design and verify the truth table of code conversion from BCD to Excess-3 using basic Logic Gates.
- 2. OBJECTIVE:
 - a. Design of different combinational circuits and their applications using basic logic gates.
 - b. Creation and observation of the excess 3 code representation sequence
 - c. Exercising the design of code conversion logic circuits,
 - d. Creating the truth table of conversion functions from BCD to EXCESS 3 code
 - e. Developing skills in simplification of specified logical functions
- 3. Apparatus Required:
 - a. Prototyping board (breadboard)
 - b. DC Power Supply 5V Battery
 - c. Light Emitting Diode (LED)
 - d. Digital ICs: 7404 :Hex Inverter 7408: Quad 2 input AND

7432: Quad 2 input OR

- e. Connecting Wires
- . Theory:

Code Converters: A code converter is a combinational circuit that must be inserted between the two systems, to make them compatible even though each uses different code for same information. It means that a code converter is a code translator from one code to the other. The code converter is used since to systems using two different codes but they need to use the same information. So the code converter is the solution.