

Iris^c Documentation

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Abstract

This document describes the C verification framework based on the Iris program logic. The Coq source is in <https://github.com/izgzhen/iris-c-coq>. We omitted many details on Iris itself, which can be found in the document and Coq formalization here <https://gitlab.mpi-sws.org/FP/iris-coq/>.

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1 Language

1.1 Definitions

Syntax. The language is a simplified version of C. It mainly consists of *Expr* (expressions), *Type* (types) and *Val* (values).

$$\begin{aligned}
\tau : \textit{Type} &::= \tau_{\text{void}} \mid \tau_{\text{null}} \mid \tau_{\text{int8}} \mid \tau_{\text{int32}} \mid \text{Ptr}(\tau) \mid \tau \times \tau \\
v : \textit{Val} &::= \text{void} \mid \text{null} \mid i \in [0, 2^8) \mid i \in [0, 2^{32}) \mid l \mid (v, v) \\
e : \textit{Expr} &::= v \mid x \mid e \oplus e \mid !e \mid !_\tau e \mid \&e \mid e.1 \mid \text{CAS}(e, e, e) : \tau \mid e.2 \mid \\
&\quad e \leftarrow e \mid \text{if}(e) \{e\} \text{ else } \{e\} \mid \text{while}(e) \{e\} \mid \text{break} \mid \text{continue} \mid \\
&\quad \text{return } e \mid f(e_1, \dots, e_n) : \tau \mid e; ; e \mid \text{alloc}_\tau(e) \mid \\
&\quad \text{let } x : \tau := e \text{ in } e \mid \text{fork}(f(e_1, \dots, e_n) : \tau)
\end{aligned}$$

We also have following definitions:

- block address $b \in \mathbb{N}$
- offset $o \in \mathbb{Z}^+$
- Memory address $l : \textit{Addr} \triangleq b \# o$
- Program $\Sigma : \textit{TEXT} \triangleq [f \mapsto \textit{Function}]$ is a list of functions indexed by names
- Function $F : \textit{Function} \triangleq (\tau_{\text{ret}} \times [x_1 : \tau_1, \dots] \times e)$ consists of return type, parameter declarations and function body.

(NOTE) The relationship between $!e$ and $!_\tau e$: The former one is only used in source, and the later one is used in the actual semantics and inference rules. By adding a pre-processing step which instantiates all source-only expression to substituted/typed ones, we can remove lexical and typing environment from the core program logic. The details are in [1.3](#).

(NOTE) Compared with C, there are several notable differences:

1. We currently don't support **for** loop and **switch**
2. C differentiates statements and expressions (so do many other program logics). For simplicity and expressivity, we merged them together into a coherent expression *Expr* instead
3. We support declarations of local variables through a syntactic sugar based on the core language (definition of default : $\textit{Type} \rightarrow \textit{Val}$ is trivial)

$$\text{let } x : \tau \text{ in } e \triangleq \text{let } x : \tau := \text{alloc}_\tau(\text{default}(\tau)) \text{ in } e$$

(NOTE) We sometimes use $c : \textit{Expr}$ to denote *condition* expression in **if** and **while**.

Evaluation Context. To make the evaluation order explicit and reflected to the logic through the bind rule, we define the *evaluation context*:

$$\begin{aligned}
K : \text{ECTX} \triangleq & \text{let } x : \tau := \bullet \text{ in } e \mid \bullet \oplus e \mid v \oplus \bullet \mid !_\tau \bullet \mid \& \bullet \mid \bullet.1 \mid \bullet.2 \mid \\
& \text{CAS}(\bullet, e, e) : \tau \mid \text{CAS}(l, \bullet, e) : \tau \mid \text{CAS}(l, v, \bullet) : \tau \mid \\
& \bullet \leftarrow e \mid l \leftarrow \bullet \mid \text{if}(\bullet) \{e\} \text{ else } \{e\} \mid \\
& \text{return } \bullet \mid f(v_1, \dots, \bullet, e_1, \dots) : \tau \mid \text{alloc}_\tau(\bullet) \mid \bullet; e \mid \\
& \text{fork}(f(v_1, \dots, \bullet, e_1, \dots) : \tau)
\end{aligned}$$

1.2 Semantics

Model. First define

- Byte-size value

$$v_{\text{byte}} : \text{Val}_{\text{byte}} \triangleq \text{!} \mid i \in [0, 2^8) \mid l_{\{0|1|2|3\}} \mid \text{null}$$

- Heap $h : \text{HEAP} \triangleq [l \mapsto v_{\text{byte}}]$
- Continuation $k : \text{CONT} \triangleq [\text{ECTX}]$
- Context $K : \text{CTX} \triangleq K_{\text{while}}(c, e, k) \mid K_{\text{call}}(k)$
- Stack $s : \text{STACK} \triangleq [\text{CTX}]$
- Shared state

$$\sigma : \text{State} \triangleq (\text{heap} : \text{HEAP}, \text{text} : \text{TEXT})$$

(NOTE) You may notice the difference between Val and Val_{byte} , it is intended to create a layer of abstraction for easier manipulation of spatial assertions and also for a clean, unified language syntax.

Small-Step Operational Semantics. We define the small-step semantics (see figure 1.2) for three sub-semantics:

- local reduction $(e, h) \rightsquigarrow_{\text{local}}^\Sigma (e', h', (t_1, t_2, \dots))$ (when the set of forked-off threads t_1, \dots is empty, we abbreviate it as $(e, h) \rightsquigarrow_{\text{local}}^\Sigma (e', h')$)
- *jump* reductions $(e, s) \rightsquigarrow_{\text{jump}}^\Sigma (e', s')$
- *while* reductions $(e, s) \rightsquigarrow_{\text{while}} (e', s')$

Then we combine them together into $(e, s, \sigma) \rightsquigarrow (e', s', \sigma', (t_1, \dots))$ point-wise.

1.3 Type System and Lexical Environment

Local Typing Rules. The types are defined in §1.1, and all values in v can be *locally* typed trivially (since v is introduced to reflect type structure in some sense). Nevertheless, due to the fact that the language of study is weakly-typed in the vein of C, we still have some “weird” rules worth documenting.

Here we define local typing judgment $\vdash_{\text{typeof}} v : \tau$ for values.

TYPEOF-VOID $\vdash_{\text{typeof}} \mathbf{void} : \tau_{\text{void}}$	TYPEOF-NULL $\vdash_{\text{typeof}} \mathbf{null} : \tau_{\text{null}}$	TYPEOF-INT8 $\vdash_{\text{typeof}} i \in [0, 2^8) : \tau_{\text{int8}}$
TYPEOF-INT32 $\vdash_{\text{typeof}} i \in [0, 2^{32}) : \tau_{\text{int32}}$	TYPEOF-NULL-PTR $\forall \tau. \vdash_{\text{typeof}} \mathbf{null} : \text{Ptr}(\tau)$	TYPEOF-PTR $\forall \tau, l. \vdash_{\text{typeof}} l : \text{Ptr}(\tau)$
TYPEOF-PROD $\frac{\vdash_{\text{typeof}} v_1 : \tau_1 \quad \vdash_{\text{typeof}} v_2 : \tau_2}{\vdash_{\text{typeof}} (v_1, v_2) : \tau_1 \times \tau_2}$		

Note that rule TYPEOF-NULL-PTR means that **null** can be of any pointer type, and TYPEOF-PTR means that a pointer can have *any* pointer type.

Lexical Environment Formally, the lexical environment for a function body consists of two parts: parameter bindings (free variables) and local bindings (introduced by **let**).

Every time we call a function, we will desugar the first part into the second part by recursively binding parameters to allocated argument values (as implemented in **let_params** and typesetted as $e[ps/l_s]$).

Now we focus on what $e[x/v : \tau]$ means in the semantics of **let**-binding: Essentially, variable x is replaced with its binding v , and type information is tagged to the untyped parts when in need. More specifically, we will replace variable with either its location (when in left-hand side) or the dereference of its location (when in right-hand side). We will produce a pointer arithmetic expression when processing the left-hand side expression, which in turn requires type inference $\vdash_{\text{tyinf}} e : \tau$ (since it is standard and trivial, we will leave out the details here).

We won’t document the algorithmic rules for rewriting expressions. You may refer to **resolve_rhs**, **resolve_lhs_outer** and **resolve_lhs_inner** for details.

$$\begin{array}{c}
\text{ES-BINOP} \quad \frac{\llbracket \text{oplus} \rrbracket(v_1, v_2) = v'}{(v_1 \oplus v_2, h) \rightsquigarrow_{local}^\Sigma (v', h)} \quad \text{ES-DEREF-TYPED} \quad \frac{\vdash_{\text{typeof}} v : \tau \quad h \vdash l \mapsto \text{encode}(v)}{(!_\tau l, h) \rightsquigarrow_{local}^\Sigma (v, h)} \quad \text{ES-FST} \quad ((v_1, v_2).1, h) \rightsquigarrow_{local}^\Sigma (v_1, h) \\
\\
\text{ES-SND} \quad ((v_1, v_2).2, h) \rightsquigarrow_{local}^\Sigma (v_2, h) \quad \text{ES-ASSIGN} \quad (l \leftarrow v, h) \rightsquigarrow_{local}^\Sigma (\text{void}, h[l \mapsto \text{encode}(v)]) \quad \text{ES-SEQ} \quad (v;; e, h) \rightsquigarrow_{local}^\Sigma (e, h) \\
\\
\text{ES-ALLOC} \quad \frac{\vdash_{\text{typeof}} v : \tau \quad \forall o'. h(b, o') = \perp}{(\text{alloc}_\tau(v), h) \rightsquigarrow_{local}^\Sigma (b\#o, h[b\#o \mapsto v])} \quad \text{ES-IF-TRUE} \quad \frac{}{(\text{if}(\text{true}) \{e_1\} \text{ else } \{e_2\}, h) \rightsquigarrow_{local}^\Sigma (e_1, h)} \\
\\
\text{ES-IF-FALSE} \quad (\text{if}(\text{false}) \{e_1\} \text{ else } \{e_2\}, h) \rightsquigarrow_{local}^\Sigma (e_2, h) \quad \text{ES-BIND}' \quad \frac{\text{is_jump}(e) = \text{False} \quad (e, h) \rightsquigarrow_{local}^\Sigma (e', h')}{((k :: ks)e, h) \rightsquigarrow_{local}^\Sigma ((k :: ks)e', h')} \\
\\
\text{ES-CAS-FAIL} \quad \frac{\vdash_{\text{typeof}} v_1 : \tau \quad \vdash_{\text{typeof}} v_2 : \tau \quad \vdash_{\text{typeof}} v : \tau \quad h \vdash l \mapsto \text{encode}(v) \quad v_1 \neq v}{(\text{CAS}(l, v_1, v_2) : \tau, h) \rightsquigarrow_{local}^\Sigma (\text{false}, h)} \\
\\
\text{ES-CAS-SUC} \quad \frac{\vdash_{\text{typeof}} v_1 : \tau \quad \vdash_{\text{typeof}} v_2 : \tau \quad h \vdash l \mapsto \text{encode}(v_1)}{(\text{CAS}(l, v_1, v_2) : \tau, h) \rightsquigarrow_{local}^\Sigma (\text{true}, h[l \mapsto \text{encode}(v_2)])} \\
\\
\text{ES-LET} \quad (\text{let } x : \tau := v \text{ in } e, h) \rightsquigarrow_{local}^\Sigma (e[x/v : \tau], h) \quad \text{ES-FORK} \quad \frac{\Sigma(f) = \text{Function}(\tau, ps, e)}{(\text{fork}(f(vs) : \tau), h) \rightsquigarrow_{local}^\Sigma (\text{void}, h, (\{e[ps/vs]\}))} \\
\\
\text{JS-RETE} \quad \frac{\forall K \in KS'. K \neq K_{call}(_)}{(k'(\text{return } v), KS' \uparrow\uparrow K_{call}(k) :: KS) \rightsquigarrow_{jump}^\Sigma (k(v), KS)} \\
\\
\text{JS-CALL} \quad \frac{\Sigma(f) = \text{Function}(\tau, ps, e)}{(k(f(vs) : \tau), KS) \rightsquigarrow_{jump}^\Sigma (e[ps/vs], K_{call}(k) :: KS)} \\
\\
\text{WS-WHILE} \quad (k(\text{while}(c) \{e\}), KS) \rightsquigarrow_{while} (\text{if}(c) \{e; \text{continue}\} \text{ else } \{\text{break}\}, K_{while}(c, e, k) :: KS) \\
\\
\text{WS-BREAK} \quad \frac{\forall K \in KS'. K \neq K_{while}(_, _, _)}{(k'(\text{break}), KS' \uparrow\uparrow K_{while}(c, e, k) :: KS) \rightsquigarrow_{while} (k(\text{void}), KS)} \\
\\
\text{WS-CONTINUE} \quad \frac{\forall K \in KS'. K \neq K_{while}(_, _, _)}{(k'(\text{continue}), KS' \uparrow\uparrow K_{while}(c, e, k) :: KS) \rightsquigarrow_{while} (k(\text{while}(c) \{e\}), KS)}
\end{array}$$

Figure 1: Semantics rules

2 Program Logic

This section describes how to build a program logic for the C language (*c.f.* §1) on top of Iris..

2.1 Ghost Resource

Using the standard Iris assertions and the ownership of ghost heap resource, we can define some basic custom assertions:

$$\begin{aligned}
l \mapsto_q v : t &\triangleq l \mapsto_q \text{encode}(v) \wedge \vdash_{\text{typeof}} v : t \\
l \mapsto v : t &\triangleq l \mapsto_1 v : t \\
l \mapsto_q - : t &\triangleq \exists v. l \mapsto_q v : t \\
f \mapsto_{\text{TEXT}} F &\triangleq [\circ [f \leftarrow \text{ag} F]]^{\text{TEXT}}
\end{aligned}$$

2.2 Weakest Precondition

Defining weakest precondition. The shared state interpreting predicate $S : \text{State} \rightarrow iProp$ for our Iris^C language is defined below, with threads local state s set to `STACK` (and initialized to \emptyset), as required for instantiating the parametric WP defined for a general class of `language`.

$$S(\sigma) \triangleq [\bullet \text{fmap}(\lambda v. (1, \text{ag} v), \sigma.\text{heap})]^{\text{HEAP}} * [\bullet \text{fmap}(\text{ag}, \sigma.\text{text})]^{\text{TEXT}}$$

$$\begin{aligned}
wp &\triangleq \mu wp. \lambda \mathcal{E}, (e, s), \Phi. \\
&(\exists v. \text{to_val}(e) = v \wedge \models_{\mathcal{E}} \Phi(v)) \vee \\
&\left(\text{to_val}(e) = \perp \wedge \right. \\
&\quad \forall \sigma. S(\sigma) \stackrel{\mathcal{E}}{\equiv} *^{\emptyset} \\
&\quad \text{red}(e, s, \sigma) * \triangleright \forall e', s', \sigma', \vec{e}. (e, s, \sigma) \rightsquigarrow (e', s', \sigma', (\vec{e})) \stackrel{\emptyset}{\equiv} *^{\mathcal{E}} \\
&\quad \left. S(\sigma') * wp(\mathcal{E}, (e', s'), \Phi) * \bigstar_{e'' \in \vec{e}} wp(\top, (e'', \text{init}_s), \lambda _ . \text{True}) \right)
\end{aligned}$$

Here are some conventions:

- If we leave away the mask \mathcal{E} , we assume it to be \top .
- Φ in post-condition might or might not take a value parameter, depending on the context.
- When we elide stack s in WP-rules, like $\text{wp } e \{ \Phi \}$, then the rules holds for all such WP with any same s . This is useful in making local WP rules looking less cluttered.

Laws of weakest precondition. The rules in figure 2.2 and figure 2.2 can all be derived.

$$\begin{array}{c}
\text{WP-VALUE} \\
\Phi(v) \vdash \text{wp}_{\mathcal{E}} v \{ \Phi \} \\
\\
\text{WP-STRONG-MONO} \\
\frac{\mathcal{E}_1 \subseteq \mathcal{E}_2}{(\forall v. \Phi(v) \Rightarrow_{\mathcal{E}_2} \Psi(v)) * \text{wp}_{\mathcal{E}_1} e \{ \Phi \} \vdash \text{wp}_{\mathcal{E}_2} e \{ \Psi \}} \\
\\
\text{WP-ATOMIC} \\
\frac{\text{atomic}(e)}{\mathcal{E}_1 \Rightarrow_{\mathcal{E}_2} \text{wp}_{\mathcal{E}_2} e \{ x. \mathcal{E}_2 \Rightarrow_{\mathcal{E}_1} P \} \vdash \text{wp}_{\mathcal{E}_1} e \{ x. P \}} \\
\\
\text{FUPD-WP} \\
\Rightarrow_{\mathcal{E}} \text{wp}_{\mathcal{E}} e \{ \Phi \} \vdash \text{wp}_{\mathcal{E}} e \{ \Phi \} \\
\\
\text{WP-FUPD} \\
\text{wp}_{\mathcal{E}} e \{ x. \Rightarrow_{\mathcal{E}} \Phi(x) \} \vdash \text{wp}_{\mathcal{E}} e \{ \Phi \}
\end{array}$$

Figure 2: General WP rules

2.3 Soundness

The soundness of WP-style program is proven by showing that it is *adequate*:

Lemma 2.1. *For all $e, s, \sigma, \Phi : \text{Val} \rightarrow \text{Prop}$,*

$$\begin{aligned}
& \text{True} \vdash \text{wp}_{\top} e \{ \Phi \} \Rightarrow \\
& (\forall v, s', \sigma'. (e, s, \sigma) \rightsquigarrow (v, s', \sigma') \Rightarrow \Phi(v)) \wedge \\
& (\forall e', \sigma'. (e, s, \sigma) \rightsquigarrow (e', s', \sigma') \Rightarrow (\exists v. e' = v) \vee \text{red}(e', s', \sigma'))
\end{aligned}$$

$$\begin{array}{c}
\text{WP-SKIP} \\
\frac{}{\triangleright \mathbf{wp}_{\mathcal{E}} e \{ \Phi \} \vdash \mathbf{wp}_{\mathcal{E}} v; e \{ \Phi \}}
\end{array}
\quad
\begin{array}{c}
\text{WP-ASSIGN} \\
\frac{\vdash_{\text{typeof}} v : \tau' \quad \text{assign_compatible}(\tau \leftarrow \tau')}{\triangleright l \mapsto - : \tau * \triangleright (l \mapsto v : \tau * \Phi) \vdash \mathbf{wp}_{\mathcal{E}} l \leftarrow v \{ \Phi \}}
\end{array}$$

$$\begin{array}{c}
\text{WP-RET} \\
\mathbf{wp}_{\mathcal{E}} k'(v); KS \{ \Phi \} \vdash \mathbf{wp}_{\mathcal{E}} k(\mathbf{return} v); K_{\text{call}}(k') :: KS \{ \Phi \}
\end{array}$$

$$\begin{array}{c}
\text{WP-BIND} \\
\frac{\text{is_jump}(e) = \text{False}}{\mathbf{wp}_{\mathcal{E}} e \{ x. \mathbf{wp}_{\mathcal{E}} k(x) \{ \Phi \} \} \vdash \mathbf{wp}_{\mathcal{E}} k(e) \{ \Phi \}}
\end{array}
\quad
\begin{array}{c}
\text{WP-OP} \\
\frac{\llbracket \text{oplus} \rrbracket(v_1, v_2) = v'}{\Phi(v') \vdash \mathbf{wp}_{\mathcal{E}} v_1 \oplus v_2 \{ \Phi \}}
\end{array}$$

$$\begin{array}{c}
\text{WP-LOAD} \\
\triangleright l \mapsto_q v : \tau * \triangleright (l \mapsto_q v : \tau * \Phi(v)) \vdash \mathbf{wp}_{\mathcal{E}} !_{\tau} l \{ \Phi \}
\end{array}
\quad
\begin{array}{c}
\text{WP-SEQ} \\
\frac{\text{is_jump}(e_1) = \text{False}}{\mathbf{wp}_{\mathcal{E}} e_1 \{ v, \mathbf{wp}_{\mathcal{E}} v; e_2 \{ \Phi \} \} \vdash \mathbf{wp}_{\mathcal{E}} e_1; ; e_2 \{ \Phi \}}
\end{array}$$

$$\begin{array}{c}
\text{WP-ALLOC} \\
\frac{\vdash_{\text{typeof}} v : \tau}{(\forall l. l \mapsto v : \tau * \Phi(l)) \vdash \mathbf{wp}_{\mathcal{E}} \text{alloc}_{\tau}(v) \{ \Phi \}}
\end{array}
\quad
\begin{array}{c}
\text{WP-IF-TRUE} \\
\triangleright \mathbf{wp}_{\mathcal{E}} e_1 \{ \Phi \} \vdash \mathbf{wp}_{\mathcal{E}} \text{if}(\text{true}) \{ e_1 \} \text{else} \{ e_2 \} \{ \Phi \}
\end{array}$$

$$\begin{array}{c}
\text{WP-IF-FALSE} \\
\triangleright \mathbf{wp}_{\mathcal{E}} e_2 \{ \Phi \} \vdash \mathbf{wp}_{\mathcal{E}} \text{if}(\text{false}) \{ e_1 \} \text{else} \{ e_2 \} \{ \Phi \}
\end{array}
\quad
\begin{array}{c}
\text{WP-FST} \\
\triangleright \Phi(v_1) \vdash \mathbf{wp}_{\mathcal{E}} (v_1, v_2).1 \{ \Phi \}
\end{array}$$

$$\begin{array}{c}
\text{WP-SND} \\
\triangleright \Phi(v_2) \vdash \mathbf{wp}_{\mathcal{E}} (v_1, v_2).1 \{ \Phi \}
\end{array}$$

$$\begin{array}{c}
\text{WP-CALL} \\
f \mapsto_{\text{TEXT}} \text{Function}(\tau, ps, e) * \triangleright (\mathbf{wp}_{\mathcal{E}} e[ps/ls]; K_{\text{call}}(k) :: KS \{ \Phi \}) \vdash \mathbf{wp}_{\mathcal{E}} k(f(ls) : \tau); KS \{ \Phi \}
\end{array}$$

$$\begin{array}{c}
\text{WP-CAS-FAIL} \\
\frac{\vdash_{\text{typeof}} v_1 : \tau \quad \vdash_{\text{typeof}} v_2 : \tau \quad v_1 \neq v}{\triangleright l \mapsto_q v : \tau * \triangleright (l \mapsto_q v : \tau * \Phi(\text{false})) \vdash \mathbf{wp}_{\mathcal{E}} \text{CAS}(l, v_1, v_2) : \tau \{ \Phi \}}
\end{array}$$

$$\begin{array}{c}
\text{WP-CAS-SUC} \\
\frac{\vdash_{\text{typeof}} v_1 : \tau \quad \vdash_{\text{typeof}} v_2 : \tau}{\triangleright l \mapsto_q v_1 : \tau * \triangleright (l \mapsto_q v_2 : \tau * \Phi(\text{false})) \vdash \mathbf{wp}_{\mathcal{E}} \text{CAS}(l, v_1, v_2) : \tau \{ \Phi \}}
\end{array}$$

$$\begin{array}{c}
\text{WP-LET} \\
\triangleright \mathbf{wp}_{\mathcal{E}} e[x/v : \tau] \{ \Phi \} \vdash \mathbf{wp}_{\mathcal{E}} \text{let } x : \tau := v \text{ in } e \{ \Phi \}
\end{array}$$

$$\begin{array}{c}
\text{WP-FORK} \\
f \mapsto_{\text{TEXT}} \text{Function}(\tau, ps, e) * \triangleright \Phi(\mathbf{void}) * \triangleright (\mathbf{wp} e[ps/vs]; \emptyset \{ _ . \text{True} \}) \vdash \mathbf{wp}_{\mathcal{E}} \text{fork}(f(vs) : \tau); ks \{ \Phi \}
\end{array}$$

$$\begin{array}{c}
\text{WP-WHILE} \\
\triangleright \mathbf{wp}_{\mathcal{E}} \text{if}(c) \{ e; \text{continue} \} \text{else} \{ \text{break} \}; K_{\text{while}}(c, e, k) :: KS \{ \Phi \} \vdash \mathbf{wp}_{\mathcal{E}} k(\text{while}(c) \{ e \}); KS \{ \Phi \}
\end{array}$$

$$\begin{array}{c}
\text{WP-BREAK} \\
\mathbf{wp}_{\mathcal{E}} k'(\mathbf{void}); KS \{ \Phi \} \vdash \mathbf{wp}_{\mathcal{E}} k(\mathbf{break}); K_{\text{while}}(c, e, k') :: KS \{ \Phi \}
\end{array}$$

$$\begin{array}{c}
\text{WP-CONTINUE} \\
\mathbf{wp}_{\mathcal{E}} k'(\text{while}(c) \{ e \}); KS \{ \Phi \} \vdash \mathbf{wp}_{\mathcal{E}} k(\text{continue}); K_{\text{while}}(c, e, k') :: KS \{ \Phi \}
\end{array}$$

Figure 3: WP rules specific to Iris^C

3 Extensions

Besides the core language and its logic, we built several extension on top of it in a compatible way.

3.1 WP-ret

We extend the unary-exit WP into a binary-exit $\mathbf{wp}_{\mathcal{E}}^+ e \{\Phi; \Phi_{\text{ret}}\}$, by defining it as below:

$$\begin{aligned}
 \mathbf{wp}^+ &\triangleq \mu \mathbf{wp}^+. \lambda \mathcal{E}, e, \Phi, \Phi_{\text{ret}}. \\
 &(\exists v. \text{to_val}(e) = v \wedge \Phi(v)) \vee \\
 &(\text{to_val}(e) = \perp \wedge \exists e_h, K. e = K(e_h) \wedge (\\
 &\quad (\forall s. \text{is_jump}(e_h) = \text{False} * \mathbf{wp}_{\mathcal{E}} e_h; s \{v. \mathbf{wp}_{\mathcal{E}}^+ K(v) \{\Phi; \Phi_{\text{ret}}\}\}) \vee \\
 &\quad (\exists v. e_h = \text{return } v * \triangleright \Phi_{\text{ret}}(v)) \vee \\
 &\quad (\exists f, ps, ls, F. \\
 &\quad \quad f \mapsto_{\text{TEXT}} \text{Function}(_, ps, F) * \\
 &\quad \quad \triangleright (\mathbf{wp}_{\mathcal{E}}^+ F[ps/ls] \{-\text{False}; v. \mathbf{wp}_{\mathcal{E}}^+ K(v) \{\Phi; \Phi_{\text{ret}}\}\})) \\
 &\quad))
 \end{aligned}$$

And this definition supports the following inference rules:

$$\begin{aligned}
 &\text{WPR-VALUE} \quad \Phi(v) \vdash \mathbf{wp}_{\mathcal{E}}^+ v \{\Phi; \Phi_{\text{ret}}\} & \text{WPR-RET} \quad \Phi_{\text{ret}}(v) \vdash \mathbf{wp}_{\mathcal{E}}^+ \text{return } v \{\Phi; \Phi_{\text{ret}}\} \\
 &\text{WPR-BIND} \quad \mathbf{wp}_{\mathcal{E}}^+ e \{x. \mathbf{wp}_{\mathcal{E}}^+ k(x) \{\Phi; \Phi_{\text{ret}}\}; \Phi_{\text{ret}}\} \vdash \mathbf{wp}_{\mathcal{E}}^+ k(e) \{\Phi; \Phi_{\text{ret}}\} \\
 &\text{WPR-SEQ} \quad \mathbf{wp}_{\mathcal{E}}^+ e_1 \{x. \mathbf{wp}_{\mathcal{E}}^+ x; e_2 \{\Phi; \Phi_{\text{ret}}\}; \Phi_{\text{ret}}\} \vdash \mathbf{wp}_{\mathcal{E}}^+ e_1; e_2 \{\Phi; \Phi_{\text{ret}}\} \\
 &\text{WPR-CALL} \quad f \mapsto_{\text{TEXT}} \text{Function}(\tau, ps, e) * \triangleright (\mathbf{wp}_{\mathcal{E}}^+ e[ps/ls] \{-\text{False}; \Phi\}) \vdash \mathbf{wp}_{\mathcal{E}}^+ k(f(ls) : \tau) \{\Phi; \Phi_{\text{ret}}\} \\
 &\text{WPR-OP} \quad \frac{\llbracket \text{oplus} \rrbracket(v_1, v_2) = v'}{\Phi(v') \vdash \mathbf{wp}_{\mathcal{E}}^+ v_1 \oplus v_2 \{\Phi; \Phi_{\text{ret}}\}} \\
 &\text{WP-CALL-R} \quad \frac{f \mapsto_{\text{TEXT}} \text{Function}(\tau, ps, e) * \triangleright \mathbf{wp}_{\mathcal{E}}^+ e[ps/ls] \{-\text{False}; v. \mathbf{wp}_{\mathcal{E}} k(v); KS \{\Phi\}\}}{\mathbf{wp}_{\mathcal{E}} k(f(ls) : \tau); KS \{\Phi\}}
 \end{aligned}$$

Note how WP-CALL, WP-RET, WP-SEQ, and WP-BIND are simplified in their new, corresponding versions, also the fact that we can recover local evaluation like WP-OP trivially (and we won't duplicate too much here).

We also features a rule to compose two styles together freely: WP-CALL-R.

3.2 Array

Using the most primitive form of aggregated structure, product type, we can easily derive array (implemented) or even **struct** in future work.

“A pointer p pointing to an array vs of n elements which are τ -typed” is expressed with:

$$p \mapsto_q vs : \tau^n$$

In implementation, vs is **varray** (**l:list val**), and τ^n is **tyarray** (**t:type**) (**n:nat**).

We also have an intermediate representation of a slice of array:

$$p \mapsto_q [v_i : \tau, \dots, v_{i+l-1} : \tau]$$

The first form is easier for allocation, since it is defined as a unified aggregate structure; but the second form is easier for use, for example, splitting or indexing. We have the following lemmas proven in Coq:

SPLIT-SLICE

$$p \mapsto_q [v_i : \tau, \dots, v_{i+l_1-1} : \tau] * p \mapsto_q [v_{i+l_1} : \tau, \dots, v_{i+l_1+l_2-1} : \tau] \equiv p \mapsto_q [v_i : \tau, \dots, v_{i+l_1+l_2-1} : \tau]$$

INDEX-SPEC

$$\frac{i < n}{\{p \mapsto_q vs : \tau^n\} !_{\tau} p + i \{v.p \mapsto_q vs : \tau^n * vs !! i = \mathbf{Some}(v)\}}$$

4 Automation

We also developed some basic tactics for automatically solving the goals, mostly related to our new language, some enhancing what Iris provides.

4.1 Tactics for “Symbolic Execution”

Similar to what exists in Iris’s heap-lang, we also provide convenient tactics including:

- **wp_bind** <p>: bind to a head term, or a term containing the head term, with pattern **p**
- **wp_assign**: make a step by evaluating assignment’s head term $l \leftarrow v$ (it also shifts following statements by applying **wp_seq** repetitively beforehand)
- **wp_load**, **wp_alloc**, **wp_op**, **wp_fst**, **wp_snd**, **wp_cas_fail**, **wp_cas_suc**, **wp_let** are similar to the one above
- **wp_skip**: skip over any value before a sequencing operator
- **wp_run**: keep executing as long as it can, which feels like doing symbolic execution automatically
- **wp_ret**: return if the head expression is already value

4.2 Tactics for Algebraic Simplification

gmap RA. **gmap_simplify**: simplify expressions involving **gmap** based on some algebraic rules

Refine RA. **rewrite_op_cfgs**: Simplify product of the configuration list component.

4.3 Misc Tactics

Inversion. **inversion_estep**, **inversion_cstep_as**, and **inversion_jstep_as** are all designed to automatically match stepping relation assumption and give proper names to the important results produced by **inversion**.

Evaluation Context Equality. **gen_eq** <H> <E1> <E2> <KS> can generate equalities between expressions **E1** and **E2**, and between **KS** and empty context. It assumes that both **E1** and **E2** are normal form, and there is equality between the filled ones: **fill_ectxs E1 KS = E2**.

5 Refinement

5.1 Spec State and Spec Code

Considering a STS composed of spec code \tilde{c} (as below), spec state $\sigma : [X \hookrightarrow v]$, and semantic rules $(\tilde{c}, \tilde{\sigma}) \rightsquigarrow_{spec} (\tilde{c}', \tilde{\sigma}')$ (as well as derived $(\tilde{c}, \tilde{\sigma}) \rightsquigarrow_{spec}^* (\tilde{c}', \tilde{\sigma}')$).

$$\tilde{c} : \text{SPECCode} ::= \text{done}(v?) \mid \text{rel}(r : \tilde{\sigma} \rightarrow v? \rightarrow \tilde{\sigma} \rightarrow \text{Prop})$$

5.2 Refinement RA

We defined a new RA **REFINE** to capture the history of spec code execution:

$$\begin{aligned} \text{VIEW} &\triangleq \text{master} \mid \text{snapshot} \\ \text{REFINE} &\triangleq \text{VIEW} \times [\text{SPECState} \times \text{SPECCode}] \end{aligned}$$

The validity of **REFINE**:

$$\sqrt{(\text{refine}(v, \emptyset))} \quad \sqrt{(\text{refine}(v, [(\tilde{\sigma}, \tilde{c})]))} \quad \frac{(\tilde{c}, \tilde{\sigma}) \rightsquigarrow_{spec} (\tilde{c}', \tilde{\sigma}') \quad \sqrt{(\text{refine}(v, [(\tilde{\sigma}, \tilde{c}) :: cs]))}}{\sqrt{(\text{refine}(v, [(\tilde{\sigma}', \tilde{c}') :: (\tilde{\sigma}, \tilde{c}) :: cs]))}}$$

The multiplication of **VIEW** and **REFINE** is defined as:

$$\begin{aligned} \text{master} \cdot _ &\triangleq \text{master} \\ _ \cdot \text{master} &\triangleq \text{master} \\ \text{snapshot} \cdot \text{snapshot} &\triangleq \text{snapshot} \\ \text{refine}(v_1, cs_1) \cdot \text{refine}(v_2, cs_2) &\triangleq \begin{cases} \text{refine}(v_1 \cdot v_2, cs_1) & |cs_1| \geq |cs_2| \\ \text{refine}(v_1 \cdot v_2, cs_2) & |cs_1| < |cs_2| \end{cases} \end{aligned}$$

The disjointness of multiplication is refined as:

$$\begin{aligned} \frac{\exists cs'. cs_1 \uparrow\uparrow cs = cs_2 \vee \exists cs'. cs_2 \uparrow\uparrow cs = cs_1}{\text{refine}(\text{snapshot}, cs_1) \# \text{refine}(\text{snapshot}, cs_2)} \quad &\text{refine}(\text{snapshot}, cs_1) \# \text{refine}(\text{master}, cs_1 \uparrow\uparrow cs_1) \\ &\text{refine}(\text{master}, cs_1 \uparrow\uparrow cs_1) \# \text{refine}(\text{snapshot}, cs_1) \end{aligned}$$

In the end, **REFINE** can be proven to be a CMRA with an unit element $\text{refine}(\text{snapshot}, \emptyset)$.

5.3 Refinement Ghost State, Invariant and Rules

The refinement ghost state **refineG** contains three part: one for *refineM*, and two for *paired* ownership of **SPECCode** and **SPECState**. The predicates are defined as below:

$$\begin{aligned}
\text{sstate}(\tilde{\sigma}) &\triangleq [\![\frac{1}{2}, \mathbf{ag}\tilde{\sigma}]\!]^{\text{SPECSTATE}} \\
\text{scode}(\tilde{c}) &\triangleq [\![\frac{1}{2}, \mathbf{ag}\tilde{c}]\!]^{\text{SPEC CODE}} \\
\text{master}'(cs) &\triangleq [\![\mathbf{refine}(\mathbf{master}, cs)\!]^{\text{REFINE}} \\
\text{master}(c) &\triangleq \exists cs. [\![\mathbf{refine}(\mathbf{master}, c :: cs)\!]^{\text{REFINE}} \\
\text{snapshot}'(cs) &\triangleq [\![\mathbf{refine}(\mathbf{snapshot}, cs)\!]^{\text{REFINE}} \\
\text{snapshot}(c) &\triangleq \exists cs. [\![\mathbf{refine}(\mathbf{snapshot}, c :: cs)\!]^{\text{REFINE}}
\end{aligned}$$

Then we define refinement invariant:

$$I_{\text{REFINE}} \triangleq \exists \tilde{\sigma}, \tilde{c}. \text{sstate}(\tilde{\sigma}) * \text{scode}(\tilde{c}) * \text{master}(\tilde{\sigma}, \tilde{c})$$

With this, we can give refinement style proofs for certain kernel APIs, using the following derived rules:

$$\frac{\text{SPEC-UPDATE} \quad (\tilde{c}, \tilde{\sigma}) \rightsquigarrow_{\text{spec}} (\tilde{c}', \tilde{\sigma}')}{\boxed{I_{\text{REFINE}}}]^{\iota} * \text{sstate}(\tilde{\sigma}) * \text{scode}(\tilde{c}) \vdash \Rightarrow \boxed{I_{\text{REFINE}}}]^{\iota} * \text{sstate}(\tilde{\sigma}') * \text{scode}(\tilde{c}') * \text{snapshot}(\tilde{\sigma}', \tilde{c}')}$$

5.4 Soundness

First we define the simulation relationship between the *Expr* and *SPEC CODE*:

$$\frac{v \Downarrow \mathbf{done}(v) \quad (e, \sigma) \rightsquigarrow (e', \sigma') \quad (\tilde{c}, \tilde{\sigma}) \rightsquigarrow_{\text{spec}}^* (\tilde{c}', \tilde{\sigma}')}{e \Downarrow c}$$

Our final soundness lemma, which is proved in Coq, is like below (the step indexing part is simplified away):

$$\frac{(e, \sigma) \rightsquigarrow_c^* (v, \sigma')}{\text{world}(\sigma) * (\boxed{I_{\text{REFINE}}}]^{\iota} * \text{sstate}(\tilde{\sigma}) * \text{scode}(\tilde{c})) * \mathbf{wp} \, e \{v. \text{sstate}(\tilde{\sigma}') * \text{scode}(\mathbf{done}(v))\} \vdash v \Downarrow c}$$

6 Misc

This section contained some key formal developments claimed but not yet mechanized in Coq for reference.

6.1 Basic Properties of Evaluation Context

Our expression $Expr$ is a recursively defined algebraic data type, which can be generalized into the following form:

$$e : Expr ::= Expr_1(A_1, e^{r_1}) \mid \dots \mid Expr_n(A_n, e^{r_n}) \mid v$$

Here, $Expr_i$ is tag for i -th class of expression, A_i is its arbitrary non-recursive payload, and e^{r_i} means it has $r_i \in \mathbb{N}$ recursive occurrences.

It is apparent that ECTX is a direct translation of $Expr$ plus some ordering considerations, though in actual Coq development we don't mechanize this fact. But we will make it explicit here to support some stronger claims than what we can do in Coq.

For some abstract $Expr$ like above, its ECTX should be a sum of sub-ECTX $_i$ for each $Expr_i$. When $r_i = 0$, ECTX $_i$ doesn't exist, now we consider $r_i > 0$, define

$$k_i : ECTX_i ::= ECTX_{i1}(A_i, e^{r_i-1}) \mid ECTX_{i2}(A_i, v^1, e^{r_i-2}) \mid \dots \mid ECTX_{ir_i}(A_i, v^{r_i-1})$$

Theorem 6.1. For any $e, e' : Expr$ and $k_{im}, k_{jn} : ECTX$,

$$k_{im}(e) = k_{jn}(e') \vdash i = j$$

Proof. Trivial. □

Theorem 6.2. For any $e, e' : Expr$ and $k_{im}, k_{in} : ECTX_i$,

$$k_{im}(e) = k_{in}(e') \vdash (m = n \wedge e = e') \vee (m \neq n \wedge (\exists v. \text{to_val}(e) = v \vee \exists v. \text{to_val}(e) = v))$$

Proof. When $m = n$, by injectivity; When $m \neq n$, we can expand the equation like below without loss of generality:

$$\begin{aligned} Expr_i(A_i, v_1, \dots, v_{m-1}, e, e_{m+1}, \dots, e_{r_i-1}) = \\ Expr_i(A'_i, v'_1, \dots, v'_{m-1}, v'_m, \dots, e', \dots) \end{aligned}$$

So $e = v'_m$ by injectivity. □

As you may observe, the expression space spanned by $k(e)$ for any k, e is not the entire expression space as an ADT. Instead, it is a subset of $Expr$ which represents well-formed ones that can appear as an immediate form according to some well-defined evaluation order. We call such e is *well-formed*.

6.2 Axioms about Evaluation Context based Semantics

Lemma 6.3 (estep is well-formed).

$$\forall e, \sigma, e', \sigma', \Gamma, es. (e, \sigma) \rightsquigarrow_{local}^\Gamma (e', \sigma') es \Rightarrow wellformed(e)$$

Proof. TODO □

Lemma 6.4 (cstep is well-formed).

$$\forall e, s, \sigma, e', s', \sigma', \Gamma, es. (e, s, \sigma) \rightsquigarrow (e', s', \sigma', (es)) \Rightarrow \text{wellformed}(e)$$

Proof. TODO □

6.3 Other Facts

Lemma 6.5. wp^+ is contractive.

Proof. Observe in the definition 3.1 that there are only two recursive uses of wp^+ :

1. In the second branch of non-value case, we use wp^+ under Φ of $\text{wp}_{\mathcal{E}} e_h \{\Phi\}$, which means that,

$$\text{wp}_{\mathcal{E}} e_h \{\dots \text{wp}^+ \dots\} \stackrel{n}{=} \text{wp}_{\mathcal{E}} e_h \{\dots \text{wp}'^+ \dots\}$$

will hold if $\text{wp}^+ \stackrel{n+1}{=} \text{wp}'^+$, which is satisfied by assumption.

2. In the third branch of non-value case, we use wp^+ under a \triangleright , which is trivial to prove by `f_contractive`.

□

6.4 Admitted Facts

There are still one or more loopholes that exist behind the documented formalization. But note that all of them are apparently true *and* harmless to leave unproven for now.

- In `lang.v`, lemma `same_type_encode_inj`'s fourth case about `int32` is admitted. Essential, this says something about encoding `int32` value with four bytes. We don't prove it now because there are not enough lemmas provided by machine integer library `Integers.v`.
- In `lang.v`, lemma `instantiate_let_preserves_not_jmp` states a trivial truth about a complex computational process.