

Iris^c Documentation

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Abstract

This document describes the control-flow based language verification framework based on the Iris program logic. The Coq source is in <https://github.com/izgzhen/iris-c-coq>. We omitted many details on Iris itself, which can be found in the document and Coq formalization here <https://gitlab.mpi-sws.org/FP/iris-coq/>.

Contents

1	Language	3
1.1	Definitions	3
1.2	Semantics	4
1.3	Type System and Lexical Environment	4
2	Program Logic	7
2.1	Ghost Resource	7
2.2	Weakest Precondition	7
2.3	Soundness	8
3	Extensions	10
3.1	Array	10
4	Automation	11
4.1	Tactics for “Symbolic Execution”	11
4.2	Tactics for Algebraic Simplification	11
4.3	Misc Tactics	11
5	Refinement	12
5.1	Spec State and Spec Code	12
5.2	Refinement RA	12
5.3	Refinement Ghost State, Invariant and Rules	12
5.4	Soundness	13

1 Language

1.1 Definitions

Syntax. The language is a simplified version of C. It mainly consists of *Expr* (expressions), *Type* (types) and *Val* (values).

$$\begin{aligned}
\tau : \textit{Type} &::= \tau_{\text{void}} \mid \tau_{\text{null}} \mid \tau_{\text{int8}} \mid \tau_{\text{int32}} \mid \text{Ptr}(\tau) \mid \tau \times \tau \\
v : \textit{Val} &::= \text{void} \mid \text{null} \mid i \in [0, 2^8) \mid i \in [0, 2^{32}) \mid l \mid (v, v) \\
e : \textit{Expr} &::= v \mid x \mid e \oplus e \mid !_{\tau} e \mid \&e \mid \text{CAS}(e, e, e) : \tau \mid e.1 \mid e.2 \mid (e, e) \mid \\
&\quad e \leftarrow e \mid \text{if}(e) \{e\} \text{else} \{e\} \mid \text{while}(e) \{e\} \mid \text{break} \mid \text{continue} \mid \\
&\quad \text{return } e \mid f(e_1, (\dots, e_n)) : \tau \mid e;; e \mid \text{alloc}_{\tau}(e) \mid \text{fork}(f()) : \tau
\end{aligned}$$

We also have following definitions:

- block address $b \in \mathbb{N}$
- offset $o \in \mathbb{Z}^+$
- Memory address $l : \textit{Addr} \triangleq b \# o$
- Program $\Gamma : \text{TEXT} \triangleq [f \mapsto \textit{Function}]$ is a list of functions indexed by names
- Function $F : \textit{Function} \triangleq (\tau_{\text{ret}} \times [x_1 : \tau_1, \dots] \times e)$ consists of return type, parameter declarations and function body.

(NOTE) Compared with C, there are several notable differences:

1. We currently don't support **for** loop and **switch**
2. C differentiates statements and expressions (so do many other program logics). For simplicity and expressivity, we merged them together into a coherent expression *Expr* instead
3. We support declarations of local variables through a syntactic sugar based on the core language (definition of default : $\textit{Type} \rightarrow \textit{Val}$ is trivial)

$$\text{let } x : \tau \text{ in } e \triangleq \text{let } x : \tau := \text{alloc}_{\tau}(\text{default}(\tau)) \text{ in } e$$

(NOTE) We sometimes use $c : \textit{Expr}$ to denote *condition* expression in **if** and **while**.

Evaluation Context. To make the evaluation order explicit and reflected to the logic through the bind rule, we define the *evaluation context*:

$$\begin{aligned}
K : \text{ECTX} &\triangleq \bullet \oplus e \mid v \oplus \bullet \mid !_{\tau} \bullet \mid \&\bullet \mid \bullet.1 \mid \bullet.2 \mid (\bullet, e) \mid (v, \bullet) \mid \\
&\quad \text{CAS}(\bullet, e, e) : \tau \mid \text{CAS}(l, \bullet, e) : \tau \mid \text{CAS}(l, v, \bullet) : \tau \mid \\
&\quad \bullet \leftarrow e \mid l \leftarrow \bullet \mid \text{if}(\bullet) \{e\} \text{else} \{e\} \mid \\
&\quad \text{return } \bullet \mid f(\bullet) : \tau \mid \text{alloc}_{\tau}(\bullet) \mid \bullet; e
\end{aligned}$$

1.2 Semantics

Model. First define

- Byte-size value

$$v_{\text{byte}} : \text{Val}_{\text{byte}} \triangleq \text{\textit{i}} \mid \text{\textit{i}} \in [0, 2^8) \mid l_{\{0|1|2|3\}} \mid \text{null}$$

- Heap $h : \text{HEAP} \triangleq [l \mapsto v_{\text{byte}}]$
- Text $\Gamma : \text{TEXT} \triangleq [f \mapsto \text{Function}]$
- Continuation $k : \text{CONT} \triangleq [\text{CTX}]$
- Context $K : \text{CTX} \triangleq K_{\text{while}}(c, e, k) \mid K_{\text{call}}(k, \epsilon)$
- Stack $s : \text{STACK} \triangleq [\text{CTX}]$
- Environment: $\epsilon : [x \mapsto (\tau, v)]$
- Shared state

$$\sigma : \text{State} \triangleq (\text{heap} : \text{HEAP}, \text{text} : \text{TEXT})$$

- Local state

$$\sigma_l : \text{LocalState} \triangleq (\text{stack} : \text{STACK}, \text{env} : \text{Env})$$

(NOTE) You may notice the difference between Val and Val_{byte} , it is intended to create a layer of abstraction for easier manipulation of spatial assertions and also for a clean, unified language syntax.

Small-Step Operational Semantics. We define the small-step semantics (see figure 1.2) for three sub-semantics:

- local reduction $(\epsilon, e, h) \rightsquigarrow_{\text{local}}^{\Gamma} (\epsilon', e', h', (t_1, t_2, \dots))$ (when the set of forked-off threads t_1, \dots is empty, we abbreviate it as $(\epsilon, e, h) \rightsquigarrow_{\text{local}}^{\Gamma} (\epsilon', e', h')$)
- *jump* reductions $(\epsilon, e, s) \rightsquigarrow_{\text{jump}}^{\Gamma} (\epsilon', e', s')$
- *while* reductions $(e, s) \rightsquigarrow_{\text{while}} (e', s')$

Then we combine them together into $(\Gamma, \epsilon, e) \rightsquigarrow (s, \sigma, \epsilon, e', (s', \sigma'))_{t_1, \dots}$ point-wise.

1.3 Type System and Lexical Environment

Local Typing Rules. The types are defined in §1.1, and all values in v can be *locally* typed trivially (since v is introduced to reflect type structure in some sense). Nevertheless, due to the fact that the language of study is weakly-typed in the vein of C, we still have some “weird” rules worth documenting.

Here we define local typing judgment $\vdash_{\text{typeof}} v : \tau$ for values.

$$\begin{array}{cccc}
\text{TYPEOF-VOID} & \text{TYPEOF-NULL} & \text{TYPEOF-INT8} & \text{TYPEOF-INT32} \\
\vdash_{\text{typeof}} \mathbf{void} : \tau_{\text{void}} & \vdash_{\text{typeof}} \mathbf{null} : \tau_{\text{null}} & \vdash_{\text{typeof}} i \in [0, 2^8) : \tau_{\text{int8}} & \vdash_{\text{typeof}} i \in [0, 2^{32}) : \tau_{\text{int32}} \\
\\
\text{TYPEOF-NULL-PTR} & \text{TYPEOF-PTR} & \text{TYPEOF-PROD} & \\
\forall \tau. \vdash_{\text{typeof}} \mathbf{null} : \text{Ptr}(\tau) & \forall \tau, l. \vdash_{\text{typeof}} l : \text{Ptr}(\tau) & \frac{\vdash_{\text{typeof}} v_1 : \tau_1 \quad \vdash_{\text{typeof}} v_2 : \tau_2}{\vdash_{\text{typeof}} (v_1, v_2) : \tau_1 \times \tau_2} &
\end{array}$$

Note that rule TYPEOF-NULL-PTR means that `null` can be of any pointer type, and TYPEOF-PTR means that a pointer can have *any* pointer type.

Lexical Environment Formally, the lexical environment for a function body consists of two parts: parameter bindings (free variables) and local bindings (introduced by `let`).

We model the C semantics that these two types of bindings are essentially the same, except that we initialize the parameter bindings by passed in nested value pair (note that it is in the value domain, rather than a Coq-level list, which simplifies the verification), and initialize the local bindings by placeholder values.

$$\begin{array}{c}
\text{ES-BINOP} \\
\frac{\llbracket \text{oplus} \rrbracket(v_1, v_2) = v'}{(\epsilon, v_1 \oplus v_2, h) \rightsquigarrow_{local}^{\Gamma} (\epsilon, v', h)} \\
\\
\text{ES-FST} \\
(\epsilon, (v_1, v_2).1, h) \rightsquigarrow_{local}^{\Gamma} (\epsilon, v_1, h) \\
\\
\text{ES-ASSIGN} \\
(\epsilon, l \leftarrow v, h) \rightsquigarrow_{local}^{\Gamma} (\epsilon, \mathbf{void}, h[l \mapsto \text{encode}(v)]) \\
\\
\text{ES-ALLOC} \\
\frac{\vdash_{\text{typeof}} v : \tau \quad \forall o'. h(b, o') = \perp}{(\epsilon, \mathbf{alloc}_{\tau}(v), h) \rightsquigarrow_{local}^{\Gamma} (\epsilon, b\#o, h[b\#o \mapsto v])} \\
\\
\text{ES-IF-FALSE} \\
(\epsilon, \mathbf{if}(\mathbf{false})\{e_1\}\mathbf{else}\{e_2\}, h) \rightsquigarrow_{local}^{\Gamma} (\epsilon, e_2, h) \\
\\
\text{ES-IF-TRUE} \\
(\epsilon, \mathbf{if}(\mathbf{true})\{e_1\}\mathbf{else}\{e_2\}, h) \rightsquigarrow_{local}^{\Gamma} (\epsilon, e_1, h) \\
\\
\text{ES-BIND'} \\
\frac{\text{is_jump}(e) = \mathbf{False} \quad (\epsilon, e, h) \rightsquigarrow_{local}^{\Gamma} (\epsilon', e', h')}{(\epsilon, (k :: ks)e, h) \rightsquigarrow_{local}^{\Gamma} (\epsilon', (k :: ks)e', h')} \\
\\
\text{ES-CAS-FAIL} \\
\frac{\vdash_{\text{typeof}} v_1 : \tau \quad \vdash_{\text{typeof}} v_2 : \tau \quad \vdash_{\text{typeof}} v : \tau \quad h \vdash l \mapsto \text{encode}(v) \quad v_1 \neq v}{(\epsilon, \mathbf{CAS}(l, v_1, v_2) : \tau, h) \rightsquigarrow_{local}^{\Gamma} (\epsilon, \mathbf{false}, h)} \\
\\
\text{ES-CAS-SUC} \\
\frac{\vdash_{\text{typeof}} v_1 : \tau \quad \vdash_{\text{typeof}} v_2 : \tau \quad h \vdash l \mapsto \text{encode}(v_1)}{(\epsilon, \mathbf{CAS}(l, v_1, v_2) : \tau, h) \rightsquigarrow_{local}^{\Gamma} (\epsilon, \mathbf{true}, h[l \mapsto \text{encode}(v_2)])} \\
\\
\text{ES-FORK} \\
\frac{\Gamma(f) = \text{Function}(\tau, [], e)}{(\epsilon, \mathbf{fork}(f() : \tau), h) \rightsquigarrow_{local}^{\Gamma} (\epsilon, \mathbf{void}, h, (\{e[ps/vs]\}))} \\
\\
\text{JS-RETE} \\
\frac{}{(\epsilon, k'(\mathbf{return} v), KS' ++ K_{call}(k, :) : KS) \rightsquigarrow_{jump}^{\Gamma} (\epsilon, k(v), KS)} \\
\\
\text{JS-CALL} \\
\frac{\Sigma(f) = \text{Function}(\tau, ps, e)}{(\epsilon, k(f(vs) : \tau), KS) \rightsquigarrow_{jump}^{\Gamma} (\epsilon, e[ps/vs], K_{call}(k, :) : KS)} \\
\\
\text{WS-WHILE} \\
(k(\mathbf{while}(c)\{e\}), KS) \rightsquigarrow_{while} (\mathbf{if}(c)\{e; \mathbf{continue}\}\mathbf{else}\{\mathbf{break}\}, K_{while}(c, e, k) :: KS) \\
\\
\text{WS-BREAK} \\
\frac{\forall K \in KS'. K \neq K_{while}(_, _, _)}{(k'(\mathbf{break}), KS' ++ K_{while}(c, e, k) :: KS) \rightsquigarrow_{while} (k(\mathbf{void}), KS)} \\
\\
\text{WS-CONTINUE} \\
\frac{\forall K \in KS'. K \neq K_{while}(_, _, _)}{(k'(\mathbf{continue}), KS' ++ K_{while}(c, e, k) :: KS) \rightsquigarrow_{while} (k(\mathbf{while}(c)\{e\}), KS)}
\end{array}$$

Figure 1: Semantics rules

2 Program Logic

This section describes how to build a program logic for the C language (*c.f.* §1) on top of Iris..

2.1 Ghost Resource

Using the standard Iris assertions and the ownership of ghost heap resource, we can define some basic custom assertions:

$$\begin{aligned}
l \mapsto_q v : t &\triangleq l \mapsto_q \text{encode}(v) \wedge \vdash_{\text{typeof}} v : t \\
l \mapsto v : t &\triangleq l \mapsto_1 v : t \\
l \mapsto_q - : t &\triangleq \exists v. l \mapsto_q v : t \\
f \mapsto_{\text{TEXT}} F &\triangleq [\circ [f \leftarrow \text{ag} F]]^{\text{TEXT}}
\end{aligned}$$

2.2 Weakest Precondition

Defining weakest precondition. The shared state interpreting predicate $S : \text{State} \rightarrow iProp$ for our Iris^C language is defined below, with threads local state s set to $\text{STACK} \times \text{Env}$ (and initialized to (\emptyset, \emptyset)), as required for instantiating the parametric WP defined for a general class of `language`.

$$S(\sigma) \triangleq [\bullet \text{fmap}(\lambda v. (1, \text{ag} v), \sigma.\text{heap})]^{\text{HEAP}} * [\bullet \text{fmap}(\text{ag}, \sigma.\text{text})]^{\text{TEXT}}$$

$$\begin{aligned}
wp &\triangleq \mu wp. \lambda \mathcal{E}, (e, s), \Phi. \\
&(\exists v. \text{to_val}(e) = v \wedge \models_{\mathcal{E}} \Phi(v)) \vee \\
&\left(\text{to_val}(e) = \perp \wedge \right. \\
&\quad \forall \sigma. S(\sigma) \overset{\mathcal{E}}{\equiv} *^{\emptyset} \\
&\quad \text{red}(e, s, \sigma) * \triangleright \forall e', s', \sigma', \vec{e}. (e, s, \sigma) \rightsquigarrow (e', s', \sigma', (\vec{e})) \overset{\emptyset}{\equiv} *^{\mathcal{E}} \\
&\quad \left. S(\sigma') * wp(\mathcal{E}, (e', s'), \Phi) * \bigstar_{e'' \in \vec{e}} wp(\top, (e'', \text{init}_s), \lambda _ . \text{True}) \right)
\end{aligned}$$

Here are some conventions:

- If we leave away the mask \mathcal{E} , we assume it to be \top .
- Φ in post-condition might or might not take a value parameter, depending on the context.
- When we elide stack s in WP-rules, like $\text{wp } e \{ \Phi \}$, then the rules holds for all such WP with any same s . This is useful in making local WP rules look less cluttered.

Laws of weakest precondition. The rules in figure 2.2 and figure 2.2 can all be derived.

$$\begin{array}{c}
\text{WP-VALUE} \\
\Phi(v) \vdash \text{wp}_{\mathcal{E}} v, s \{ \Phi \} \\
\\
\text{WP-ATOMIC} \\
\frac{\text{atomic}(e)}{\varepsilon_1 \Vdash^{\varepsilon_2} \text{wp}_{\varepsilon_2} e, s \left\{ x. \varepsilon_2 \Vdash^{\varepsilon_1} P \right\} \vdash \text{wp}_{\varepsilon_1} e, s \{ x. P \}} \\
\\
\text{WP-STRONG-MONO} \\
\frac{\mathcal{E}_1 \subseteq \mathcal{E}_2}{(\forall v. \Phi(v) \Vdash_{\mathcal{E}_2} \Psi(v)) * \text{wp}_{\mathcal{E}_1} e \{ \Phi \} \vdash \text{wp}_{\mathcal{E}_2} e \{ \Psi \}} \\
\\
\text{FUPD-WP} \\
\frac{}{\Vdash_{\mathcal{E}} \text{wp}_{\mathcal{E}} e \{ \Phi \} \vdash \text{wp}_{\mathcal{E}} e \{ \Phi \}} \\
\\
\text{WP-FUPD} \\
\text{wp}_{\mathcal{E}} e \{ x. \Vdash_{\mathcal{E}} \Phi(x) \} \vdash \text{wp}_{\mathcal{E}} e \{ \Phi \}
\end{array}$$

Figure 2: General WP rules

$$\begin{array}{c}
\text{WP-SKIP} \\
\triangleright \text{wp}_{\mathcal{E}} e, s \{ \Phi \} \vdash \text{wp}_{\mathcal{E}} v; e, s \{ \Phi \} \\
\\
\text{WP-ASSIGN} \\
\frac{\vdash_{\text{typeof}} v : \tau' \quad \text{assign_compatible}(\tau \leftarrow \tau')}{\triangleright l \mapsto - : \tau * \triangleright (l \mapsto v : \tau * \Phi) \vdash \text{wp}_{\mathcal{E}} l \leftarrow v, s \{ \Phi \}} \\
\\
\text{WP-RET} \\
\text{wp}_{\mathcal{E}} k'(v), (KS, \Omega_1) \{ \Phi \} \vdash \text{wp}_{\mathcal{E}} k(\text{return } v), (K_{\text{call}}(k', \Omega_1) :: KS, \Omega_2) \{ \Phi \} \\
\\
\text{WP-BIND} \\
\frac{\text{is_jump}(e) = \text{False}}{\text{wp}_{\mathcal{E}} e, s \{ x. \text{wp}_{\mathcal{E}} k(x), s \{ \Phi \} \} \vdash \text{wp}_{\mathcal{E}} k(e), s \{ \Phi \}} \\
\\
\text{WP-OP} \\
\frac{\llbracket \text{oplus} \rrbracket(v_1, v_2) = v'}{\Phi(v') \vdash \text{wp}_{\mathcal{E}} v_1 \oplus v_2 \{ \Phi \}} \\
\\
\text{WP-LOAD} \\
\triangleright l \mapsto_q v : \tau * \triangleright (l \mapsto_q v : \tau * \Phi(v)) \vdash \text{wp}_{\mathcal{E}} !_{\tau} l \{ \Phi \}
\end{array}$$

Figure 3: WP rules specific to Iris^c, part I

2.3 Soundness

The soundness of WP-style program is proven by showing that it is *adequate*:

Lemma 2.1. *For all $e, s, \sigma, \Phi : \text{Val} \rightarrow \text{Prop}$,*

$$\begin{aligned}
& \text{True} \vdash \text{wp}_{\top} e \{ \Phi \} \Rightarrow \\
& (\forall v, s', \sigma'. (e, s, \sigma, v) \rightsquigarrow (s', \sigma', \Rightarrow, \Phi)(v)) \wedge \\
& (\forall e', \sigma'. (e, s, \sigma, e') \rightsquigarrow (s', \sigma', \Rightarrow, ()) \exists v. e' = v) \vee \text{red}(e', s', \sigma')
\end{aligned}$$

$$\begin{array}{c}
\text{WP-SEQ} \\
\frac{\text{is_jump}(e_1) = \text{False}}{\text{wp}_{\mathcal{E}} e_1, s \{v, \text{wp}_{\mathcal{E}} v;; e_2, s \{\Phi\}\} \vdash \text{wp}_{\mathcal{E}} e_1;; e_2, s \{\Phi\}} \\
\\
\text{WP-ALLOC} \\
\frac{\vdash_{\text{typeof}} v : \tau}{(\forall l. l \mapsto v : \tau \multimap \Phi(l)) \vdash \text{wp}_{\mathcal{E}} \text{alloc}_{\tau}(v), s \{\Phi\}} \\
\\
\text{WP-IF-TRUE} \\
\triangleright \text{wp}_{\mathcal{E}} e_1, s \{\Phi\} \vdash \text{wp}_{\mathcal{E}} \text{if}(\text{true}) \{e_1\} \text{else} \{e_2\}, s \{\Phi\} \\
\\
\text{WP-IF-FALSE} \qquad \text{WP-FST} \\
\triangleright \text{wp}_{\mathcal{E}} e_2, s \{\Phi\} \vdash \text{wp}_{\mathcal{E}} \text{if}(\text{false}) \{e_1\} \text{else} \{e_2\}, s \{\Phi\} \qquad \triangleright \Phi(v_1) \vdash \text{wp}_{\mathcal{E}} (v_1, v_2).1, s \{\Phi\} \\
\\
\text{WP-SND} \\
\triangleright \Phi(v_2) \vdash \text{wp}_{\mathcal{E}} (v_1, v_2).1, s \{\Phi\} \\
\\
\text{WP-CALL} \\
f \mapsto_{\text{TEXT}} \text{Function}(\tau, ps, e) * \triangleright (\text{wp}_{\mathcal{E}} e[ps/ls], (K_{\text{call}}(k, \Omega') :: KS, \Omega) \{\Phi\}) \vdash \text{wp}_{\mathcal{E}} k(f(ls) : \tau), (KS, \Omega') \{\Phi\} \\
\\
\text{WP-CAS-FAIL} \\
\frac{\vdash_{\text{typeof}} v_1 : \tau \quad \vdash_{\text{typeof}} v_2 : \tau \quad v_1 \neq v}{\triangleright l \mapsto_q v : \tau * \triangleright (l \mapsto_q v : \tau \multimap \Phi(\text{false})) \vdash \text{wp}_{\mathcal{E}} \text{CAS}(l, v_1, v_2) : \tau, s \{\Phi\}} \\
\\
\text{WP-CAS-SUC} \\
\frac{\vdash_{\text{typeof}} v_1 : \tau \quad \vdash_{\text{typeof}} v_2 : \tau}{\triangleright l \mapsto_q v_1 : \tau * \triangleright (l \mapsto_q v_2 : \tau \multimap \Phi(\text{false})) \vdash \text{wp}_{\mathcal{E}} \text{CAS}(l, v_1, v_2) : \tau, s \{\Phi\}} \\
\\
\text{WP-FORK} \\
f \mapsto_{\text{TEXT}} \text{Function}(\tau, \emptyset, e) * \triangleright \Phi(\text{void}) * \triangleright (\text{wp } e, (\emptyset, \emptyset) \{_.\text{True}\}) \vdash \text{wp}_{\mathcal{E}} \text{fork}(f() : \tau) vs, s \{\Phi\} \\
\\
\text{WP-WHILE} \\
\triangleright \text{wp}_{\mathcal{E}} \text{if}(c) \{e; \text{continue}\} \text{else} \{\text{break}\}, (K_{\text{while}}(c, e, k) :: KS, \Omega) \{\Phi\} \vdash \text{wp}_{\mathcal{E}} k(\text{while}(c) \{e\}), (KS, \Omega) \{\Phi\} \\
\\
\text{WP-BREAK} \\
\text{wp}_{\mathcal{E}} k'(\text{void}), (KS, \Omega) \{\Phi\} \vdash \text{wp}_{\mathcal{E}} k(\text{break}), (K_{\text{while}}(c, e, k') :: KS, \Omega) \{\Phi\} \\
\\
\text{WP-CONTINUE} \\
\text{wp}_{\mathcal{E}} k'(\text{while}(c) \{e\}), (KS, \Omega) \{\Phi\} \vdash \text{wp}_{\mathcal{E}} k(\text{continue}), (K_{\text{while}}(c, e, k') :: KS, \Omega) \{\Phi\}
\end{array}$$

Figure 4: WP rules specific to Iris^c , part II

3 Extensions

Besides the core language and its logic, we built several extension on top of it in a compatible way.

3.1 Array

Using the most primitive form of aggregated structure, product type, we can easily derive array (implemented) or even **struct** in future work.

“A pointer p pointing to an array vs of n elements which are τ -typed” is expressed with:

$$p \mapsto_q vs : \tau^n$$

In implementation, vs is **varray** (**l:list val**), and τ^n is **tyarray** (**t:type**) (**n:nat**).

We also have an intermediate representation of a slice of array:

$$p \mapsto_q [v_i : \tau, \dots, v_{i+l-1} : \tau]$$

The first form is easier for allocation, since it is defined as a unified aggregate structure; but the second form is easier for use, for example, splitting or indexing. We have the following lemmas proven in Coq:

SPLIT-SLICE

$$p \mapsto_q [v_i : \tau, \dots, v_{i+l_1-1} : \tau] * p \mapsto_q [v_{i+l_1} : \tau, \dots, v_{i+l_1+l_2-1} : \tau] \equiv p \mapsto_q [v_i : \tau, \dots, v_{i+l_1+l_2-1} : \tau]$$

INDEX-SPEC

$$\frac{i < n}{\{p \mapsto_q vs : \tau^n\} !_{\tau} p + i \{v. p \mapsto_q vs : \tau^n * vs !! i = \text{Some}(v)\}}$$

4 Automation

We also developed some basic tactics for automatically solving the goals, mostly related to our new language, some enhancing what Iris provides.

4.1 Tactics for “Symbolic Execution”

Similar to what exists in Iris’s heap-lang, we also provide convenient tactics including:

- **wp_bind** <p>: bind to a head term, or a term containing the head term, with pattern **p**
- **wp_assign**: make a step by evaluating assignment’s head term $l \leftarrow v$ (it also shifts following statements by applying **wp_seq** repetitively beforehand)
- **wp_load**, **wp_alloc**, **wp_op**, **wp_fst**, **wp_snd**, **wp_cas_fail**, **wp_cas_suc**, **wp_let** are similar to the one above
- **wp_skip**: skip over any value before a sequencing operator
- **wp_run**: keep executing as long as it can, which feels like doing symbolic execution automatically
- **wp_ret**: return if the head expression is already value

4.2 Tactics for Algebraic Simplification

gmap RA. **gmap_simplify**: simplify expressions involving **gmap** based on some algebraic rules

Refine RA. **rewrite_op_cfgs**: Simplify product of the configuration list component.

4.3 Misc Tactics

Inversion. **inversion_estep**, **inversion_cstep_as**, and **inversion_jstep_as** are all designed to automatically match stepping relation assumption and give proper names to the important results produced by **inversion**.

Evaluation Context Equality. **gen_eq** <H> <E1> <E2> <KS> can generate equalities between expressions E1 and E2, and between KS and empty context. It assumes that both E1 and E2 are normal form, and there is equality between the filled ones: **fill_ectxs** E1 KS = E2.

5 Refinement

5.1 Spec State and Spec Code

Considering a STS composed of spec code \tilde{c} (as below), spec state $\sigma : [X \hookrightarrow v]$, and semantic rules $(\tilde{c}, \tilde{\sigma}) \rightsquigarrow_{spec} (\tilde{c}', \tilde{\sigma}')$ (as well as derived $(\tilde{c}, \tilde{\sigma}) \rightsquigarrow_{spec}^* (\tilde{c}', \tilde{\sigma}')$).

$$\tilde{c} : \text{SPEC CODE} ::= \text{done}(v?) \mid \text{rel}(r : \tilde{\sigma} \rightarrow v? \rightarrow \tilde{\sigma} \rightarrow \text{Prop})$$

5.2 Refinement RA

We defined a new RA **REFINE** to capture the history of spec code execution:

$$\begin{aligned} \text{VIEW} &\triangleq \text{master} \mid \text{snapshot} \\ \text{REFINE} &\triangleq \text{VIEW} \times [\text{SPEC STATE} \times \text{SPEC CODE}] \end{aligned}$$

The validity of **REFINE**:

$$\sqrt{(\text{refine}(v, \emptyset))} \quad \sqrt{(\text{refine}(v, [(\tilde{\sigma}, \tilde{c})]))} \quad \frac{(\tilde{c}, \tilde{\sigma}) \rightsquigarrow_{spec} (\tilde{c}', \tilde{\sigma}') \quad \sqrt{(\text{refine}(v, [(\tilde{\sigma}, \tilde{c}) :: cs]))}}{\sqrt{(\text{refine}(v, [(\tilde{\sigma}', \tilde{c}') :: (\tilde{\sigma}, \tilde{c}) :: cs]))}}$$

The multiplication of **VIEW** and **REFINE** is defined as:

$$\begin{aligned} \text{master} \cdot _ &\triangleq \text{master} \\ _ \cdot \text{master} &\triangleq \text{master} \\ \text{snapshot} \cdot \text{snapshot} &\triangleq \text{snapshot} \\ \text{refine}(v_1, cs_1) \cdot \text{refine}(v_2, cs_2) &\triangleq \begin{cases} \text{refine}(v_1 \cdot v_2, cs_1) & |cs_1| \geq |cs_2| \\ \text{refine}(v_1 \cdot v_2, cs_2) & |cs_1| < |cs_2| \end{cases} \end{aligned}$$

The disjointness of multiplication is refined as:

$$\begin{aligned} \frac{\exists cs'. cs_1 \uparrow\uparrow cs = cs_2 \vee \exists cs'. cs_2 \uparrow\uparrow cs = cs_1}{\text{refine}(\text{snapshot}, cs_1) \# \text{refine}(\text{snapshot}, cs_2)} \quad &\text{refine}(\text{snapshot}, cs_1) \# \text{refine}(\text{master}, cs_1 \uparrow\uparrow cs_1) \\ &\text{refine}(\text{master}, cs_1 \uparrow\uparrow cs_1) \# \text{refine}(\text{snapshot}, cs_1) \end{aligned}$$

In the end, **REFINE** can be proven to be a CMRA with an unit element $\text{refine}(\text{snapshot}, \emptyset)$.

5.3 Refinement Ghost State, Invariant and Rules

The refinement ghost state **refineG** contains three part: one for *refineM*, and two for *paired* ownership of **SPEC CODE** and **SPEC STATE**. The predicates are defined as below:

$$\begin{aligned}
\text{sstate}(\tilde{\sigma}) &\triangleq [\![\frac{1}{2}, \mathbf{ag}\tilde{\sigma}]\!]^{\text{SPECSTATE}} \\
\text{scode}(\tilde{c}) &\triangleq [\![\frac{1}{2}, \mathbf{ag}\tilde{c}]\!]^{\text{SPEC CODE}} \\
\text{master}'(cs) &\triangleq [\![\mathbf{refine}(\text{master}, cs)\!]^{\text{REFINE}} \\
\text{master}(c) &\triangleq \exists cs. [\![\mathbf{refine}(\text{master}, c :: cs)\!]^{\text{REFINE}} \\
\text{snapshot}'(cs) &\triangleq [\![\mathbf{refine}(\text{snapshot}, cs)\!]^{\text{REFINE}} \\
\text{snapshot}(c) &\triangleq \exists cs. [\![\mathbf{refine}(\text{snapshot}, c :: cs)\!]^{\text{REFINE}}
\end{aligned}$$

Then we define refinement invariant:

$$I_{\text{REFINE}} \triangleq \exists \tilde{\sigma}, \tilde{c}. \text{sstate}(\tilde{\sigma}) * \text{scode}(\tilde{c}) * \text{master}(\tilde{\sigma}, \tilde{c})$$

With this, we can give refinement style proofs for certain kernel APIs, using the following derived rules:

$$\frac{\text{SPEC-UPDATE} \quad (\tilde{c}, \tilde{\sigma}) \rightsquigarrow_{\text{spec}} (\tilde{c}', \tilde{\sigma}')}{\boxed{I_{\text{REFINE}}^l} * \text{sstate}(\tilde{\sigma}) * \text{scode}(\tilde{c}) \vdash \Rightarrow \boxed{I_{\text{REFINE}}^l} * \text{sstate}(\tilde{\sigma}') * \text{scode}(\tilde{c}') * \text{snapshot}(\tilde{\sigma}', \tilde{c}')}$$

5.4 Soundness

First we define the simulation relationship between the *Expr* and *SPEC CODE*:

$$\frac{v \Downarrow \mathbf{done}(v) \quad (e, \sigma, \sigma_l) \rightsquigarrow (e', \sigma', \sigma'_l) \quad (\tilde{c}, \tilde{\sigma}) \rightsquigarrow_{\text{spec}}^* (\tilde{c}', \tilde{\sigma}')}{e \Downarrow c}$$

Our final soundness lemma, which is proven in Coq, is like below (the step indexing part is simplified away):

$$\frac{(e \dashv\vdash t_1, \sigma) \rightsquigarrow_c^n ((v, s_l) \dashv\vdash t_2, \sigma')}{\text{world}(\sigma) * (\boxed{I_{\text{REFINE}}^l} * \text{sstate}(\tilde{\sigma}) * \text{scode}(\tilde{c})) * \mathbf{wp} \, e \{v. \text{sstate}(\tilde{\sigma}') * \text{scode}(\mathbf{done}(v))\} * \mathbf{wptp}(t_1) \vdash \triangleright^{n+2} v \Downarrow c}$$