

Iris^c Documentation

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Abstract

This document describes the C verification framework based on the Iris program logic. The Coq source is in <https://github.com/izgzhen/iris-os>. We omitted many details on Iris itself, which can be found in the document and Coq formalization here <https://gitlab.mpi-sws.org/FP/iris-coq/>.

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1 Language

1.1 Definitions

Syntax. The language is a simplified version of C. It mainly consists of *Prim* (assembly primitives), *Expr* (expressions), *Type* (types) and *Val* (values).

$$\begin{aligned}
\tau : \text{Type} &::= \tau_{\text{void}} \mid \tau_{\text{null}} \mid \tau_{\text{int8}} \mid \tau_{\text{int32}} \mid * \tau \mid \tau \times \tau \\
v : \text{Val} &::= \text{void} \mid \text{null} \mid i \in [0, 2^8) \mid i \in [0, 2^{32}) \mid l \mid (v, v) \\
p : \text{Prim} &::= \text{cli} \mid \text{sti} \\
e : \text{Expr} &::= v \mid x \mid e \oplus e \mid !e \mid !_\tau e \mid \&e \mid e.1 \mid e.2 \\
&\quad \text{skip} \mid p \mid e \leftarrow e \mid \text{if}(e) \{e\} \text{ else } \{e\} \mid \text{while}_e(e) \{e\} \mid \\
&\quad \text{return } e \mid f(e_1, \dots, e_n) \mid e; e \mid \text{alloc}_\tau(e)
\end{aligned}$$

We also have following non-syntactical definitions:

- block address $b \in \mathbb{N}$
- offset $o \in \mathbb{Z}^+$
- Memory address $l : \text{Addr} \triangleq b \# o$
- Program $\Sigma : \text{TEXT} \triangleq [x \mapsto \text{Function}]$ is a list of functions indexed by names
- Function $F : \text{Function} \triangleq (\tau_{\text{ret}} \times (x_1 : \tau_1, \dots) \times e)$ consists of return type, parameter declarations and function body.

(NOTE) The relationship between $!e$ and $!_\tau e$: The former one is only used in source, and the later one is used in the actual semantics and inference rules. By adding a pre-processing step which instantiates all source-only expression to substituted/typed ones, we can remove lexical and typing environment from core program logic. The details are in [1.3](#)

(NOTE) Compared with C, there are several notable differences:

1. We currently don't support declarations of local variables, **for** loop and **switch**, **break** and **continue**
2. C differentiates statements and expressions (so do many other program logic). For simplicity and expressivity, we merged them together into a coherent expression *Expr* instead

Evaluation Context. To make the evaluation order explicit and reflected into the logic through the bind rule, we define evaluation context:

$$\begin{aligned}
K : \text{ECTX} &\triangleq \bullet \oplus e \mid v \oplus \bullet \mid !\bullet \mid \&\bullet \mid \bullet.1 \mid \bullet.2 \mid \\
&\quad \bullet \leftarrow e \mid l \leftarrow \bullet \mid \text{if}(\bullet) \{e\} \text{ else } \{e\} \mid \text{while}_e(\bullet) \{e\} \mid \\
&\quad \text{return } \bullet \mid f(v_1, \dots, \bullet, e_1, \dots) \mid \text{alloc}_\tau(\bullet) \mid \bullet; e
\end{aligned}$$

1.2 Semantics

Model. First define

- byte-size value

$$v_{\text{byte}} : \text{Val}_{\text{byte}} \triangleq \text{\textit{\texttt{!}}} \mid i \in [0, 2^8) \mid l_{\{0|1|2|3\}} \mid \text{\textit{\texttt{null}}}$$

- Heap $h : \text{HEAP} \triangleq [l \mapsto v_{\text{byte}}]$
- Continuation $K : \text{CONT} \triangleq [\text{CTX}]$
- Stack $s : \text{STACK} \triangleq [\text{CONT}]$
- Whole state

$$\sigma : \text{State} \triangleq (\text{heap} : \text{HEAP}, \text{text} : \text{TEXT}, \text{stack} : \text{STACK})$$

(NOTE) You may notice the difference between Val and Val_{byte} , it is intended to create a layer of abstraction for easier manipulation of spatial assertions and also for a clean, unified language syntax.

Small-Step Operational Semantics. We define the small-step semantics (see figure 1.2) for both local reduction $((e, h) \rightarrow_{\text{local}} (e', h'))$ and non-local reductions $((e, s : \text{STACK}) \rightarrow_{\text{jump}}^{\Sigma} (e', s' : \text{STACK}))$, and then combine them together $((e, \sigma) \rightarrow_c (e', \sigma'))$ point-wise.

$\frac{\text{ES-BINOP} \quad \llbracket \text{oplus} \rrbracket(v_1, v_2) = v'}{(v_1 \oplus v_2, h) \rightarrow_{\text{local}} (v', h)}$	$\frac{\text{ES-DEREF-TYPED} \quad \vdash_{\text{typeof}} v : \tau \quad h \vdash l \mapsto \text{encode}(v)}{(!_{\tau} l, h) \rightarrow_{\text{local}} (v, h)}$	$\frac{\text{ES-FST}}{((v_1, v_2).1, h) \rightarrow_{\text{local}} (v_1, h)}$
$\frac{\text{ES-SND}}{((v_1, v_2).2, h) \rightarrow_{\text{local}} (v_2, h)}$	$\frac{\text{ES-ASSIGN}}{(l \leftarrow v, h) \rightarrow_{\text{local}} (\text{skip}, h[l \mapsto \text{encode}(v)])}$	$\frac{\text{ES-SEQ}}{(v; e, h) \rightarrow_{\text{local}} (e, h)}$
$\frac{\text{ES-ALLOC} \quad \vdash_{\text{typeof}} v : \tau \quad \forall o'. h(b, o') = \perp}{(\text{alloc}_{\tau}(v), h) \rightarrow_{\text{local}} (b\#o, h[b\#o \mapsto v])}$	$\frac{\text{ES-WHILE-TRUE}}{(\text{while}_c(\text{true}) \{s\}, h) \rightarrow_{\text{local}} (s; \text{while}_c(c) \{s\}, h)}$	
$\frac{\text{ES-WHILE-FALSE}}{(\text{while}_c(\text{false}) \{s\}, h) \rightarrow_{\text{local}} (\text{skip}, h)}$	$\frac{\text{ES-BIND'} \quad \text{is_jump}(e) = \text{False} \quad (e, h) \rightarrow_{\text{local}} (e', h')}{((k :: ks)e, h) \rightarrow_{\text{local}} ((k :: ks)e', h')}$	
$\frac{\text{JS-RETE} \quad \text{unfill}(k(\text{return } v)) = (k', \text{return } v)}{(k'(\text{return } v), k :: ks) \rightarrow_{\text{jump}}^{\Sigma} (k(v), ks)}$	$\frac{\text{JS-CALL} \quad \Sigma(f) = \text{Function}(_, ps, e)}{(k(f(ls)), ks) \rightarrow_{\text{jump}}^{\Sigma} (e[ps/ls], k :: ks)}$	

Figure 1: Semantics rules

1.3 Type System and Lexical Environment

Local Typing Rules. The types are defined in §1.1, and all values in v can be *locally* typed trivially (since v is introduced to reflect type structure in some sense). Nevertheless, due to the fact that language of study is weakly-typed in the vein of C, we still have some “weird” rules worth documenting.

Here we define local typing judgment $\vdash_{\text{typeof}} v : \tau$ for values.

TYPEOF-VOID $\vdash_{\text{typeof}} \mathbf{void} : \tau_{\text{void}}$	TYPEOF-NULL $\vdash_{\text{typeof}} \mathbf{null} : \tau_{\text{null}}$	TYPEOF-INT8 $\vdash_{\text{typeof}} i \in [0, 2^8) : \tau_{\text{int8}}$	TYPEOF-INT32 $\vdash_{\text{typeof}} i \in [0, 2^{32}) : \tau_{\text{int32}}$
TYPEOF-NULL-PTR $\forall \tau. \vdash_{\text{typeof}} \mathbf{null} : * \tau$	TYPEOF-PTR $\forall \tau, l. \vdash_{\text{typeof}} l : * \tau$	TYPEOF-PROD $\frac{\vdash_{\text{typeof}} v_1 : \tau_1 \quad \vdash_{\text{typeof}} v_2 : \tau_2}{\vdash_{\text{typeof}} (v_1, v_2) : \tau_1 \times \tau_2}$	

Note that rule TYPEOF-NULL-PTR means that **null** can be of any pointer type, and TYPEOF-PTR means that a pointer can have *any* pointer type.

Lexical Environment In Iris^C , before we do any real work over a function body, variables are all replaced with their bindings, and type information is tagged to the untyped parts when in need. More specifically, we will replace variables with either their location (when in left-hand side) or the dereference of their location (when in right-hand side). We will produce a pointer arithmetic expression when processing the left-hand side expression, which in turn requires type inference $\vdash_{\text{tyinf}} e : \tau$ (since it is standard and trivial, we will leave out the details here).

Now we write down the algorithmic rules for rewriting left-hand side expression $(\langle e \rangle)_{\text{LHS}}$ and right-hand side expression $(\langle e \rangle)_{\text{RHS}}$. Essentially, $(\langle e \rangle)_{\text{LHS}}$ will rewrite the left-hand side e into its location in memory, while $(\langle e \rangle)_{\text{RHS}}$ will rewrite variables into the dereference of its locations.

Note that environment $\Gamma : \text{Env} \triangleq [x \mapsto (\tau, l)]$ is assumed, and if any operation fails, like missing something in Γ , then the rule will indicate it in the actual implementation. And also, the cases not covered are defined trivially.

$$\begin{aligned}
(\langle e_1 \leftarrow e_2 \rangle)_{\text{LHS}} &= (\langle e_1 \rangle)_{\text{LHS}} \leftarrow e_2 \\
(\langle x \rangle)_{\text{LHS}} &= \Gamma(x).l \\
(\langle !e \rangle)_{\text{LHS}} &= (\langle e \rangle)_{\text{RHS}} \\
(\langle e.1 \rangle)_{\text{LHS}} &= (\langle e \rangle)_{\text{LHS}} \\
(\langle e.2 \rangle)_{\text{LHS}} &= (\langle e \rangle)_{\text{LHS}} + \text{sizeof}(\tau_1) \quad \text{if } \vdash_{\text{tyinf}} (\langle e \rangle)_{\text{LHS}} : *(\tau_1 \times \tau_2) \\
(\langle l \rangle)_{\text{LHS}} &= l \\
\\
(\langle e_1 \leftarrow e_2 \rangle)_{\text{RHS}} &= e_1 \leftarrow (\langle e_2 \rangle)_{\text{RHS}} \\
(\langle x \rangle)_{\text{RHS}} &= !_{\Gamma(x).t} \Gamma(x).l \\
(\langle !e \rangle)_{\text{RHS}} &= !_{\tau} (\langle e \rangle)_{\text{RHS}} \quad \text{if } \vdash_{\text{tyinf}} e : * \tau
\end{aligned}$$

2 Program Logic

This section describes how to build a program logic for the C language (*c.f.* §1) on top of the base logic of Iris.

2.1 Extended Assertions

Using the standard Iris assertions and the ownership of ghost heap resource, we can define some basic custom assertions:

$$\begin{aligned}
l \mapsto_q v : t &\triangleq l \mapsto_q \text{encode}(v) \wedge \vdash_{\text{typeof}} v : t \\
l \mapsto v : t &\triangleq l \mapsto_1 v : t \\
l \mapsto_q - : t &\triangleq \exists v. l \mapsto_q v : t \\
\text{own_stack}(s) &\triangleq \left[\left(\frac{1}{2}, \text{ags} \right) \right]^{\text{STACK}} \\
f \mapsto_{\text{TEXT}} F &\triangleq \left[\circ \left[f \leftarrow \text{ag} F \right] \right]^{\text{TEXT}}
\end{aligned}$$

2.2 Weakest Precondition

Finally, we can define the core piece of the program logic, the assertion that reasons about program behavior: Weakest precondition, from which Hoare triples can be derived.

Defining weakest precondition. The state interpreting predicate $S : \text{State} \rightarrow iProp$ for our Iris^C language is defined below, as required for instantiating the parametric WP of Iris (whose definition is also copied below for reference).

$$S(\sigma) \triangleq \left[\bullet \text{fmap}(\lambda v. (1, \text{ag} v), \sigma.\text{heap}) \right]^{\text{HEAP}} * \left[\bullet \text{fmap}(\text{ag}, \sigma.\text{text}) \right]^{\text{TEXT}} * \left[\left(\frac{1}{2}, \sigma.\text{stack} \right) \right]^{\text{STACK}}$$

$$\begin{aligned}
wp &\triangleq \mu wp. \lambda \mathcal{E}, e, \Phi. \\
&(\exists v. \text{to_val}(e) = v \wedge \models_{\mathcal{E}} \Phi(v)) \vee \\
&\left(\text{to_val}(e) = \perp \wedge \right. \\
&\quad \forall \sigma. S(\sigma) \stackrel{\mathcal{E}}{\equiv} *^{\emptyset} \\
&\quad \text{red}(e, \sigma) * \triangleright \forall e', \sigma'. (e, \sigma) \rightarrow_c (e', \sigma') \stackrel{\emptyset}{\equiv} *^{\mathcal{E}} \\
&\quad \left. S(\sigma') * wp(\mathcal{E}, e', \Phi) \right)
\end{aligned}$$

Here are some conventions:

- If we leave away the mask \mathcal{E} , we assume it to be \top .
- Φ in post-condition might or might not take a value parameter, depending on the context.

Laws of weakest precondition. The following rules can all be derived:

$$\begin{array}{c}
\text{WP-VALUE} \\
\Phi(v) \vdash \text{wp}_{\mathcal{E}} v \{\Phi\} \\
\\
\text{WP-STRONG-MONO} \\
\frac{\mathcal{E}_1 \subseteq \mathcal{E}_2}{((\forall v. \Phi(v) \models_{\mathcal{E}_2} \Psi(v)) \wedge (\forall v. \Phi_{\text{ret}}(v) \models_{\mathcal{E}_2} \Psi_{\text{ret}}(v))) * \text{wp}_{\mathcal{E}_1} E_{\text{cur}} \{\Phi\} \vdash \text{wp}_{\mathcal{E}_2} E_{\text{cur}} \{\Psi\} \Psi_{\text{ret}}} \\
\\
\begin{array}{cc}
\text{FUP-WP} & \text{WP-FUP} \\
\frac{}{\models_{\mathcal{E}} \text{wp}_{\mathcal{E}} E_{\text{cur}} \{\Phi\} \vdash \text{wp}_{\mathcal{E}} E_{\text{cur}} \{\Phi\}} & \frac{}{\text{wp}_{\mathcal{E}} E_{\text{cur}} \{x. \models_{\mathcal{E}} \Phi(x)\} x. \models_{\mathcal{E}} \Phi_{\text{ret}}(x) \vdash \text{wp}_{\mathcal{E}} e \{\Phi\}}
\end{array} \\
\\
\text{WP-SKIP} \\
\triangleright \text{wp}_{\mathcal{E}} e \{\Phi\} \vdash \text{wp}_{\mathcal{E}} v; e \{\Phi\} \\
\\
\text{WP-RET} \\
\text{own_stack}(k' :: ks) * (\text{own_stack}(ks) \multimap \text{wp}_{\mathcal{E}} k'(v) \{\Phi\}) \vdash \text{wp}_{\mathcal{E}} k(\text{return } v) \{\Phi\} \\
\\
\begin{array}{cc}
\text{WP-BIND} & \text{WP-OP} \\
\frac{\text{is_jump}(e) = \text{False}}{\text{wp}_{\mathcal{E}} e \{x. \text{wp}_{\mathcal{E}} k(x) \{\Phi\}\} \vdash \text{wp}_{\mathcal{E}} k(e) \{\Phi\}} & \frac{\llbracket \text{oplus} \rrbracket(v_1, v_2) = v'}{\Phi(v') \vdash \text{wp}_{\mathcal{E}} v_1 \oplus v_2 \{\Phi\}}
\end{array} \\
\\
\text{WP-ASSIGN} \\
\frac{\vdash_{\text{typeof}} v : \tau' \quad \text{assign_compatible}(\tau \leftarrow \tau')}{\triangleright l \mapsto - : \tau * \triangleright (l \mapsto v : \tau \multimap \Phi) \vdash \text{wp}_{\mathcal{E}} l \leftarrow v \{\Phi\}} \\
\\
\begin{array}{cc}
\text{WP-LOAD} & \text{WP-SEQ} \\
\triangleright l \mapsto_q v : \tau * \triangleright (l \mapsto_q v : \tau \multimap \Phi(v)) \vdash \text{wp}_{\mathcal{E}} !_{\tau} l \{\Phi\} & \frac{\text{is_jump}(e_1) = \text{False}}{\text{wp}_{\mathcal{E}} e_1 \{v, \text{wp}_{\mathcal{E}} v; e_2 \{\Phi\}\} \vdash \text{wp}_{\mathcal{E}} e_1; e_2 \{\Phi\}}
\end{array} \\
\\
\begin{array}{cc}
\text{WP-WHILE-TRUE} & \text{WP-WHILE-FALSE} \\
\triangleright \text{wp}_{\mathcal{E}} s; \text{while}_c(c) \{s\} \{\Phi\} \vdash \text{wp}_{\mathcal{E}} \text{while}_c(\text{true}) \{s\} \{\Phi\} & \triangleright \Phi(\text{void}) \vdash \text{wp}_{\mathcal{E}} \text{while}_c(\text{false}) \{s\} \{\Phi\}
\end{array} \\
\\
\text{WP-WHILE-INV} \\
\frac{\begin{array}{c} \text{is_jump}(s) = \text{False} \quad \text{is_jump}(c) = \text{False} \\ \forall \Phi. (I * (\forall v. (v = \text{false} * Q(\text{void})) \vee (v = \text{true} * I)) \multimap \Phi(v)) \multimap \text{wp } c \{\Phi\} \\ \forall \Phi. (I * (I \multimap \Phi(\text{void}))) \multimap \text{wp } s \{\Phi\} \end{array}}{I \vdash \text{wp while}_c(c) \{s\} \{Q\}} \\
\\
\begin{array}{cc}
\text{WP-FST} & \text{WP-SND} \\
\triangleright \Phi(v_1) \vdash \text{wp}_{\mathcal{E}} (v_1, v_2).1 \{\Phi\} & \triangleright \Phi(v_2) \vdash \text{wp}_{\mathcal{E}} (v_1, v_2).1 \{\Phi\}
\end{array} \\
\\
\text{WP-ALLOC} \\
\frac{\vdash_{\text{typeof}} v : \tau}{(\forall l. l \mapsto v : \tau \multimap \Phi(l)) \vdash \text{wp}_{\mathcal{E}} \text{alloc}_{\tau}(v) \{\Phi\}} \\
\\
\text{WP-CALL} \\
f \mapsto_{\text{TEXT}} \text{Function}(_, ps, e) * \text{own_stack}(ks) * \triangleright (\text{own_stack}(k :: ks) \multimap \text{wp}_{\mathcal{E}} e[ps/ls] \{\Phi\}) \vdash \text{wp}_{\mathcal{E}} k(f(ls)) \{\Phi\}
\end{array}$$

2.3 Extending Weakest Precondition

We extend the unary-exit WP into a binary-exit $\text{wp}_{\mathcal{E}}^+ e \{\Phi; \Phi_{\text{ret}}\}$, by defining it as below:

$$\begin{aligned} \text{wp}^+ &\triangleq \mu \text{wp}^+. \lambda \mathcal{E}, e, \Phi, \Phi_{\text{ret}}. \\ &(\exists v. \text{to_val}(e) = v \wedge \Phi(v)) \vee \\ &(\text{to_val}(e) = \perp \wedge \exists e_h, K. e = K(e_h) \wedge (\\ &\quad (\text{is_jump}(e_h) = \text{False} * \text{wp}_{\mathcal{E}}^+ e_h \{v. \text{wp}_{\mathcal{E}}^+ K(v) \{\Phi; \Phi_{\text{ret}}\}\}) \vee \\ &\quad (\exists v. e_h = \text{return } v * \triangleright \Phi_{\text{ret}}(v)) \vee \\ &\quad (\exists f, ps, ls, F. \\ &\quad \quad f \mapsto_{\text{TEXT}} \text{Function}(_, ps, F) * \\ &\quad \quad \triangleright (\text{wp}_{\mathcal{E}}^+ F[ps/ls] \{-. \text{False}; v. \text{wp}_{\mathcal{E}}^+ K(v) \{\Phi; \Phi_{\text{ret}}\}\})) \\ &\quad)) \end{aligned}$$

And this definition supports the following inference rules:

$$\begin{array}{c} \text{WPR-VALUE} \qquad \qquad \qquad \text{WPR-RET} \\ \Phi(v) \vdash \text{wp}_{\mathcal{E}}^+ v \{\Phi; \Phi_{\text{ret}}\} \qquad \qquad \Phi_{\text{ret}}(v) \vdash \text{wp}_{\mathcal{E}}^+ \text{return } v \{\Phi; \Phi_{\text{ret}}\} \\ \\ \text{WPR-BIND} \\ \text{wp}_{\mathcal{E}}^+ e \{x. \text{wp}_{\mathcal{E}}^+ k(x) \{\Phi; \Phi_{\text{ret}}\}; \Phi_{\text{ret}}\} \vdash \text{wp}_{\mathcal{E}}^+ k(e) \{\Phi; \Phi_{\text{ret}}\} \\ \\ \text{WPR-CALL} \\ f \mapsto_{\text{TEXT}} \text{Function}(_, ps, e) * \triangleright (\text{wp}_{\mathcal{E}}^+ e[ps/ls] \{-. \text{False}; \Phi\}) \vdash \text{wp}_{\mathcal{E}}^+ k(f(ls)) \{\Phi; \Phi_{\text{ret}}\} \\ \\ \text{WPR-OP} \\ \frac{\llbracket \text{oplus} \rrbracket(v_1, v_2) = v'}{\Phi(v') \vdash \text{wp}_{\mathcal{E}}^+ v_1 \oplus v_2 \{\Phi; \Phi_{\text{ret}}\}} \end{array}$$

Note how WP-CALL, WP-RET, and WP-BIND are simplified in their new, corresponding versions, also the fact that we can recover local evaluation like WP-OP trivially (and we won't duplicate too much here).

2.4 Soundness

The soundness of WP-style program is proven by showing that it is *adequate*: For all $e, \sigma, \Phi : \text{Val} \rightarrow \text{Prop}$,

$$\begin{aligned} \text{True} &\vdash \text{wp}_{\top} e \{\Phi\} \rightarrow \\ &(\forall v, \sigma'. (e, \sigma) \rightarrow_c (v, \sigma') \rightarrow \Phi(v)) \wedge \\ &(\forall e', \sigma'. (e, \sigma) \rightarrow_c (e', \sigma') \rightarrow (\exists v. e = v) \vee \text{red}(e', \sigma')) \end{aligned}$$

(NOTE) However, that doesn't look strong enough.

3 Automation

We also developed some basic tactics for automatically solving the goals, mostly related to our new language, some enhancing what Iris provides.

3.1 Tactics for “Symbolic Execution”

Similar to what exists in Iris’s heap-lang, we also provide convenient tactics including:

- `wp_bind <p>`: bind to a head term, or a term containing the head term, with pattern `p`
- `wp_assign`: make a step by evaluating assignment’s head term $l \leftarrow v$ (it also shifts following statements by applying `wp_seq` repetitively beforehand)
- `wp_load`, `wp_op` are similar to the one above
- `wp_skip`: skip over any value before a sequencing operator
- `wp_run`: keep executing as long as any of the above applies, which feels like doing symbolic execution automatically

3.2 Other Tactics

- `gmap_simplify`: simplify expressions involving `gmap` based on some algebraic rules

4 Refinement

4.1 Spec State and Spec Code

Considering a STS composed of spec code \tilde{c} (as below), spec state $\sigma : [X \hookrightarrow v]$, and semantic rules $(\tilde{c}, \tilde{\sigma}) \rightarrow_{spec} (\tilde{c}', \tilde{\sigma}')$ (as well as $(\tilde{c}, \tilde{\sigma}) \rightarrow_{spec}^* (\tilde{c}', \tilde{\sigma}')$ trivially derived).

$$\tilde{c} : \text{SPEC CODE} ::= \text{done}(v?) \mid \text{rel}(r : \tilde{\sigma} \rightarrow v? \rightarrow \tilde{\sigma} \rightarrow \text{Prop}) \mid \dots$$

4.2 Refinement RA

We defined a new RA **REFINE** to capture the history of spec code execution:

$$\begin{aligned} \text{VIEW} &\triangleq \text{master} \mid \text{snapshot} \\ \text{REFINE} &\triangleq \text{VIEW} \times [\text{SPEC STATE} \times \text{SPEC CODE}] \end{aligned}$$

The validity of **REFINE**:

$$\sqrt{(\text{refine}(v, \emptyset))} \quad \sqrt{(\text{refine}(v, [(\tilde{\sigma}, \tilde{c})]))} \quad \frac{(\tilde{c}, \tilde{\sigma}) \rightarrow_{spec} (\tilde{c}', \tilde{\sigma}') \quad \sqrt{(\text{refine}(v, [(\tilde{\sigma}, \tilde{c}) :: cs]))}}{\sqrt{(\text{refine}(v, [(\tilde{\sigma}', \tilde{c}') :: (\tilde{\sigma}, \tilde{c}) :: cs]))}}$$

The multiplication of **VIEW** and **REFINE** is defined as:

$$\begin{aligned} \text{master} \cdot _ &\triangleq \text{master} \\ _ \cdot \text{master} &\triangleq \text{master} \\ \text{snapshot} \cdot \text{snapshot} &\triangleq \text{snapshot} \\ \text{refine}(v_1, cs_1) \cdot \text{refine}(v_2, cs_2) &\triangleq \begin{cases} \text{refine}(v_1 \cdot v_2, cs_1) & |cs_1| \geq |cs_2| \\ \text{refine}(v_1 \cdot v_2, cs_2) & |cs_1| < |cs_2| \end{cases} \end{aligned}$$

The disjointness of multiplication is refined as:

$$\begin{aligned} \frac{\exists cs'. cs_1 \mathrel{++} cs = cs_2 \vee \exists cs'. cs_2 \mathrel{++} cs = cs_1}{\text{refine}(\text{snapshot}, cs_1) \# \text{refine}(\text{snapshot}, cs_2)} \quad & \text{refine}(\text{snapshot}, cs_1) \# \text{refine}(\text{master}, cs_1 \mathrel{++} cs_1) \\ & \text{refine}(\text{master}, cs_1 \mathrel{++} cs_1) \# \text{refine}(\text{snapshot}, cs_1) \end{aligned}$$

In the end, **REFINE** can be proven to be a CMRA with an unit element $\text{refine}(\text{snapshot}, \emptyset)$.

4.3 Refinement Ghost State, Invariant and Rules

The refinement ghost state `refineG` contains three part: one for *refineM*, and two for *paired* ownership of `SPECCode` and `SPECState`. The predicates are defined as below:

$$\begin{aligned}
\text{sstate}(\tilde{\sigma}) &\triangleq \llbracket (\frac{1}{2}, \text{ag}\tilde{\sigma}) \rrbracket^{\text{SPECSTATE}} \\
\text{scode}(\tilde{c}) &\triangleq \llbracket (\frac{1}{2}, \text{ag}\tilde{c}) \rrbracket^{\text{SPECCode}} \\
\text{master}'(cs) &\triangleq \llbracket \text{refine}(\text{master}, cs) \rrbracket^{\text{REFINE}} \\
\text{master}(c) &\triangleq \exists cs. \llbracket \text{refine}(\text{master}, c :: cs) \rrbracket^{\text{REFINE}} \\
\text{snapshot}'(cs) &\triangleq \llbracket \text{refine}(\text{snapshot}, cs) \rrbracket^{\text{REFINE}} \\
\text{snapshot}(c) &\triangleq \exists cs. \llbracket \text{refine}(\text{snapshot}, c :: cs) \rrbracket^{\text{REFINE}}
\end{aligned}$$

Then we define refinement invariant:

$$I_{\text{REFINE}} \triangleq \exists \tilde{\sigma}, \tilde{c}. \text{sstate}(\tilde{\sigma}) * \text{scode}(\tilde{c}) * \text{master}(\tilde{\sigma}, \tilde{c})$$

With this, we can give refinement style proofs for certain kernel APIs, using the following derived rules:

$$\frac{\text{SPEC-UPDATE} \quad (\tilde{c}, \tilde{\sigma}) \rightarrow_{\text{spec}} (\tilde{c}', \tilde{\sigma}')}{\boxed{I_{\text{REFINE}}}^{\iota} * \text{sstate}(\tilde{\sigma}) * \text{scode}(\tilde{c}) \vdash \Rightarrow \boxed{I_{\text{REFINE}}}^{\iota} * \text{sstate}(\tilde{\sigma}') * \text{scode}(\tilde{c}') * \text{snapshot}(\tilde{\sigma}', \tilde{c}')}$$

5 Misc

This section contained some key formal developments claimed but not yet mechanized in Coq for reference.

5.1 Basic Properties of Evaluation Context

Our expression $Expr$ is a recursively defined algebraic data type, which can be generalized into the following form:

$$e : Expr ::= Expr_1(A_1, e^{r_1}) \mid \dots \mid Expr_n(A_n, e^{r_n}) \mid v$$

Here, $Expr_i$ is tag for i -th class of expression, A_i is its arbitrary non-recursive payload, and e^{r_i} means it has $r_i \in \mathbb{N}$ recursive occurrences.

It is apparent that ECTX is a direct translation of $Expr$ plus some ordering considerations, though in actual Coq development we don't mechanize this fact. But we will make it explicit here to support some stronger claims than what we can do in Coq.

For some abstract $Expr$ like above, its ECTX should be a sum of sub-ECTX $_i$ for each $Expr_i$. When $r_i = 0$, ECTX $_i$ doesn't exist, now we consider $r_i > 0$, define

$$k_i : ECTX_i ::= ECTX_{i1}(A_i, e^{r_i-1}) \mid ECTX_{i2}(A_i, v^1, e^{r_i-2}) \mid \dots \mid ECTX_{ir_i}(A_i, v^{r_i-1})$$

Theorem 5.1. *For any $e, e' : Expr$ and $k_{im}, k_{jn} : ECTX$,*

$$k_{im}(e) = k_{jn}(e') \vdash i = j$$

Proof. Trivial. □

Theorem 5.2. *For any $e, e' : Expr$ and $k_{im}, k_{in} : ECTX_i$,*

$$k_{im}(e) = k_{in}(e') \vdash (m = n \wedge e = e') \vee (m \neq n \wedge (\exists v. \text{to_val}(e) = v \vee \exists v. \text{to_val}(e) = v))$$

Proof. When $m = n$, by injectivity; When $m \neq n$, we can expand the equation like below without loss of generality:

$$\begin{aligned} Expr_i(A_i, v_1, \dots, v_{m-1}, e, e_{m+1}, \dots, e_{r_i-1}) = \\ Expr_i(A'_i, v'_1, \dots, v'_{m-1}, v'_m, \dots, e', \dots) \end{aligned}$$

So $e = v'_m$ by injectivity. □

As you may observe, the expression space spanned by $k(e)$ for any k, e is not the entire expression space as an ADT. Instead, it is a subset of $Expr$ which represents well-formed ones that can appear as an immediate form according to some well-defined evaluation order. We call such e is *well-formed*.

5.2 Axioms about Evaluation Context based Semantics

In the following lemmas, we assume all involved e to be *well-formed*.

Lemma 5.3. $\forall e : Expr, k : \text{CONT}. \text{is_enf}(e) \rightarrow \text{unfill}(k(e)) = (k, e)$

Proof. Let's prove inductively w.r.t k . When k is empty, since e is in normal form, so unfill it will only return the same thing. And inductively, since fill and unfill should cancel out, the final conclusion is proved trivially as well. \square

Lemma 5.4. $\forall e, e_h, k. \text{unfill}(e) = (k, e_h) \rightarrow \text{is_enf}(e_h) \wedge e = k(e_h)$

Proof. First, e can't be a value, so we just need to consider e in each legal case, which basically mean that either e is normal form itself, or $e = K(e')$ and $\exists k', k = K :: k' \wedge \text{unfill}(e') = (k', e_h)$.

1. In the first case, k is forced to be \emptyset and $e_h = e$, so this case is proved
2. In the second case, we can inductively know that e_h is normal form and $e' = k'(e_h)$, so $e = K(k'(e_h)) = (K :: k')(e_h) = k(e_h)$, so this case is proved as well

\square

Lemma 5.5. *Induction scheme I for Expr:*

$$\begin{aligned} \forall P : Expr \rightarrow Prop. (\forall e. \text{is_enf}(e) \rightarrow P(e)) \rightarrow \\ (\forall e, k : \text{CONT}. \text{to_val}(e) = \perp \rightarrow P(e) \rightarrow P(k(e))) \rightarrow \\ (\forall e. \text{to_val}(e) = \perp \rightarrow P(e)) \end{aligned}$$

Proof. We want to *inductively* prove that for any non-value, *well-formed* e , $P(e)$ holds. Note that P here can be any proposition.

The base case is when e is in normal form, which corresponds to the first condition; The inductive case is that for any well-formed e' that is not in normal form, it must be of the form $k(e)$ for some k, e . With each proved given sufficient inductive assumption, we know that any well-formed e must satisfy P . \square

Lemma 5.6. *Induction scheme for CONT:*

$$\begin{aligned} \forall P : \text{CONT} \rightarrow Prop. (P(\emptyset)) \rightarrow \\ (\forall k. (\forall k', |k'| < |k| \rightarrow P(k')) \rightarrow P(k)) \rightarrow \\ (\forall k. P(k)) \end{aligned}$$

Proof. Intuitively, this is about property of natural number as the length of k . When we know $P(\emptyset)$, we know $\forall K_1. P(K_1 :: [])$, then we know $\forall K_2. \forall K_1. P(K_2 :: K_1 :: [])$ Until for arbitrarily big, finite n , $\forall K_n, K_{n-1}, \dots, K_1. P(K_n :: K_{n-1} :: \dots :: K_1 :: [])$, or for any finite k , $P(k)$ holds. \square

Lemma 5.7. *Induction scheme II for Expr:*

$$\begin{aligned} \forall P : Expr \rightarrow Prop. (\forall e. \text{is_enf}(e) \rightarrow P(e)) \rightarrow \\ (\forall e, k : \text{CONT}. \text{is_enf}(e) \rightarrow (\forall k'. |k'| < |k| \rightarrow P(k'(e))) \rightarrow P(k(e))) \rightarrow \\ (\forall e. \text{to_val}(e) = \perp \rightarrow P(e)) \end{aligned}$$

Proof. First, the e in the final goal is implicitly well-formed, which means that $e = K(e_h)$ for some K, e_h . So we prove inductively on the length of such K , in a similar way to the last lemma, and reach our final conclusion. \square

Lemma 5.8. *Partial injectivity of fill:*

$$\frac{\text{to_val}(e) = \perp \quad \text{is_enf}(e_h) \quad k(e) = k'(e_h)}{\exists k''. k' = k ++ k'' \quad e = k''(e_h)}$$

Proof. Since e is well-formed and not a value, so it can be unfilled such that $e = k''(e'_h)$, then by `cont_inj`, $e_h = e'_h$ and $k' = k ++ k''$, which proves our claim \square

Lemma 5.9. *Local step has a focus:*

$$\frac{(e_1, h_1) \rightarrow_{\text{local}} (e_2, h_2)}{\exists e'_1, e'_2, k. \text{is_enf}(e'_1) \wedge e_1 = k(e'_1) \wedge e_2 = k(e'_2) \wedge (e'_1, h_1) \rightarrow_{\text{local}} (e'_2, h_2)}$$

Proof. By inverting the assumption, we know there are two possibilities:

1. If e_1 is already in normal form, then we just need to let $e'_1 = e_1, e'_2 = e_2$, and $k = \emptyset$.
2. We know that there are some k'', e''_1, e''_2 , where $|k''| > 0$, such that $e_1 = k''(e''_1), e_2 = k''(e''_2)$, and $(e''_1, h_1) \rightarrow_{\text{local}} (e''_2, h_2)$. Since by the metric of depth $||_d$, $|e''_1|_d < |e_1|_d$, so we can derive inductively that there exists e'_1, e'_2, k' that

$$\text{is_enf}(e'_1) \wedge e'_1 = k'(e'_1) \wedge e'_2 = k'(e'_2) \wedge (e'_1, h_1) \rightarrow_{\text{local}} (e'_2, h_2)$$

Now let $k = k'' ++ k'$, and use the same e'_1, e'_2 , we can prove the original existential goal. \square

5.3 Other Facts

Lemma 5.10. *wp^+ is contractive.*

Proof. Observe in the definition 2.3 that there are only two recursive uses of wp^+ :

1. In the second branch of non-value case, we use wp^+ under Φ of $\text{wp}_{\mathcal{E}} e_h \{\Phi\}$, which means that,

$$\text{wp}_{\mathcal{E}} e_h \{\dots \text{wp}^+ \dots\} \stackrel{n}{=} \text{wp}_{\mathcal{E}} e_h \{\dots \text{wp}'^+ \dots\}$$

will hold if $\text{wp}^+ \stackrel{n+1}{=} \text{wp}'^+$, which is satisfied by assumption.

2. In the third branch of non-value case, we use wp^+ under a \triangleright , which is trivial to prove by `f_contractive`. \square

5.4 Admitted Facts

There are still one or more loopholes that exists behind the documented formalization. But note that all of them are harmless to leave unproven for now.

- In `lang.v`, lemma `same_type_encode_inj`'s fourth case on `int32` is admitted. Essential, this is about encoding `int32` value with four bytes. It is trivially true, and even if it is false, the soundness of logic won't suffer. We don't prove it now because there are not enough lemmas exposed from `Integers.v`.