

# Iris-OS Documentation

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## Abstract

This document describes formally the OS verification framework based on the Iris program logic. The latest versions of Iris document and the Iris Coq formalization can be found in the git repository at <https://gitlab.mpi-sws.org/FP/iris-coq/>.

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# 1 Language

## 1.1 Definitions

**Syntax.** The language is a simplified version of C. It consists of *Stmts* (statements), *Prim* (primitives), *Expr* (expressions), *Type* (types) and *Val* (values).

Memory address  $l : \text{Addr} \triangleq b \# o$  where block address  $b \in \mathbb{N}$ , offset  $o \in \mathbb{Z}^+$ .

$$\begin{aligned}
\tau : \text{Type} &::= \tau_{\text{void}} \mid \tau_{\text{null}} \mid \tau_{\text{int8}} \mid \tau_{\text{int32}} \mid * \tau \mid \tau \times \tau \\
v : \text{Val} &::= \text{void} \mid \text{null} \mid i \in [0, 2^8) \mid i \in [0, 2^{32}) \mid l \mid (v, v) \\
e : \text{Expr} &::= v \mid x \mid e \oplus e \mid !e \mid !_\tau e \mid \&e \mid e \text{ as } \tau \mid e.1 \mid e.2 \\
p : \text{Prim} &::= \text{cli} \mid \text{sti} \\
s : \text{Stmts} &::= \text{skip} \mid p \mid e \leftarrow e \mid \text{if}(e) \{s\} \text{ else } \{s\} \mid \text{while}_e(e) \{s\} \mid \\
&\quad \text{return} \mid \text{return } e \mid f(e_1, \dots, e_n) \mid s; s
\end{aligned}$$

XXX: Explain two type of dereference.

**Program.** A program is considered to be a set of functions, each identified by its name. Each function is a triple of return type  $\tau_{\text{ret}}$ , parameter declarations  $(x_1 : \tau_1, \dots)$ , and function body  $s$ .

**Evaluation Context.** To make the evaluation order explicit and reusable by the WP-BIND rule, we define evaluation context. Compared to a simple expression-based language which has only one kind of context  $K$ , we define two contexts  $K_e, K_s$  for both *Expr* and *Stmts*:

$$\begin{aligned}
K_e : \text{ECTX} &\triangleq \bullet \mid \bullet \oplus e \mid v \oplus \bullet \mid !\bullet \mid \&\bullet \mid \bullet \text{ as } \tau \mid \bullet.1 \mid \bullet.2 \\
K_s : \text{SCTX} &\triangleq \bullet \leftarrow e \mid l \leftarrow \bullet \mid \text{if}(\bullet) \{s\} \text{ else } \{s\} \mid \text{while}_e(\bullet) \{s\} \mid \\
&\quad \text{return } \bullet \mid f(v_1, \dots, \bullet, e_1, \dots)
\end{aligned}$$

Now we can define *context*:  $K : \text{CTX} \triangleq (K_e, K_s)$ .

## 1.2 Semantics

**Model.** Define  $c : \text{Code} \triangleq (E_{\text{cur}} : \{\text{Expr} + \text{Stmts}\}, K_{\text{cur}} : \text{CTX}, K^* : [\text{CTX}])$ , in which

- $E_{\text{cur}}$  is the “current evaluation”, which could be either an expression  $e^\dagger$  or a statement  $s^\dagger$ .
- $K_{\text{cur}}$  is the “current context”, which semantically means rest code to execute in current frame.
- $K^*$  is the “previous contexts”, which semantically means previous frames on call stack

Next, we define byte-size value and memory model:

$$v_{\text{byte}} : \text{Val}_{\text{byte}} \triangleq \text{ } \not\text{ } \mid i \in [0, 2^8) \mid l_{\{0|1|2|3\}} \mid \text{null}$$

$$\begin{array}{c}
\text{ES-BINOP} \\
\frac{\llbracket \text{oplus} \rrbracket(v_1, v_2) = v'}{\sigma \vdash v_1 \oplus v_2 \rightarrow_{Expr} v'} \\
\\
\text{ES-DEREF} \\
\frac{\vdash_{\text{tychk}} v : t \quad \sigma \vdash l \mapsto \text{encode}(v)}{\sigma \vdash !_{\tau} l \rightarrow_{Expr} v} \\
\\
\text{ES-FST} \\
\sigma \vdash (v_1, v_2).1 \rightarrow_{Expr} v_1 \\
\\
\text{ES-SND} \\
\sigma \vdash (v_1, v_2).2 \rightarrow_{Expr} v_2
\end{array}$$

Figure 1: Semantics of expression evaluation

$$\begin{array}{c}
\text{SS-ASSIGN} \\
(l \leftarrow v, \sigma) \rightarrow_{Stmts} (\text{skip}, \sigma[l \mapsto \text{encode}(v)]) \\
\\
\text{SS-SEQ} \\
(\text{skip}; s, \sigma) \rightarrow_{Stmts} (s, \sigma) \\
\\
\text{SS-SEQ-HEAD} \\
\frac{(s_1, \sigma) \rightarrow_{Stmts} (s'_1, \sigma')}{(s_1; s_2, \sigma) \rightarrow_{Stmts} (s'_1; s_2, \sigma')}
\end{array}$$

Figure 2: Semantics of statements execution

We define state  $\sigma : \text{State} \triangleq [l \mapsto v_{\text{byte}}]$ , i.e. a heap in which every address maps to a byte-level value.

You may notice the difference between  $Val$  and  $Val_{\text{byte}}$ , it is intentional to create a layer of abstraction for easier manipulation of spatial assertions and also a clean, unified language syntax.

**Small-Step Operational Semantics.** We define the HNF small step semantics for both expression  $(\sigma \vdash e \rightarrow_{Expr} e')$  and statements  $((s, \sigma) \rightarrow_{Stmts} (s', \sigma'))$ , and then combine them together  $((e^\dagger, \sigma) \rightarrow_{\text{cur}} (e'^\dagger, \sigma), (s^\dagger, \sigma) \rightarrow_{\text{cur}} (s'^\dagger, \sigma'))$ .

XXX: Define  $\sigma \vdash l \mapsto v_{\text{byte}}$

### 1.3 Type System and Environment

**Local Typing Rules.** The types are defined in §1.1, and all values in  $v$  can be *locally* typed trivially (since  $v$  is introduced to reflect type structure in some sense). Nevertheless, due to the fact that language of study is weakly-typed in the vein of C, we still have some “weird” rules worth documenting.

Here we define local typing judgment  $\vdash_{\text{tychk}} v : \tau$  for values.

$$\begin{array}{c}
\text{TYCHK-VOID} \quad \vdash_{\text{tychk}} \text{void} : \tau_{\text{void}} \quad \text{TYCHK-NULL} \quad \vdash_{\text{tychk}} \text{null} : \tau_{\text{null}} \quad \text{TYCHK-INT8} \quad \vdash_{\text{tychk}} i \in [0, 2^8) : \tau_{\text{int8}} \quad \text{TYCHK-INT32} \quad \vdash_{\text{tychk}} i \in [0, 2^{32}) : \tau_{\text{int32}} \\
\\
\text{TYCHK-NULL-PTR} \quad \forall \tau. \vdash_{\text{tychk}} \text{null} : * \tau \quad \text{TYCHK-PTR} \quad \forall \tau, l. \vdash_{\text{tychk}} l : * \tau \quad \text{TYCHK-PROD} \quad \frac{\vdash_{\text{tychk}} v_1 : \tau_1 \quad \vdash_{\text{tychk}} v_2 : \tau_2}{\vdash_{\text{tychk}} (v_1, v_2) : \tau_1 \times \tau_2}
\end{array}$$

**Typing Environment** When variables are introduced, we will need an environment  $\Gamma : Env \triangleq [x \mapsto (\tau, l)]$  to “unfold” the meaning of variables.

In Iris-OS, variables are all unfolded before the running the function body, which saves the program logic from caring about the lexical environment. During this unfolding, we will replace variables with either their location (left-hand side) or the dereference of their location (right-hand side). And we will also produce a pointer arithmetic expression when processing the left-hand side expression, which requires type inference  $\vdash_{\text{tyinf}} e : \tau$  (since it is standard and trivial, we will leave out the details here).

Then we define the rules for “interpreting” left-hand side expression ( $\langle e \rangle_{\text{LHS}}$  or  $\langle s \rangle_{\text{LHS}}$ ) and right-hand side expression ( $\langle e \rangle_{\text{RHS}}$  or  $\langle s \rangle_{\text{RHS}}$ ). In all rules below, a  $\Gamma$  is implicitly captured, and if any operation on  $\Gamma$  failed, then the rule will be returning “invalid” in implementation.

$$\begin{aligned}
\langle e_1 \leftarrow e_2 \rangle_{\text{LHS}} &= \langle e_1 \rangle_{\text{LHS}} \leftarrow e_2 \\
\langle x \rangle_{\text{LHS}} &= \Gamma(x).l \\
\langle !e \rangle_{\text{LHS}} &= \langle e \rangle_{\text{RHS}} \\
\langle e.1 \rangle_{\text{LHS}} &= \langle e \rangle_{\text{LHS}} \\
\langle e.2 \rangle_{\text{LHS}} &= \langle e \rangle_{\text{LHS}} + \text{sizeof}(\tau) \quad \text{if } \vdash_{\text{tyinf}} \langle e \rangle_{\text{LHS}} : *(\tau_1 \times \tau_2) \\
\langle l \rangle_{\text{LHS}} &= l
\end{aligned}$$

$$\begin{aligned}
\langle e_1 \leftarrow e_2 \rangle_{\text{RHS}} &= e_1 \leftarrow \langle e_2 \rangle_{\text{RHS}} \\
\langle x \rangle_{\text{RHS}} &= !_{\Gamma(x).t} \Gamma(x).l \\
\langle !e \rangle_{\text{RHS}} &= !_\tau \langle e \rangle_{\text{RHS}} \quad \text{if } \vdash_{\text{tyinf}} e : *\tau
\end{aligned}$$

The cases not covered are defined recursively (and trivially)

## 2 Program Logic

This section describes how to build a program logic for the C language (*c.f.* §1) on top of the base logic of Iris.

### 2.1 Extended Assertions

Using the standard Iris assertions and the ownership of ghost heap resource, we can define some basic custom assertions:

$$\begin{aligned} l \mapsto_q v : t &\triangleq l \mapsto_q \text{encode}(v) \wedge \vdash_{\text{tychk}} v : t \\ l \mapsto v : t &\triangleq l \mapsto_1 v : t \\ l \mapsto_q - : t &\triangleq \exists v. l \mapsto_q v : t \end{aligned}$$

### 2.2 Weakest Precondition

Finally, we can define the core piece of the program logic, the assertion that reasons about program behavior: Weakest precondition, from which Hoare triples will be derived.

**Defining weakest precondition.** While we have fixed the program state  $\sigma$  in language definition, but it can be any state, as long as it has a predicate  $S : \text{State} \rightarrow iProp$  that interprets the physical state as an Iris assertion. For our heap state,  $S(\sigma) \triangleq \text{Phy}(\bullet \text{fmap}(\lambda v. (1, \text{agv}), \sigma))$ .

$$\begin{aligned} wp &\triangleq \mu wp. \lambda \mathcal{E}, E_{\text{cur}}, \Phi, \Phi_{\text{ret}}. \\ &(\exists v. \text{to\_val}(E_{\text{cur}}) = v \wedge \models_{\mathcal{E}} \Phi(v)) \vee \\ &(\exists v. \text{to\_ret\_val}(E_{\text{cur}}) = v \wedge \models_{\mathcal{E}} \Phi_{\text{ret}}(v)) \vee \\ &\left( \text{to\_val}(E_{\text{cur}}) = \perp \wedge \text{to\_ret\_val}(E_{\text{cur}}) = \perp \wedge \right. \\ &\quad \forall \sigma. S(\sigma) \stackrel{\mathcal{E}}{\equiv} *^{\emptyset} \\ &\quad \text{red}(e, \sigma) * \triangleright \forall E'_{\text{cur}}, \sigma'. (E_{\text{cur}}, \sigma \rightarrow E'_{\text{cur}}, \sigma') \stackrel{\emptyset}{\equiv} *^{\mathcal{E}} \\ &\quad \left. S(\sigma') * wp(\mathcal{E}, E'_{\text{cur}}, \Phi, \Phi_{\text{ret}}) \right) \end{aligned}$$

Here are some conventions:

- If we leave away the mask, we assume it to default to  $\top$ .
- We will leave  $\dagger$  out when writing  $E_{\text{cur}}$  in WP.
- $\Phi$  in post-condition might or might not take a value parameter, depending on the context.

**Laws of weakest precondition.** The following rules can all be derived:

$$\begin{array}{c}
\text{WP-VALUE} \quad \Phi(v) \vdash \text{wp}_{\mathcal{E}} v \{\Phi; \Phi_{\text{ret}}\} \quad \text{WP-SKIP} \quad \Phi(\text{void}) \vdash \text{wp}_{\mathcal{E}} v \{\Phi; \Phi_{\text{ret}}\} \quad \text{WP-RET} \quad \Phi_{\text{ret}}(v) \vdash \text{wp}_{\mathcal{E}} \text{return } v \{\Phi; \Phi_{\text{ret}}\} \\
\\
\text{WP-STRONG-MONO} \quad \frac{\mathcal{E}_1 \subseteq \mathcal{E}_2}{((\forall v. \Phi(v) \Rightarrow_{\mathcal{E}_2} \Psi(v)) \wedge (\forall v. \Phi_{\text{ret}}(v) \Rightarrow_{\mathcal{E}_2} \Psi_{\text{ret}}(v))) * \text{wp}_{\mathcal{E}_1} E_{\text{cur}} \{\Phi; \Phi_{\text{ret}}\} \vdash \text{wp}_{\mathcal{E}_2} E_{\text{cur}} \{\Psi; \Psi_{\text{ret}}\}} \\
\\
\text{FUP-WP} \quad \frac{}{\Rightarrow_{\mathcal{E}} \text{wp}_{\mathcal{E}} E_{\text{cur}} \{\Phi; \Phi_{\text{ret}}\} \vdash \text{wp}_{\mathcal{E}} E_{\text{cur}} \{\Phi; \Phi_{\text{ret}}\}} \\
\\
\text{WP-FUP} \quad \text{wp}_{\mathcal{E}} E_{\text{cur}} \{x. \Rightarrow_{\mathcal{E}} \Phi(x); x. \Rightarrow_{\mathcal{E}} \Phi_{\text{ret}}(x)\} \vdash \text{wp}_{\mathcal{E}} e \{\Phi; \Phi_{\text{ret}}\} \\
\\
\text{WP-BIND} \quad \text{wp}_{\mathcal{E}} e \{x. \text{wp}_{\mathcal{E}} K(x) \{\Phi; \Phi_{\text{ret}}\}; \Phi_{\text{ret}}\} \vdash \text{wp}_{\mathcal{E}} K(e) \{\Phi; \Phi_{\text{ret}}\} \quad \text{WP-OP} \quad \frac{\llbracket \text{oplus} \rrbracket(v_1, v_2) = v'}{\Phi(v') \vdash \text{wp}_{\mathcal{E}} v_1 \oplus v_2 \{\Phi; \Phi_{\text{ret}}\}} \\
\\
\text{WP-ASSIGN} \quad \frac{\vdash_{\text{tychk}} v : \tau' \quad \sqrt{(\tau \leftarrow \tau')}}{l \mapsto - : \tau * (l \mapsto v : \tau * \Phi) \vdash \text{wp}_{\mathcal{E}} l \leftarrow v \{\Phi; \Phi_{\text{ret}}\}} \\
\\
\text{WP-ASSIGN-OFFSET} \quad \frac{\vdash_{\text{tychk}} v_2 : \tau'_2 \quad \sqrt{(\tau_2 \leftarrow \tau'_2)}}{b\#o \mapsto (v_1, -) : \tau_1 \times \tau_2 * (b\#o \mapsto (v_1, v_2) : \tau_1 \times \tau_2 * \Phi) \vdash \text{wp}_{\mathcal{E}} b\#(o + \text{sizeof}(\tau_1)) \leftarrow v_2 \{\Phi; \Phi_{\text{ret}}\}} \\
\\
\text{WP-LOAD} \quad l \mapsto_q v : \tau * (l \mapsto_q v : \tau * \Phi(v)) \vdash \text{wp}_{\mathcal{E}} !_{\tau} l \{\Phi; \Phi_{\text{ret}}\} \\
\\
\text{WP-SEQ} \quad \text{wp}_{\mathcal{E}} s_1 \{\text{wp}_{\mathcal{E}} s_2 \{\Phi; \Phi_{\text{ret}}\}; \Phi_{\text{ret}}\} \vdash \text{wp}_{\mathcal{E}} s_1; s_2 \{\Phi; \Phi_{\text{ret}}\} \quad \text{WP-WHILE-TRUE} \quad \frac{\text{wp}_{\mathcal{E}} s; \text{while}_e(e) \{s\} \{\Phi; \Phi_{\text{ret}}\}}{\text{wp}_{\mathcal{E}} \text{while}_e(\text{true}) \{s\} \{\Phi; \Phi_{\text{ret}}\}} \\
\\
\text{WP-WHILE-FALSE} \quad \frac{\Phi(\text{void})}{\text{wp}_{\mathcal{E}} \text{while}_e(\text{false}) \{s\} \{\Phi; \Phi_{\text{ret}}\}} \\
\\
\text{WP-WHILE-INV} \quad \frac{\forall \Phi. (I * (\forall v. (v = \text{false} * Q(\text{void})) \vee (v = \text{true} * I)) \multimap \Phi(v)) \multimap \text{wp } e \{\Phi; \text{True}\} \quad \forall \Phi. (I * (I \multimap \Phi(\text{void}))) \multimap \text{wp } s \{\Phi; \Phi_{\text{ret}}\}}{I \vdash \text{wp while}_e(e) \{s\} \{Q; \Phi_{\text{ret}}\}}
\end{array}$$