

Ismail Guedira

Engineering student in Integrated Electronic
Circuits

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Education

- 2013 – Present **Engineering School at Grenoble Institute of Technology.**
- Exchange student at EPFL (École Polytechnique Fédérale de Lausanne).
 - Specialization program on Integrated Electronic Systems, studying digital, analog integrated circuits and RF designs
- 2011 – 2013 **Post-secondary preparatory school, Lycée Descartes, Tours.**
Two-year intensive program in mathematics and physics preparing for the national competitive exam for entry to engineering schools

Experience

- Sept. 2015 **Design of a Bandgap tension reference, EPFL, Neuchâtel, Suisse.**
Present Semester project with the ESPLAB (Electronic and Signal Processing Laboratory), the main purpose of this project was the design of tension reference weakly sensitive to power supply, parasitic noise and temperature variation.
- May 2015 **Rolls-Royce Civil Nuclear Instrumentation & Control, Meylan, France,**
to August 2015 Internship period (4 months).
Integrate Mathcad software into the electronic process flow.
With the joint effort of the whole electronic design team, we established an efficient methodology to use Mathcad for time and efficiency saving
- February 2015 **Project : Design of Analog-to-Digital Converter.**
to April 2015 During a mixed analog and digital project, I have achieved every steps from system study to the layout.
- August 2014 **ST Microelectronics, Crolles, France,**
Internship Period (5 weeks).
During these weeks in a clean room through night shifts, I was responsible for preparing test wafers for the different units in the Crolles plant.
- February 2014 **Vice-President at the Junior Enterprise.**
to February 2015
 - Manage a team of 38 people,
 - Organisation of the School - Firm Forum,
 - Prospecting new partners and negotiate contracts,
 - Recruitment and Formation of the new team,
 - Presenting the association in various events and forums

Skills

- Computer Science Programmation en langage C : Réalisation d'un émulateur de microprocesseur MIPS
- Software Modélisation et simulation de circuits analogique et numérique avec **Cadence**, de circuits de communications numériques avec **Matlab** et **Simulink** et de circuits micro-ondes avec **ADS**
- Anglais **fluent – BULATS - C1 83 / 100**

Interests

- Basket-Ball Pratique régulière en club pendant 10 ans et dans le cadre du sport universitaire actuellement