EEGR 471 Final Presentation Template

USE A MORGAN THEME

Introduction

- Team Name
- Team Members

Design

- Explain and Talk about the ASMD_Multiplier in your own owns
- What is it used for.
- Why would it be helpful

Verification

- Talk about your verification plan
    - Stimulus
    - Monitoring outputs
    - Coverage

Synthesis

- Talk about issues in synthesis
  - Your labs in Logic equilvalency

Chip & Board

- Talk about the chip

- Efabless board:
  - https://github.com/efabless/caravel_board/blob/main/docs/Caravel_Board_Specs%20_clean.pdf
  - https://github.com/efabless/caravel_board/blob/main/docs/caravel_datasheet.pdf

- How would you design a board. Any improvements you would add?

Visual Inspection

- What do you check for
- What are obvious signs of a bad board

Board Test

- When do you add power

- What do you look for when you add power

- How do you check the clock, inputs/outputs.
  - See apple bring-up slides in sections directory.
  - https://github.com/isomoye/EEGR471/blob/main/sections/Apple_Bring-Up_And_Test_Morgan.pdf

JTAG

- What is boundary scan.
  - Look in notes and slides
- Talk about XJTAG
  - Look in notes and slides
  - We used the XJTAG xjlin2 in class
    - https://www.xjtag.com/products/hardware/xjlink-xjlink2-controller/
  - What are the benefits
  - How would you use it to test the board?

Human Machine Interface

- Explain HMI in your words
- List all HMI's into the caravel board that you find
  - Think Lab 7
- How would you test each HMI
- Talk about lab 8