

CST Part IA: Digital Design, SV 1
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1 Ex1

	A	B	\bar{a}	$\overline{\bar{a} + b}$	\bar{b}	$\overline{a + \bar{b}}$	x
1.1.1	0	0	1	1	1	1	0
	0	1	1	0	0	1	1
	1	0	0	1	1	0	1
	1	1	0	1	0	1	0
	0	0	1	0	1	0	1
	0	1	1	0	0	1	0
	1	0	0	1	1	0	0
	1	1	0	0	0	0	1

The first circuit is called XOR.

the second circuit is called NOR.

$$1.2.1 \quad a.b.c + a.c.\bar{c} = a.b.(c + \bar{c}) = ab$$

$$1.2.2 \quad a(\bar{a} + b) = a\bar{a} + ab = 0 + ab = ab$$

$$1.2.3 \quad (a + c)(\bar{a} + b)$$

$$\bar{a}c + ab + cb$$

$$\bar{a}c + ab + (\bar{a} + a)cb$$

$\bar{a}c + ab$ (adsorption) btw. this is consensus

$$1.2.4 \quad (a + c)(a + d)(b + c)(b + d)$$

$$\underline{\bar{a}c + \bar{a}\bar{d}} + \underline{\bar{b}\bar{c} + \bar{b}\bar{d}}$$

$$\bar{a} + \bar{b} + \bar{c} + \bar{d}$$

$$ab + cd$$

$$1.2.x \quad x + (\bar{x}y) = (x + \bar{x})(x + y) = 1.(x + y) = x + y$$

To prove the second equation, take complement on both side. $\overline{x + (\bar{x}y)} = \overline{x + y}$

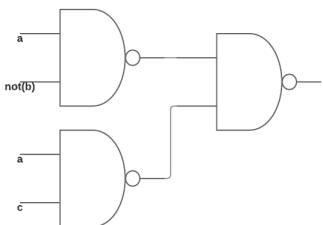
Then the LHS will be: $\bar{x}(x + \bar{y}) = \bar{x}x + \bar{x}\bar{y} = \bar{x}\bar{y}$

So proved: $\overline{x + y} = \bar{x}\bar{y}$

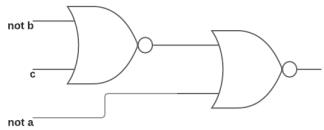
To prove the third one, just replace x and y with \bar{x} and \bar{y}
and take implement on the both side on the second equation.

so proved: $\bar{x} + \bar{y} = \bar{x}\bar{y}$

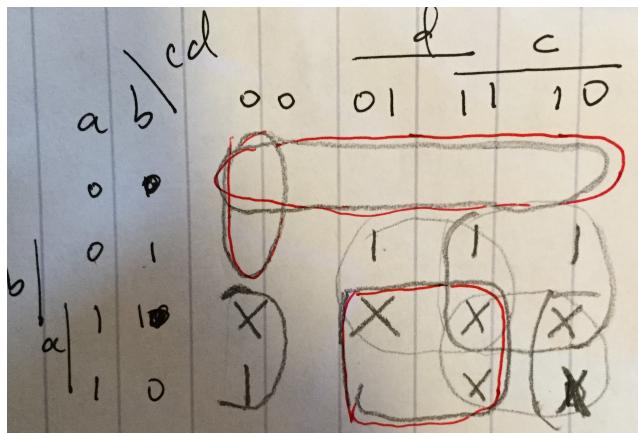
$$1.7.a \quad a\bar{b} + ac = \overline{\bar{a}\bar{b}.ac}$$



$$1.7.b \quad a(\bar{b} + c) = \overline{\bar{b} + c} + \bar{a}$$



$$1.8 \quad g = 0101 + 0110 + 0111 + 1000$$



$$g = bd + bc + a\bar{d}$$

$$\bar{g} = \bar{a}\bar{b} + \bar{a}\bar{c}\bar{d} + ad$$

$$g = (a+b)(a+c+d)(\bar{a}+\bar{d})$$

$$1.11 \quad (a) 100 \ 50 \ 25 \ 12 \ 6 \ 3 \ 1$$

reverse 0010011 is 01100100

38 19 9 4 2 1

reverse 011001 is 100110

not 00100110 is 11011001

then +1 is 11011010

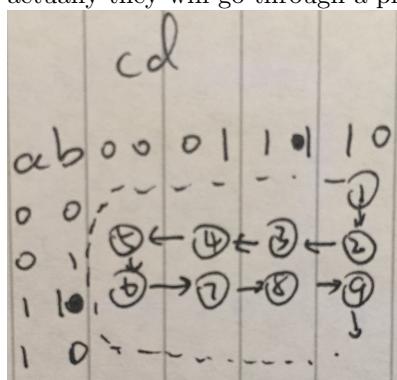
so 100 = 01100100₂ and -38 = 11011010₂

01100100 + 11011010 = 100111110

remove head 1 and this is 00111110 or (58)₁₀ or (3E)₁₆ which is the attempted answer.

0010 0110 0111 0101 0100 1100 1101 1111 1110 1010

actually they will go through a properly drawn 4-4 K-map in a zigzag way.



2 Ex2

I guess those are lists of binary code but no idea how to decode..

3 Ex3

(a) There are 16 possibility.

A	B	output
0	0	x
0	1	x
1	0	x
1	1	x

One truth table means one possible function. Then to fill each x with 0 or 1.

There are 2^4 possibility.

(b) $\bar{a} = \overline{0+a}$

notice $\overline{\bar{a} + \bar{b}} = a.b$
so $ab = \overline{0+a} + \overline{0+b}$

(c) $\bar{a} = (0 \equiv a) = (1 \oplus a)$

$a+b = (ab) \oplus (a \oplus b)$

4 Ex4



(a) The common demonstra-

tion of DeMorgan theorem can be shown on a Venn diagram.

If we have two set a and b as the Venn diagram shown.

Then we have:

1. For \overline{ab} we can see it exactly cover the area of $\bar{a} + \bar{b}$. So they are equal.

$$\overline{abc\dots} = \bar{a} + \bar{b} + \bar{c}\dots$$

2. For $\overline{a+b}$ this actually means neither of a or b is in the set.

In this way, we can also say both of not a or not b is in the set.

$$\overline{a+b+c\dots} = \bar{a}\bar{b}\bar{c}\dots$$

(b) Minterms: the junction of all variables in complemented or uncomplemented form

Essential terms: covers a term that no other term covers it. (Do you mean Essential Prime terms or not?)

prime terms: cannot be further combined.

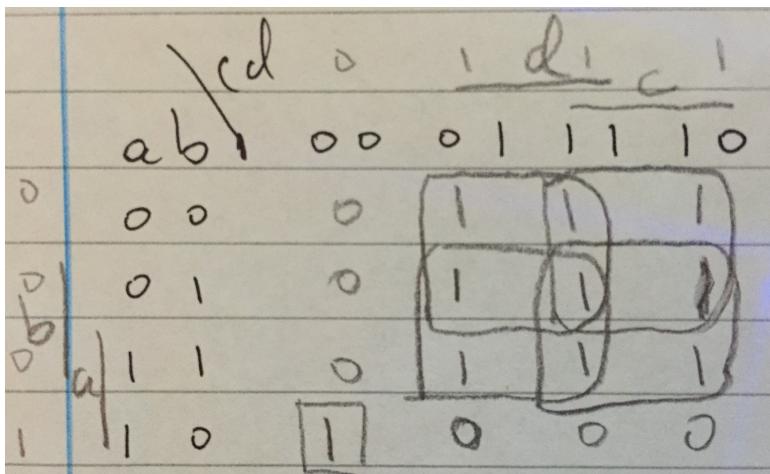
(c) sum of products form: disjunctions of junction (OR of AND variables).

minimised sum of products form: simplified SOP by K-map or Q-M method

disjunctive normal form: the disjunction of its minterms

(d) All circles are essential prime term.

$$\text{minimum SOP: } bd + bc + \bar{a}d + \bar{a}c + \bar{a}\bar{b}\bar{c}\bar{d}$$



(e) Don't care ab means we can put x in $(a,b) = (1,1)$ row on the K-map.

In this way, Only 3 circle is left and all of them are prime and essential. $\bar{a}d + \bar{a}c + a\bar{c}\bar{d}$

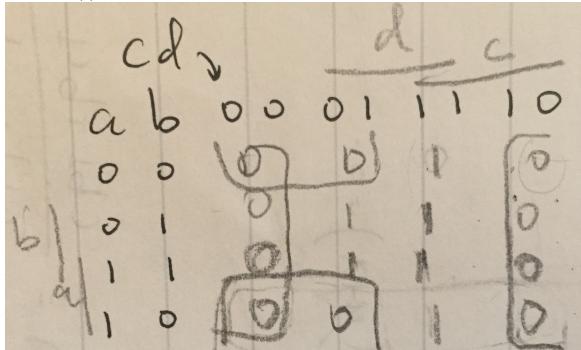
(f) For this question:

$$\bar{f} = \bar{a}\bar{c}\bar{d} + b\bar{c}\bar{d} + a\bar{b}d + a\bar{c}d$$

$$\text{minimum POS: } f = (a+c+d)(\bar{b}+c+d)(\bar{a}+b+\bar{d})(\bar{a}+c+\bar{d})$$

Obviously, they have the same number of terms and literals.

A NEW K-MAP:



$$\text{SOP: } g = bd + cd \text{ 4 terms (3 gates)}$$

$$\text{POS: } g = d(b+c) \text{ 3 terms (2 gates)}$$

Note under this situation, POS gets 1 less term than SOP.

5 Ex5

$$(a) \frac{(X+Y)(X+Z)}{\bar{X}\bar{Y} + \bar{X}\bar{Z}} \\ \frac{\bar{X}(Z+\bar{Y})}{X+YZ}$$

(b) The same as 1.2.3.

(c) Tristate Buffer

$$(d) (A+B+\bar{C}DE)(A+\bar{D}+E)(\bar{A}+C) \\ (AC + \bar{A}(B+\bar{C}DE))(A+\bar{D}+E) \\ (AC + \bar{A}B + \bar{A}\bar{C}DE)(A+\bar{D}+E) \\ AC + A\bar{C}\bar{D} + ACE + 0 + \bar{A}\bar{D}B + \bar{A}BE + 0 + 0 + \bar{A}\bar{C}DE \\ AC + \bar{A}\bar{D}B + \bar{A}BE + \bar{A}\bar{C}DE$$

- (e) cell adjacency: 010 011 001 000 100 101 111 110
method: $a(n-1)$ and reversed $a(n-1)$ then add 0 or 1 to head.

			$\overbrace{de \quad 0 \quad 0 \quad 0 \quad 1 \quad 1 \quad 1}^{\text{PE}}$	\overbrace{b}^{D}
a	b	c	1 1 1	0
0 1 0	1	1	1 1 1	0
0 1 1	1	1	1 1 1	0
0 0 1	0	0	0 0 0	0
0 0 0	0	0	0 0 1	0
1 0 0	0	0	0 0 0	0
1 0 1	1	1	1 1 1	1
1 1 1	1	1	1 1 1	1
1 1 0	0	0	0 0 0	0

- (f) picture shows the Q-M method.

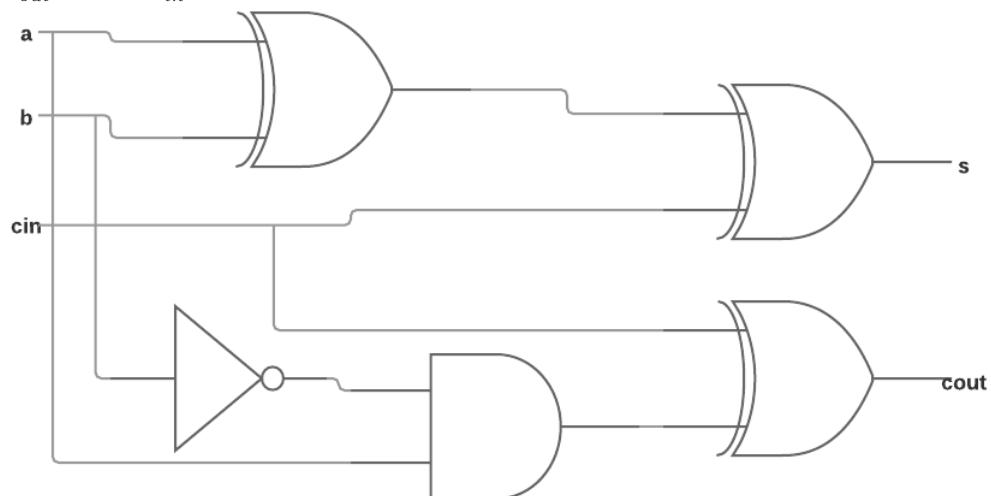
$A\bar{C} + \bar{A}\bar{B}B + \bar{A}BE + \bar{A}CDE$	
3	0 0 0 1 1 ✓ ①
8 1 6	0 1 0 0 0 ✓ ②
9 1 7	0 1 0 0 1 ✓ ③
1 1 1 9	0 1 0 1 1 ✓ ④
1 2 2 4	0 1 1 0 0 ✓ ⑤
1 3 2 5	0 1 1 0 1 ✓ ⑥
1 5 2 7	0 1 1 1 1
2 0 4 0	1 0 1 0 0 ✓ ⑦
2 1 4 1	1 0 1 0 1 ✓ ⑧
2 2 4 2	1 0 1 0 1 ✓ ⑨
2 3 4 3	1 0 1 1 0 ✓ ⑩
2 8 5 6	1 1 1 0 0 ✓ ⑪
2 9 5 7	1 1 1 0 1
3 0 5 8	1 1 1 1 0 ⑫
3 1	1 1 1 1 1
	0 1 - 0 -
	0 1 1 1 1 ✓ ⑬
	1 1 1 - 1 ✓
	1 - 1 1 1 ✓
	2 1 2 2 2 3 2 6 2 9 3 0 3)
$\bar{A}CDE$	0 1 0 0 ⑭ X
	0 - 0 1 1 ⑮
	- 1 1 0 1
	0 1 - -
	- 1 1 - 1
	AC - 1 - -
	Let $\bar{A}B\bar{D}$ 0 1 - 0 -

6 Ex6

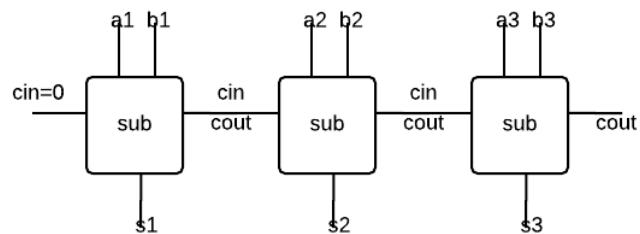
<i>a</i>	<i>b</i>	<i>c_{in}</i>	<i>s</i>	<i>c_{out}</i>
0	0	0	0	0
0	1	0	1	0
1	0	0	1	1
1	1	0	0	0
0	0	1	1	1
0	1	1	0	0
1	0	1	0	1
1	1	1	1	1

$$s = a \oplus b \oplus c_{in}$$

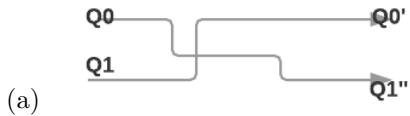
$$c_{out} = a \bar{b} \oplus c_{in}$$



The circuit above have input a b cin and output s and cout.
 cin: 1 means need to subtract 1 0 means not.
 cout: 1 means need to borrow 1 from higher digital 0 means not.



7 Ex7



- (b) The 0 0 means clear so once the first input is 0 0 then the output H should also be clear. For C, once b is logical 0 then it must be 0 (no need for add 1 to next digit) otherwise it is also clear 0 0.

A1	A0	B1	B0	H1	H0	C1	C0
0	0	0	0	0	0	0	0
0	0	0	1	0	0	1	0
0	0	1	0	0	0	0	0
0	1	0	0	0	0	1	0
0	1	0	1	0	1	0	1
0	1	1	0	1	0	1	1
1	0	0	0	0	0	0	0
1	0	0	1	1	0	0	1
1	0	1	0	0	1	0	0

$$\begin{array}{r}
 \begin{array}{c} B_1 B_0 \\ \hline A_1 & A_0 & 0 & 0 & 0 & 1 & 1 & 1 & 0 \end{array} \\
 \begin{array}{c} B_0 H_1 \\ \hline B_1 \end{array}
 \end{array}$$

0	0	0	0	X	0		
A ₀	0	1	0	0	X	1	
A ₁	1	0	X	X	X	X	
	1	0	0	1	X	0	

$$H_1 = A_1 B_0 + A_0 B_1$$

$$\begin{array}{r}
 \begin{array}{c} B_1 B_0 \\ \hline A_1 & A_0 & 0 & 0 & 0 & 1 & 1 & 1 & 0 \end{array} \\
 \begin{array}{c} B_0 H_0 \\ \hline B_1 \end{array}
 \end{array}$$

0	0		X				
A ₀	0	1		1	X		
A ₁	1	0	X	X	X	X	
	1	0	0	1	X	1	

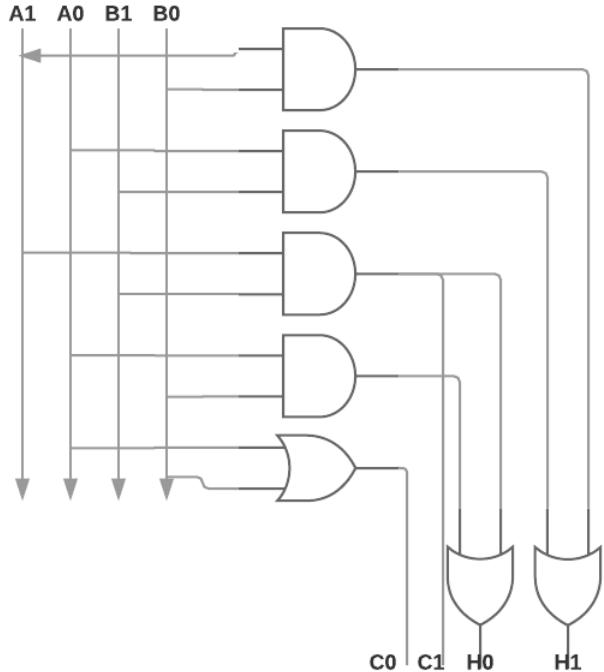
$$H_0 = A_0 B_0 + A_1 B_1$$

B_0	B_1	C_1
A_1	A_0	C_1
0	0	X
0	1	X
1	0	X
1	1	1

$$C_1 = A_1 B_1$$

B_0	B_1	C_0	C_1
A_1	A_0	C_0	C_1
0	0	1	X
0	1	1	X
1	0	X	X
1	1	1	X

$$C_0 = A_0 + B_0$$



(c) This needs 7 gates. Original circuit needs 3 gates.

(d) They both have $2T$ (ps) delay.

8 Ex8

(a) $\bar{U} = U\bar{X} + \bar{U}\bar{W}\bar{Y} + \bar{U}\bar{W}\bar{Z} + \bar{U}\bar{Z}Y + X\bar{W}\bar{U}$

(b) This is exactly the same as Ex5(e).

As change A to U, B to W, C to X, D to Y, E to Z.
so $P = UX + \bar{U}\bar{Y}W + \bar{U}WZ + \bar{U}\bar{X}YZ$

(c) picture

