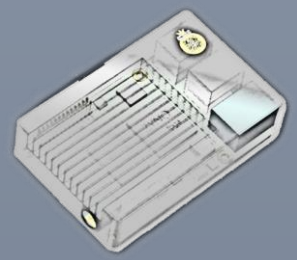

Free Software
FOUNDATION
自由软件基金会
RV一起学
GNU
• Chess
● BACKGAMMON
● GNOME Sudoku
音:吉牛
Only
99
+1
best wish & donated.
• Full-stack
join 全栈学习公社 Hacker and Company...
RV4KIDs
● gitee.com/RV4Kids/
RVWeekly
SIGer 学习小组
—青少年开源文化期刊
• CPU
• GPU
• FPGA
• ASIC
Shiliu Pi 99
DLP games 1099 + 262 民族棋中国史 = LuckyLudii
民族棋机器博弈排行榜
PI400 替代方案
JAVA + FCscript + C
中文 PYthon JS LuaJIT
OS: Debian GNU/Linux 10 (buster) aarch64
Host: Raspberry Pi 4 Model B Rev 1.4
Kernel: 5.10.0-openEuler-21.03-rpi
1. LAMP 站
2. 爬虫机器
3. 语音助手
4. 内网穿透
5. 智能网关
课程:
RV4Kids:
- RVWeekly
- PLCT
- EEworld
- B站
专业:
1. 计算机组成与设计: RISC-V 2. RISC-V 指令集开源软件生态 3. 方舟 编译技术入门与实战
4. V8 for RISC-V
5. Verilog 入门教学
6. 开发一个RISC-V上的操作系统
学习: 建立自己的知识地图
QEMU
FPGA
RV32I
YARVI
QEMU + FPGA => SoC
FLAME?
99个 PI回收 PICYCLE
右幅派 漂流计划

https://gitee.com/shiliupi/shiliupi99

```
=== Shiliu Si 99 ===
```



FSBL

UBoot

bbl

Kernel

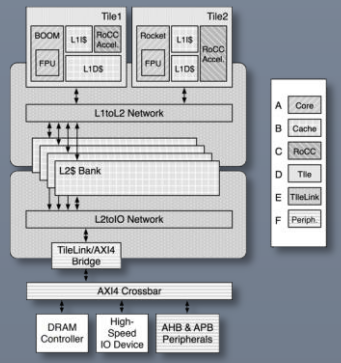
Shiliu Pi 99

Root filesystem

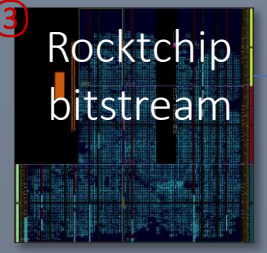
OpenWrt

linux

LuCI



③ Rocktchip bitstream



⑧ SSH

console

⑦ Ethernet

ZYNQ 7020

ZedBoard ⑥

HTIF ④

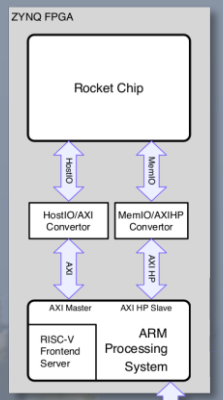
⑤ ARM

(Processing System)

PS

RV

PL (Programmable Logic)



DRAM

1. **Target Application** (riscv binary)
2. **RISC-V Kernel** (RISC-V Linux)
3. **Rocket Chip** (Core on FPGA <> host ARM on Zynq via AXI)
4. (RISC-V) **Front-end Server** (on ARM core <> Rocket Chip)
5. **Zynq ARM Core** (Linux on Soc)
6. **Development Board** (ZedBoard/Shiliu PI&SD card)
7. **External Communication** (PC <> FPGA over Ethernet.)
8. **User Interface** (SSH Client terminal on PC.)

RV4KIDs

bbl(UC)

Berkeley Bootloader

openEuler(中科) ②

linux

GNU Chess ①

Step1: Project Setup

- Download the files needed
- Project setup

Step2: Genetate the files for zedboard

- Bitstream
- FSBL(first stage boot loader)
- uboot for the Zynq ARM Core
- boot.bin
- Other files

Step3: Test

- SSH root@192.168.1.5
- Booting Up and Interfacting with the RISC-V Rocket Core

④ The **HTIF** is the Host/Target interface, which has the front-end server (**riscv-fesvr**, running on your host compter) communicating with the target design (Sodor). The riscv-fesvr loads the binary into the Sodor memory via the HTIF mem ports, and then uses the Control/Status Registers to bring the core out of reset. Once the program is finished, Sodor tells the riscv-fesvr it is finished via the tohost CSR and simulation ends.

HTIF is a non-standard tool for Berkeley processors, so there's no documentation on it. It is going to disappear soon, as the RISC-V platform spec is released and the cores are updated to be self-hosting.

WLAN: ShiliuPi99

root@99.99.99.99.

~# ./run-rv-linux.sh

gnuchess

white(1): e4

my move is: ?

Shiliu Pi 99

1. Model 2. Reconstruct 3. Map 4. 贡献 5. 建立. 6. 丰富

DLP games 1099 + 262 民族棋中国史 = LuckyLudii

民族棋
机器博弈
排行榜

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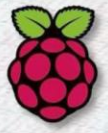

PI400
替代
方案

1. LAMP 站
2. 爬虫机器
3. 语音助手
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5. 智能网关

课程:

1. 数字系统设计
2. 计算机体系结构
3. 软硬件接口
4. 操作系统
5. 计算机软硬件算法

RV4Kids:

- RVWeekly
- PLCT
- EEworld
- B站

专业:

- 自动化
- 微电子
- 电子信息
- 计算机

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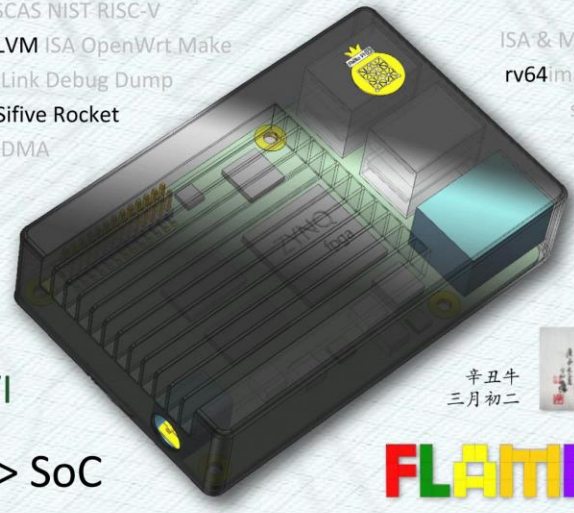
学习: *建立自己的知识地图*

浪潮 浙江大学 清华大学 蜂鸟 ISCAS NIST RISC-V
芯来 Proteus GCC LLVM ISA OpenWrt Make
Link Debug Dump
Sifive Rocket
DMA

QEMU

ISA & MMU
rv64imafdc
sv39



RV32I
YARVI

QEMU + FPGA => SoC

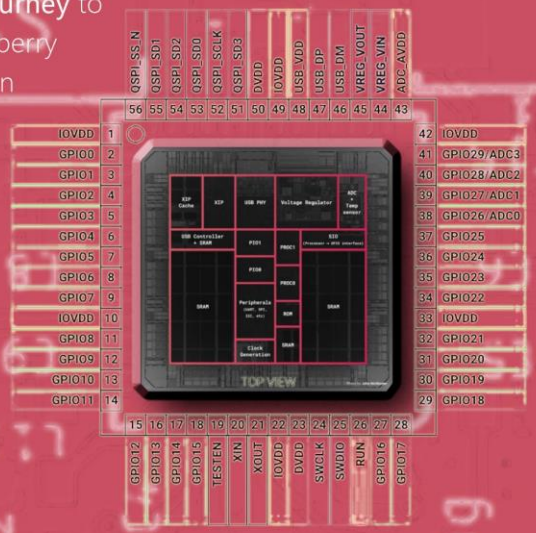
99个
PI回收
PICYCLE

石榴派
漂流
计划

辛丑牛
三月初二

FLAME?

- Raspberry Silicon: Raspberry **Pi Pico** $4
- Raspberry Silicon: **RP2040** $1
- The **journey** to Raspberry Silicon



RV4KIDs

第2期 2nd

SIGer 学习小组

2021.6.20

—青少年开源文化期刊

- RVWeekly

- Shiliu PI -> Shiliupi Silicon
  = **Shiliu Si** = 石榴核
- Shiliu PI 99 risc-v
  on **FPGA**

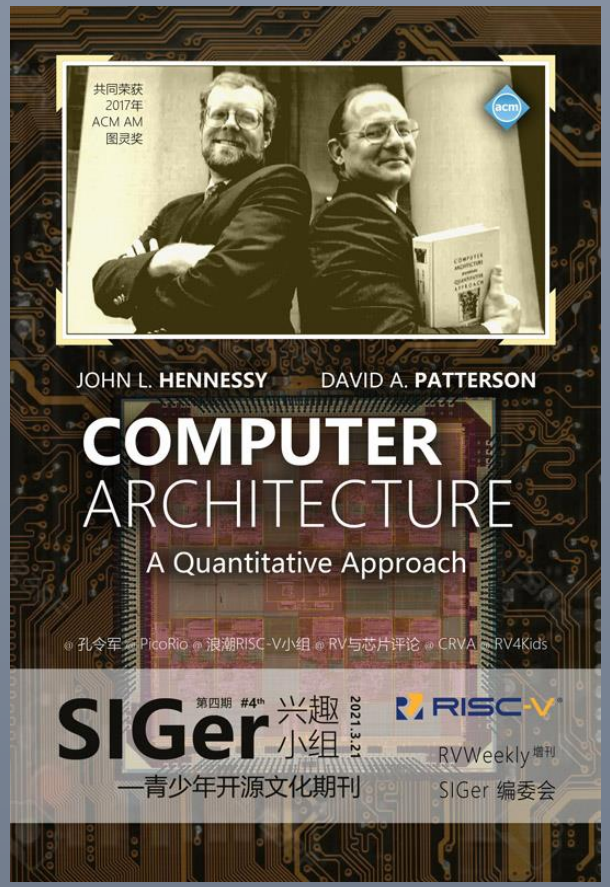
SIGer 编委会

A microcontroller **Datasheet**

RV4Kids 为科普而生！
让我们展开双臂拥抱青少年

国内版 国际版

RV4kids 开源 Microsoft Bing



RV4kids 开源 百度一下