



Track: use ne

Design Rules Checker

- Refill all zones before performing DRC Test for parity between PCB and schematic
 Report all errors for each track

[Violations \(9\)](#) [Unconnected Items \(0\)](#) [Schematic Parity \(not run\)](#) [Ignored Tests \(5\)](#)

▼ Warning: Silkscreen clipped by board edge

Rectangle on Edge.Cuts
Reference field of Motor_IRS1

▼ Warning: Silkscreen clipped by solder mask

Reference field of U6

▼ Warning: Silkscreen clipped by solder mask

Rectangle of U6 on F.Silkscreen

▼ Warning: Silkscreen clipped by solder mask

Reference field of J1

▼ Warning: Silkscreen clipped by solder mask

Reference field of C2

Show: All Errors 0 Warnings 9 Exclusions

[Delete Marker](#) [Delete All Markers](#)

[Run DRC](#)

[Close](#)