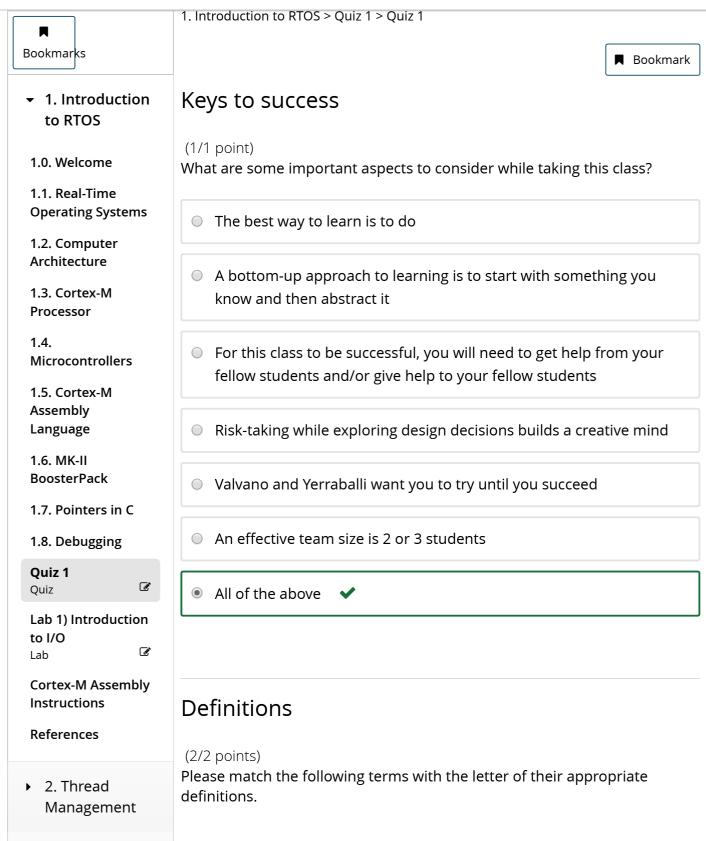


▶ 3. Time

Management

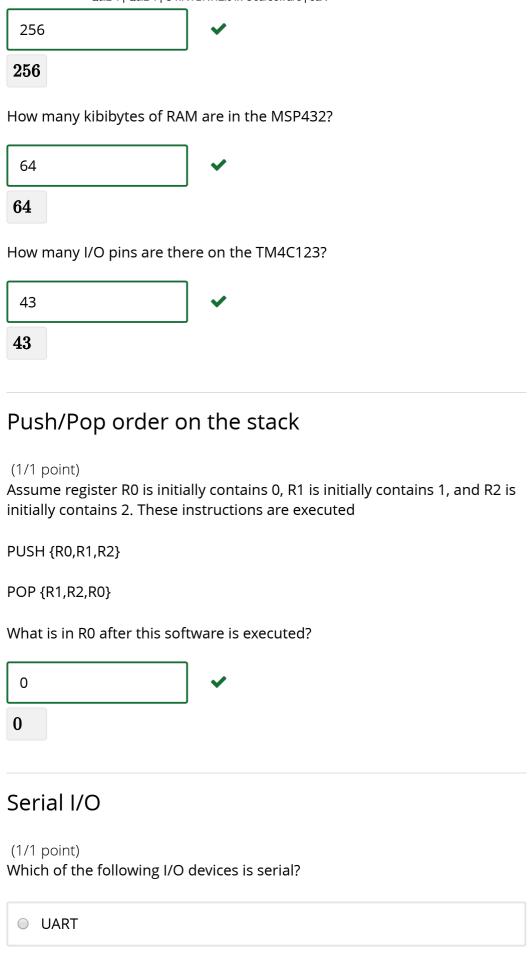
## UTAustinX: UT.RTBN.12.01x Realtime Bluetooth Networks

Help



4.5.1	Latency		
<ul><li>4. Real-time Systems</li></ul>	С	A. A guarantee to meet all deadlines	
<ul><li>Discussion</li><li>Board</li></ul>	<b>✓</b>		
Doard	Evolution	1	
	D	B. Amount of information transferred or processed per time	
	•		
	Real time		
	А	C. Time delay from request to service	
	<b>✓</b>		
	Bandwidth		
	В	D. Incremental change to improve performance,	
		features and reliability	
	· ·		
	Operating system		
	E	E. Software layer between the application software and the hardware	
	<b>✓</b>	and the natuwate	
	Regular versus	s Real-time OS	
	11100011011		
	<ul> <li>(1/1 point)</li> <li>Which type of OS is simpler than the other?</li> <li>● Regular OS</li> <li>● RTOS ✓</li> <li>Which type of OS is fairness more important than timeliness?</li> </ul>		
	● Regular OS ✔		

O RTOS		
Which type of OS is average bandwidth more important than upper and low limits of bandwidth?		
● Regular OS ✔		
○ RTOS		
Which type of OS requires one to know all the hardware components?		
Regular OS		
● RTOS ✔		
Which type of OS handles plug and play? In other words a new hardware		
device can be added and the OS figures out how to use it.		
Regular OS		
RTOS		
Flash ROM		
(1/1 point) How many bytes in a kibibyte?		
1024		
1024		
How many kibibytes of Flash ROM are in the MSP432 and TM4C123?		



○ SSI or SPI				
O I2C				
O CAN				
Ethernet				
All of the above				
LCD interface				
(1/1 point) Which of the following I/O devices does the LCD in this lab use?				
○ UART				
● SSI or SPI ✔				
○ I2C				
O PWM				
ADC				
None of the above				
Accessing memory from assembly language				
(1/1 point) Let <b>Data</b> be a 32-bit global variable. Consider these assembly instructions:				

LDR R0,=Data What is the effect of executing these instructions?				
R0 has the contents of the <b>Data</b>				
R0 has the address of the current instruction				
R0 has the address of the variable <b>Data</b>				
None of the above				
ARM Architecture Procedure Call Standard				
(1/1 point) Which of the following is an AAPCS rule?				
The stack must be balanced				
● The first input parameter, if it exists, is passed in R0 ✓				
The output parameter, if it exists, is returned in R1				
The stack must be aligned to 4 bytes (word-aligned)				
<ul> <li>When one function calls another, the return address is pushed on the stack</li> </ul>				
Correct: The second, third and fourth input parameters would be passed in R1, R2, R3 respectively				
Debugging Terms				
(2/2 points)				

	Please match the following terms with the letter of their appropriate definitions.	
	Black-box testing C	A. A measure of the degree of perturbation caused in program performance by the debugging instrument itself
	Logic analyzer  D	B. Measures voltage amplitude versus time
	Intrusiveness A	C. Observe the inputs and outputs without looking inside
	Oscilloscope  B	D. Multiple channel digital storage device
	Dump E ✓	E. Record strategic information into arrays

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