

Circuit Theory and Electronics Fundamentals

Masters of Aerospace Engineer, Técnico, University of Lisbon

Laboratory Report

Group 37

Afonso Magalhães, nº95765
Fábio Monteiro, nº95786
Leonardo Encarnação, nº95816

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1 Introduction

The objective of this laboratory assignment is to choose an architecture of the Envelope Detector and Voltage Regulator circuits to build an AC/DC converter. We did this while paying attention to the merit of the project designed.

This merit is calculated exactly as the next equation:

$$M = \frac{1}{\text{cost} * (\text{ripple}(V_0) + \text{average}(V_0 - 12) + 1e - 6)} \quad (1)$$

Being the cost the following:

- cost = cost of resistors + cost of capacitors + cost of diodes
- cost of resistors = 1 monetary unit (MU) per kOhm
- cost of capacitors = 1 MU/ μF
- cost of diodes = 0.1 MU per diode

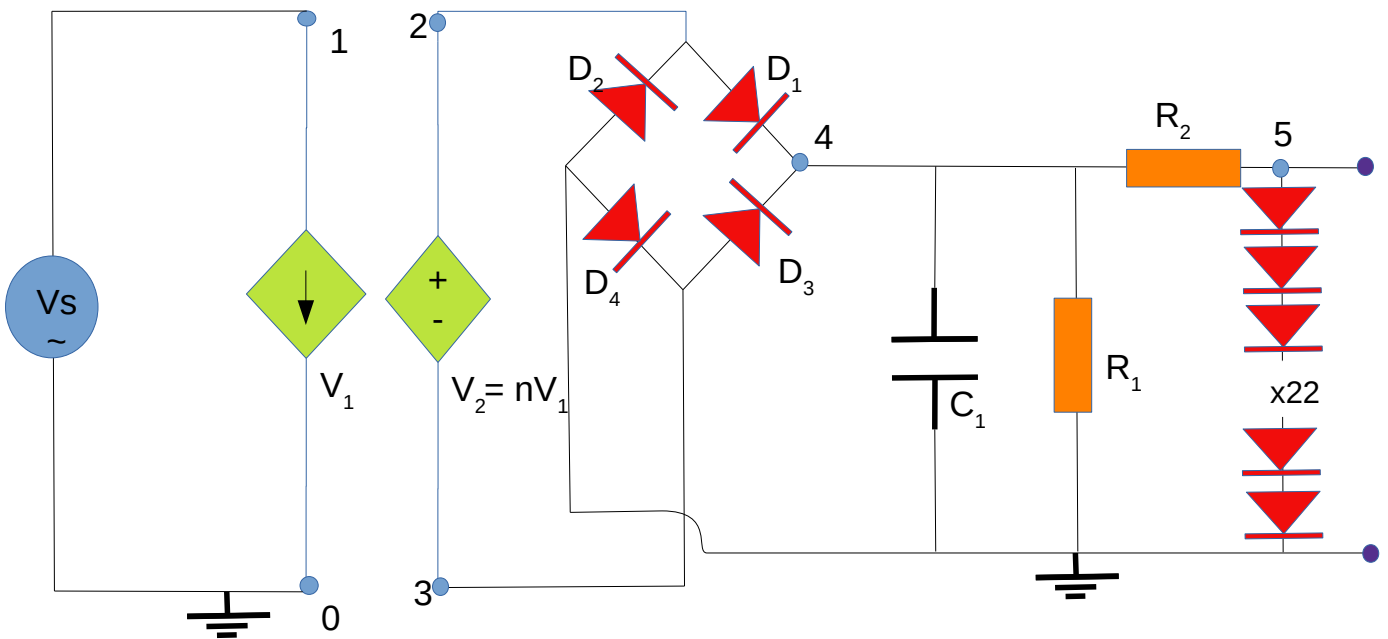


Figure 1: AC/DC Converter

In Section 2, a theoretical analysis of the circuit is presented. In Section 3, the circuit is analysed by simulation, and the results are compared to the theoretical results obtained in Section 2. The conclusions of this

study are outlined in Section 4.

2 Theoretical Analysis

In this section we will analyse theoretical our AC/DC converter circuit.

To do so, and because there were several things to be analysed, we divided the following subsections in the three different sectors that our circuit has and each one will be detailed separately.

Initially, we used a transformer to turn $V_s=230V$ in a smaller value, being $V_r=230/n$, with n equal to 11, so that the rest of the circuit can approximate it to 12 V that are requested. However, we want a DC voltage and not the initial AC voltage. In order to achieve that, we are going to describe the three different sectors that are in the circuit that is shown in the Introduction.

2.1 First sector: Bridge circuit

The Bridge circuit is composed by the four diodes that are shown in the first figure.

The four diodes labelled D_1 to D_4 are arranged in "series pairs" with only two diodes conducting current during each half cycle. During the positive half cycle of the supply, diodes D_1 and D_4 conduct in series while diodes D_2 and D_3 are reverse biased and the current flows through the load.

Summarizing, these four diodes work as a full wave rectifier which means, they transform the AC current in an equal amplitude unidirectional current, which corresponds to the blue plot in figure 2. To compute all of that we had to take the absolute value of the transformed voltage V_r .

2.2 Second sector: Envelope detector circuit (rectifier + capacitor)

Then, we use a capacitor in order to reduce the magnitude of the voltage making it closer to a DC (yellow plot). In order to compute this, we discovered when are the diodes ON and OFF.

The equation that describes t_{OFF} is:

$$t_{OFF} = \frac{1}{2 * w * \arctan(1/(w * R_1 * C))} \quad (2)$$

It's 2 times the angular frequency just because we are using a full wave rectifier.

However to be more precise, we used the newton raphson method in the expressions obtained using the Kirchoff laws to determine both t_{OFF} and t_{ON} .

Periodically,

$$\begin{cases} V_0 = V_r & t < t_{OFF} \\ V_0 = V_s * \cos(w * t_{OFF}) * \exp(-(t - t_{OFF})/(R_1 * C)) & t > t_{OFF} \end{cases} \quad (3)$$

due to the capacitor.

The ripple voltage is basically $\max(V_0) - \min(V_0)$.

To make the next point clear v_O will be renamed to ??

Name - Value	
Ripple	4.022790e-04
Average	2.299980e+01

Table 1: Ripple and average envelope values

2.3 Third sector: Voltage regulator circuit

At last, in the third sector, a series of 20 diodes reduce the noise making the current an almost perfect DC (orange plot). By calculating the $v0_{average}$ from the second sector, we are able to see if the voltage difference between $v5$ and $v0$ is limited by the maximum voltage that the diodes can handle. This only happens if the average is greater than that maximum.

Right now, we have the voltage due to the DC so we still need the voltage due to AC. It is possible to compute that in octave, calculating rD which is the resistance of each diode and also:

$$ac_{v0} = num_{diodes} * rD / (num_{diodes} * rd + R2) * (V_{envelope} - average_{env}) \quad (4)$$

To finish, $v0 = ac_{v0} + dc_{v0}$. The average must be approximately 12V.

Name - Value	
Ripple	1.915614e-05
Average	-6.434483e-15

Table 2: Ripple and average regulator values

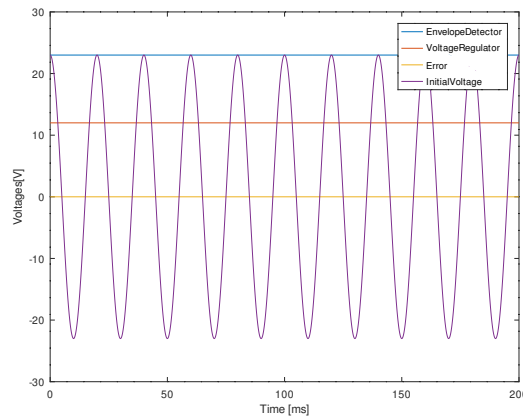


Figure 2: Input voltage of the secondary circuit ($v(2)$), output Voltage of the Envelope Detector ($v(4)$), VoltageRegulator ($v(5)$), and $v(5)-12$

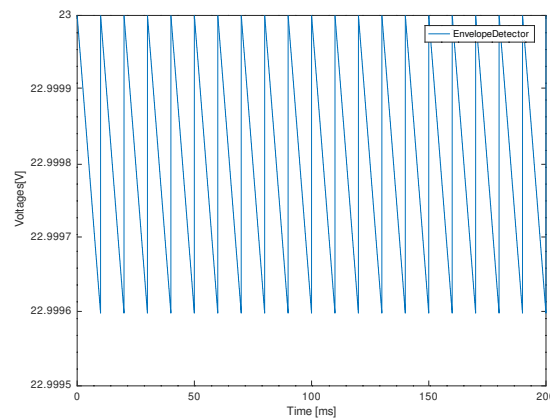


Figure 3: Output Voltage in Envlope Detector

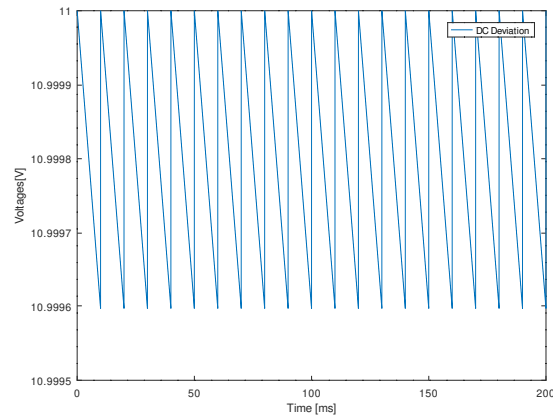


Figure 4: Output DC Deviation

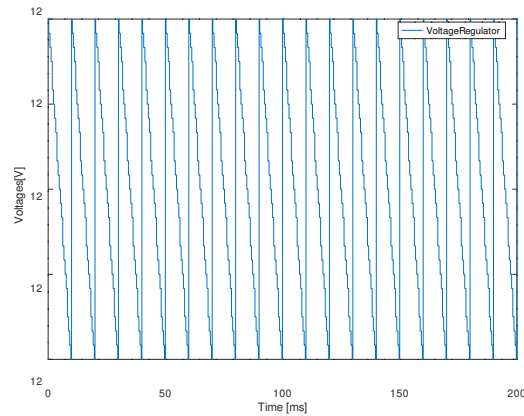


Figure 5: Output Voltage

3 Simulation Analysis

In this section, Ngspice was used in order to simulate the AC/DC converter. Some modifications were made from the original circuit in order of simplification. First of all, the transformer was replaced by an ideal using a dependent current source (instead of the primary) and a dependent voltage source (instead of the secondary) and this allowed us to not having to model a real transformer.

Then, the values of n (parameter of dependency sources), the C (capacitance) of the capacitor and the values of the resistance of the resistors were being adjusted through trial and error in order to achieve the maximum accuracy of the output voltage, since the initial goal was to get this value close as possible to 12 V.

3.1 Second sector: Envelope detector circuit (rectifier + capacitor)

Then we go to the second sector, analysing in NGspice comparison with Octave is clear in figures x e y.

This next table represents the Ngspice results for the output of the envelope.

Name - Value	
maximum(v(4))-minimum(v(4))	1.447754e-01
mean(v(4))	1.946163e+01

Table 3: Results for the output of the envelope

After analysis of the tables above, some discrepancies are observed. Nevertheless, these are due to the osilations that naturally occur in ngspice. These happen because the diodes used are non-linear components, which means that a linear relaton between the current and the voltage does not exist. The exponential function that comes into the equations leads to these type of oscilation.

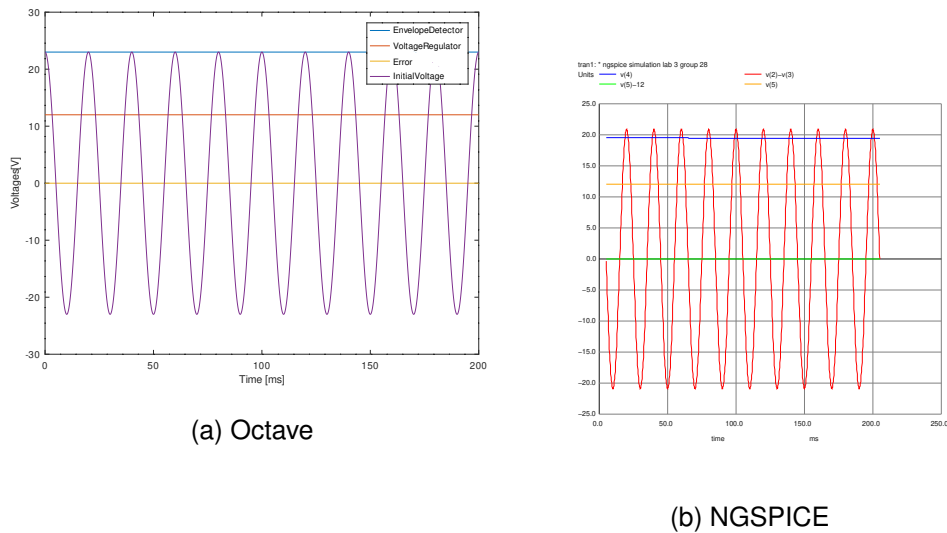


Figure 6: Input voltage of the secondary circuit (v(2)), output Voltage of the Envelope Detector (v(4)), Voltage Regulator (v(5)), and v(5)-12

3.2 Third sector: Voltage regulator circuit

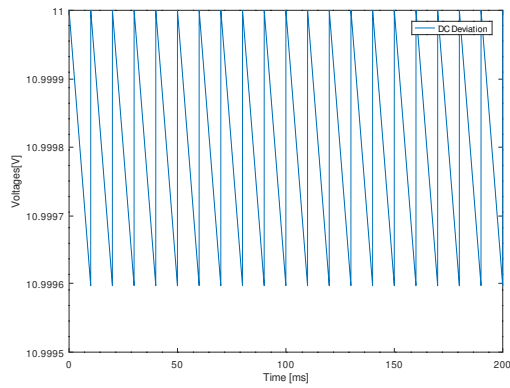
Then we go to the third sector, analysing in NGspice comparison with Octave is clear in figures x e y.

This next table represents the Ngspice results for the voltage regulator.

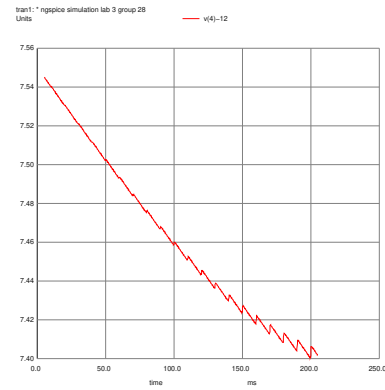
Name - Value	
maximum(v(5))-minimum(v(5))	9.368325e-03
mean(v(5))	1.199502e+01

Table 4: Results for the voltage regulator

The oscilations between theorectial and simulation results that happened in the output voltage of the envelope detector are extended to the voltage regulator for the same reasons. Therefore, it was expected to happen a small discrepancy between the results of both models. Nevertheless, we believe that once theoutput voltage is aproximately 12V, as wanted, the model worked successfully. The implemented circuit gave us a MERIT of 0.36 in NGSpice and 11.68 in Octave.

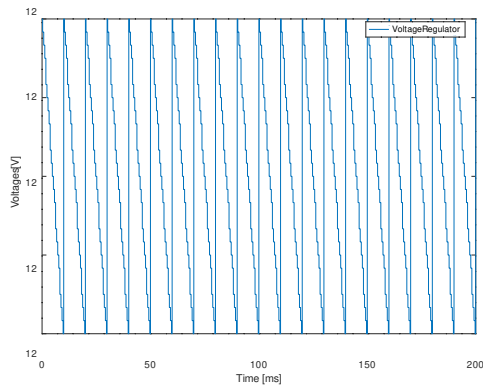


(a) Octave

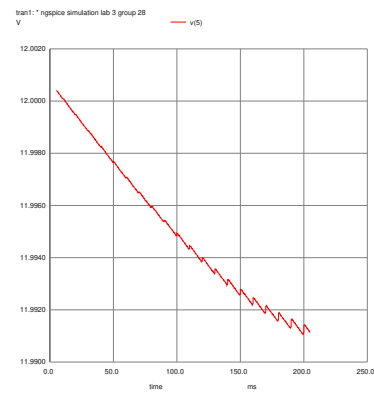


(b) NgSpice

Figure 7: Output DC Deviation



(a) Octave



(b) NGSPICE

Figure 8: Output Voltage

4 Conclusion

In this laboratory assignment, the goal specified in the introduction has been achieved with a great merit. All analyses have been performed both theoretically using the Octave maths tool and by circuit simulation using the Ngspice tool. When comparing these last two we conclude that there aren't any disparity between the results and therefore no errors associated. So, we conclude that the architecture that we used can be validated.