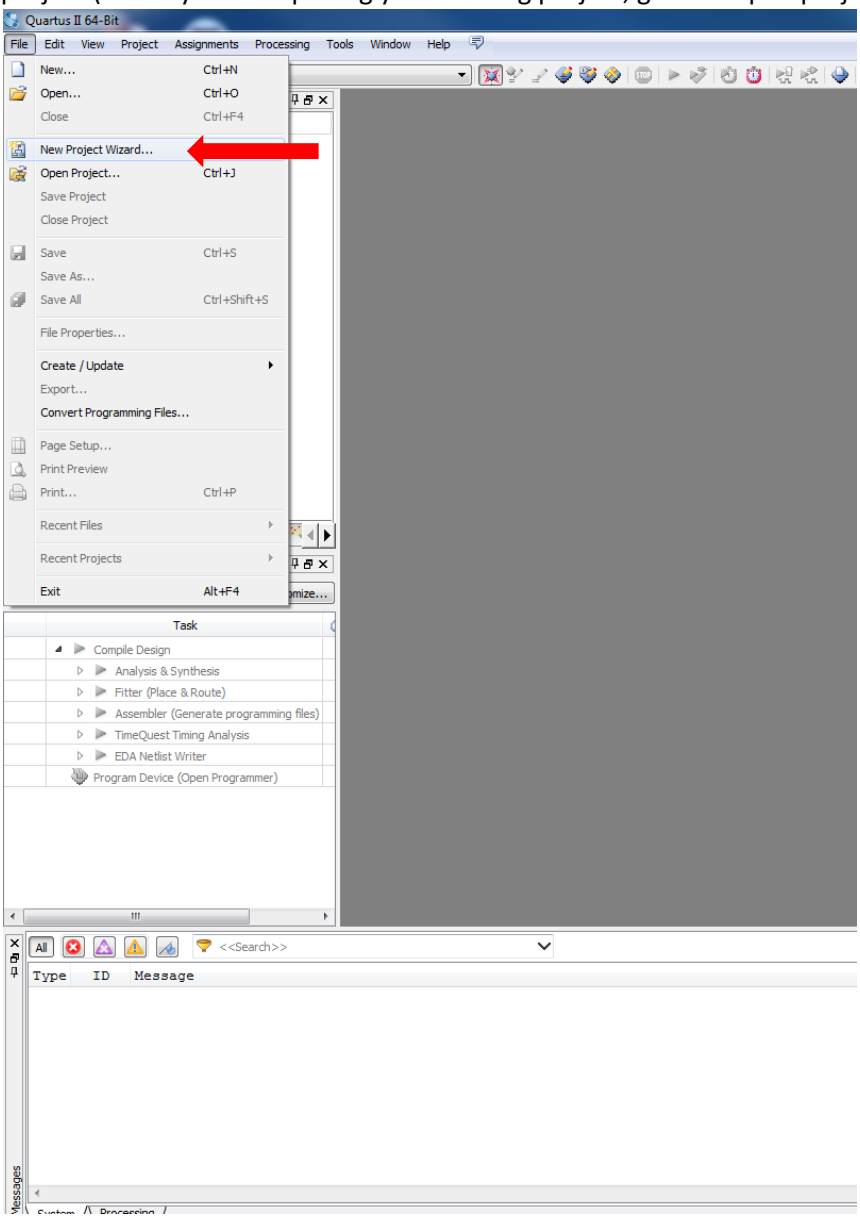
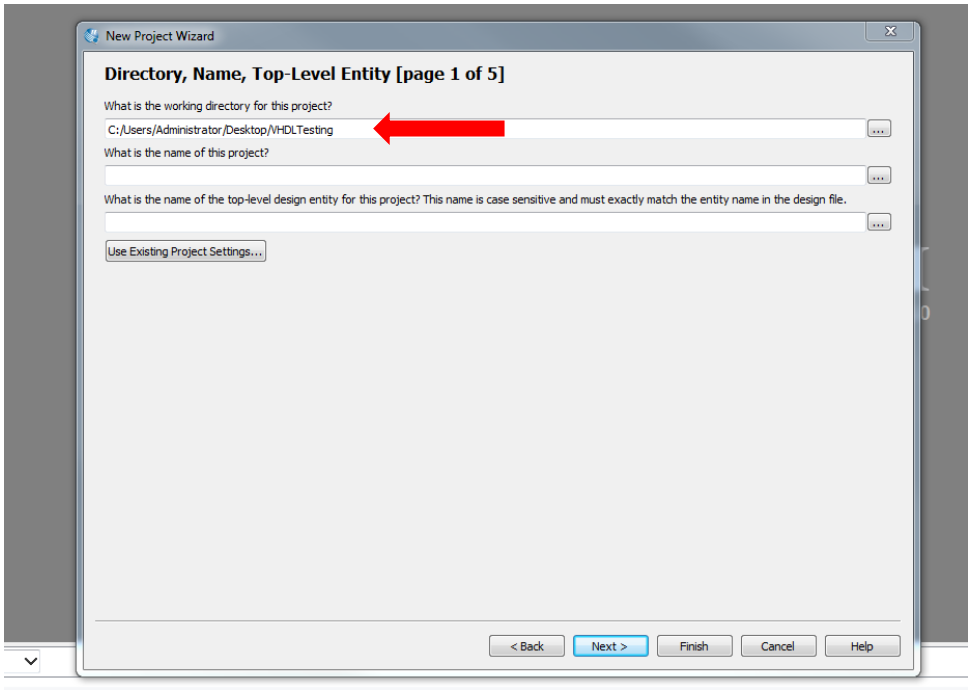
Quartus II (13.0aps Tutorial)

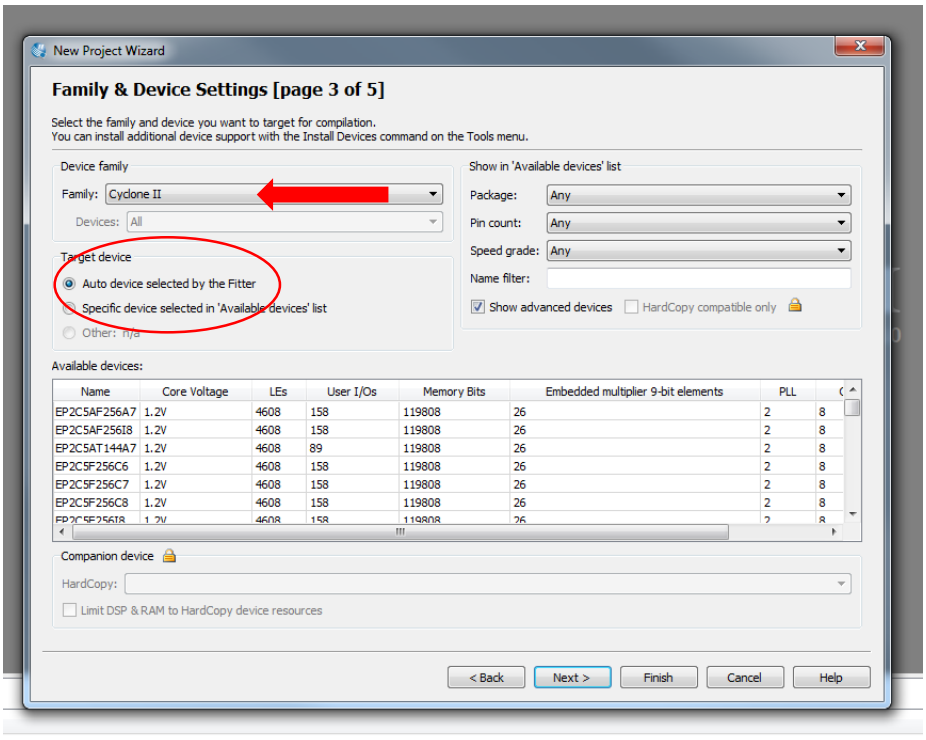
1. Create a folder to store your project on desktop.
2. Open Quartus II 13.0aps.
3. Create New project (When you are opening your existing project, go with open project.)

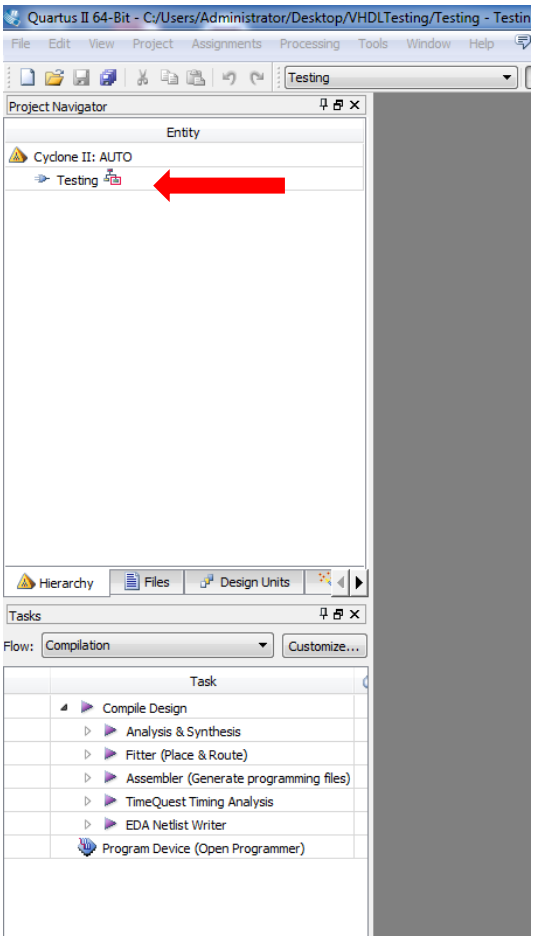


4. Select the project directory to the folder you create on the desktop and name your project.

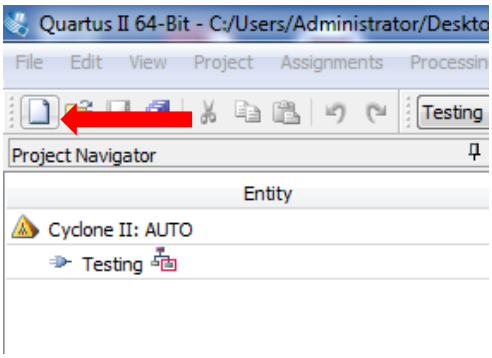


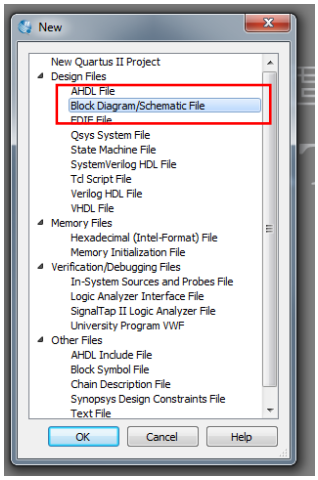
5. Then Click Next till you see the below window and select Cyclone II for device family and select “auto device selected by the filter”.



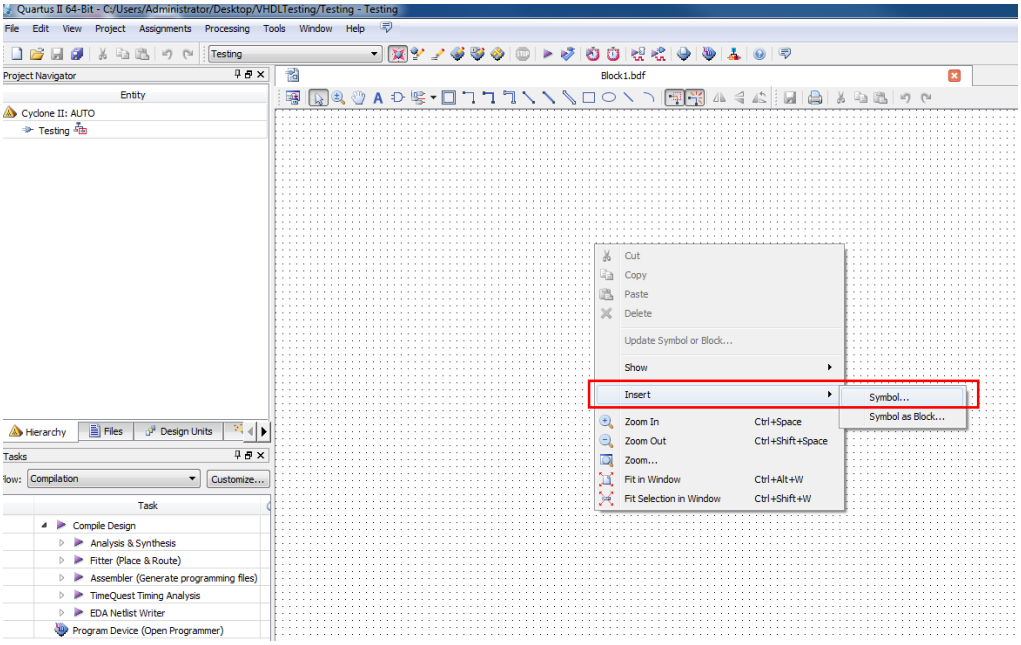
6. After you create the project you can see the new project in Project Navigator window.
7. 

8. Then you can start adding design to your project.
9. First add the Schematic block file.

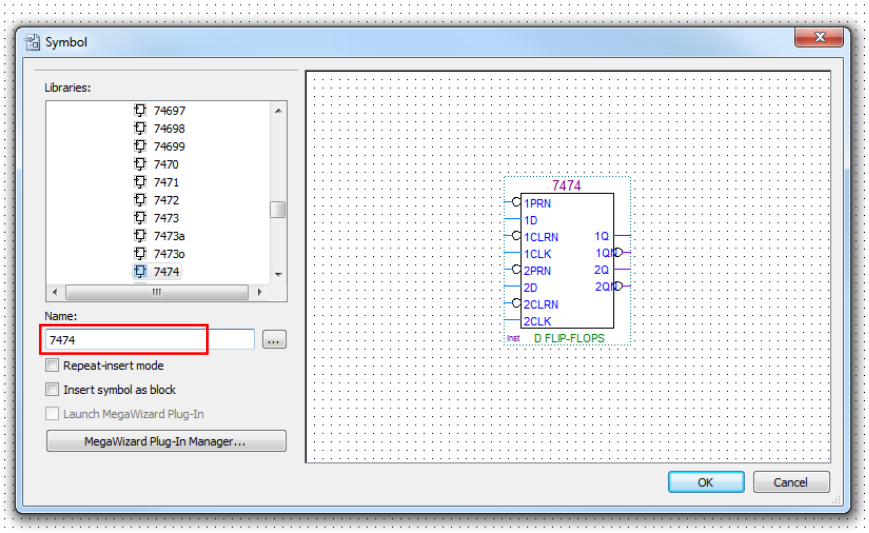




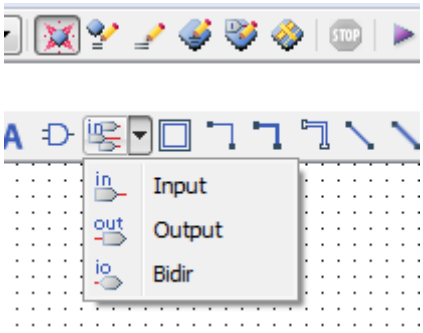
10. Then the block file will open on your project and you can start designing



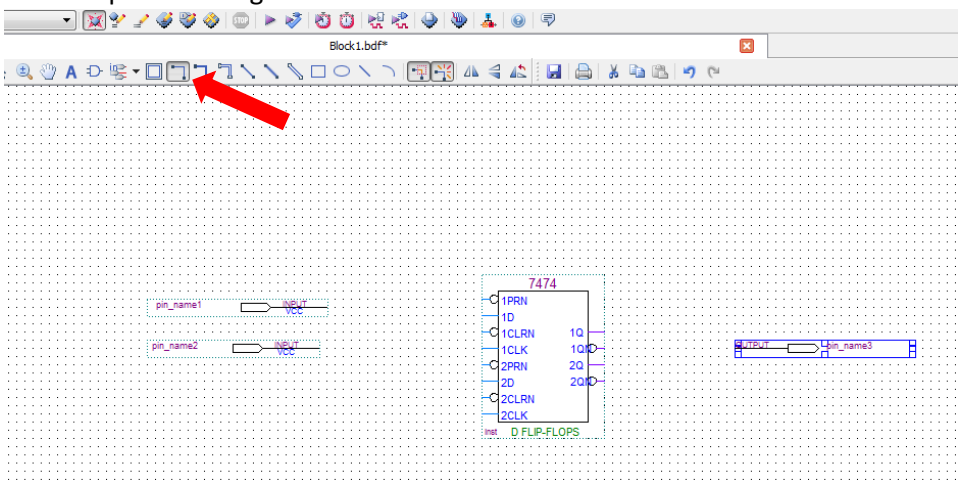
11. You can call any logic components or IC by simply type the name.



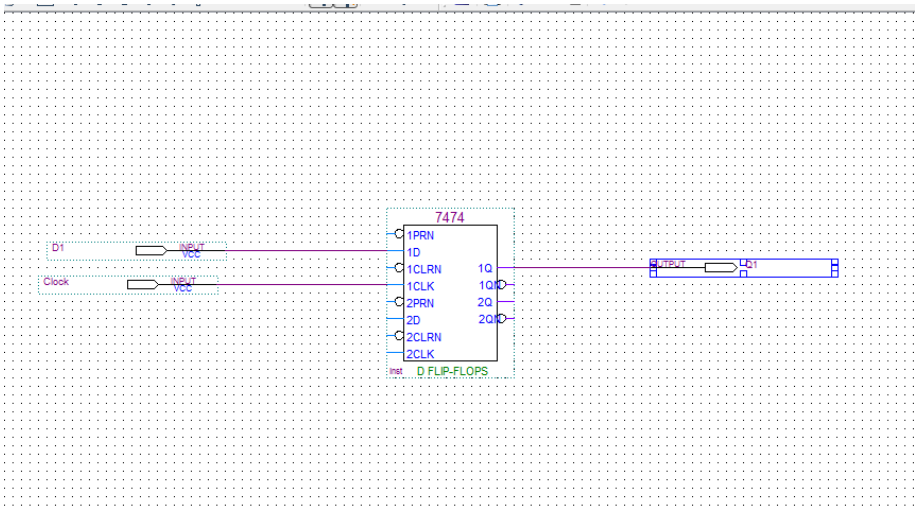
12. Add input and output pin.



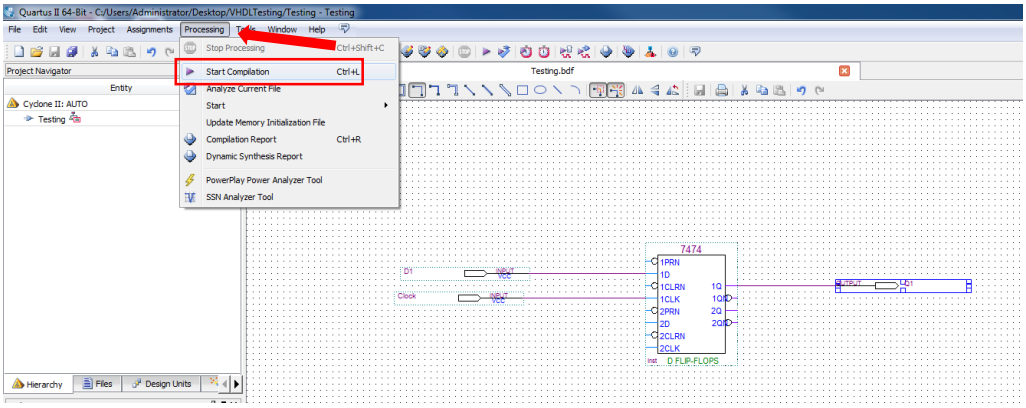
13. Connect the component using the connector.



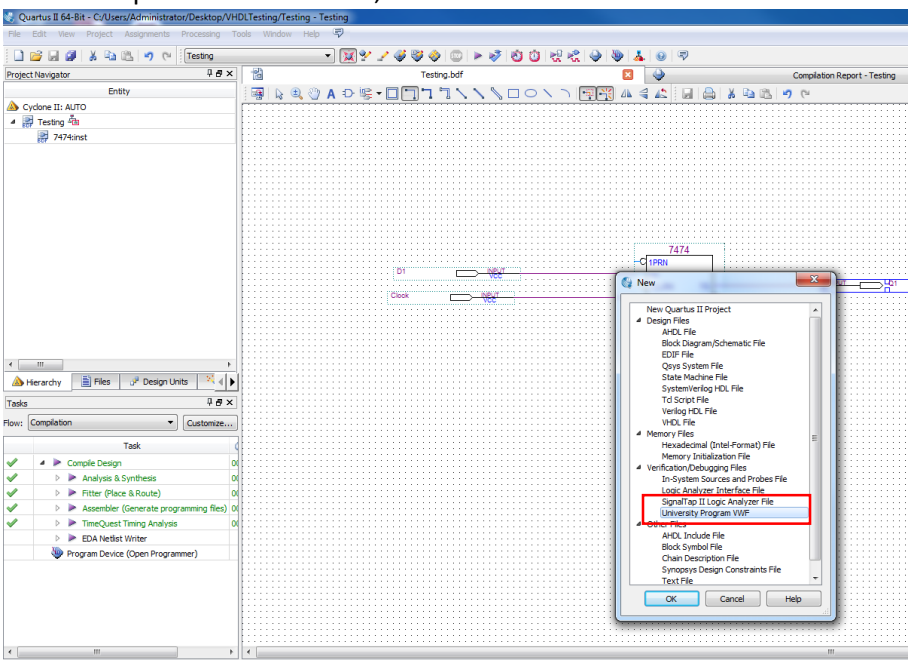
14. Name the input and output pin properly by double clocking on it. Quartus does not allow any space or special character in pin name.



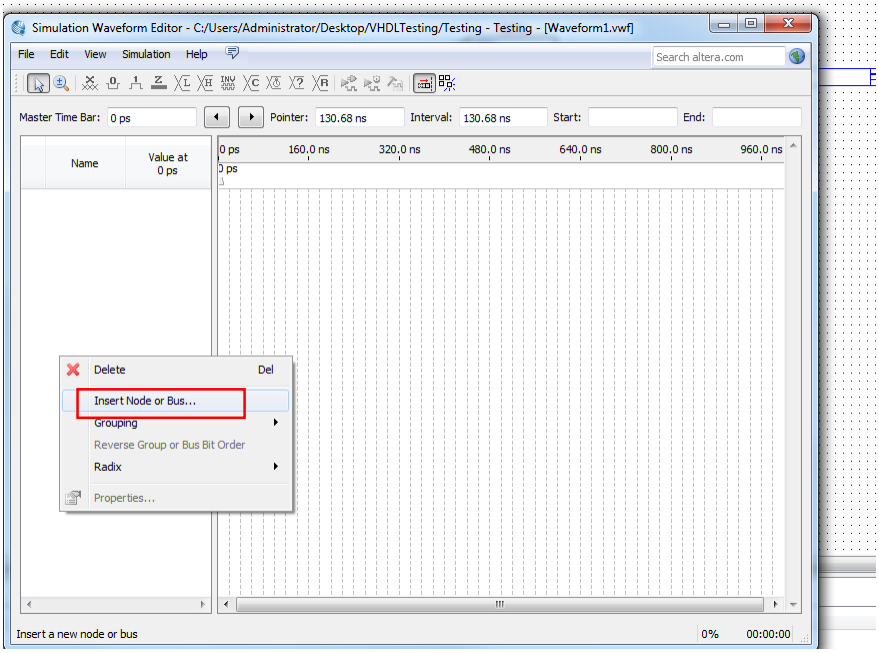
15. After you finish design, save it and compile.

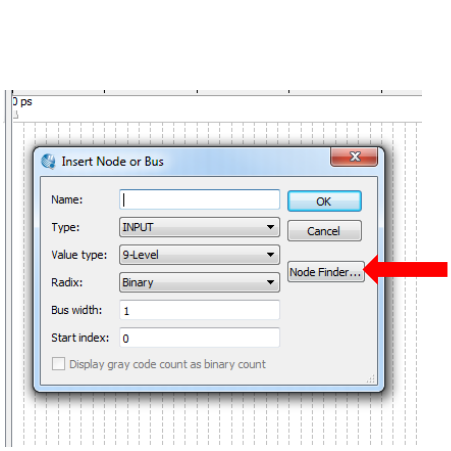


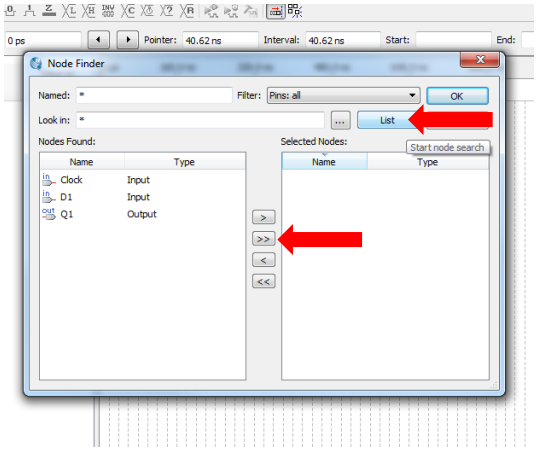
16. When compilation is successful, create a waveform vector file for simulation.



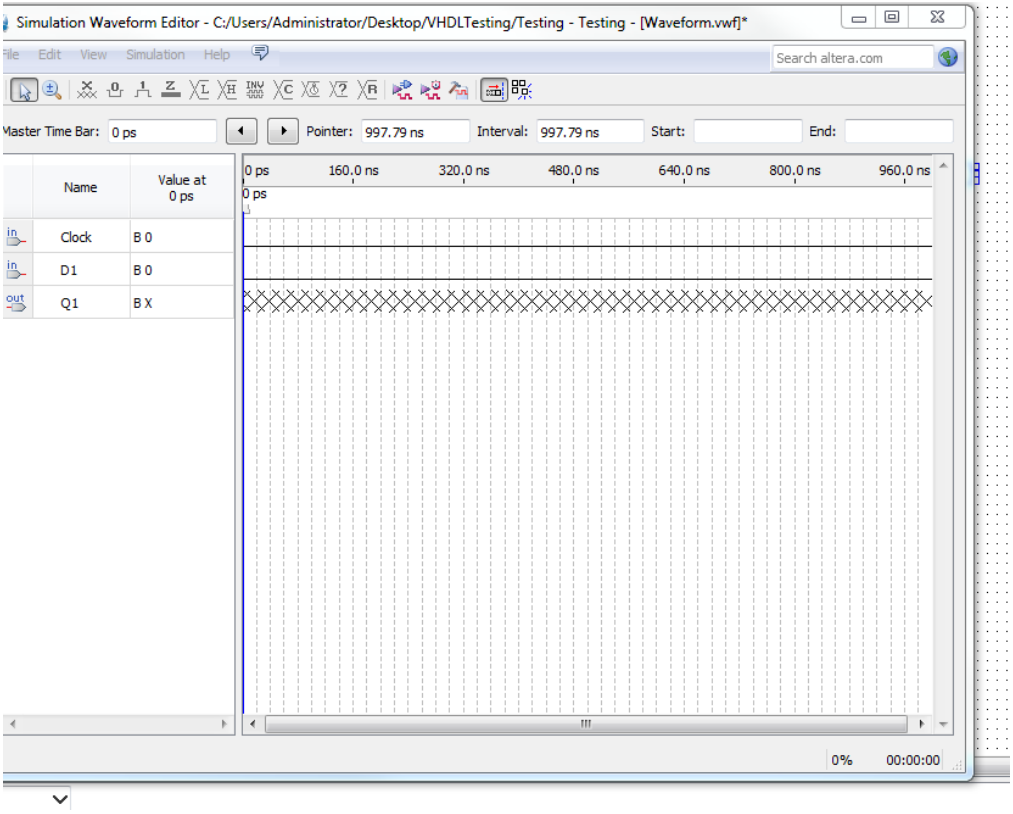
17. Add input and output pin the file.



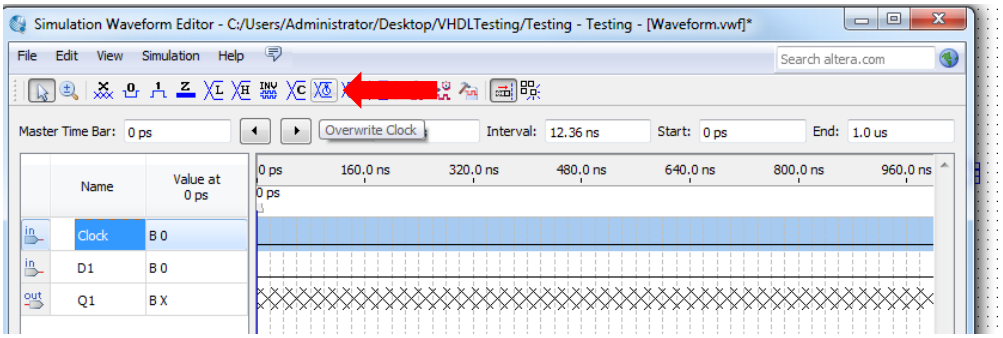




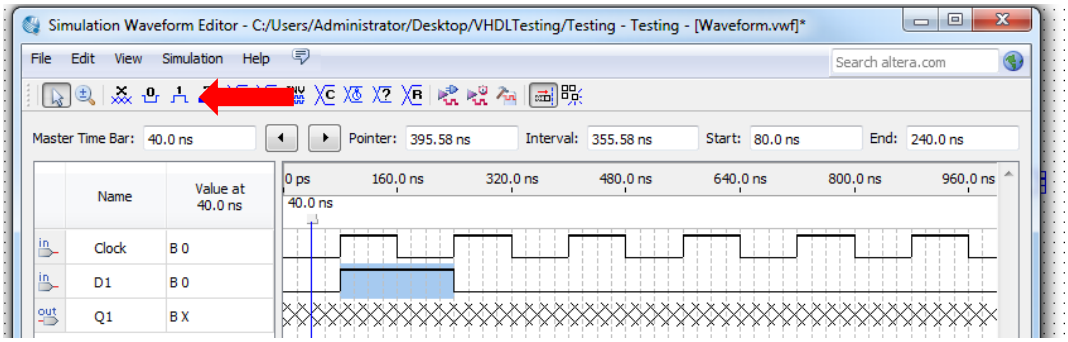
18. Add your pins and Ok. Now you can see your pins in the vwf file.



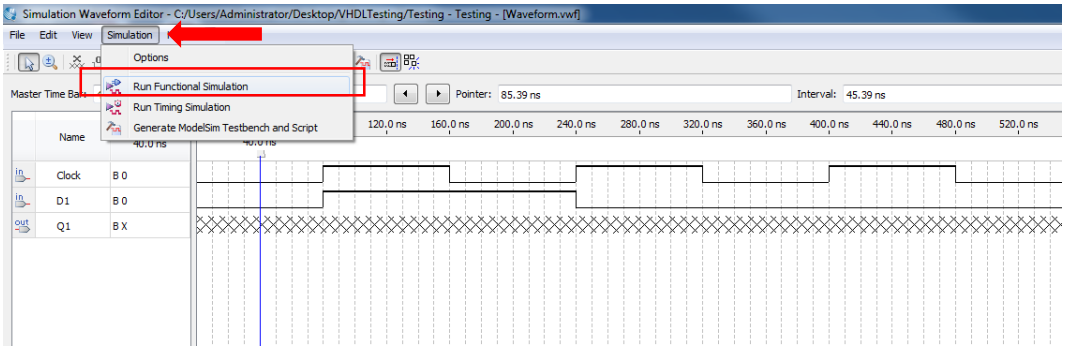
19. For clock input use overwrite clock with appropriate period.



20. To give differet input value for different clock cycle, select the input part by clocking from the starting period and dragging it till the end period.



21. After setting desire inputs, save your vwf file and run functional simulation.



22. At the end of simulation, another result window will pop up.