

# Isuri Devindi

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I'm a 3<sup>rd</sup> year computer engineering undergraduate, currently seeking internship opportunities in the fields of **Computer Vision** and **Machine Learning**.

## EDUCATION

<b>University of Peradeniya</b> <i>BSc. in Computer Engineering</i>	2018 Nov - Present <i>GPA: 4.00/4.00</i>
<b>Hillwood College, Kandy</b> <i>G.C.E. Advanced Level Examination</i> <i>District Rank - 6, National Rank - 113</i>	2004 - 2017 <i>Z-Score: 2.2768</i>

## ACHIEVEMENTS

<b>IEEEExtreme 14.0</b>   24 hour global algorithmic programming competition <i>Country Rank - 68, Global Rank - 724 (Out of 2000+ teams)</i>	2020
<b>Hacktitude</b>   An inter-university hackathon organized by the company 99x <i>Rank - 32 (Out of 200+ teams)</i>	2022
<b>Hackfest</b>   An inter-university hackathon organized by the University of Peradeniya <i>Rank (Healthcare category) - 1 (Out of top 20 teams)</i>	2022
<b>Hackdown</b>   An inter-university coding competition organized by IEEE WIE Student branch of University of Moratuwa, Sri Lanka <i>Rank - 37 (Out of 100+ teams)</i>	2019
<b>CIMA Certificate level completion</b>	2018
<b>Fit-in-Deutsch 2</b> <i>Passed with 73 marks (out of 80)</i>	2014

## PROJECTS

<b>Oral Cavity Region Detection System</b>   <a href="#">project page</a>   <a href="#">repository</a>	2022 - Present
<ul style="list-style-type: none"><li>A web-based application that can be used to upload images of an oral cavity and segment the anatomical structures present in the image using a machine learning model.</li><li>Contribution: Explored the potential of U-Net and Mask R-CNN models in developing a machine learning solution to segment the oral cavity images.</li><li>Technology: TensorFlow, Keras, React.js, Express.js, MongoDB, Node.js</li><li>Techniques: <b>U-Net</b> , <b>Mask R-CNN</b></li></ul>	
<b>Reconstructing highly degraded license plates</b>   <a href="#">report</a>   <a href="#">video</a>   <a href="#">colab</a>	2022
<ul style="list-style-type: none"><li>Demonstration of the efficacy of traditional image processing techniques to reconstruct highly degraded images of license plates obtained from CCTV footage, when the source of degradation is unknown.</li><li>Technology: Python, OpenCV, EasyOCR</li><li>Techniques: <b>Otsu thresholding, Morphological transformation, Contouring, Spatial and Frequency domain filtering and Degradation modeling(Wiener Filter)</b></li></ul>	
<b>Remote Proctoring system</b>   <a href="#">project page</a>   <a href="#">repository</a>   <a href="#">video</a>	2021 - Present
<ul style="list-style-type: none"><li>A single device with video streaming facility which integrates the hardware and software components needed to conduct virtual proctoring of an examination in a university.</li><li>Contribution: Designing a scalable web application for administrators of the university and proctors of examinations. Developing the hardware solution for the device using Raspberry Pi micro-controller.</li><li>Technology: React.js, Express.js, MongoDB, Node.js</li><li>Techniques: Handling and <b>synchronization</b> of API requests &amp; responses with promised-based library <b>Axios</b>.</li></ul>	

## Compiler for COOL Language | repository

2022

- The combination of a lexer, parser, semantic analyzer, and code generator that can be used to compile programs written in the COOL programming language.
- Technology: C++
- Techniques: Utilization of concepts such as **Finite State Machines**, **Abstract Syntax Trees** and tools such as **Flex** and **Bison**, to convert COOL to MIPS assembly language.

## Database system for business to business trade | repository

2020

- A fully functional database to organize transactions between businesses with a user-friendly interface.
- Contribution: Developing the database
- Technology: MySQL, PHP, Django

## Tool to generate and display fractals | repository

2020

- A tool to display two fractal sets: Mandelbrot and Julia set, according to user preferences.
- Technology: Java
- Techniques: **Multi-threading**, **Synchronization Primitives**

## 8-bit single cycle processor | Verilog-HDL

2020

- An 8-bit single cycle CPU with associated memory hierarchy. The processor includes an ALU, register file, control logic, forwarding unit, data memory, data cache, instruction memory and instruction cache.
- Technology: Java
- Techniques: **RISC-V**, **Caching**

## EXPERIENCE

### Technical article writer | Medium blog | Syncfusion blog | Enlear Pvt. Ltd.

2021 Feb - Present

### Casual Instructor | *Department of Computer Engineering, University of Peradeniya*

#### **CO253: Introduction to Programming and Networking**

2021 Nov- 2022 Jan

Supervised weekly 2hr long online lab sessions based on C programming Language

#### **CO222: Programming Methodology**

2021 May- 2021 Sep

Supervised weekly 2hr long online lab sessions, created questions for online quizzes based on the C programming Language

## TECHNICAL SKILLS

<b>Languages</b>	C, Python, Java, HTML/CSS, JavaScript, SQL, Verilog HDL, ARM Assembly Language
<b>Frameworks</b>	React.js, Express.js
<b>Developer Tools</b>	Git, Google Cloud Platform, VS Code
<b>Libraries</b>	TensorFlow, Keras, OpenCV, NumPy, Matplotlib, pandas

## SOFT SKILLS

**Technical Writing:** Currently working as a technical writer at Enlear Private Limited, writing content related to software application development.

**Public Speaking:** Moderated several virtual events organized by IET On Campus University of Peradeniya and virtual and physical events organized by the Faculty of Engineering, University of Peradeniya.

## EXTRA-CURRICULAR ACTIVITIES & LEADERSHIPS HELD

Member of the <u>Web Consultation team</u> of University of Peradeniya	2020 - Present
Secretary in IET on Campus of the University of Peradeniya	2022 - Present
President of the Music Society of the University of Peradeniya	2021 - 2022
Member of the Rotaract club of the University of Peradeniya	2020 - Present
Member of the Dramatic Society of the University of Peradeniya	2020 - Present

## REFERENCES

Dr. Isuru Nawinne | isurunawinne@eng.pdn.ac.lk

*Senior Lecturer, Department of Computer Engineering, Faculty of Engineering, University of Peradeniya, Sri Lanka*