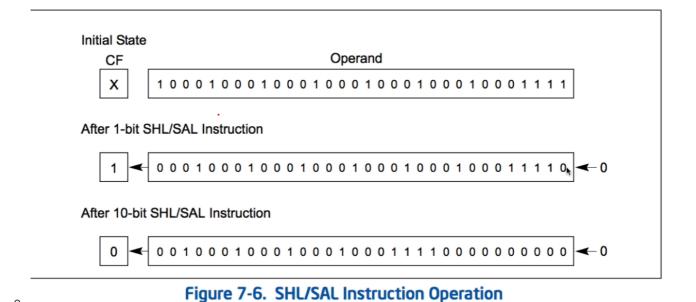
#### **Bit Shifting Instruction**

SHL/SAL

## SHL/SAL



• SHR

### SHR

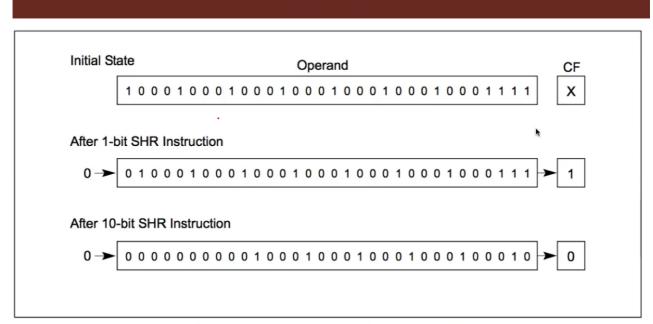


Figure 7-7. SHR Instruction Operation

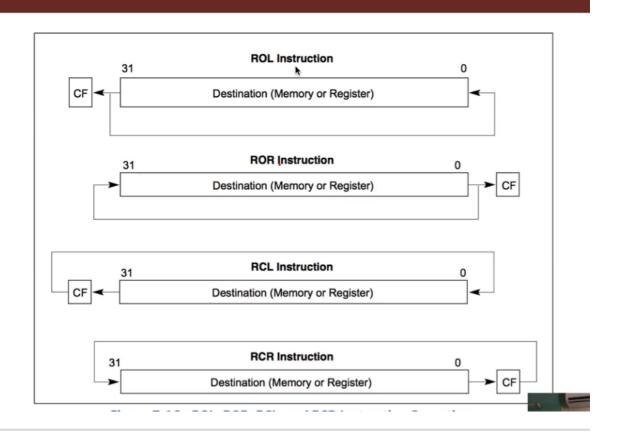
0

# 

ROL

## **Rotate Instructions**

1 1 1 0 0 0 1 0 0 0 1 0 0 0 1 0 0 0 1 0 0 0 1 0 0 0 1 0 0 0 1 0 0 0 1 0 0 0 1 1



```
global _start
section .text
start:
   mov rax, 0x0000000ffffffff
   sal rax, 32
   sal rax, 1
   clc
   mov rax, 0x0000000ffffffff
   shr rax, 1
    shr rax, 31
   mov rax, 0x0000000ffffffff
    sar rax, 1
    clc
   mov rax, 0xffffffffffffff
    sar rax, 1
   clc
   mov rax, 0x0123456789abcdef
   ror rax, 8
   ror rax, 12
   ror rax, 44
    ; exit the program gracefully
   mov rax, 0x3c
   mov rdi, 0
   syscall
section .data
   var1 dq 0x1111111111111111
   var2 dq 0x0
```