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Computer Architecture
Requested Information from other Teams

Team 1 - Storage/Mem Hierarchy & I/O :

We need the following from team 1.

- how much memory do we need to address?
- do we need special load and store functions?
- what type of memory hierarchy are we going to design?
- what will we be storing?

Team 3 - Processor pipeline, Bus, Registers:

We need the following from team 3

- how many registers do we need to support?
- what type of hardware do you envision to build?
- how many stages will there be?
- do we need to support multi-fetch
- size of the registers
- immediate fields?
- jumps?
- hardware and ISA are very closely connected
- types of arithmetic operations

Team 4 - Power consumption & Testing/Performance:

- current plan to test concept
- alternative plans to test concept
- possible power/clock constraints
- schedule for testing plan

Team 5 - Integration/Project Management:

- proposed schedule for team integration
- overall design decisions for digital clock
- Basic functionality that clock should perform
- Advanced functionality that clock should perform