

Comment	Description	Designator	Footprint	LibRef	Quantity
+5V		+5V	TPTH	TP	1
ACQ		ACQ	TPTH	TP	1
100nF		CD1, CD2, CD3, CD4, CD5, CD7, CD8, CD9, CD10, CD11, CD12, CD13, CD14, CD15	0805	CAP	14
10nF		CD6	0805	CAP	1
CLK		CLK	TPTH	TP	1
NE555		CLK555	*DIP794W45P25 4L1060H508Q8	NE555	1
CLK/100		CLK/100	TPTH	TP	1
COMP		COMP	TPTH	TP	1
1uF		CR1, CR2, CR3, CR4, CR5	0805	CAP	5
DIODO		DCLK1, DCLK2	DO41	DIODO	2
CD74HC4511E	High Speed CMOS Logic BCD-to-7 Segment Latch/Decoder/Driver 16-PDIP - 55 to 125	DEC1, DEC2	*DIP254P762X50 8-16	CD74HC4511E	2
UNIDAD	Display 7 Segmentos Catodo Común	DISP1	DISPLAY_LED_0. 3"	DISPLAY 7SEG K	1
DECENA	Display 7 Segmentos Catodo Común	DISP2	DISPLAY_LED_0. 3"	DISPLAY 7SEG K	1
POWER		J1	MOLEX5	MOLEX5	1
JOYSTICK		J2	MOLEX5	MOLEX5	1
RAMP_IN		J_In_Rampa	JUMPER2	JUMPER	1
RAMP_OUT		J_Out_Rampa	JUMPER2	JUMPER	1
SWEEP		LED_RED	LED3	LED	1
MATCH		LED_Y	LED3	LED	1
270		R4, RE1	0805	RES	2
RAMP		RAMP	TPTH	TP	1
1k		RC1	0805	RES	1
2K2		RCLK1, RCLK4	0805	RES	2
4K7		RCLK2	0805	RES	1
150K		RCLK3	0805	RES	1
3K9		RCLK5	0805	RES	1
20K		RCOUNTER	0805	RES	1
470		RD1, RD2, RD3, RD4, RD5, RD6, RD7, RD8, RD9, RD10, RD11, RD12, RD13, RD14	0805	RES	14
RESET		RESET	TPTH	TP	1
47K		RR1	0805	RES	1
100K		RR2	0805	RES	1
10k		RR3	0805	RES	1
500K		RV_FREQ, RV_GAIN, RV_OFFSET	PRESET V	PRESET	3
RAMP_IN		TPRAMP1	TPTH	TP	1
RAMP_OUT		TPRAMP2	TPTH	TP	1
CD4518BE		U1	*DIP254P762X50 8-16	CD4518BE	1
CD4073BE	IC: digital; AND; Channels: 3; Inputs: 3; CMOS; THT; DIP1	U2	*DIP254P762X50 8-14	CD4073BE	1
LM311N	IC VOLTAGE COMPARATOR 8-DIP	UC1	*DIP254P762X48 0-8	LM311N	1
LM7805		UCOMP	TO-220 V	LM7805	1
CD4013BE	Flip Flop D-Type Pos-Edge 2-Element 14-Pin PDIP	UE1	*DIP254P762X50 8-14	CD4013BE	1
74HC04N	Inverter 6-Element CMOS 14-Pin PDIP Inverter 6-Element CMOS 14-Pin PDIP	UE2	*DIP254P762X42 0-14	74HC04N	1
SN74HC08N	AND Gate 4-Element 2-IN CMOS 14-Pin PDIP Tube	UE3	*DIP254P762X50 8-14	SN74HC08N	1
CD4017BE	IC Mos 4017/CD4017BE HARRIS	UF1, UF2	*DIP254P762X50 8-16	CD4017BE	2
TL082JG	JFET-Input Operational Amplifier	UR1, UR2	JG008	TL082JG	2
V_JOY		Vx	TPTH	TP	1