

## Port Conversion Matrix

CCIE RS v5 Workbook – Narbik

Main Device	Interface		Connected Device	Interface	
	Narbik	MyLab		Narbik	MyLab
R1	Fa0/0	Fa1/0	SW1	Fa0/1	Eth0/0
	Fa0/1	Fa1/1	SW2	Fa0/1	Eth0/0
	Se1/2	Se2/2	R2	Se1/1	Se2/1
	Se1/3	Se2/3	R3	Se1/1	Se2/1
	Se1/4	Se2/4	R4	Se1/1	Se2/1
	Se1/5	Se2/5	R5	Se1/1	Se2/1
	Se1/6	Se2/6	R6	Se1/1	Se2/1
R2	Fa0/0	Fa1/0	SW1	Fa0/2	Eth0/1
	Fa0/1	Fa1/1	SW2	Fa0/2	Eth0/1
	Se1/1	Se2/1	R1	Se1/1	Se2/2
	Se1/3	Se2/3	R3	Se1/2	Se2/2
	Se1/4	Se2/4	R4	Se1/2	Se2/2
	Se1/5	Se2/5	R5	Se1/2	Se2/2
	Se1/6	Se2/6	R6	Se1/2	Se2/2
R3	Fa0/0	Fa1/0	SW1	Fa0/3	Eth0/2
	Fa0/1	Fa1/1	SW2	Fa0/3	Eth0/2
	Se1/1	Se2/1	R1	Se1/3	Se2/3
	Se1/2	Se2/2	R2	Se1/3	Se2/3
	Se1/4	Se2/4	R4	Se1/3	Se2/3
	Se1/5	Se2/5	R5	Se1/3	Se2/3
	Se1/6	Se2/6	R6	Se1/3	Se2/3
R4	Fa0/0	Fa1/0	SW1	Fa0/4	Eth0/3
	Fa0/1	Fa1/1	SW2	Fa0/4	Eth0/3
	Se1/1	Se2/1	R1	Se1/1	Se2/4
	Se1/2	Se2/2	R2	Se1/2	Se2/4
	Se1/3	Se2/3	R3	Se1/3	Se2/4
	Se1/5	Se2/5	R5	Se1/5	Se2/4
	Se1/6	Se2/6	R6	Se1/6	Se2/4
R5	Fa0/0	Fa1/0	SW1	Fa0/5	Eth1/0
	Fa0/1	Fa1/1	SW2	Fa0/5	Eth1/0
	Se1/1	Se2/1	R1	Se1/1	Se2/5
	Se1/2	Se2/2	R2	Se1/2	Se2/5
	Se1/3	Se2/3	R3	Se1/3	Se2/5
	Se1/4	Se2/4	R4	Se1/4	Se2/5
	Se1/6	Se2/6	R6	Se1/6	Se2/5
R6	Fa0/0	Fa1/0	SW1	Fa0/6	Eth1/1
	Fa0/1	Fa1/1	SW2	Fa0/6	Eth1/1
	Se1/1	Se2/1	R1	Se1/1	Se2/6
	Se1/2	Se2/2	R2	Se1/2	Se2/6
	Se1/3	Se2/3	R3	Se1/3	Se2/6
	Se1/4	Se2/4	R4	Se1/4	Se2/6
	Se1/5	Se2/5	R5	Se1/5	Se2/6
R7	Fa0/0	Fa1/0	SW1	Fa0/7	Eth1/2
	Fa0/1	Fa1/1	SW2	Fa0/7	Eth1/2
R8	Fa0/0	Fa1/0	SW1	Fa0/8	Eth1/3
	Fa0/1	Fa1/1	SW3	Fa0/8	Eth0/0
R9	Fa0/0	Fa1/0	SW1	Fa0/9	Eth2/0
	Fa0/1	Fa1/1	SW3	Fa0/9	Eth0/1
	Se0/0/0	Se2/0	R10	Se0/0/0	Se2/0
	Se0/1/0	Se2/1	R10	Se0/1/0	Se2/1

# Port\_Matrix

R10	Fa0/0	Fa1/0	SW1	Fa0/10	Eth2/1
	Fa0/1	Fa1/1	SW4	Fa0/10	Eth0/0
	Se0/0/0	Se2/0	R9	Se0/0/0	Se2/0
	Se0/1/0	Se2/1	R9	Se0/1/0	Se2/1
SW1	Fa0/1	Eth0/0	R1	Fa0/0	Fa1/0
	Fa0/2	Eth0/1	R2	Fa0/0	Fa1/0
	Fa0/3	Eth0/2	R3	Fa0/0	Fa1/0
	Fa0/4	Eth0/3	R4	Fa0/0	Fa1/0
	Fa0/5	Eth1/0	R5	Fa0/0	Fa1/0
	Fa0/6	Eth1/1	R6	Fa0/0	Fa1/0
	Fa0/7	Eth1/2	R7	Fa0/0	Fa1/0
	Fa0/8	Eth1/3	R8	Fa0/0	Fa1/0
	Fa0/9	Eth2/0	R9	Fa0/0	Fa1/0
	Fa0/10	Eth2/1	R10	Fa0/0	Fa1/0
	Fa0/18	Eth3/0	SW2	Fa0/18	Eth3/0
	Fa0/19	Eth3/1	SW2	Fa0/19	Eth3/1
	Fa0/20	Eth3/2	SW2	Fa0/20	Eth3/2
	Fa0/21	Eth4/0	SW3	Fa0/21	Eth4/0
	Fa0/22	Eth4/1	SW3	Fa0/22	Eth4/1
	Fa0/23	Eth5/0	SW4	Fa0/23	Eth5/0
	Fa0/24	Eth5/1	SW4	Fa0/24	Eth5/1
SW2	Fa0/1	Eth0/0	R1	Fa0/1	Fa1/1
	Fa0/2	Eth0/1	R2	Fa0/1	Fa1/1
	Fa0/3	Eth0/2	R3	Fa0/1	Fa1/1
	Fa0/4	Eth0/3	R4	Fa0/1	Fa1/1
	Fa0/5	Eth1/0	R5	Fa0/1	Fa1/1
	Fa0/6	Eth1/1	R6	Fa0/1	Fa1/1
	Fa0/7	Eth1/2	R7	Fa0/1	Fa1/1
	Fa0/18	Eth3/0	SW1	Fa0/18	Eth3/0
	Fa0/19	Eth3/1	SW1	Fa0/19	Eth3/1
	Fa0/20	Eth3/2	SW1	Fa0/20	Eth3/2
	Fa0/21	Eth4/0	SW3	Fa0/21	Eth4/0
	Fa0/22	Eth4/1	SW3	Fa0/22	Eth4/1
	Fa0/23	Eth5/0	SW4	Fa0/23	Eth5/0
	Fa0/24	Eth5/1	SW4	Fa0/24	Eth5/1
SW3	Fa0/8	Eth0/0	R8	Fa0/1	Fa1/1
	Fa0/9	Eth0/1	R9	Fa0/1	Fa1/1
	Fa0/19	Eth3/0	SW4	Fa0/19	Eth3/0
	Fa0/20	Eth3/1	SW4	Fa0/20	Eth3/1
	Fa0/21	Eth4/0	SW1	Fa0/21	Eth4/0
	Fa0/22	Eth4/1	SW1	Fa0/22	Eth4/1
	Fa0/23	Eth5/0	SW2	Fa0/23	Eth5/0
	Fa0/24	Eth5/1	SW2	Fa0/24	Eth5/1
SW4	Fa0/10	Eth0/0	R10	Fa0/1	Fa1/1
	Fa0/19	Eth3/0	SW3	Fa0/19	Eth3/0
	Fa0/20	Eth3/1	SW3	Fa0/20	Eth3/1
	Fa0/21	Eth4/0	SW2	Fa0/21	Eth4/0
	Fa0/22	Eth4/1	SW2	Fa0/22	Eth4/1
	Fa0/23	Eth5/0	SW1	Fa0/23	Eth5/0
	Fa0/24	Eth5/1	SW1	Fa0/24	Eth5/1