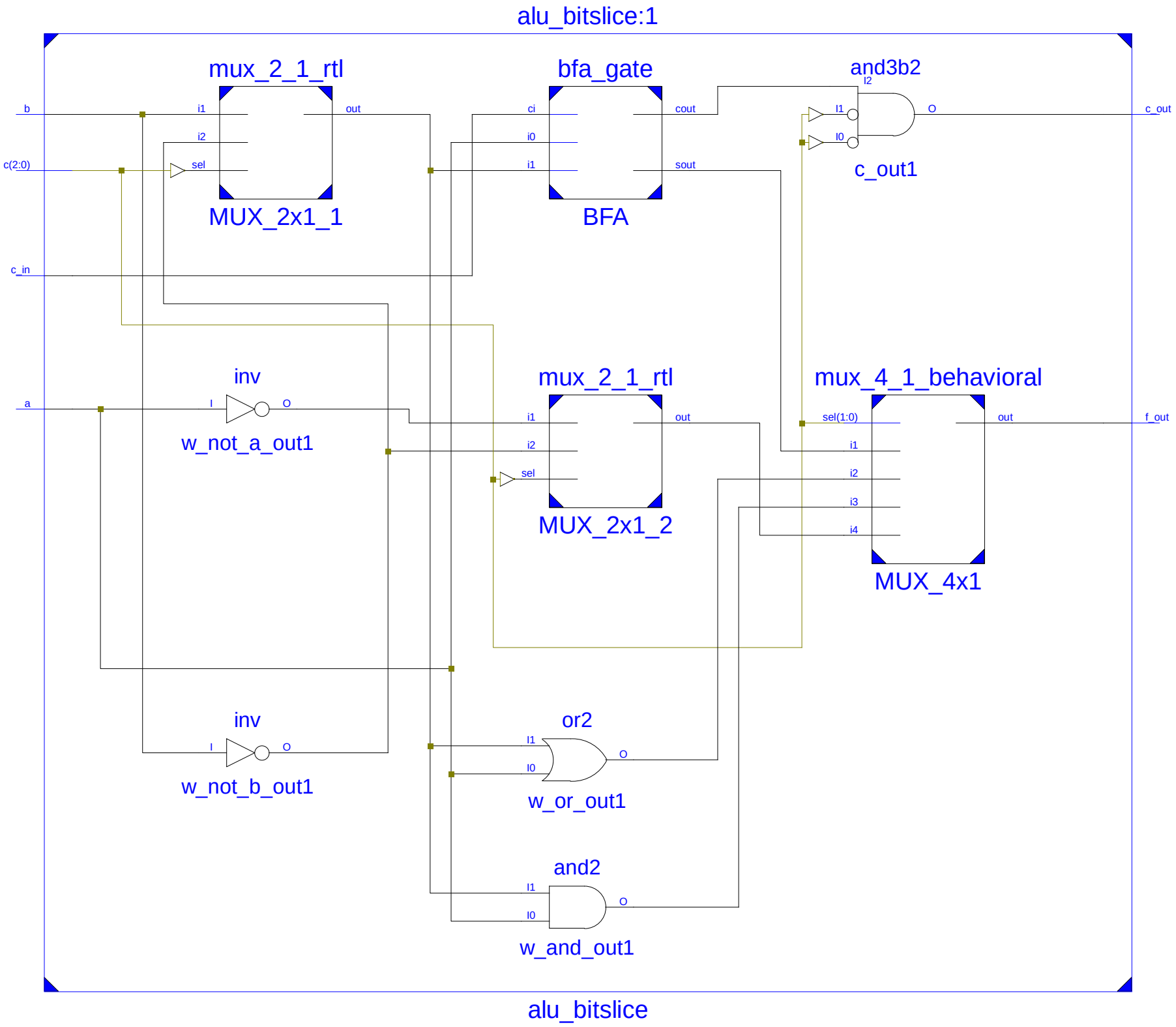
alu_bitslice:1
mux_2_1_rtl
MUX_2x1_1
bfa_gate
BFA
and3b2
c_out1
b
c(2:0)
c_in
a
c_out
i1
i2
sel
out
ci
i0
cout
sout
I2
I1
I0
O
inv
w_not_a_out1
mux_2_1_rtl
MUX_2x1_2
mux_4_1_behavioral
MUX_4x1
sel(1:0)
i3
i4
f_out
I
inv
w_not_b_out1
or2
w_or_out1
and2
w_and_out1
alu_bitslice