

# ITI PARASHAR

## APPLICATION DESIGN ENGINEER

### CONTACT

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### PROFILE

As an IT professional with 6 months of experience in automation activity using .NET C#, I have gained a strong foundation in software development and automation practices. My proficiency in .NET C# programming language, coupled with my experience in developing automated test scripts and building robust automation frameworks, allows me to effectively automate manual processes and streamline workflows. Apart from my technical skills, I am a detail-oriented individual with excellent problem-solving skills and a strong ability to work in a team environment. I am committed to staying up-to-date with emerging trends and technologies in the IT industry, and am eager to contribute my skills to a dynamic and fast-paced organization.

### SKILLS

- C#, .NET
- VBA EXCEL
- JAVA
- PYTHON
- DATA PREPRATION AND DATA VALIDATION

### EXPERIENCE

#### Alstom Transport India Limited

2022-present

I contributed to the development and execution of automated test scripts for desktop applications using C# .net framework and excel VBA macros, resulting in significant improvements in testing efficiency and accuracy. I am well-versed in using industry-standard tools such as Visual Studio, gitLab and have experience in integrating test automation frameworks with continuous integration and deployment (CI/CD) pipelines.

#### Malwa Institute of Science and Technology

2017-2018

As a dedicated and experienced assistant professor with a specialization in Electronics and Communication, I have contributed to the academic and professional growth of students in multiple engineering colleges. Over a year of teaching experience, I have developed strong technical knowledge and effective communication skills that have enabled me to mentor and guide students towards achieving their academic and career goals.

### EDUCATION

#### PG-DAC

Jan 2022- oct 2022

PG diploma in development and advance computing from CDAC Bangalore,

#### ME

2014-2016

ME in embedded system from SVITS Indore

#### BE

2010-2014

BE in Electronics and communication Engineering from SIRT Bhopal

### PROJECT

#### Low power VLSI circuits:

Impact of various logics on power dissipation.

#### Publication:

1. Design and Analysis of low power hybrid logic

INDIACOM-2017 IEEE Conference ID – 40353

2. Analysis of various VLSI logic on CLA MICRO-2016

TEQUIP-II, WBUT in association with IEEE(ESD) and IASTM

### ONLINE BUS RESERVATION SYSTEM

#### Technology used:

Spring boot, Hibernate, node js