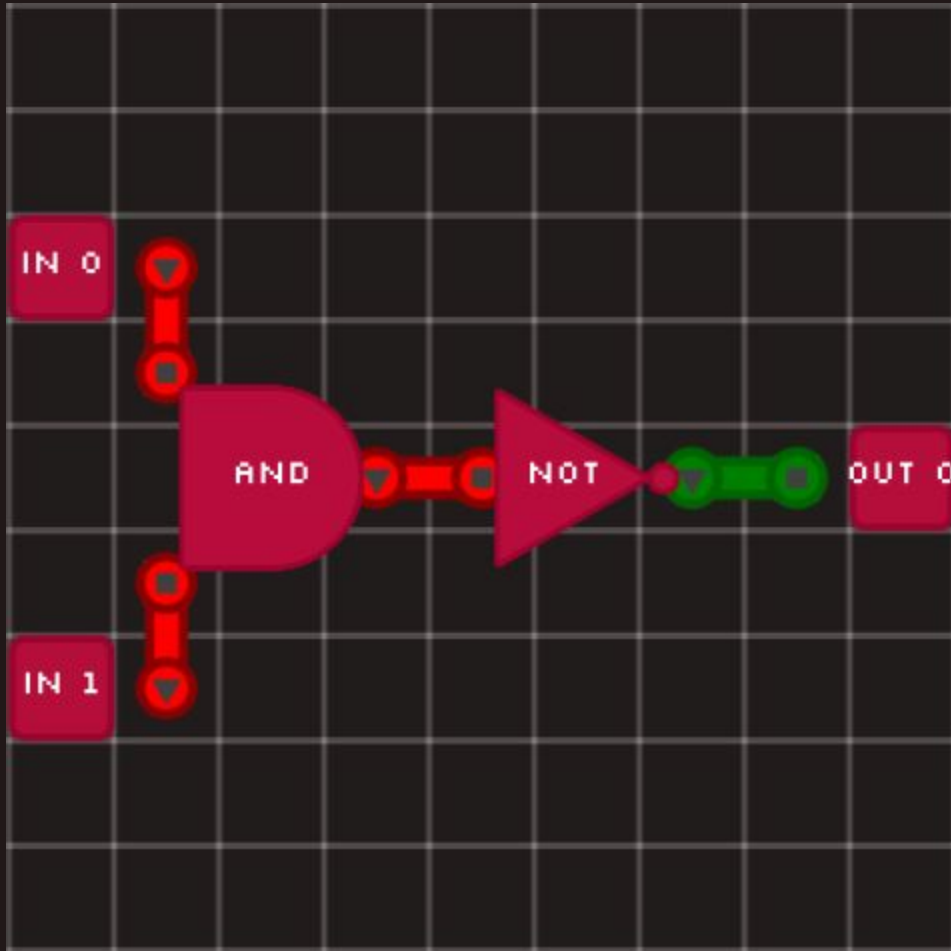


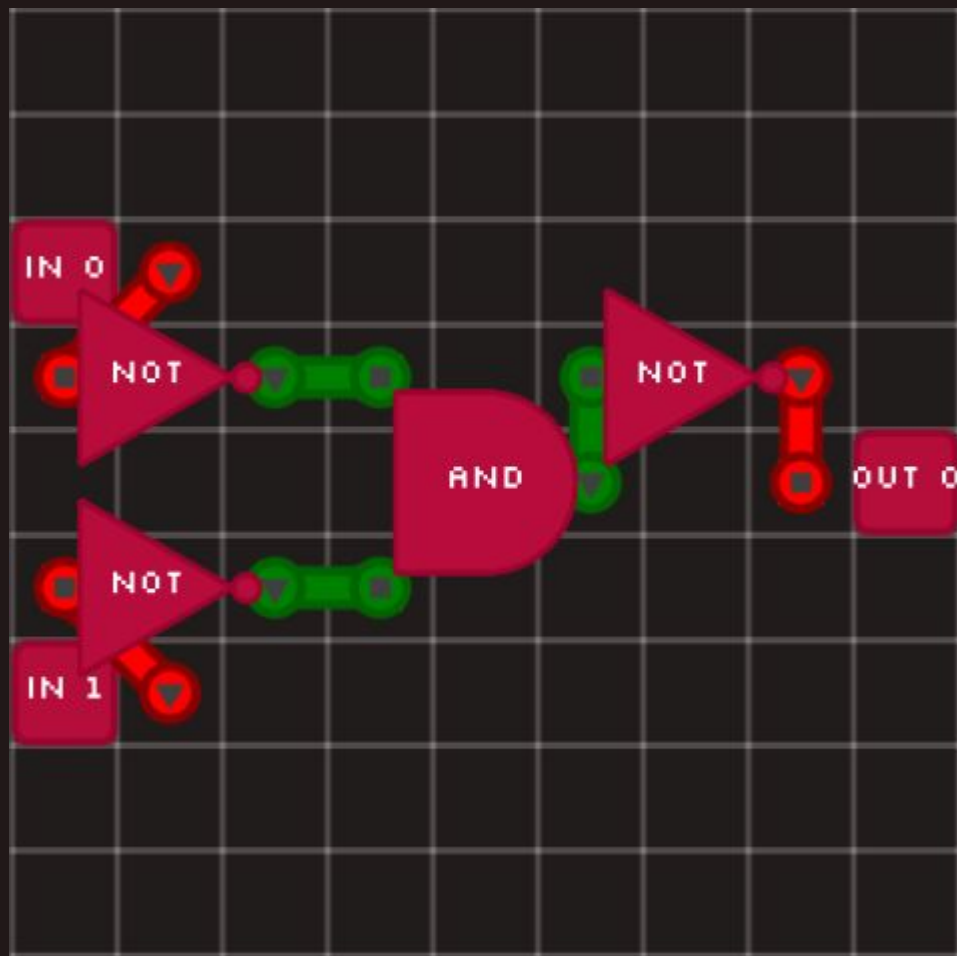
Day 1

Solutions

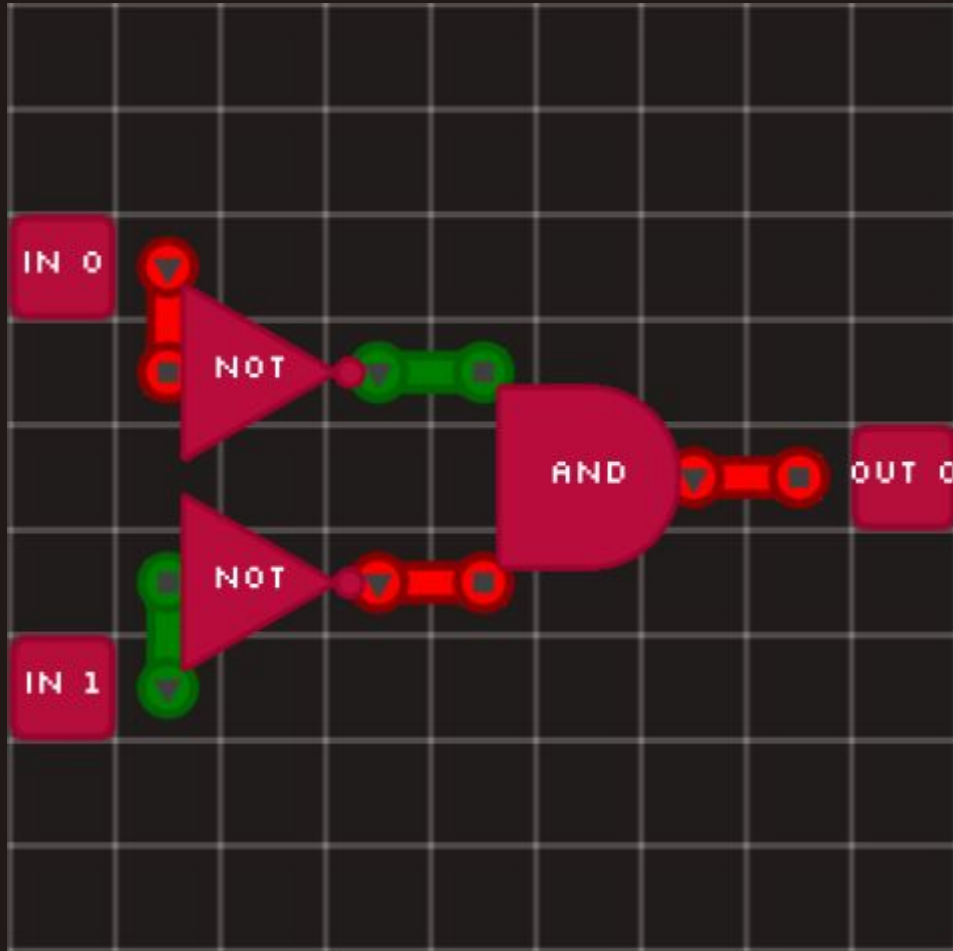
NAND GATE



OR GATE



NOR GATE



Challenges

Some of these gates are enough to make the rest of the gates. Which are they?

NAND, NOR. They can be used to create *NOT* gates by connecting their inputs together.

These are called *Universal Gates*

What is De Morgan's laws and how do they apply here?

Demorgan's law describes how you can convert between AND, NAND, OR, and NOR, by inverting their inputs and outputs.