

A complete web enabled Education Administration Software

Sr. No.	Topic Name
1	Introduction of COA
2	Register Transfer Language
3	Register Transfer
4	Bus and Memory Transfers
5	Arithmetic Micro-operations
6	Logic Micro-operations
7	Shift Micro-operations
8	Arithmetic logic shift unit
9	Arithmetic logic shift unit continued
10	Instruction Cycle
11	Memory-Reference Instructions
12	Register reference instructions
13	Input - Output Instructions
14	General Register Organization
15	Stack Organization
16	Stack Organization Continued
17	Instruction Formats
18	Addressing Modes
19	Addressing Modes Continued
20	Addition and Subtraction (Signed Magnitude and Signed 2's Complement)
21	Multiplication Algorithms (Binary Multiplication for Signed Magnitude
22	Booth's Algorithm
23	Booth's Algorithm Continued
24	Numerical Based on Booth's Algorithm
25	Division Algorithms (Restoring and Non-Restoring)
26	Numerical Based on Division Algorithms (Restoring and Non-Restoring) Continued
27	Memory Hierarchy
28	Main Memory

Sr. No.	Topic Name
29	Auxiliary Memory
30	Associative Memory
31	Cache Memory
32	Virtual Memory
33	Memory Management Hardware
34	Memory Management Hardware Continued
35	Peripheral Devices
36	Input-Output Interfaces
37	Asynchronous Data Transfer
38	Modes of Transfer
39	Priority Interrupt
40	Direct Memory Access (DMA)
41	Revision / Backup