Bits Description:

11	10	9	8	7	6	5	4	3	2	1	0
FIO	**ZIO**	**TUD**	**PLR**	**FE**	**ZE**	**TE**	**PE**	**ZCE**	**TCE**	**LE**	**MR**

ControlByte:

- Size of variable(in bits) - 16
- Occupied bits – 12

Index	Index Name	Stands for	Value Mean
0	MR	Mode Recognition Bit	0 - Manual 1 - Automatic
1	LE	LED Enable Bit	0 - Disable 1 - Enable
2	TCE	Thermal Cam Enable Bit	0 - Disable 1 - Enable
3	ZCE	Zoom Cam Enable Bit	0 - Disable 1 - Enable
4	PE	Pan Enable Bit	0 - Disable 1 - Enable
5	TE	Tilt Enable Bit	0 - Disable 1 - Enable
6	ZE	Zoom Enable Bit	0 - Disable 1 - Enable
7	FE	Focus Enable Bit	0 - Disable 1 - Enable
8	PLR	Pan Left/Right	0 - Right 1 - Left
9	TUD	Tilt Up/Down	0 - Down 1 - Up
10	ZIO	Zoom In/Out	0 - Out 1 - In
11	FIO	Focus In/Out	0 - Out 1 – In

Any changes to the bits from 8 to 11 will reflect only when their respective bits are enabled.

Example: 0 0 1 1 0 0 1 1 0 0 0 1 = 561 in decimal(will be received in this form)
1st bit represents automatic mode | 4th & 5th bit are set to 1 means pan & tilt are enabled, so 8th & 9th will reflect Pan left & Tilt up. To disable the tilt & pan the telescope to right the message bit will become : 0 0 1 0 0 0 0 1 0 0 0 1 = 529 in decimal.