



# Lab 14 模擬結果作業

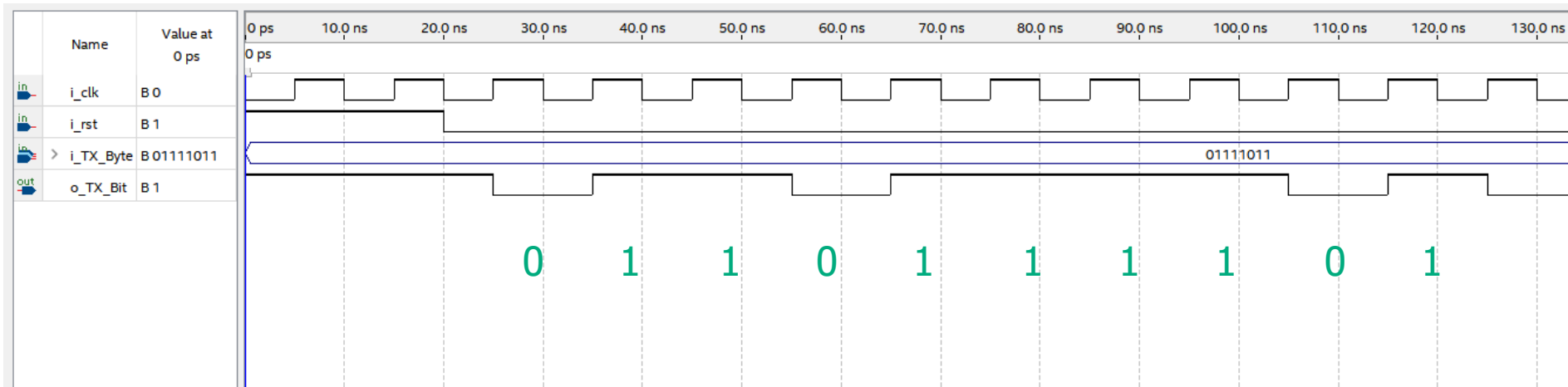
(2022/05/31)

資工二

S0954010 謝宥宣

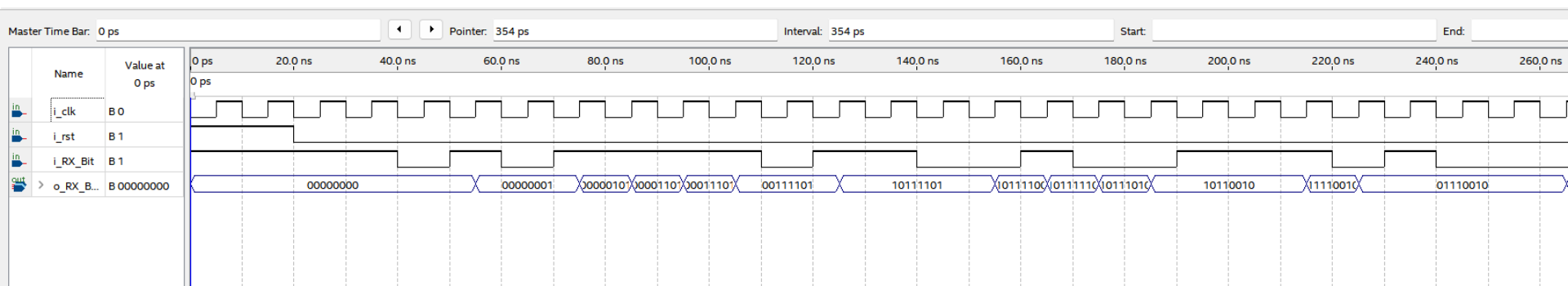
# Design 1: UART\_TX

## Simulation Results



# Design 3: UART\_RX

## Simulation Results



# Design 6: freq\_div\_n\_top

## Simulation Results

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