# **Education**

## B. Eng. | Sept 2018 – April 2022 | Carleton University | 3.6 GPA

· Major: Computer Systems Engineering

· Related coursework: Computer System Design, Object Oriented Development, Data Structures and Algorithms, Real-time Concurrent Systems, Computer Systems Development Projects, Operating Systems.

# **Experience**

## Teaching Assistant | Carleton University | January 2021 – Current

Role Description:

· Evaluate, debug and provide feedback on Java and Gradle projects;

· Assist students in understanding and debugging Java code;

· Assist professor in creating challenging and informative deliverables to enhance learning experience.

## Student Outreach Representative | Carleton University | January 2020 – Current

Role Description:

·Reaching out to prospective students and answering queries about program specifics and university life.

·Documenting interactions between students and callers.

## Project Engineering Intern | Hatch Ltd. | May 2019 –September 2019

Role Description:

· Light rail transit project contribution: streamlined data analysis, quality assurance and design support.

· In-house integration software development and documentation.

# **Skills**

## Programming

· Strong understanding of memory handling, object-oriented programming, and embedded real-time systems;

· Working knowledge of hardware programming and real-time concurrent systems.

· Experienced in Java, C, Verilog, VHDL, Python, Git and SQLite.

## Communication

· Excelled in engineering communications course;

· Vice President of Communications and co-founder of FPGA club.

· Functioned as key link between younger cadets and adult staff in chain of command;

## Leadership

· Skills gained through leadership of cadets at multiple levels over 5 years;

· Assigned as head of flights and entire squadron.

# **Accolades**

⋅ J. Lorne Gray Scholarship – 2019

⋅ Eric Sigurdson Award – 2020

· Dean’s Honor List recipient – 2019 and 2020.