Thesis Outline

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| | Chapter 1: Introduction (3-5 pages) |
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| | chapter 2: Hardware Description |
| | Chapter 2: Hardware Description - Ardware description - Sampling time issues |
| - | - motor encoders |
| - | - vate gy ro |
| - | - supes us serial |
| | |
| | -# of signals |
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| | Chapter 3: Model Eguations |
| | - theoretical |
| | - getting the parameters |
| - | |
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| | Simulate Cale for flerible testbed |
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| | Chapter 4: Control System Decogn & Smiletis |
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| | chepter 5: Mondevare Resulto |
| | LQR-based integal tracking |
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| - | Chapter 6: Drawing & Future Work |
| | Chapter 6: Doming & Future Work |