# Lab 5: Carry Lookahead Adder

## 1 Introduction

The purpose of this assignment is to study the right wiring way in TTL digital logic circuits and perform 2-bit carry lookahead adder (CLA) function.

## 2 Equipment Required

Digital Logic Trainer

1 each, 7486 ,7432 and 7408 integrated circuits

## 3 Instructions

Use some chips to construct circuits that perform CLA function.

3.1 Write the expression of Si and Ci for CLA

3.2 Draw the logic diagram for CLA.

## 4 Knowledge Gains from Experiment