

Subhash Chandra Pandey

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PROFILE

A Final Year Student at Indian Institute of Technology, Kharagpur, India pursuing M. Tech. degree in Microelectronics and VLSI Design. Trained in proposing as well as executing research project. Sincere, hard working and dependable team player. Detail oriented and innovative improviser.

EDUCATION

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| 2007 - Present | <p>Postgraduate Studies at Indian Institute of Technology Kharagpur, India.</p> <ul style="list-style-type: none">❖ Currently pursuing Master of Technology (M. Tech.) in Microelectronics and VLSI Design, as a final Year student from Electronics and Electrical Communication Engineering Department, IIT Kharagpur.❖ Currently holding a Cumulative Grade Point Average (CGPA) of 6.7 on 10 after 2 Semesters. |
| 2001-2005 | <p>Graduation from Babu Banarasi Das National Institute of Technology & Management Lucknow, U.P., India.</p> <ul style="list-style-type: none">❖ Rewarded Bachelor of Technology (B. Tech.) Degree in Electronics and Communication Engineering by Uttar Pradesh Technical University, Lucknow, U.P., India.❖ Got first class with 65.86%. |
| 1997-1999 | <p>Higher Secondary Education from Government Inter College, Allahabad, U.P., India.</p> <ul style="list-style-type: none">❖ Secured 67% in Physics, Chemistry and Mathematics and overall 62.80% from Uttar Pradesh Board, U.P. |
| 1995-1997 | <p>Secondary Education from P.P.I.C. walipur sultanpur, U.P., India.</p> <ul style="list-style-type: none">❖ Secured 70.5% in Science and Mathematics and overall 67.83% from Uttar Pradesh Board, U.P.❖ Stood 3rd in the school in 10th Board Examination. |

PROJECT

M. Tech Project: Currently working on “**RF Sputtering of High-K Dielectrics on Silicon**” as my M. Tech. project under the supervision of **Prof. C. K. Maiti**, Electronics and Electrical Communication Engineering Department, IIT Kharagpur.

B. Tech Project: Successfully completed “**Communication Based on LASER**” as my B. Tech project under the guidance of **Prof. B.K.Gurge**, Electronics and Communication Engineering Department, B.B.D.N.I.T. & M. Lucknow.

Term Projects:

- ❖ “**High gain high bandwidth OPAMP**” using CADENCE circuit design tool under the guidance of **Prof. S Banerjee** Department of E&ECE, IIT Kharagpur.
- ❖ “**Hartley Transform (Radix-4)**” using Verilog under the supervision of **Prof. S. Banerjee**, ECE Department, IIT Kharagpur.

TECHNICAL SKILLS

Programming Languages: C, Verilog HDL.

Software:

- ❖ NI/Lab View version 8.5.
- ❖ Electronics Simulation Software.
 - Circuit Maker
 - Electronic Workbench
- ❖ Circuit Design Tools
 - Cadence Design Tool for Back End Design and Analysis.
 - Xilinx Integrated Software Environment for Front End Design and Analysis.
- ❖ Simulation Software
 - Process Simulation: Taurus Suprem, Taurus Process.
 - Device Simulation: Taurus Device.

Operating System Windows (98, XP, VISTA), Linux, MS-DOS

AWARDS AND ACHIEVEMENTS

- ❖ Awarded for securing 3rd rank in school in 10th Board Examination.
- ❖ Got **1644th All India Rank** in GATE – 2007.
- ❖ Worked as a ‘**Graduate Apprenticeship Trainees**’ in **Hindustan Aeronautics Limited – 2005-06**.
- ❖ Selected for the job as a ‘**Junior Executive officer**’ in **Airports Authority of India- 2007**.

EXTRA CURRICULAR ACTIVITIES

- ❖ Member of organizing team of Technology Fest of BBDNIT&M Lucknow 2004-05.

PERSONAL DETAILS

Name	:	Subhash Chandra Pandey
Email ID	:	subhashiitkh@gmail.com
Contact No.	:	(+91)-97327-04145
Date of Birth	:	05 Feb. 1984.
Father's name	:	Shri Jwala Prasad Pandey
Sex	:	Male
Marital Status	:	Single
Nationality	:	Indian
Hobbies	:	Playing chess, Table-tennis, watching Cricket and Movies.
Permanent Address	:	Vill. - Pure Pandit Acharya, Post- Badanpur, Dist. – Sultanpur, (U.P.), India PIN – 227812
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