180845/170845/120845
/030845/031065B

4th Sem. Branch: Computer, IT
Sub : Computer Organization

Time : 3 Hrs.   M.M. : 100

SECTION-A

Note: Multiple type Questions. All Questions are compulsory.   **(10x1=10)**

Q.1   Pipe-Lining is a unique feature of _____.

    a)   RISC   b)   CISC

    c)   ISA   d)   IANA

Q.2   RAM is

    a)   Volatile Memory   b)   Static Memory

    c)   Garbage Memory   d)   Low speed memory

Q.3   The ALU makes use of _____ to store the intermediate results.

    a)   Accumulators   b)   Registers

    c)   Heap   d)   Stack

Q.4   The addressing mode, where you directly specify the operand value is _____.

    a)   Immediate   b)   Direct

    c)   Definite   d)   Relative

Q.5   The method which offers higher speeds of I/O transfer is_____.

    a)   Interrupts   b)   Memory Mapping

    c)   Program-Controlled I/O   d)   DMA

Q.6   Virtual memory consists of

    a)   Dynamic RAM   b)   Static RAM

    c)   Magnetic Memory   d)   None of these

Q.7   MIPS stands for

    a)   Mandatory instruction/Sec

    b)   Millions of instructions/Sec

    c)   Most of instruction/Sec

    d)   Many instructions/Sec

Q.8   An address generated by CPU is generally referred as_____

    a)   Physical Address   b)   Associative Address

    c)   Referral Address   d)   Logical Address

Q.9   Which of the following is at the bottom of the hierarchy of computer memory .

    a)   Cache   b)   RAM

    c)   Secondary   d)   Register

Q.10   Full form of SIMD

    a)   Single instruction, multiple data

    b)   Single instruction, many data

    c)   Single information, many data

    d)   Single information, multiple data

Section-B

Note: Objective type questions. All questions are compulsory. **(10x1=10)**

Q.11 Access time = _____ + _____.

Q.12 LBA stands for _____

Q.13 POST is the function of BIOS (True/False)

Q.14 Which type of cache is used in CPU?

Q.15 Define Multiport memory.

Q.16 Write two main operations performed on stack.

Q.17 ANSI stands for _____.

Q.18 Hard wired control units is a rigid application (True/False)

Q.19 Define Hit rate.

Q.20 Parallel computers are either _____ or MIMD.

Section-C

Note: Short answer type Question. Attempt any twelve questions out of fifteen Questions. **(12x5=60)**

Q.21 Give difference between micro programmed and hard wired control.

Q.22 List basic characteristics of multiprocessor.

Q.23 Explain RAM and ROM chips?

Q.24 Compare internal and external interrupts.

Q25 What are the components of memory management unit.

Q.26 What do you mean by Demand paging.

Q.27 Write a short note on one-address instructions.

Q.28 Write a note on Associative memory.

Q.29 Discuss general register organization.

Q.30 Write a note on Boot strap loader.

Q.31 Discuss

a) Register Indirect address mode

b) implied address mode.

Q.32 Why memory address map is required. Explain.

Q.33 Explain different types of Auxiliary memory.

Q.34 Write a note on hyper cube structure.

Q.35 Write a note on Time shared common bus.

Section-D

Note: Long answer questions. Attempt any two question out of three Questions. **(2x10=20)**

Q.36 What do you mean by RISC. What are their characteristics? How RISC is different from CISC architecture. Explain.

Q.37 Explain magnetic disk? Explain data organization and format in magnetic disk.

Q.38 Explain Flynn's Classification?