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Roll No.....

30845/31065B

4th Sem, **Branch :** Computer Engineering

**Subject :** Computer Organization

**Time : 3 Hrs.**

**M.M. : 100**

### **SECTION-A**

**Note:** Multiple choice questions. All questions are compulsory. (10x1=10)

- Q.1 Output device is (CO-1)  
a) Keyboard      b) Printer  
c) Mouse      d) Scanner
- Q.2 RISC stands for (CO-1)  
a) Reduced Instruction set computer  
b) Read Instruction set computer  
c) Reduced Instruction set coming  
d) Reduced Input self computer
- Q.3 RAM is (CO-2)  
a) Volatile memory      b) Static memory  
c) Garbage memory      d) Low speed memory
- Q.4 An address generated by CPU is generally referred as \_\_\_\_\_ (CO-2)  
a) Physical address      b) Associative address  
c) Referral address      d) Logical address
- Q.5 1GB = \_\_\_\_\_ Bytes (CO-1)  
a) 1,000      b) 1,000,000,000,000  
c) 1,000,000,000      d) 1,000,000
- Q.6 Which of the following is not type of ROM (CO-3)

- a) PROM      b) EEPROM  
c) EAROM      d) MEPROM

Q.7 Name the parallel processing (CO-4)

- a) SIMD, MIMD      b) MIMD  
c) MISD, SISD      d) All of above

Q.8 Parallel processor is (CO-4)

- a) Data flow architecture  
b) CMOS  
c) BIOS  
d) RISC

Q.9 Input device is (CO-1)

- a) Keyboard      b) Printer  
c) Plotter      d) DMP

Q.10 RAM can be \_\_\_\_\_ (CO-3)

- a) DRAM      b) ROM  
c) PROM      d) MEPROM

### **SECTION-B**

**Note:** Objective type questions. All questions are compulsory. (10x1=10)

- Q.11 Register and memory are the types of \_\_\_\_\_ (CO-1)
- Q.12 The \_\_\_\_\_ stores intermediate data used during the execution of the instructions (CO-1)
- Q.13 ALU performs micro operations for executing the \_\_\_\_\_. (CO-1)
- Q.14 A \_\_\_\_\_ is a storage device that stores information in such a manner that the item stored last is the first item retrieved. (CO-1)
- Q.15 DRAM stands for \_\_\_\_\_. (CO-2)

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- Q.16 ROM can be \_\_\_\_\_, \_\_\_\_\_ and \_\_\_\_\_. (CO-2)  
 Q.17 DMA stands for \_\_\_\_\_. (CO-3)  
 Q.18 I/O Bus consists of \_\_\_, \_\_\_ and \_\_\_\_\_. (CO-3)  
 Q.19 A parallel MIMD systems, communication is essential for \_\_\_ processing. (CO-4)  
 Q.20 \_\_\_ defines the information carrying capacity of the networks. (CO-4)

### **SECTION-C**

**Note :** Short answer type questions. Attempt any twelve questions out of fifteen questions. (12x5=60)

- Q.21 Explain Instruction format. (CO-1)  
 Q.22 Explain Hardwired vs. Micro programmed Control. (CO-1)  
 Q.23 Describe main characteristics of CISC Architectures? (CO-1)  
 Q.24 Describe main characteristics of RISC Architectures? (CO-1)  
 Q.25 Discuss 1) Direct Address Mode, 2) Indirect Address Mode. (CO-1)  
 Q.26 Explain internal interrupts and external interrupts. (CO-1)  
 Q.27 Write note on 1) SRAM, 2) DRAM (CO-2)  
 Q.28 Why virtual memory is used in computer system. (CO-2)  
 Q.29 Write the components of memory management unit? (CO-2)  
 Q.30 Write the difference between static RAM and dynamic RAM? (CO-2)  
 Q.31 Write the functions of BIOS. (CO-3)

- Q.32 Write short note on synchronous and asynchronous data transfer. (CO-3)  
 Q.33 Write difference between programmed I/O and Interrupt I/O. (CO-3)  
 Q.34 Explain various types of pipelining. (CO-4)  
 Q.35 Explain Reverse Polish Notation. (CO-4)

### **SECTION-D**

**Note :** Long Answer type question. Attempt any two questions out of three questions. (2x10=20)

- Q.36 Write Note on  
   1) Explain the characteristics of RISC processor. (CO-1)  
   2) Explain various page replacement policies. (CO-2)  
 Q.37 What is DMA? Explain DMA transfer in computer system with the help of diagram. (CO-3)  
 Q.38 Write note on  
   1) Discuss characteristics of computer architecture. (CO-4)  
   2) Explain inter connection of network.

**Note :** Course Outcome (CO) mentioned in the question paper is for official purpose only.