Q.29 Explain instruction cycle, machine cycle and T-State. (CO5)

Q.30 What are differences between edge triggered and level triggered interrupts? (CO6)

Q.31 Explain various hardware interrupts of 8085 microprocessor. (CO6)

Q.32 Explain DMA scheme of data transfer. (CO7)

Q.33 Explain Serial data input and Serial data output. (CO7)

Q.34 What are different operating modes of 8255? (CO8)

Q.35 Explain memory mapped I/O scheme and I/O mapped I/O scheme. (CO3)

SECTION-D

Note: Long answer type questions. Attempt any two questions out of three questions. (2x10=20)

Q.36 Draw and explain block diagram of 8085 microprocessor . (CO2)

Q.37 Write assembly language program for arranging 10 numbers in ascending order. (CO4)

Q.38 Explain the block diagram of 8251 communication Interface Adapter. (CO8)

(**Note:** Course outcome/CO is for office use only)

**5th Sem / Elect & Eltx Engg
Subject:- Microprocessor**

Time : 3Hrs.   M.M. : 100

SECTION-A

Note: Multiple choice questions. All questions are compulsory (10x1=10)

Q.1 Which of the following is correct for microprocessor Intel 8085? (CO1)
   a)   4 bits microprocessor
   b)   8 bits microprocessor
   c)   16 bits microprocessor
   d)   32 bits microprocessor

Q.2 Which of the following is not a special function register? (CO1)
   a)   Program counter   b)   Stack pointer
   c)   Accumulator   d)   Instruction pointer

Q.3 How many flags does 8085 have? (CO2)
   a)   5   b)   6
   c)   7   d)   8

Q.4 Which of the following is a non-vectored input? (CO6)
   a)   TRAP   b)   RST 7.5
   c)   RST 6.5   d)   INTR

Q.5 MOV B, C is a _____ byte instruction.   (CO3)

    a) 1   b) 2

    c) 3   d) 4

Q.6 What is stored in the H & L general-purpose register?
                                                   (CO3)

    a) Opcode

    b) Address of memory

    c) Address of next instruction

    d) Temporary data.

Q.7 Which of the following interfacing IC is a DMA controller?   (CO8)

    a) 8253   b) 8255

    c) 8257   d) 8279

Q.8 In 8085, which is the first machine cycle   (CO5)

    a) Opcode fetch cycle b) Memory read cycle

    c) Memory write cycle d) I/O read cycle

Q.9 Which instruction is required to rotate the content of accumulator one bit right along with carry?   (CO1)

    a) RLC   b) RAL

    c) RRC   d) RAR

Q.10 Register pair used to indicate memory   (CO3)

    a) B and C   b) H and L

    c) D and E   d) W and Z

SECTION-B

Note: Objective type questions. All questions are compulsory.   (10x1=10)

Q.11 Define Microcomputer.   (CO1)

Q.12 Define program counter.   (CO2)

Q.13 Define PROM.   (CO3)

Q.14 Define assembly language.   (CO4)

Q.15 Define fetch cycle.   (CO5)

Q.16 What is non-maskable interrupt?   (CO6)

Q.17 What is the function of RIM instruction?   (CO7)

Q.18 What is the use of 8279?   (CO8)

Q.19 Define Stack .   (CO2)

Q.20 Define addressing mode.   (CO4)

SECTION-C

Note: Short answer type questions. Attempt any twelve questions out of fifteen questions.   (12x5=60)

Q.21 What is the impact of microprocessor on modern society ?   (CO1)

Q.22 Write a short note on "Evolution of microprocessor".   (CO1)

Q.23 Explain the demultiplexing of address/ data bus.(CO2)

Q.24 Write a short note on "Bus organization of 8085".   (CO2)

Q.25 Explain concept of memory mapping.   (CO3)

Q.26 What are differences between static RAM and dynamic RAM?   (CO3)

Q.27 What is instruction ?Explain instruction format of 8085.   (CO4)

Q.28 Explain any three instructions from machine control group of instructions.   (CO4)