

No. of Printed Pages : 4  
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**4th Sem / Computer, Computer(For Speech and  
Hearing Impaired)**

**Subject : Computer Organisation & Architecture**

Time : 3 Hrs.

M.M. : 60

**SECTION-A**

**Note:** Multiple choice questions. All questions are compulsory (6x1=6)

Q.1 Cache Memory act between (CO-2)

- a) CPU and Ram      b) Ran and Rom
- c) CPU and Hard Disk d) All of the above

Q.2 SDRAM stands for (CO-2)

- a) System Dynamic Random Access Memory
- b) Synchronous Dynamic Random Access Memory
- c) Both A and B
- d) None of the above

Q.3 Control words consists of (CO-1)

- a) 10 bits      b) 14 bits
- c) 20 bits      d) None of the above

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Q.4 Memory unit that communicates directly with the CPU is (CO-2)

- a) Secondary Memory b) Main Memory
- c) Shared Memory      d) Cache Memory

Q.5 Zero Address Instruction can be used in (CO-1)

- a) Stack
- b) General Register Organization
- c) RISC
- d) Single Accumulator Oragnization

Q.6 POST is (CO-3)

- a) Power On self Test
- b) Post Self Test
- c) Pre Off Self Test
- d) None of the above

**SECTION-B**

**Note:** Objective/ Completion type questions. All questions are compulsory. (6x1=6)

Q.7 BIOS stands for \_\_\_\_\_ (CO-3)

Q.8 RISC stands for \_\_\_\_\_ (CO-1)

Q.9 A memory unit accessed by content is called \_\_\_\_\_ (CO-2)

Q.10 SISD is \_\_\_\_\_ (CO4)

Q.11 DMA stands for \_\_\_\_\_ (CO-3)

Q.12 Data Flow Architecture is a type of Parallel Processors. (T/F) (CO-4)

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## SECTION-C

**Note:** Short answer type questions. Attempt any eight questions out of ten questions. (8x4=32)

- Q.13 Explain RAM chip with the help of diagram. (CO-2)
- Q.14 Differentiate between RISC and CISC processors. (CO-1)
- Q.15 Explain General Register Organization with the help of diagram. (CO-1)
- Q.16 Write a note a memory hierarchy. (CO-2)
- Q.17 Define Parallel processing. List Flynn's classification of parallel computers. (CO-4)
- Q.18 Define Multiprocessor. Elaborate its different types. (CO-4)
- Q.19 Differentiate between Synchronous and Asynchronous data transfer. (CO-5)
- Q.20 Define addressing mode and its types. (CO-1)
- Q.21 List different Mapping Techniques. (CO-2)
- Q.22 Differentiate between Hardwired and Micro programmed Control. (CO-1)

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## SECTION-D

**Note:** Long answer type questions. Attempt any two questions out of three questions. (2x8=16)

- Q.23 Explain BIOS and its different functions. (CO-3)
- Q.24 Explain types of Instruction format along with examples. (CO-1)
- Q.25 Write a note on
- (a) DMA data transfer. (CO-3)
  - (b) Handshaking. (CO-5)

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