**CS431– COMPILER DESIGN LAB**

**CYCLE 1**

1. Design and implement a lexical analyzer for given language using C and the lexical Analyzer should ignore redundant spaces, tabs and new lines.
2. Write program to find ε – closure of all states of any given NFA with ε transition.
3. Write program to convert NFA with ε transition to NFA without ε transition.
4. Write program to convert NFA to DFA.
5. Implementation of Lexical Analyzer using Lex Tool.
6. Generate YACC specification for a few syntactic categories.

a) Program to recognize a valid arithmetic expression that uses operator +, – , \* and /.

b) Program to recognize a valid variable which starts with a letter followed by any

number of letters or digits.

c) Implementation of Calculator using LEX and YACC

d) Convert the BNF rules into YACC form and write code to generate abstract

Syntax tree

7. Write a program to minimize any given DFA

**CYCLE 2**

8. Develop an operator precedence parser for a given language.

9. Write program to find Simulate First and Follow of any given grammar.

10. Construct a recursive descent parser for an expression.

11. Construct a Shift Reduce Parser for a given language.

12. Write a program to perform loop unrolling.

13. Write a program to perform constant propagation.

14. Implement Intermediate code generation for simple expressions.

15. Implement the back end of the compiler which takes the three address code and

produces the 8086 assembly language instructions that can be assembled and run

using an 8086 assembler. The target assembly instructions can be simple move, add,

sub, jump etc.