

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

HYDERABAD - 500 085, TELANGANA STATE, INDIA

TS 506645



H.T No.: 09291A04B6

College Code : 29 (V R E C, NIZAMABAD)

Mr. Mohd Waseemuddin

S/o. Mohd Khatib Moinuddin

having fulfilled the academic requirements and passed the examination
held during *May, 2013* in *First Class With Distinction*
has this day been admitted by the Executive Council to the Degree of

Bachelor Of Technology
Electronics & Communication Engineering

Given under the Seal of the University

Date: 04-09-2014



DIRECTOR OF EVALUATION

REGISTRAR

VICE CHANCELLOR

CMM No
21071003254



4520107109430

PC No
31071003248



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

HYDERABAD - 500 085, ANDHRA PRADESH, INDIA

CONSOLIDATED MARKS MEMO / CREDIT SHEET



BACHELOR OF TECHNOLOGY- ELECTRONICS & COMMUNICATION ENGINEERING

CMM. No.: **c 0298395**

Serial No.: 21071003254

Name: **MOHD WASEEMUDDIN**

Name of the College: 29-V R E C, NIZAMABAD

Month & Year of Final Exam: May, 2013

Hall Ticket No.: 09291A04B6

Year of Admission: 2009-2010

Class Awarded: **FIRST CLASS WITH DISTINCTION**

S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS	S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS
	Maximum Marks in Theory	25	75	100			Maximum Marks in Lab	25	50	75	
I YEAR											
1	ENGLISH	23	58	81	4	2	MATHEMATICS - I	24	56	80	6
3	MATHEMATICAL METHODS	25	69	94	6	4	ENGINEERING PHYSICS	22	66	88	4
5	ENGINEERING CHEMISTRY	24	45	69	4	6	COMPUTER PROGRAMMING & DATA STRUCTURES	21	34	55	6
7	ENGINEERING DRAWING	23	58	81	4	8	COMPUTER PROGRAMMING LAB	25	49	74	4
9	ENGG. PHYSICS & ENGG. CHEMISTRY LAB	24	50	74	4	10	ENGLISH LANGUAGE COMMUNICATION SKILLS LAB	25	46	71	4
11	IT WORKSHOP & ENGINEERING WORKSHOP	23	47	70	4						
I SEMESTER											
II YEAR											
1	MATHEMATICS-III	25	50	75	3	1	PRINCIPLES OF ELECTRICAL ENGINEERING	22	68	90	3
2	PROBABILITY THEORY & STOCHASTIC PROCESSES	25	44	69	3	2	ELECTRONIC CIRCUIT ANALYSIS	25	31	56	4
3	ENVIRONMENTAL STUDIES	25	52	77	3	3	PULSE & DIGITAL CIRCUITS	25	29	54*	4
4	ELECTRIC CIRCUITS	22	38	60	4	4	SWITCHING THEORY & LOGIC DESIGN	25	31	56	4
5	ELECTRONIC DEVICES & CIRCUITS	22	33	55	4	5	ELECTROMAGNETIC THEORY & TRANSMISSION LINES	24	40	64	4
6	SIGNALS & SYSTEMS	24	31	55	4	6	ELECTRICAL ENGINEERING LAB	25	47	72	2
7	ELECTRONIC DEVICES & CIRCUITS LAB	24	49	73	2	7	ELECTRONIC CIRCUIT ANALYSIS LAB	25	46	71	2
8	BASIC SIMULATION LAB	24	41	65	2	8	PULSE & DIGITAL CIRCUITS LAB	24	49	73	2
I SEMESTER											
III YEAR											
1	CONTROL SYSTEMS	23	50	73	4	1	MANAGERIAL ECONOMICS & FINANCIAL ANALYSIS	25	56	81	4
2	COMPUTER ORGANIZATION	24	31	55	4	2	OBJECT ORIENTED PROGRAMMING	22	48	70	4
3	ANTENNAS & WAVE PROPAGATION	23	54	77	3	3	DIGITAL COMMUNICATIONS	25	30	55	3
4	ELECTRONIC MEASUREMENTS & INSTRUMENTATION	24	35	59	4	4	MICROPROCESSORS & MICROCONTROLLERS	24	57	81	4
5	ANALOG COMMUNICATIONS	24	32	56	3	5	DIGITAL SIGNAL PROCESSING	22	43	65	4
6	IC APPLICATIONS	23	32	55*	3	6	MICROPROCESSORS & MICROCONTROLLERS (LAB)	24	48	72	2
7	ANALOG COMMUNICATIONS (LAB)	24	48	72	2	7	DIGITAL SIGNAL PROCESSING (LAB)	22	50	72	2
8	IC APPLICATIONS (LAB)	24	50	74	2	8	ADVANCED ENGLISH COMMUNICATION SKILLS (LAB)	25	48	73	2
I SEMESTER											
IV YEAR											
1	MANAGEMENT SCIENCE	22	37	59	3	1	CELLULAR & MOBILE COMMUNICATIONS	25	41	66	3
2	VLSI DESIGN	25	64	89	4	2	RADAR SYSTEMS	25	57	82	3
3	MICROWAVE ENGINEERING	25	38	63	3	3	DIGITAL DESIGN THROUGH VERILOG HDL	25	50	75	3
4	COMPUTER NETWORKS	23	47	70	4	4	INDUSTRY ORIENTED MINI PROJECT	-	44	44	2
5	DIGITAL IMAGE PROCESSING	25	44	69	3	5	SEMINAR	50	-	50	2
6	OPTICAL COMMUNICATIONS	25	47	72	4	6	MAJOR PROJECT	49	147	196	10
7	E-CAD AND VLSI LAB	25	50	75	2	7	COMPREHENSIVE VIVA	-	96	96	2
8	MICROWAVE ENGG. AND DIGITAL COMMNS. LAB	25	50	75	2						

(* Project Internal= 50, External=150)

Number of Credits registered for : 200 Aggregate Marks Secured for best: 193

Aggregate Marks Secured : 4064 OUT OF 5200 (78.15%)

Date of Issue : June 21, 2013

(see overleaf for Rules concerned to award of class)

A indicates 'ABSENT'



(*Courses registered but not counted for calculation of aggregate)

CONTROLLER OF EXAMINATIONS

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
HYDERABAD - 500 085, ANDHRA PRADESH, INDIA

College: 29 - V R E C, NIZAMABAD

31071003248

HT No: 09291A04B6

SI. No. PC 00442392



PROVISIONAL CERTIFICATE

21071003254

This is to certify that Mr. MOHD WASEEMUDDIN
son of Mr. MOHD KHATIB MOINUDDIN
passed B. Tech. (ELECTRONICS & COMMUNICATION ENGINEERING) degree
examination of this University, held in May, 2013 and that he was
placed in FIRST CLASS WITH DISTINCTION.

He has satisfied all the requirements for the award of the degree.

Verified by
Hyderabad - A.P.


Controller of Examinations


REGISTRAR

Date: June 24, 2013

